# NAVI14 GDDR6 4PCS,8L 3xDPs +1xHDMI

# 105-D332xx-00B RevB Desktop board(110W TGP)

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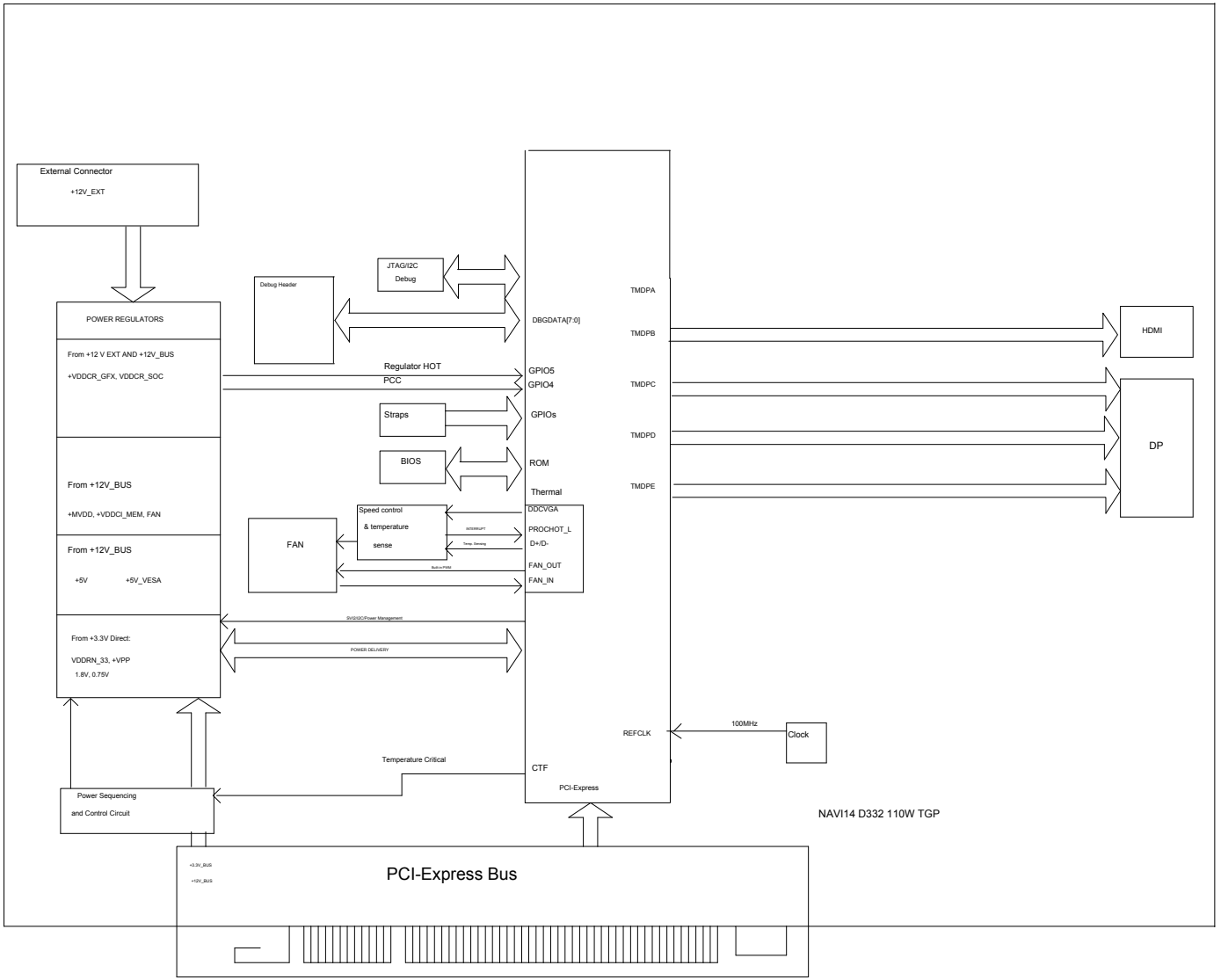


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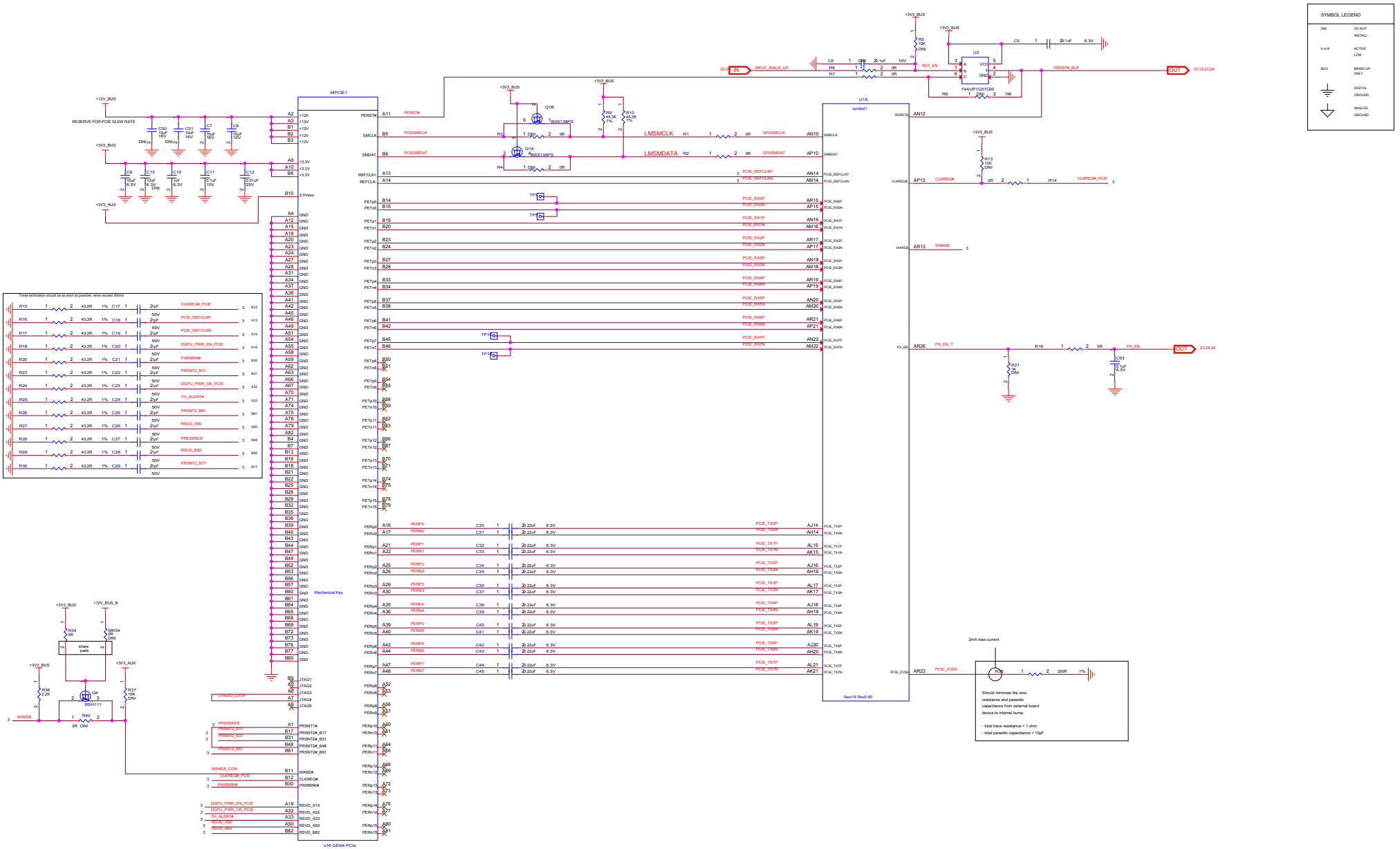
(3) NAVI14 PCIe Edge Connector

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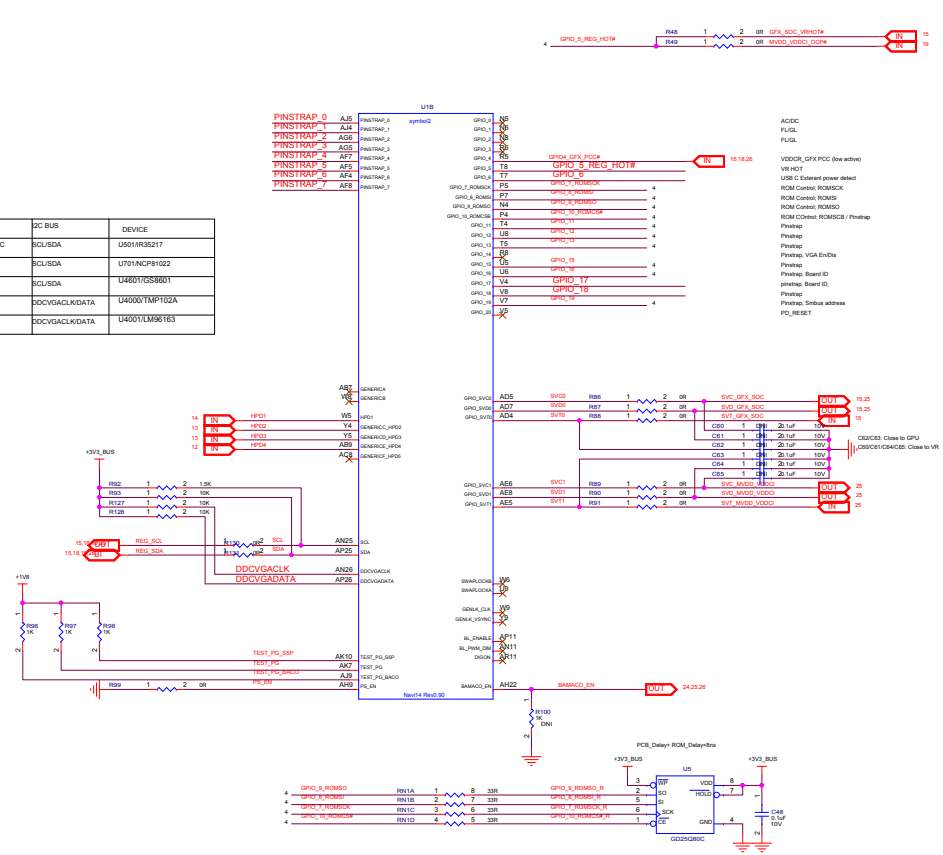
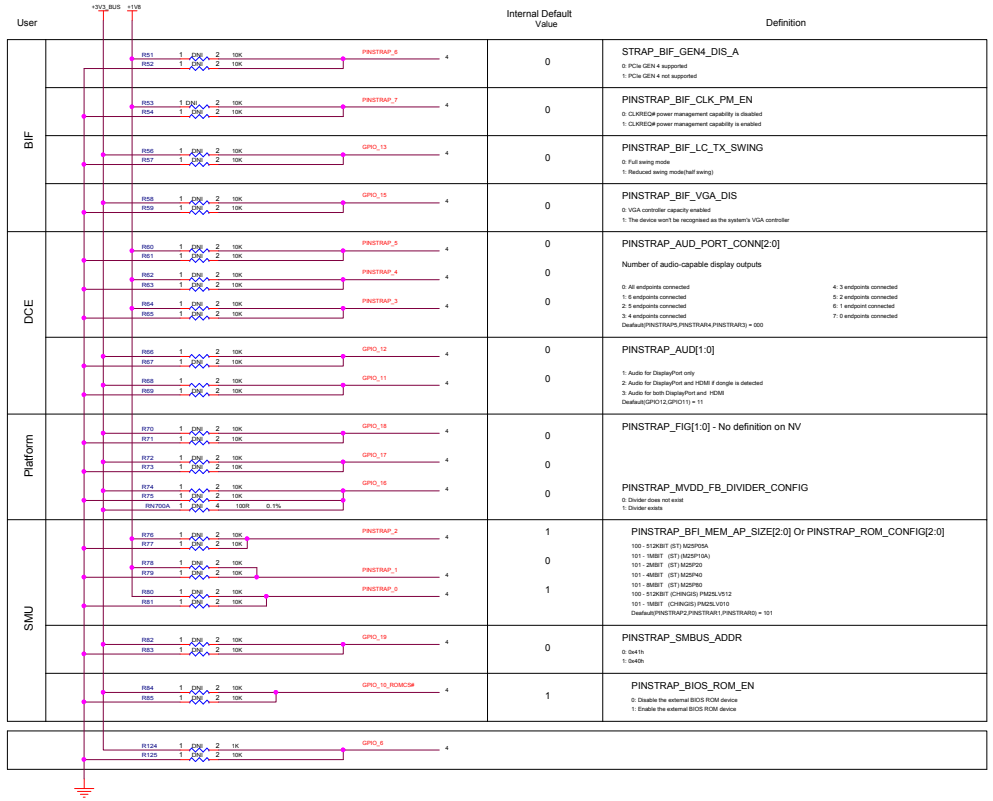
(4) NAVI14 GPIOs & ROM

| 7-BIT I2C ADDRESS | 8-BIT I2C ADDRESS | FUNCTION | I2C BUS | DEVICE |
|---|---|---|---|---|
| 0x30 | 0x60 | VDDCR_GFX/SOC | SCL/SDA | U501/IR35217 |
| 0x22 | 0x44 | MVDD/VDDCI | SCL/SDA | U701/NCP81022 |
| 0x53 | 0xA6 | PCCpreserved | SCL/SDA | U4601/GS8601 |
| 0x48 | 0x90 | G6 Temperture | DDCVGACLK/DATA | U4000/TMP102A |
| 0x4C | 0x98 | EXT SENSOR | DDCVGACLK/DATA | U4001/LM96163 |




Note: Internal PU/PD at GPIO pad is ~40k strength in 14nm design spec

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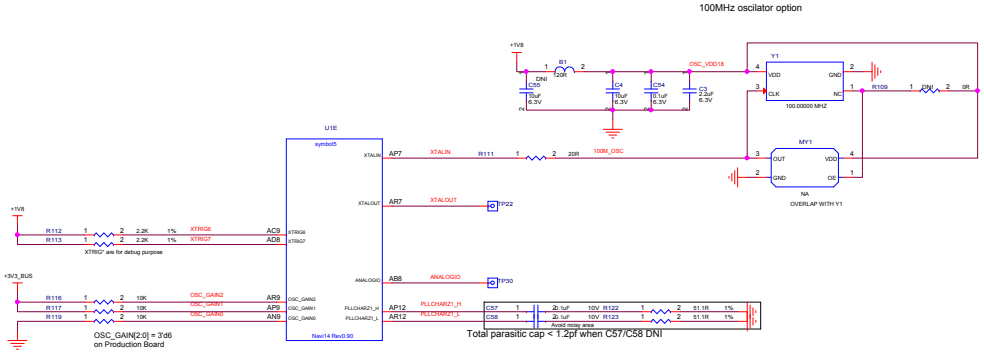


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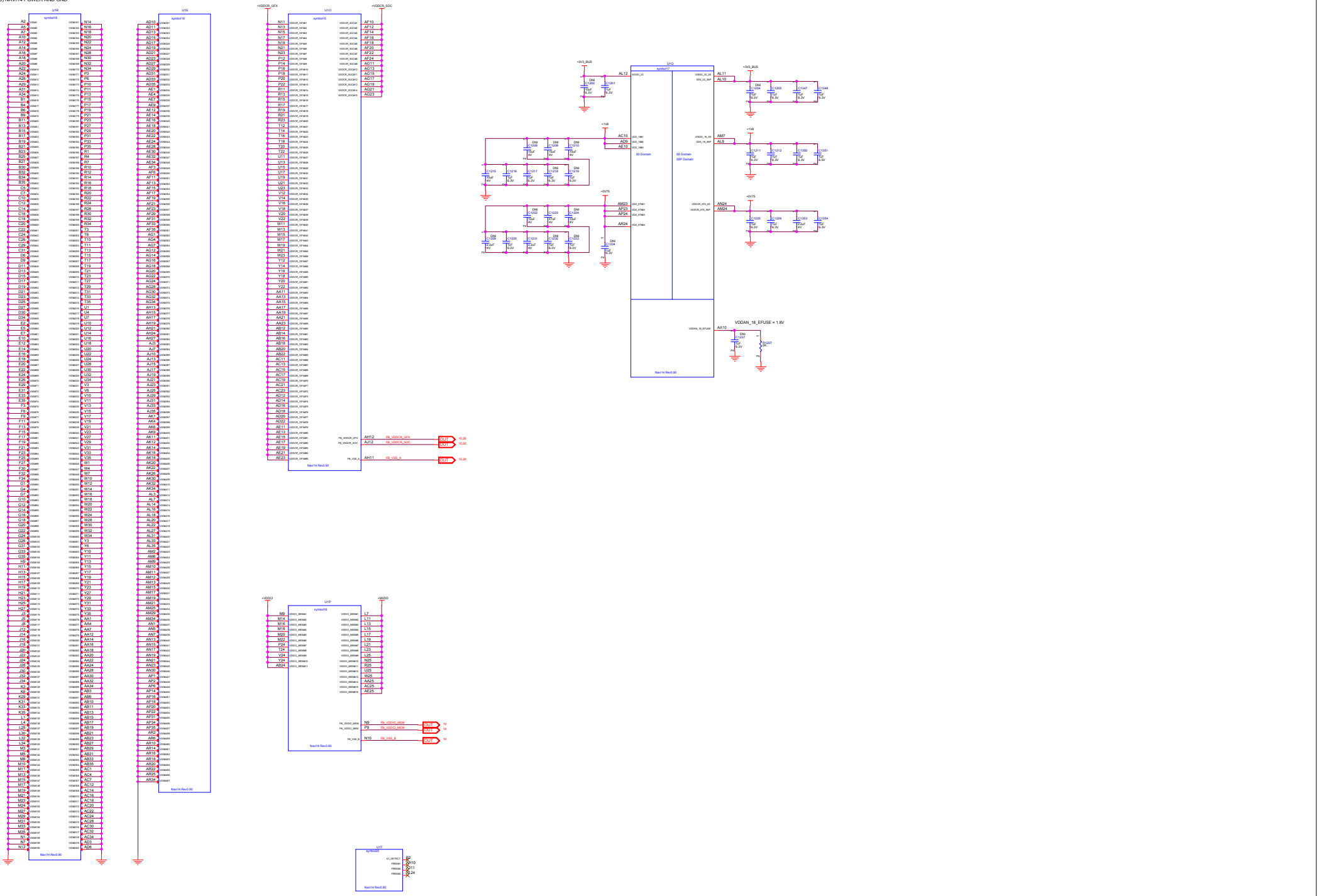
(6) NAVI14 POWER AND GND



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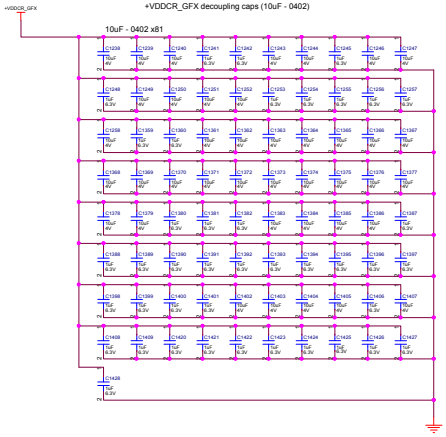


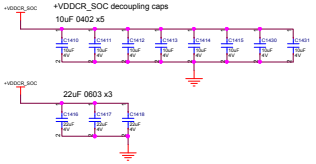


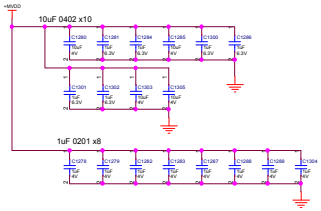


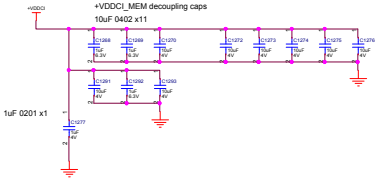


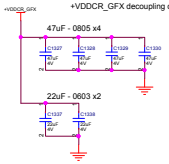


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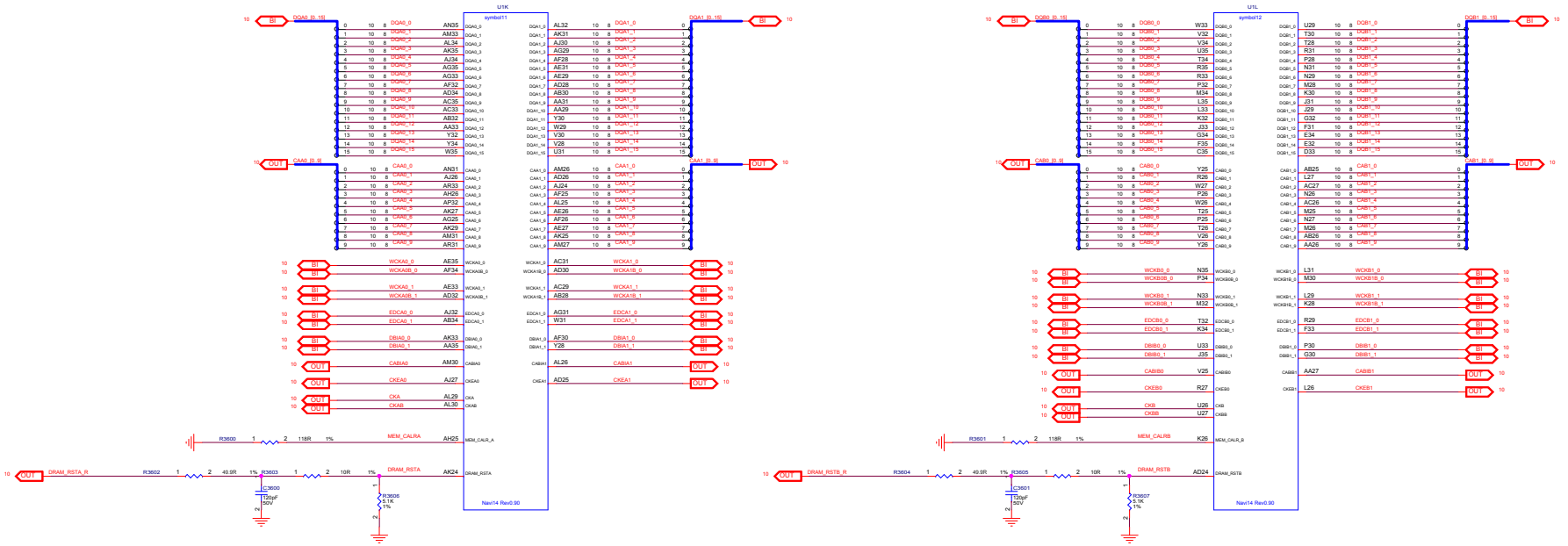
(8) NAVI14 MEM INTERFACE CH A/B

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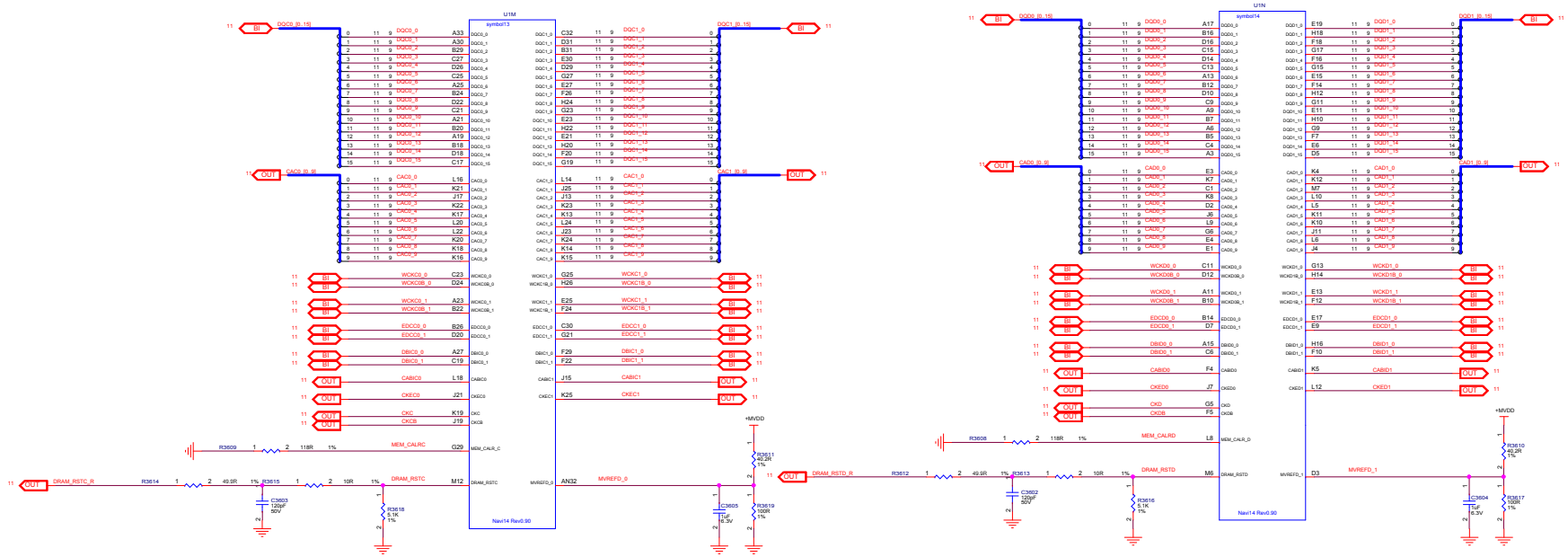
(9) NAVI14 MEM INTERFACE CH C/D

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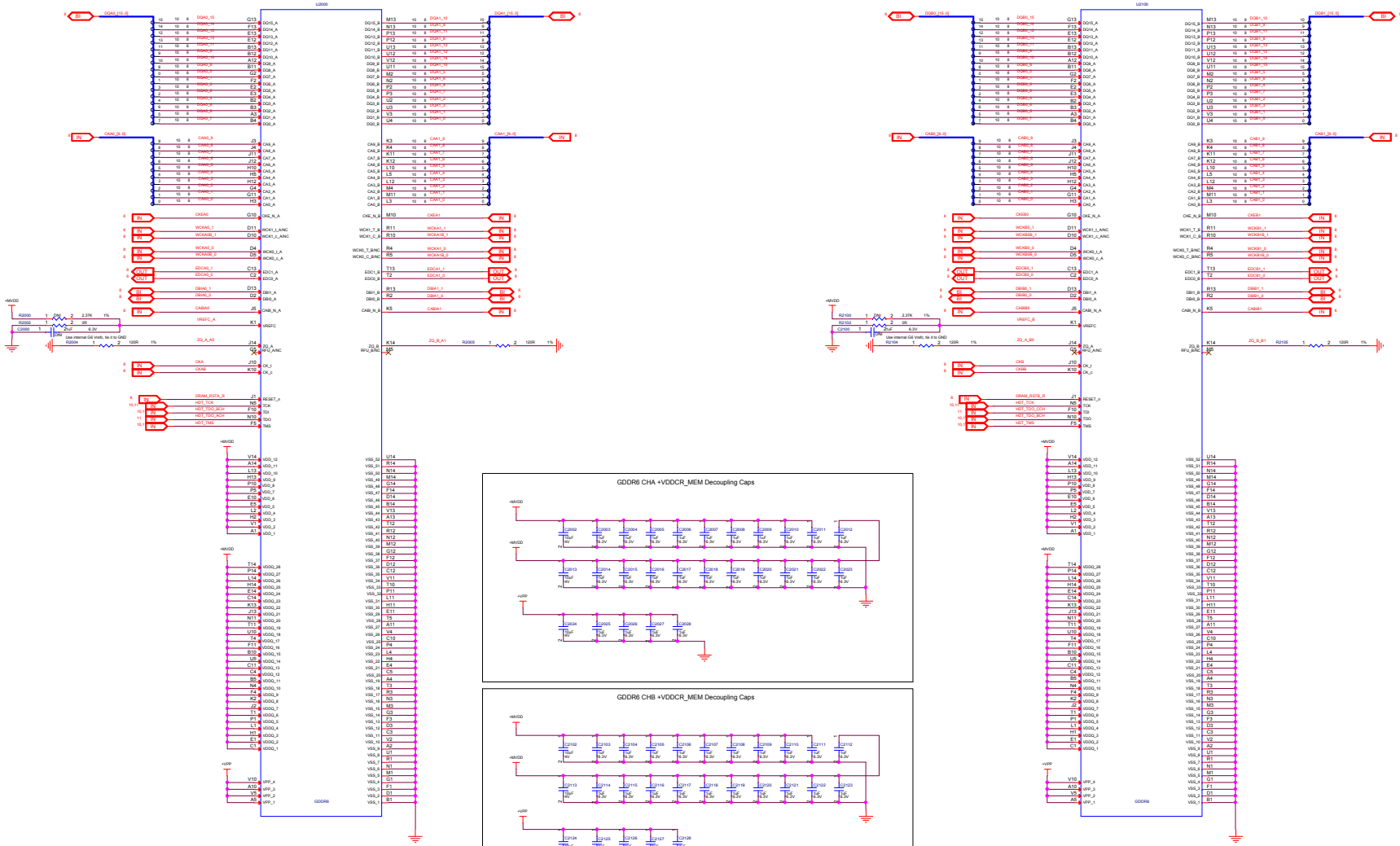

GDDR6 CHA +VDDCR_MEM Decoupling Caps



GDDR6 CHB +VDDCR_MEM Decoupling Caps



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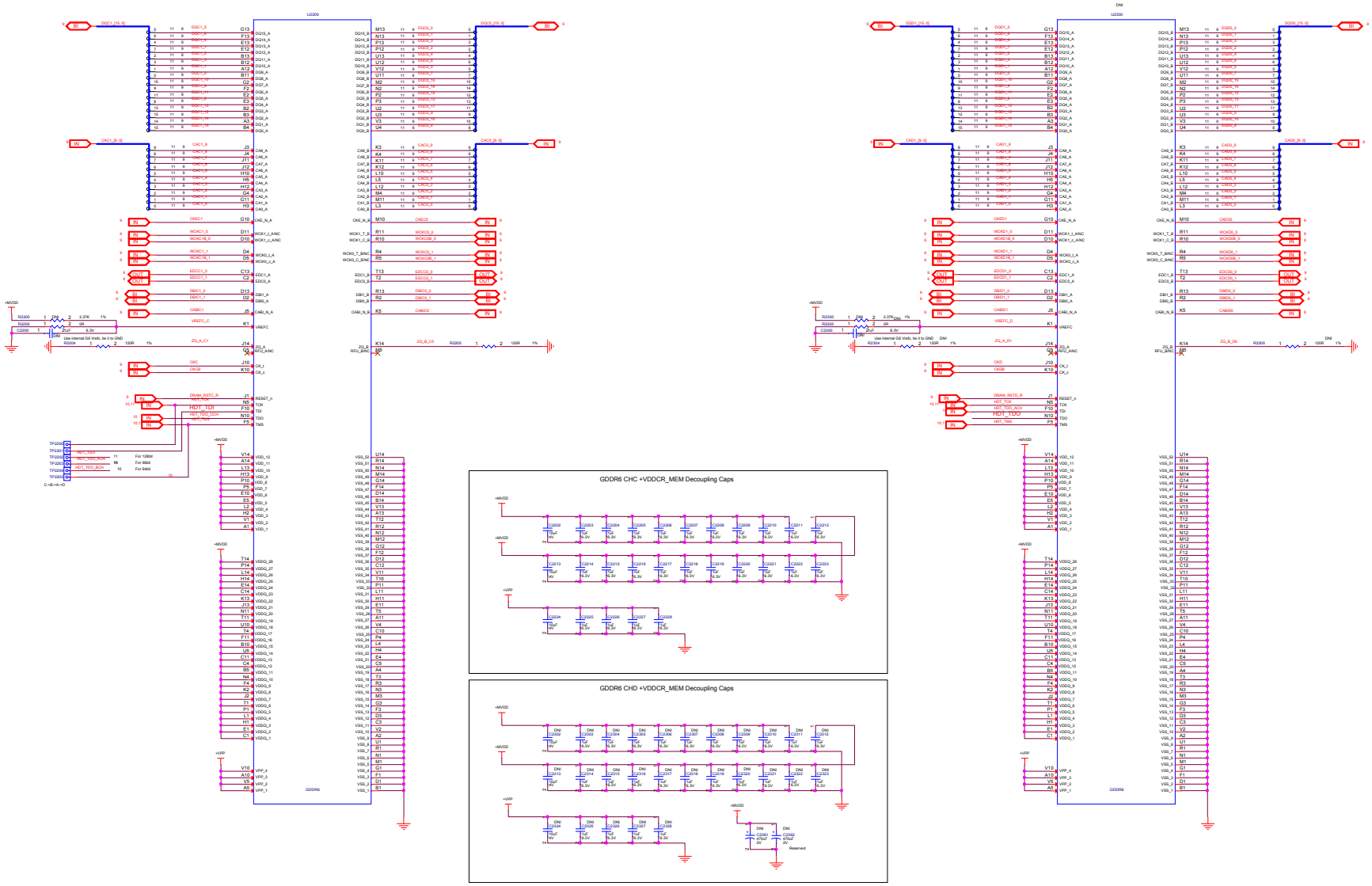

GDDR6 CHC +VDDCR_MEM Decoupling Caps

GDDR6 CHD +VDDCR_MEM Decoupling Caps

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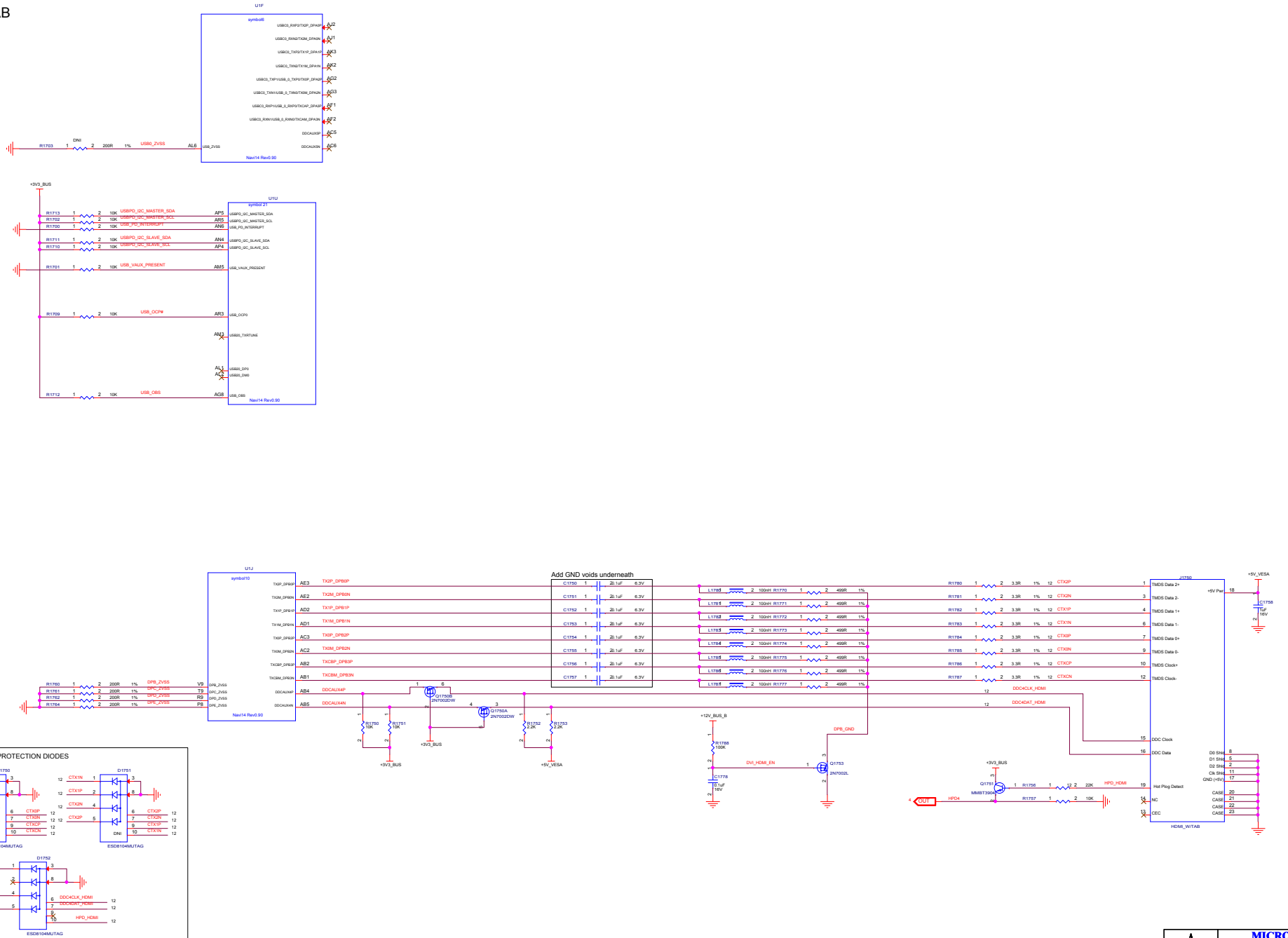
(12) TMDP AB

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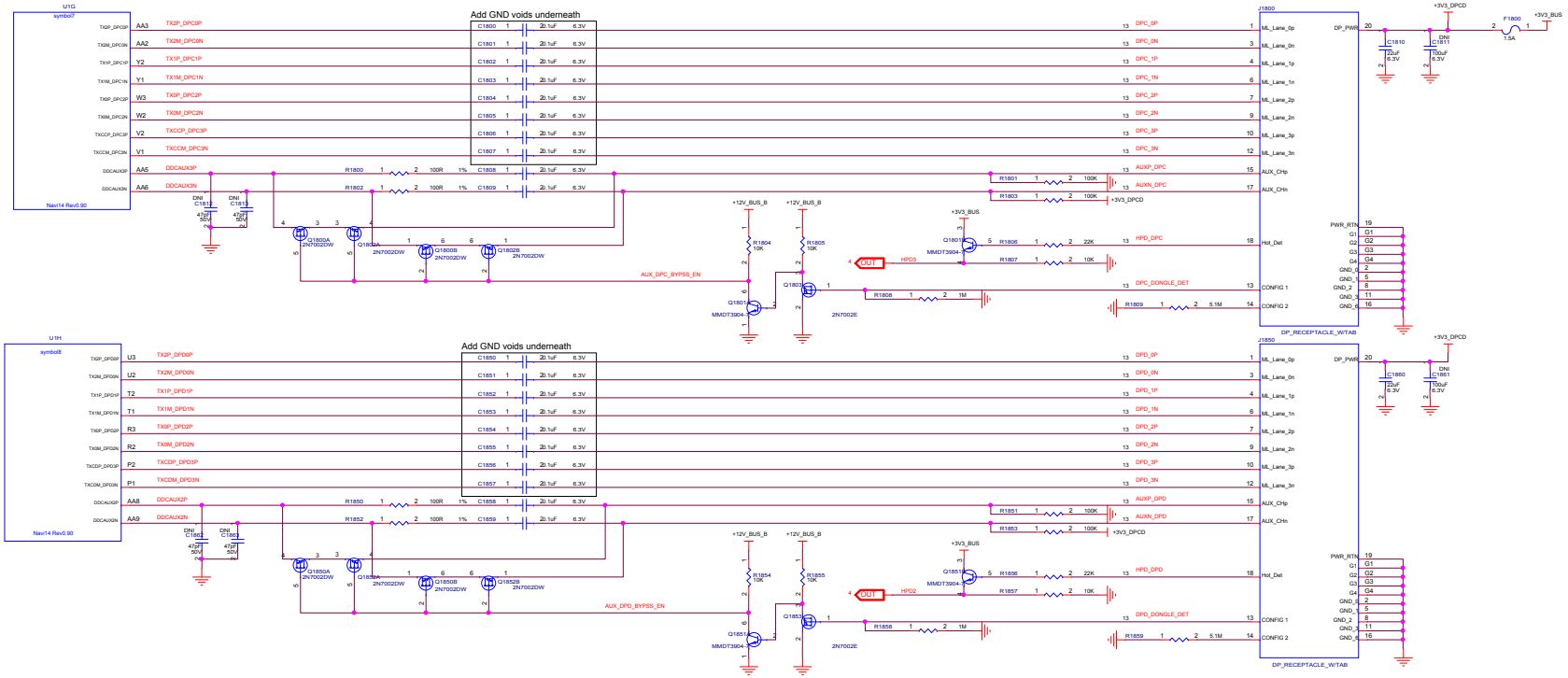
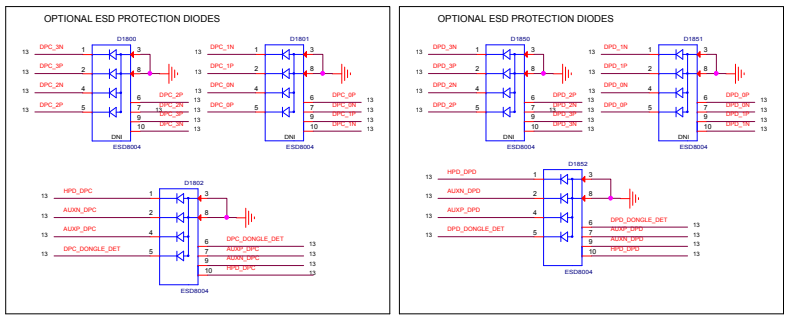
(13) TMDP D/E







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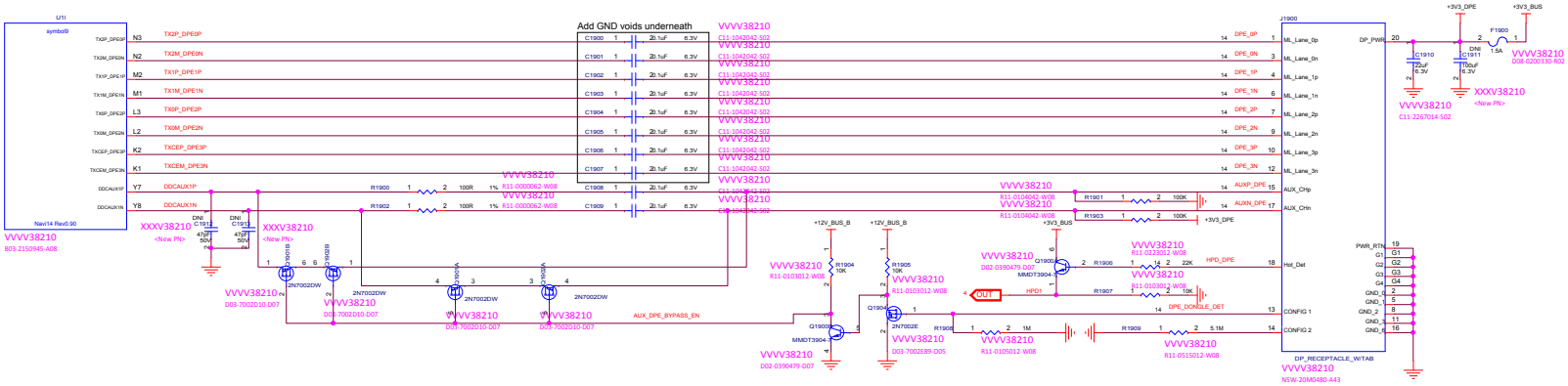


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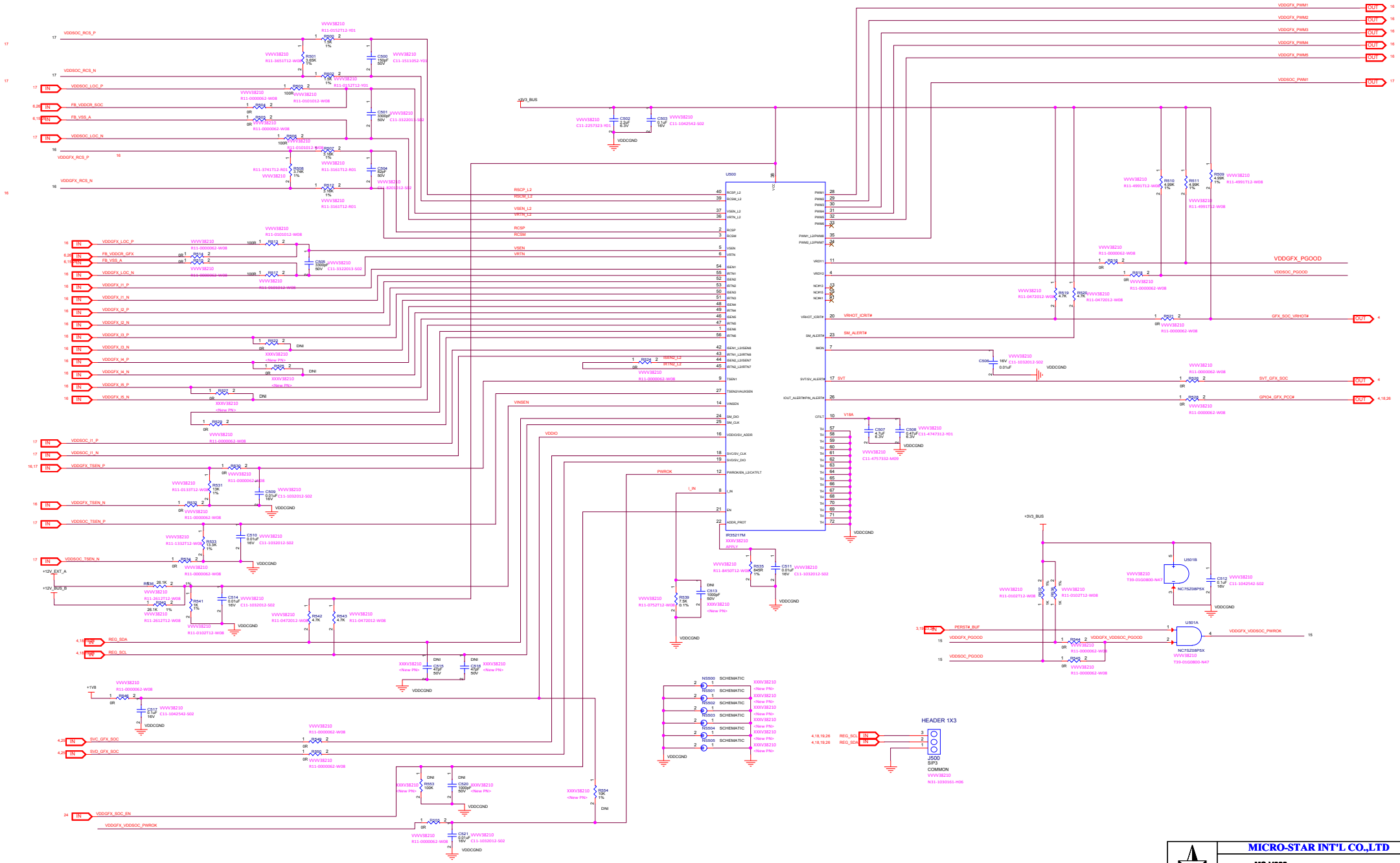
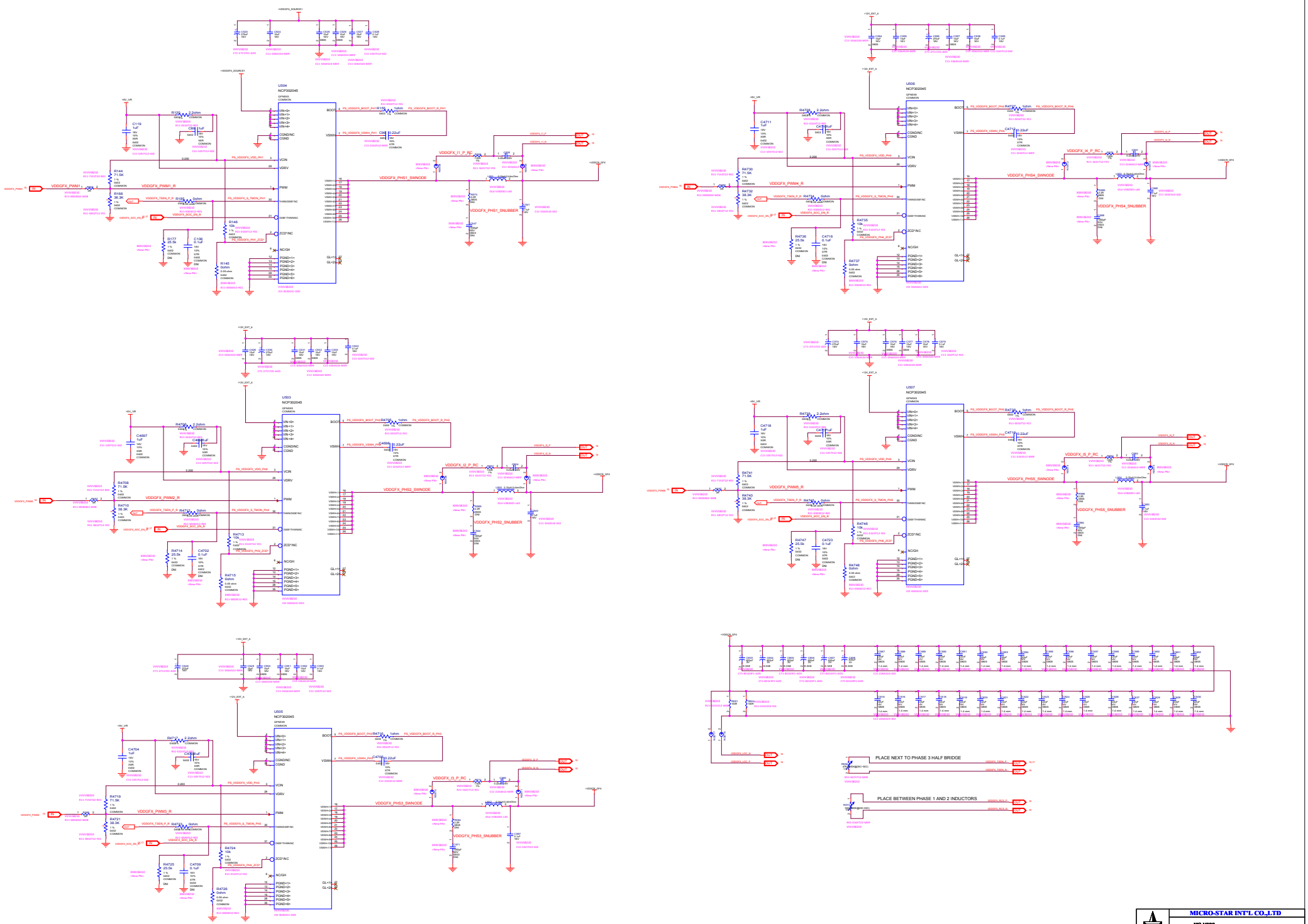
# REG - VDDGFX/VDDSOC CTRL



# REG - VDDGFX

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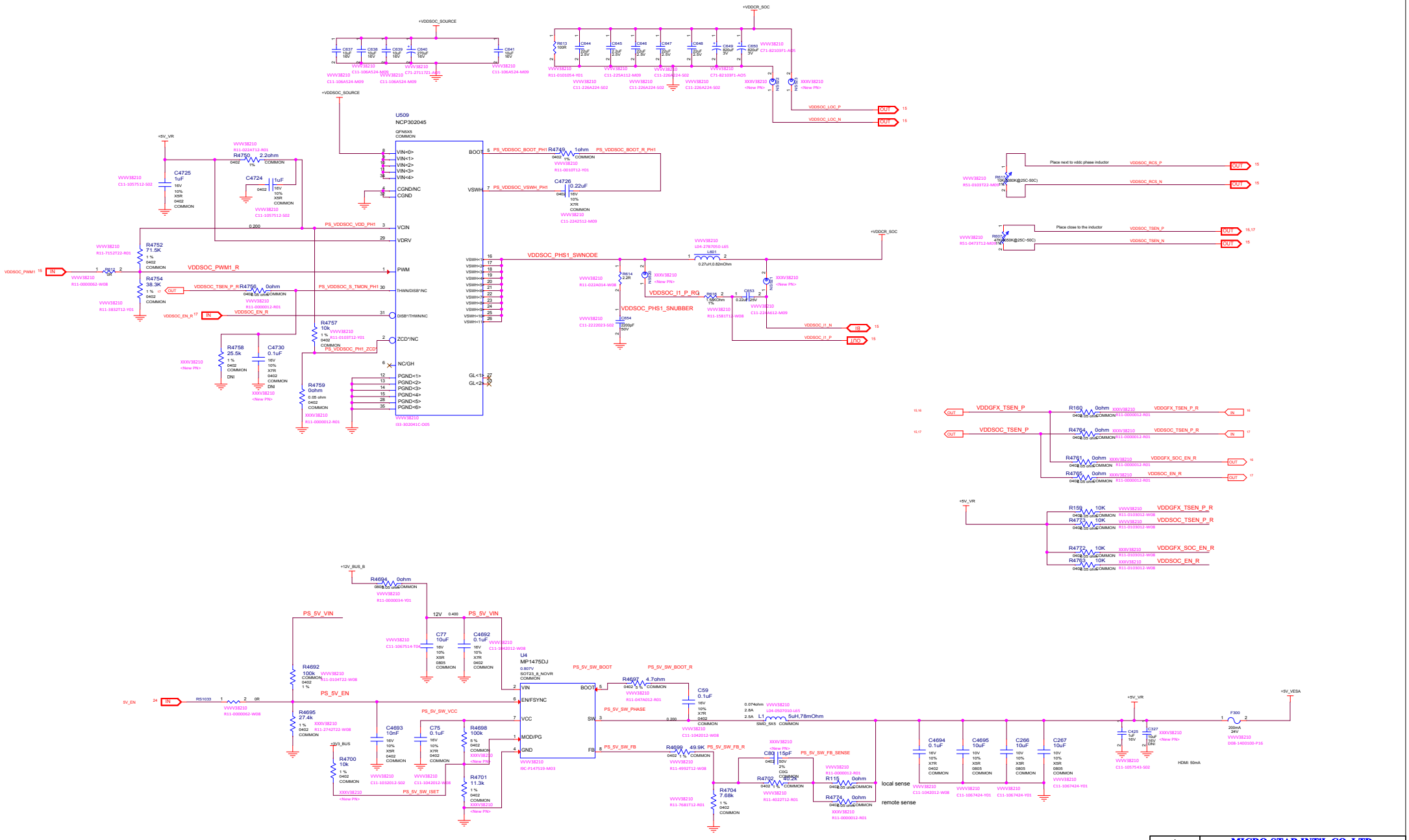
REG: VDDSOC




0.8V*(1+40.2K/7.68K)=4.9875V

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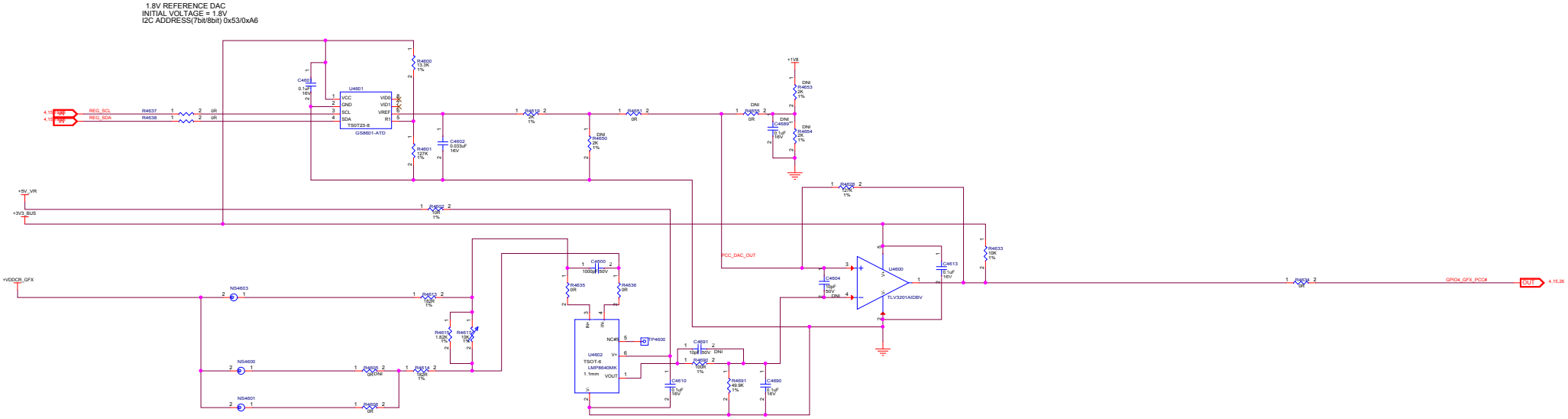


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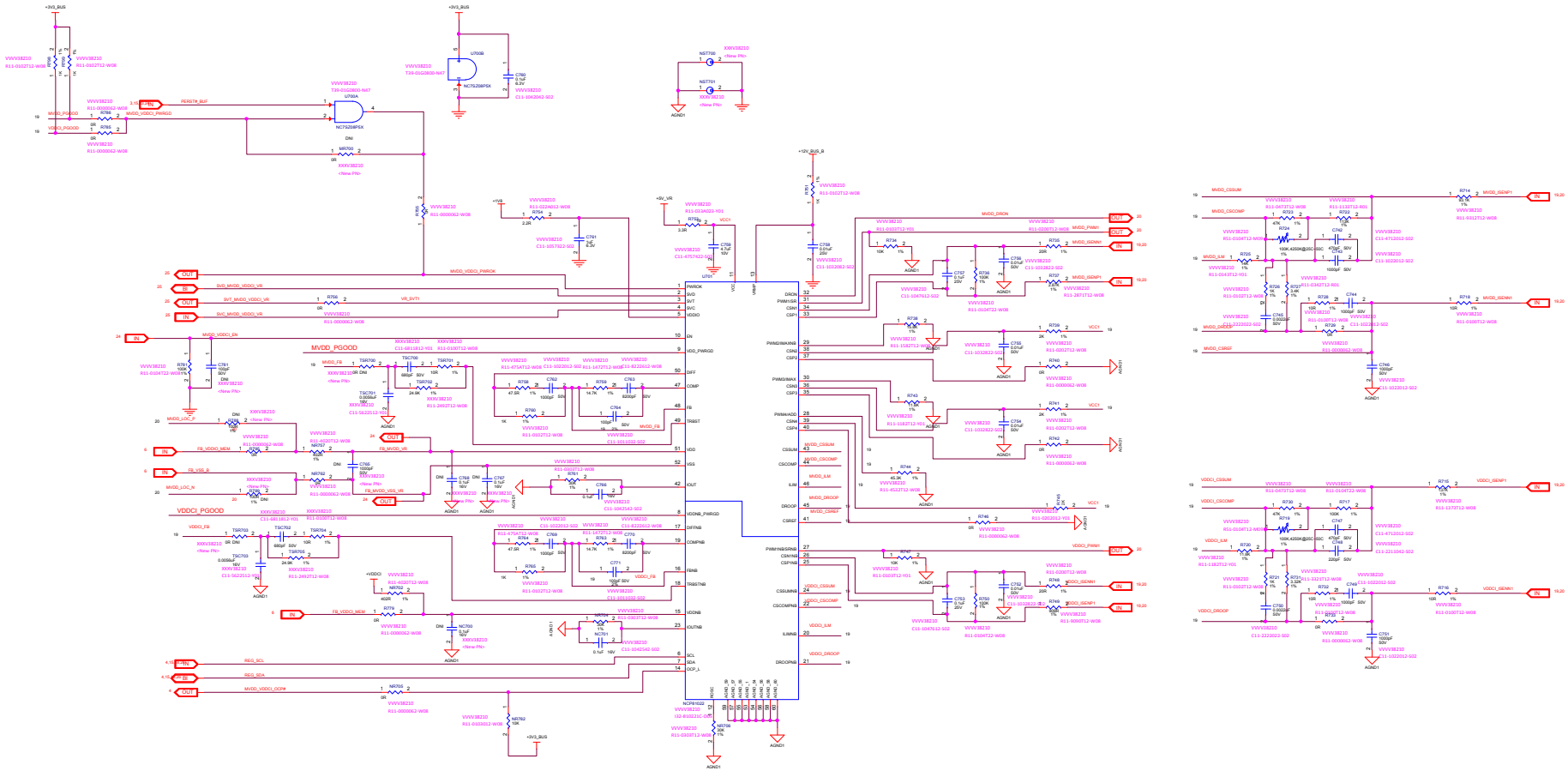

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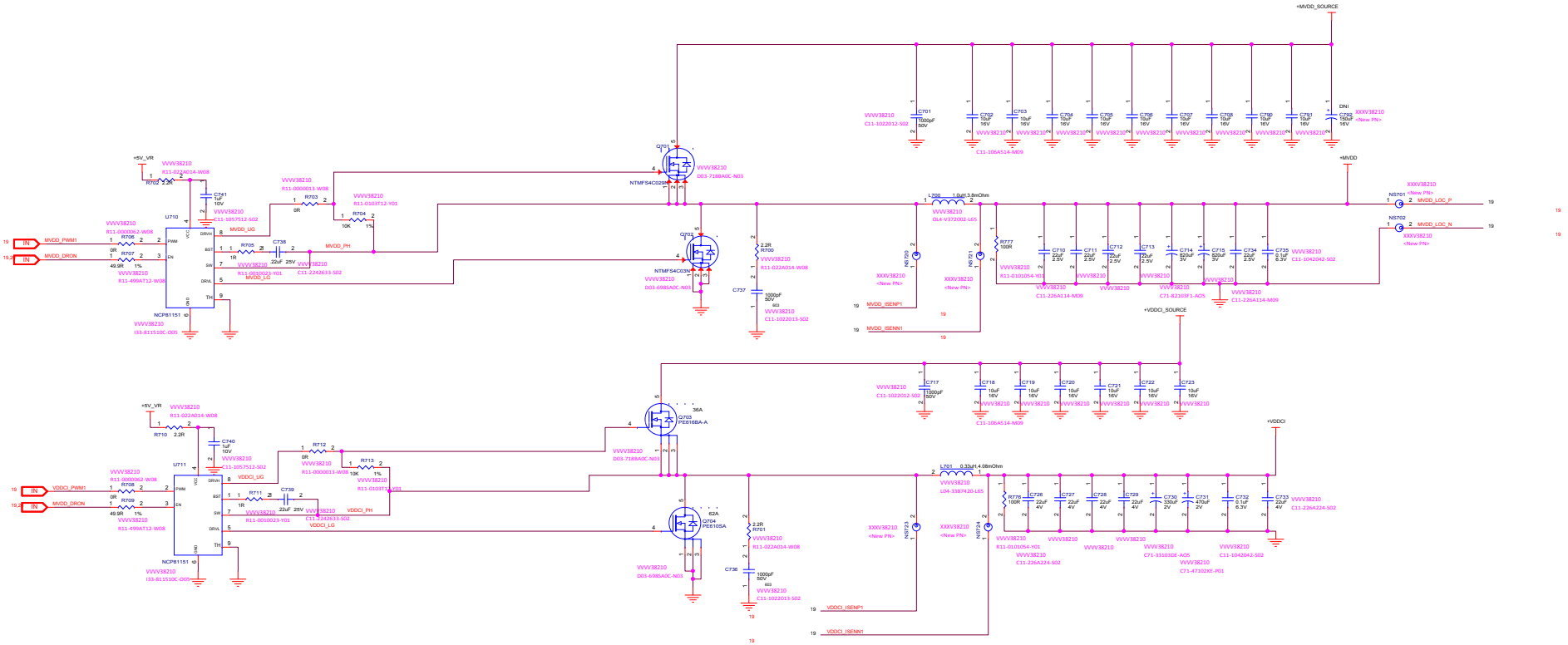
REG - MVDD/VDDCI

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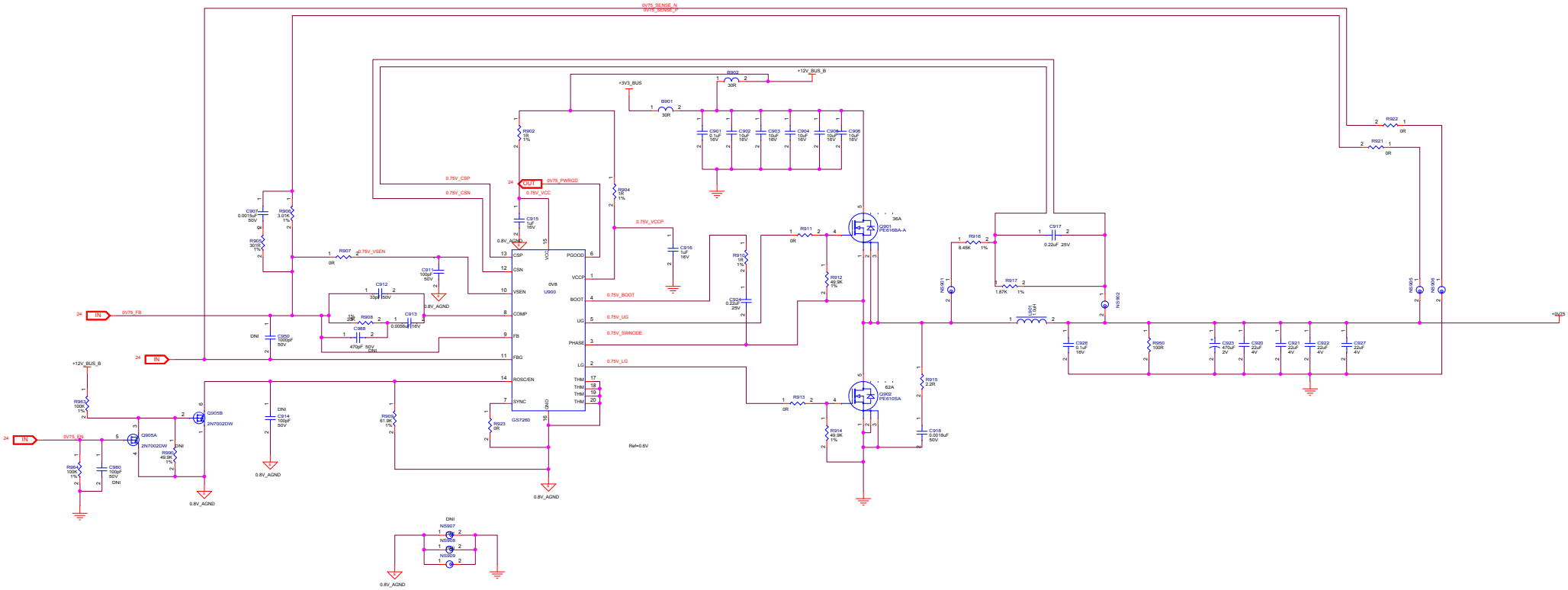
REG - 0.75V

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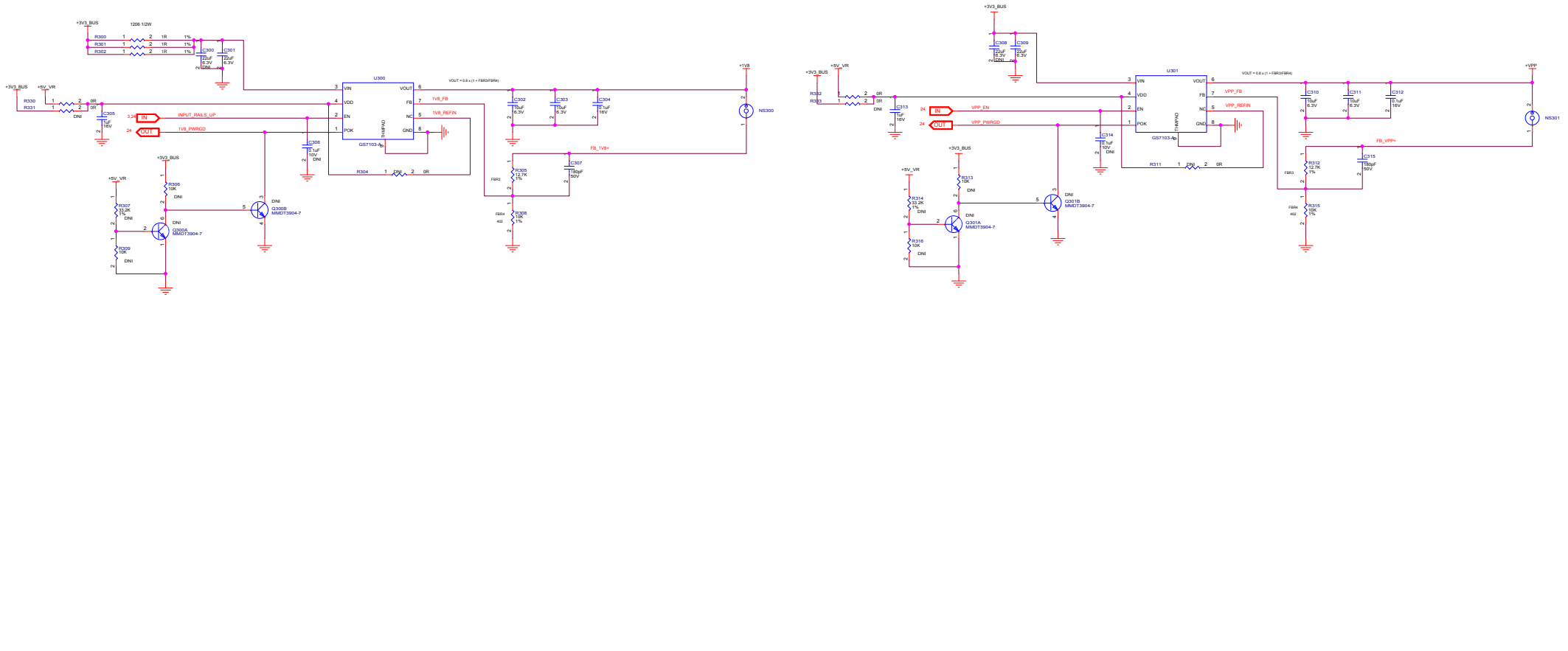
# LDO - 1V8/VPP/5V

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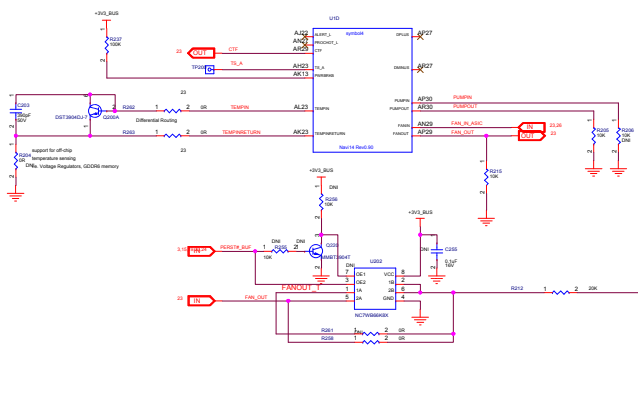


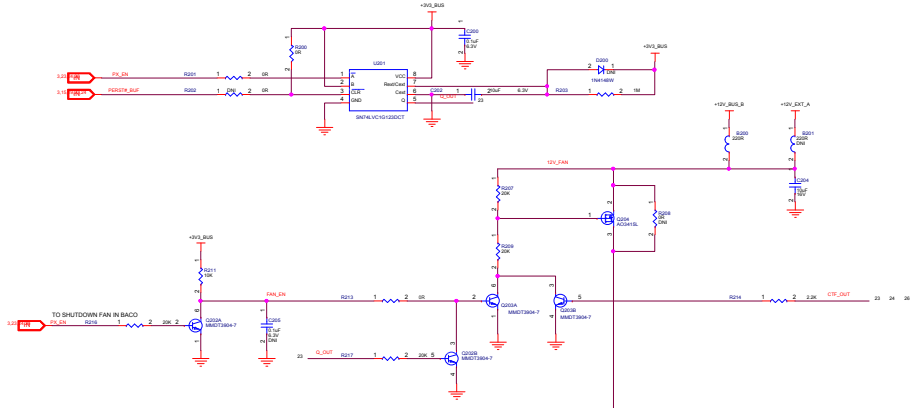


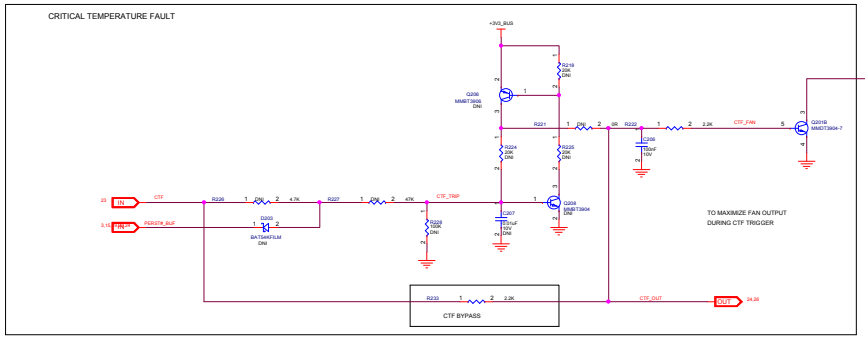


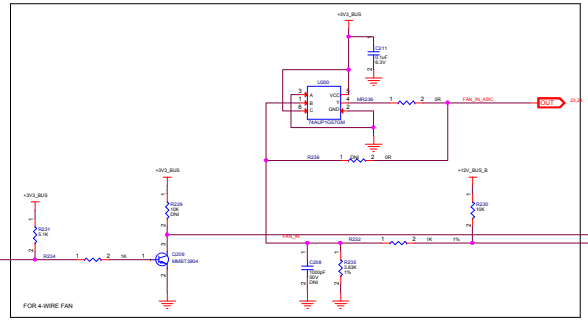


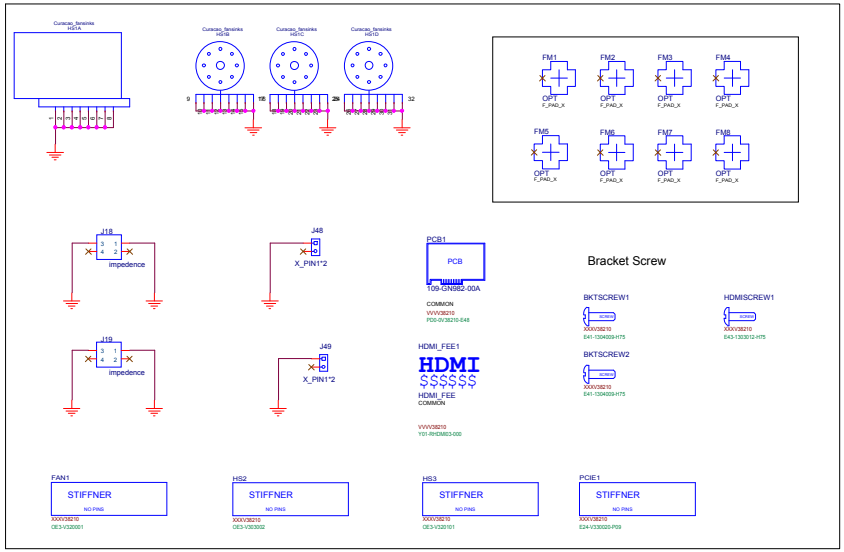


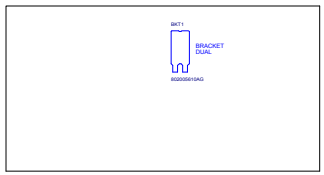


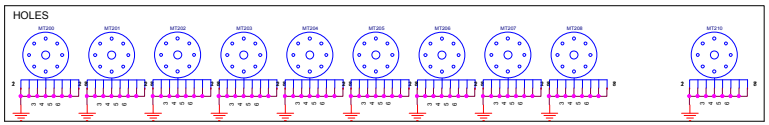


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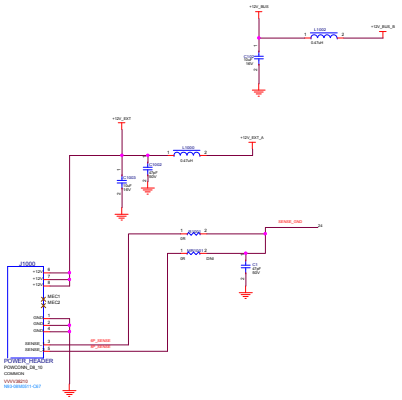


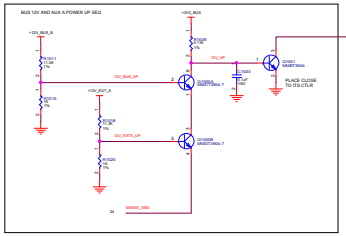


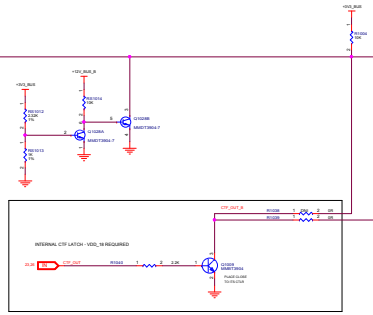


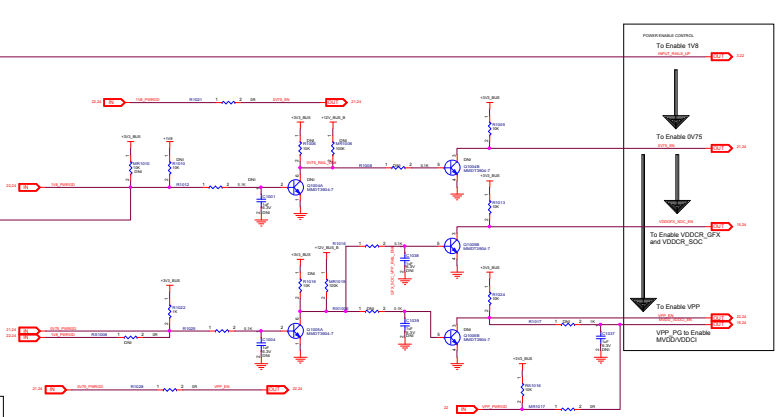


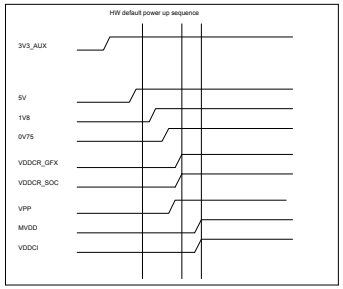


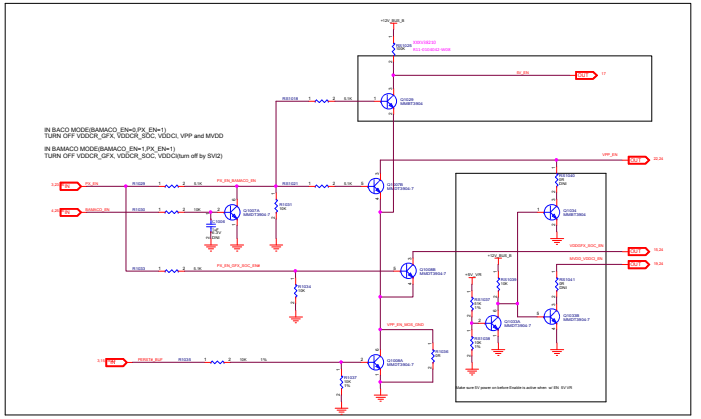


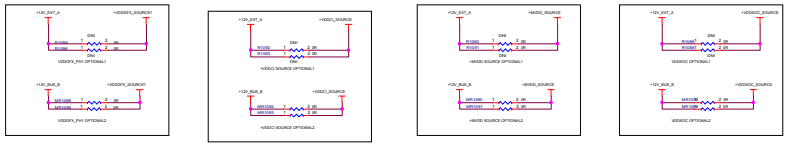

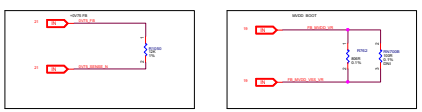


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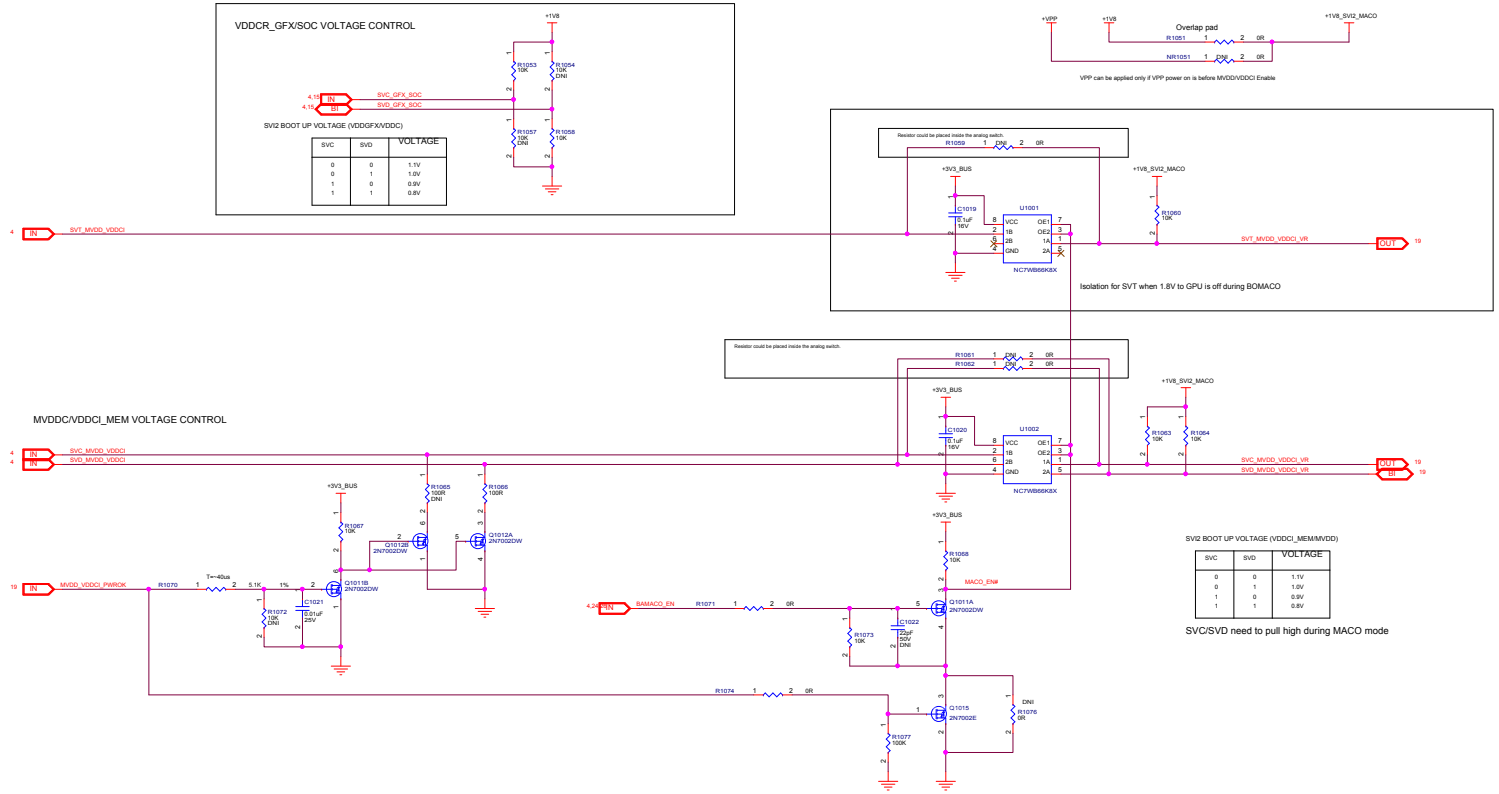
(28) SVI2

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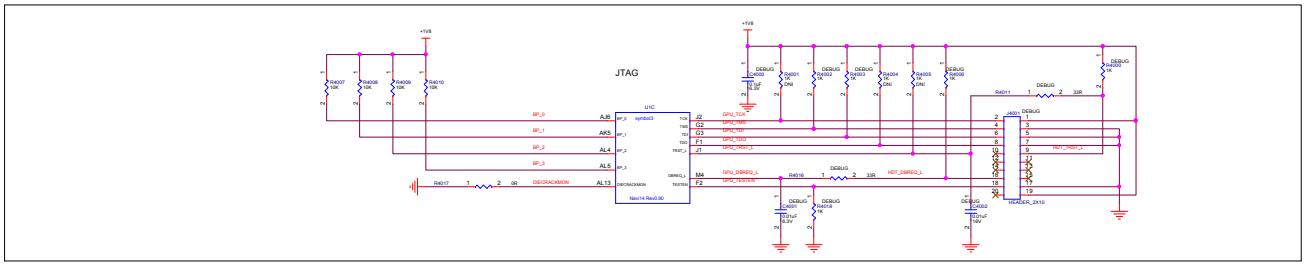
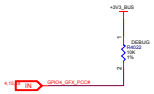
(29) DEBUG CIRCUIT



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DOCUMENT NUMBER: 105-D33200-00B

DATE: Mon Apr 22 08:33:14 2019

SHEET NUMBER: 27 OF 27

REV: B

# REVISION HISTORY

ENGINEER: Billy Deng

NOTES: NOTE



AMD-PLATFORM HARDWARE ENG
No.669,HUANKE ROAD
SHANGHAI,CHINA 201203

| SCH Rev | PCB Rev | Date | REVISION DESCRIPTON |
|---|---|---|---|
| 1.00 | 00A | 08/09/2018 | Initial schematic |
| 1.00 | 00B | 12/28/2018<br>04/16/2019 | change all 4172010501G parts to 4172010500G<br>ADD D1752 D1802 D1852 D1952<br>ADD R795 AND CHANGE MVDD LOCAL SENSE POINT<br>ADD R1092 R1093 MR1092 MR1093 and reassign gfx phase1-3 input source<br><br>ADD R4653 R4654 R4655 C4689<br><br>Change 701: 0.33uH/5260116000G R749: 909R/3160909000G NR704:30K/3160300200G R743: 18K/3160180200G C747:470pF/4170047100G C748:220pF/4 1700221006 |

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