

2019/08/28:

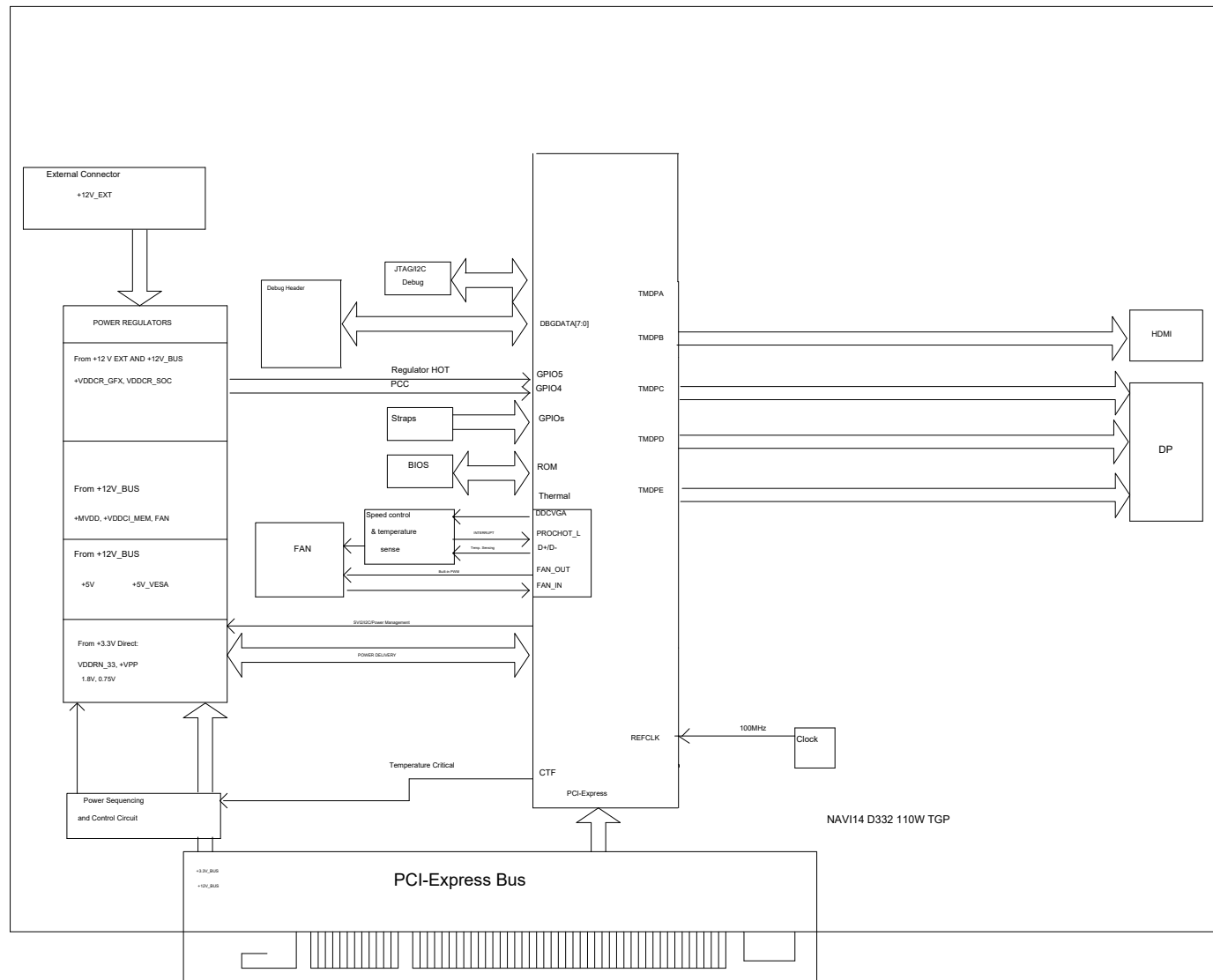
PAGE14: 上件DP
PAGE24: 改反向6 & 8 PIN COLAY
ADD FUSE
PAGE27: ADD MCU

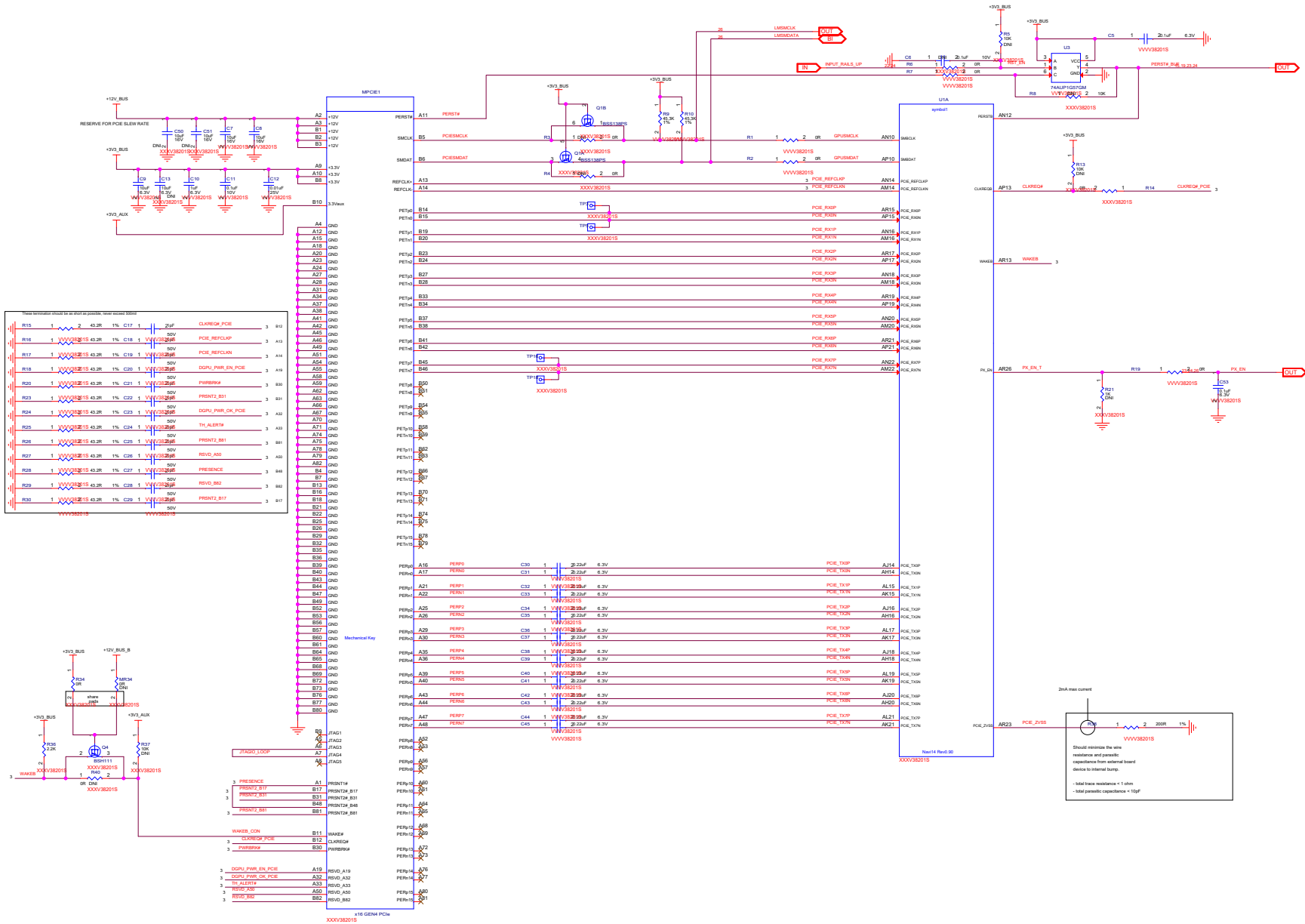
NAVI14 GDDR6 4PCS,8L 3xDPs +1xHDMI

105-D332xx-00B RevB Desktop board(110W TGP)

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7	NAVI14 - Decaps		
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24	POWER MANAGEMENT		
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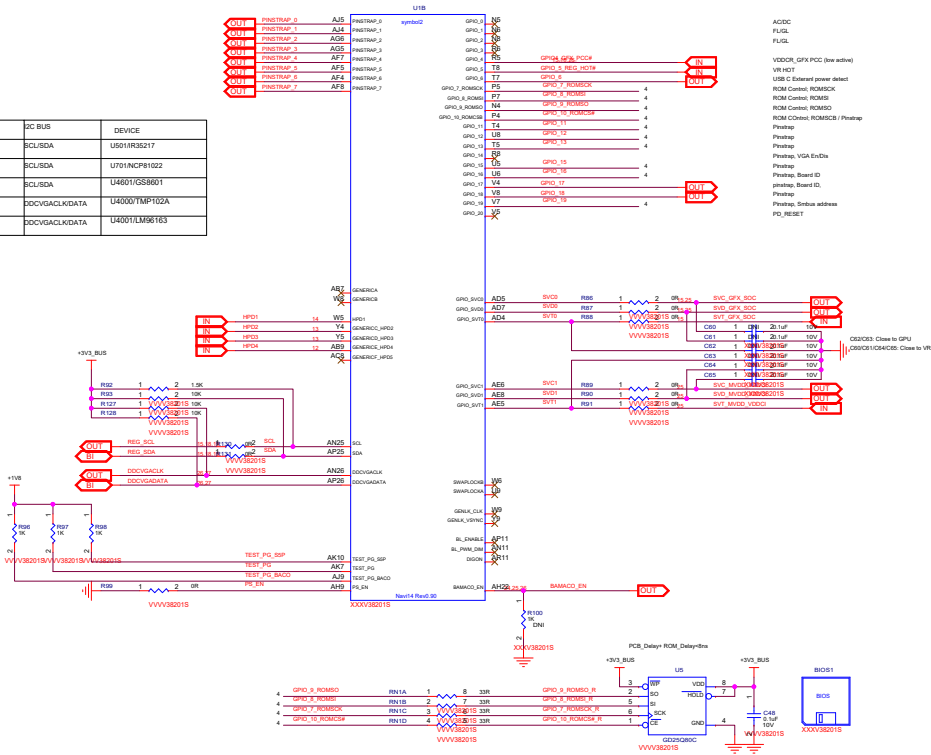




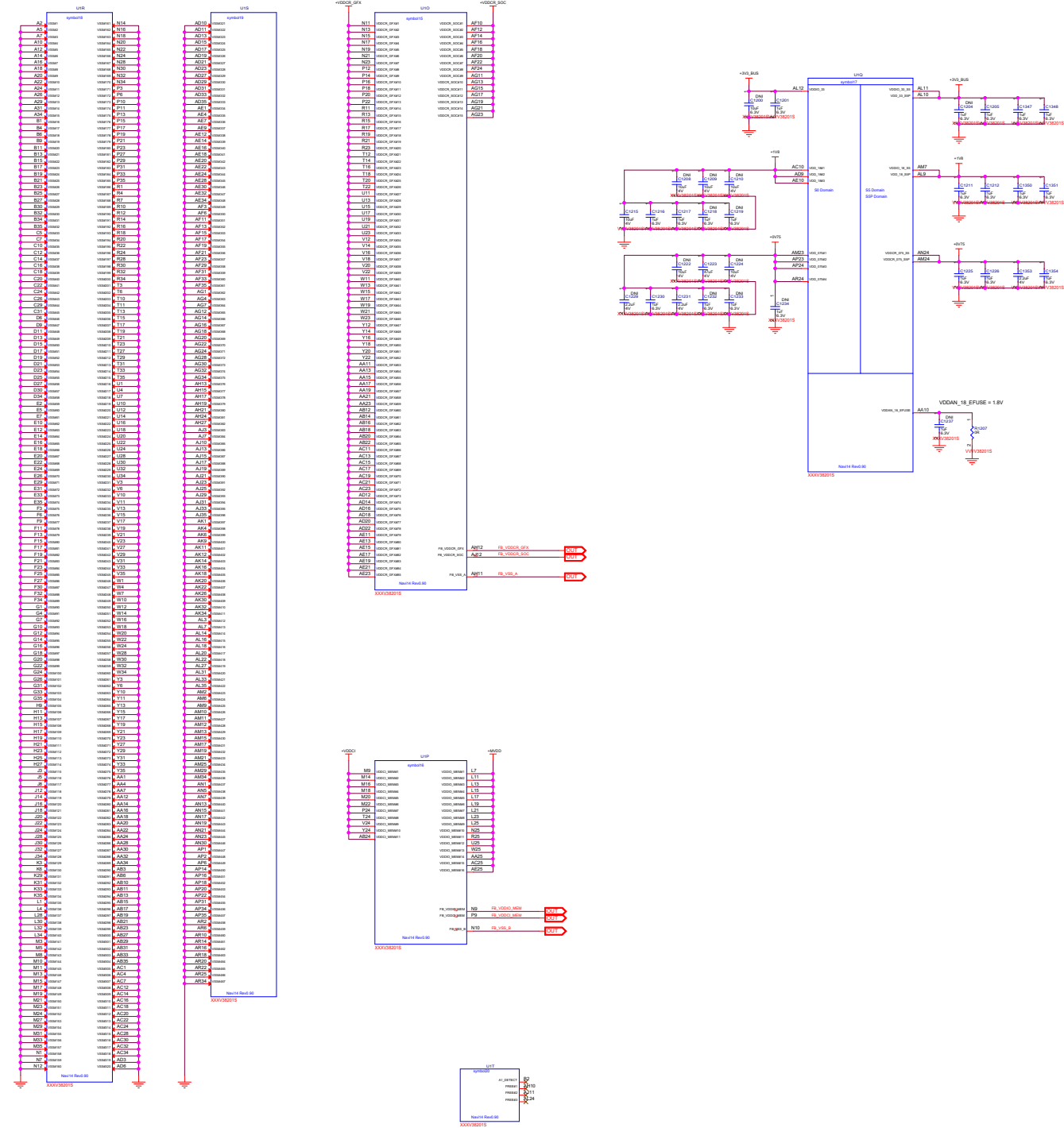
SYMBOL LEGEND	
SW	SWITCH
RES	RESISTOR
1 0 0	ACTIVE LOW
BUZ	BUSING UP ONLY
	DIGITAL
	GROUND
	ANALOG
	GROUND

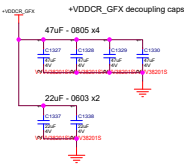
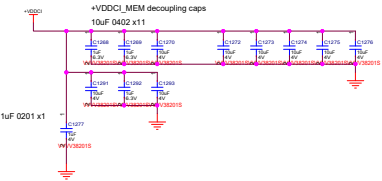
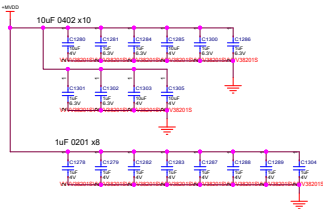
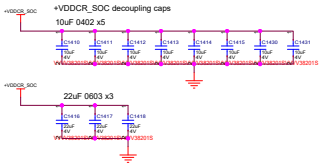
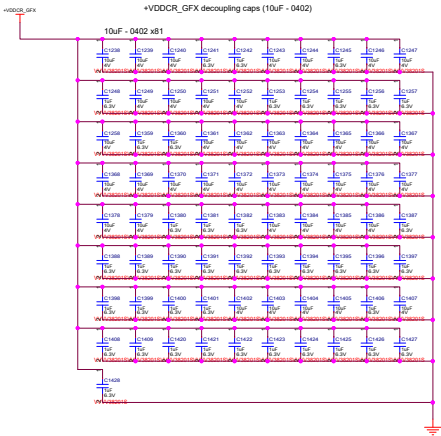
Note: Internal PULLUP at GPIO pad is ~40k strength in 14nm design spec

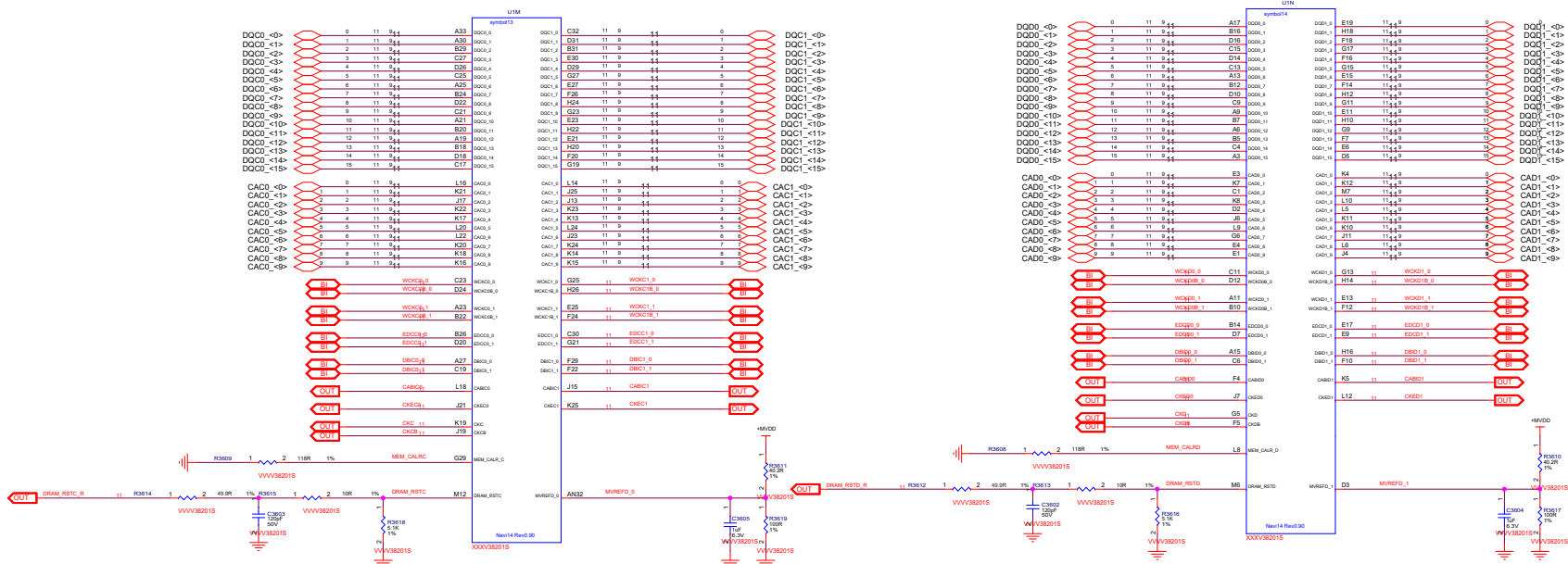
HW ID	FUNCTION	DC BUS	DEVICE
0x0	0x60	VDDOR_GFX/SDC	SCL/SDA
0x2	0x44	MVSDVSDC	SCL/SDA
0x3	0xA6	PCypressresv	SCL/SDA
0x48	0x50	GS Temperature	SDC/GACLKDATA
0x4C	0x98	EXT SENSOR	SDC/GACLKDATA

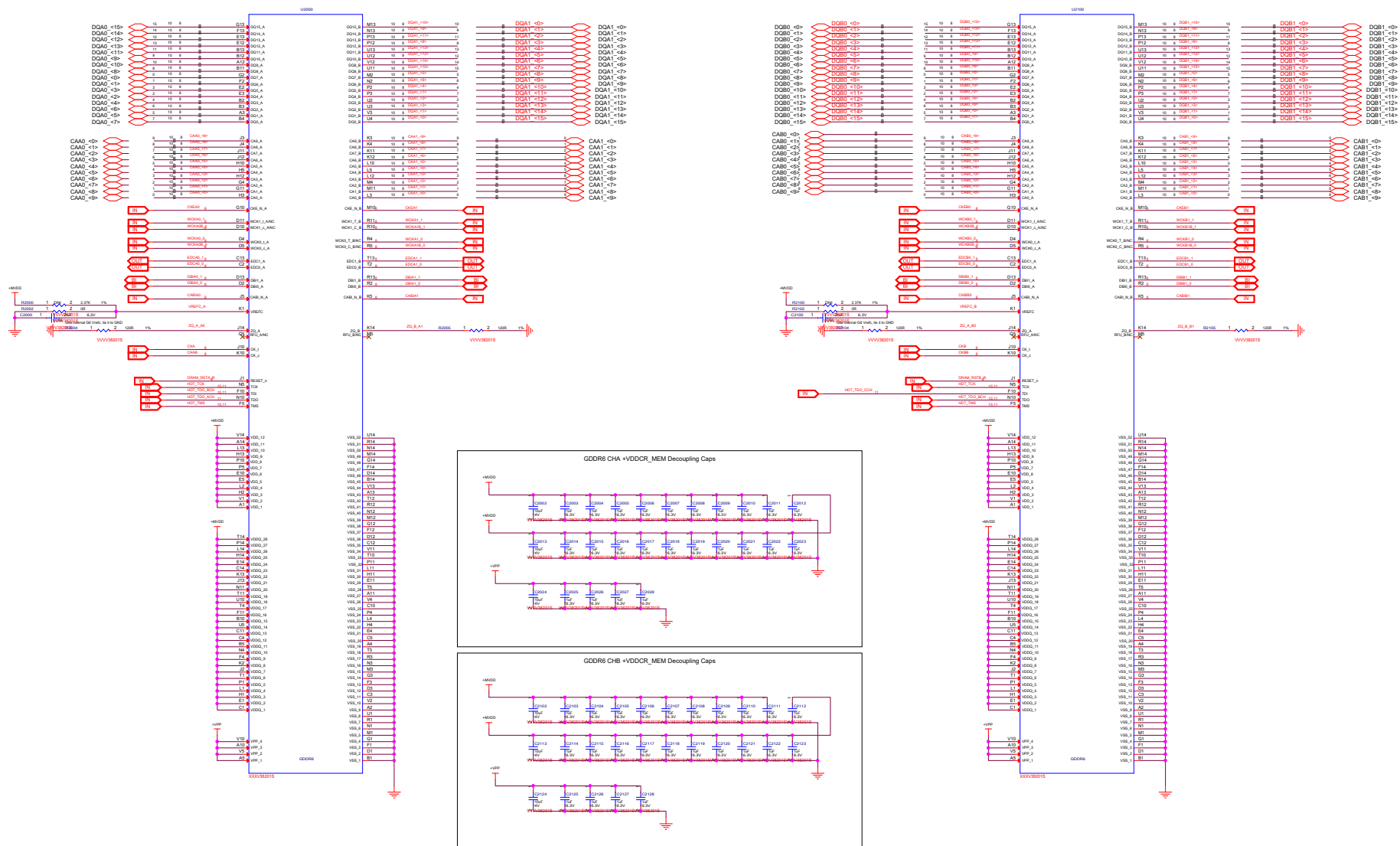


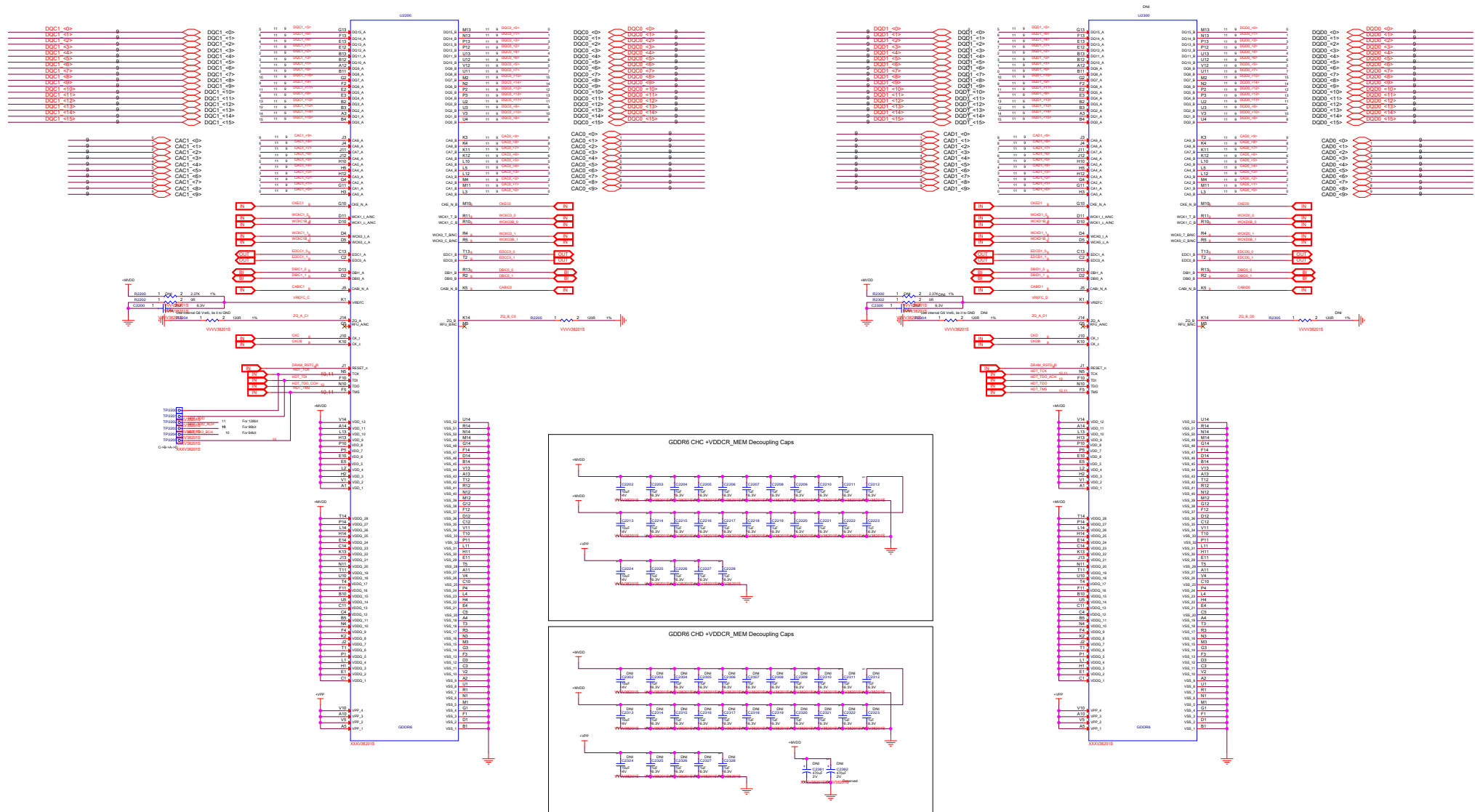
User	Internal Default Value	Definition
BIF	0	STRAP_BIF_GEN4_DIS_A 0: PCH GEN 4 supported 1: PCH GEN 4 not supported
	0	PINSTRAP_BIF_CLK_PM_EN 0: CLKREQD power management capability is disabled 1: CLKREQD power management capability is enabled
	0	PINSTRAP_BIF_LC_TX_SWING 0: Full swing mode 1: Reduced swing (noisy/half swing)
	0	PINSTRAP_BIF_VGA_DIS 0: VGA controller capacity enabled 1: The device won't be recognized as the system's VGA controller
DCE	0	PINSTRAP_AUD_PORT_CONN[2:0] Number of audio-capable display outputs 0: All endpoints connected 1: 5 endpoints connected 2: 3 endpoints connected 3: 4 endpoints connected Default: (PINSTRAP_AUD_PORT_CONN[2:0] = 000)
	0	PINSTRAP_AUD[1:0] 1: Audio for DisplayPort only 2: Audio for DisplayPort and HDMI if dongle is detected 3: Audio for both DisplayPort and HDMI Default: (PINSTRAP_AUD[1:0] = 11)
	0	PINSTRAP_FIG[1:0] - No definition on NV
	0	PINSTRAP_MVDD_FB_DIVIDER_CONFIG 0: Divider does not exist 1: Divider exists
Platform	0	PINSTRAP_BFI_MEM_AP_SIZE[2:0] Or PINSTRAP_ROM_CONFIG[2:0] 100 - 512KB (BT) M2P0A 101 - 1MB (BT) M2P10A 102 - 32MB (BT) M2P20A 103 - 4MB (BT) M2P30A 104 - 1MB (BT) M2P40A 105 - 1MB (BT) M2P50A 106 - 1MB (BT) M2P60A 107 - 1MB (BT) M2P70A 108 - 1MB (BT) M2P80A 109 - 1MB (BT) M2P90A 110 - 1MB (BT) M2P00A 111 - 1MB (BT) M2P10A 112 - 1MB (BT) M2P20A 113 - 1MB (BT) M2P30A 114 - 1MB (BT) M2P40A 115 - 1MB (BT) M2P50A 116 - 1MB (BT) M2P60A 117 - 1MB (BT) M2P70A 118 - 1MB (BT) M2P80A 119 - 1MB (BT) M2P90A Default: (PINSTRAP_BFI_MEM_AP_SIZE[2:0] = 000)
	0	PINSTRAP_SMBUS_ADDR 0: 0x41h 1: 0x42h
	1	PINSTRAP_BIOS_ROM_EN 0: Disable the external BIOS ROM device 1: Enable the external BIOS ROM device
	0	PINSTRAP_BIOS_ROM_DIS

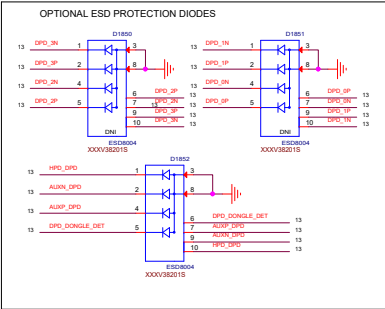
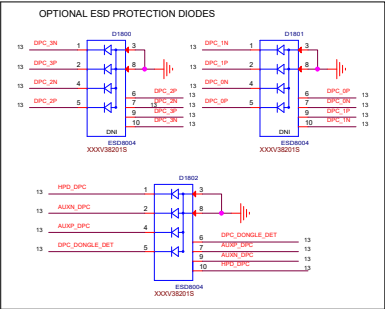
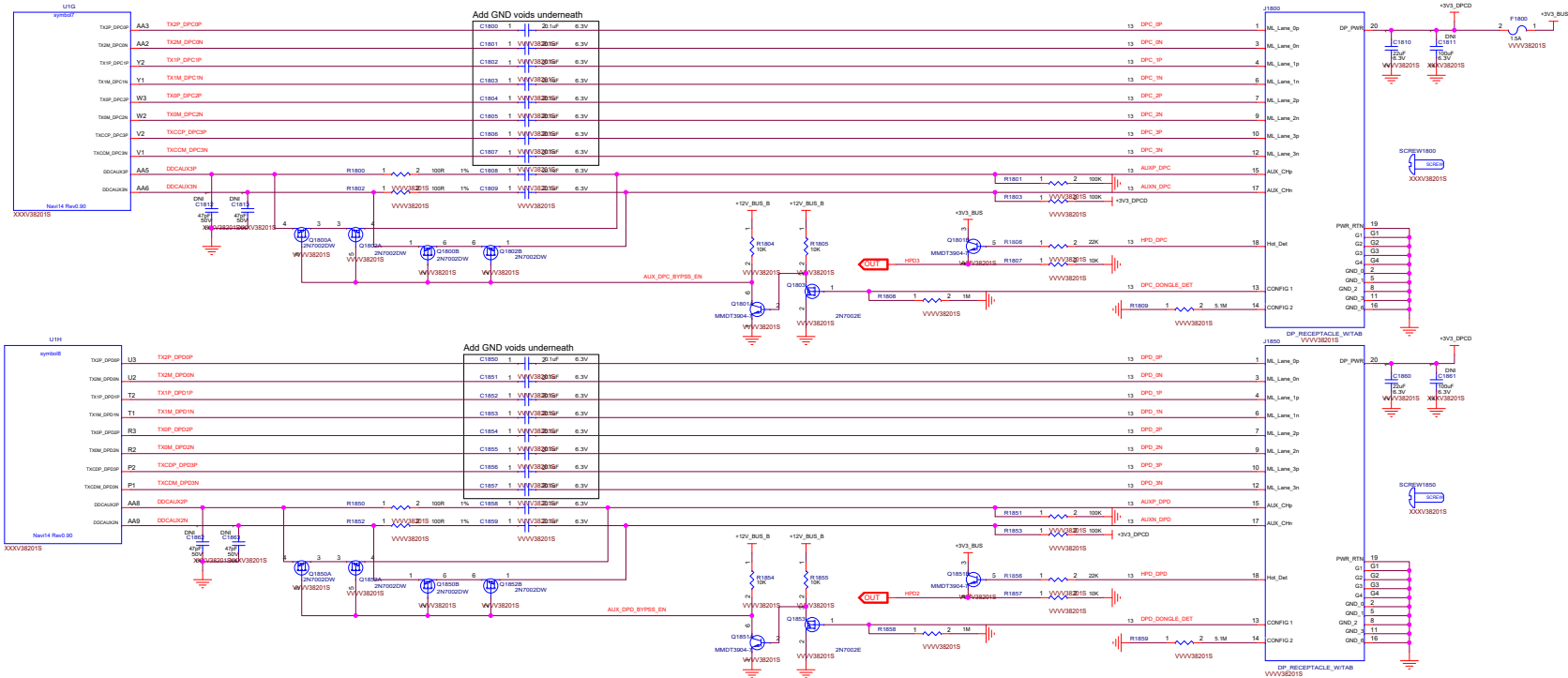


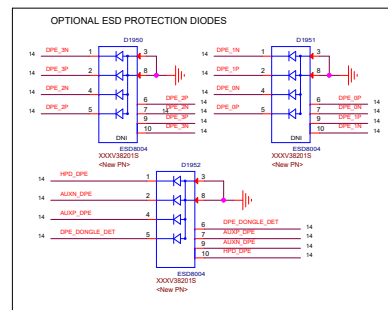




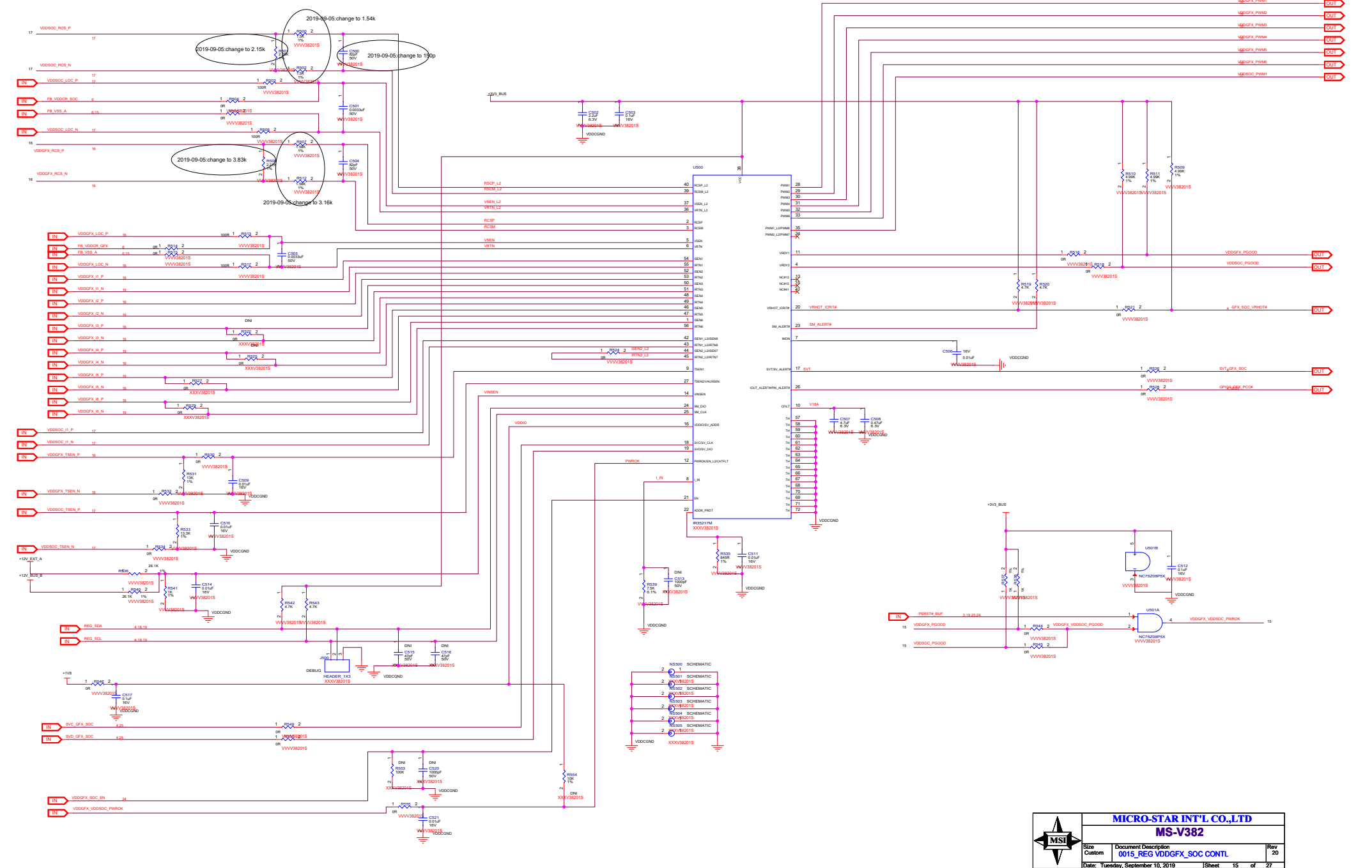


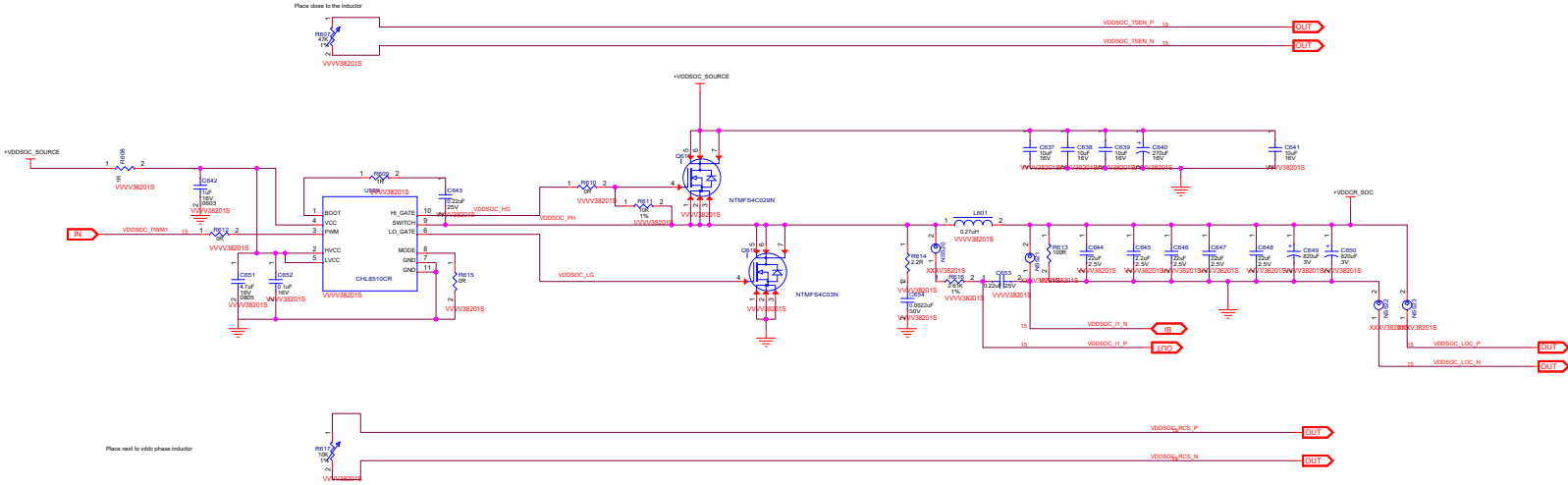


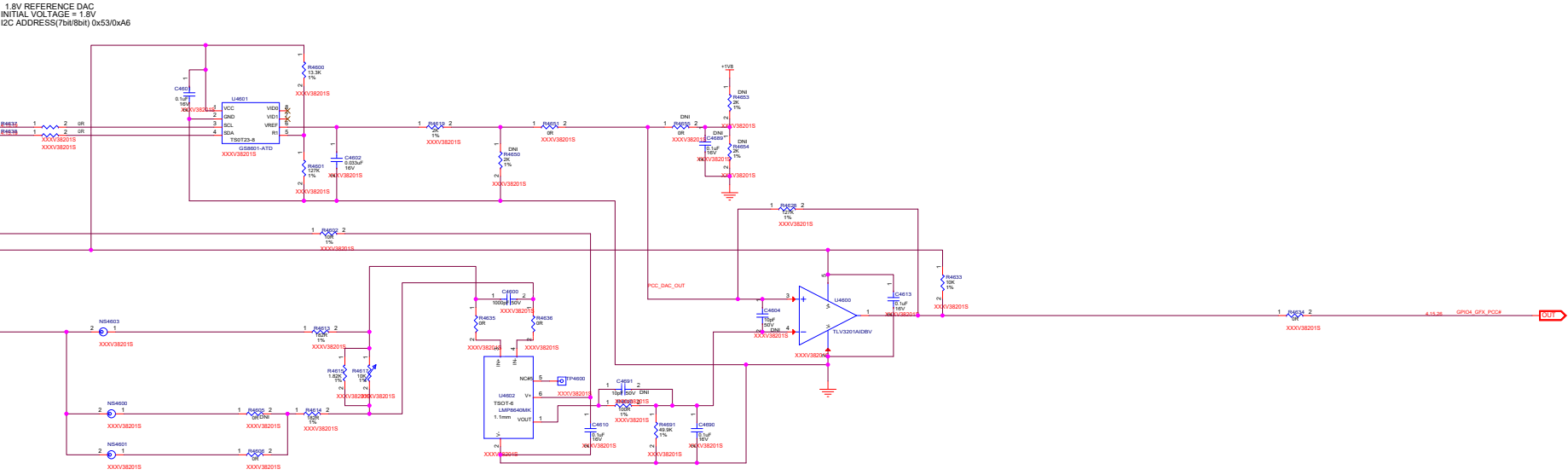


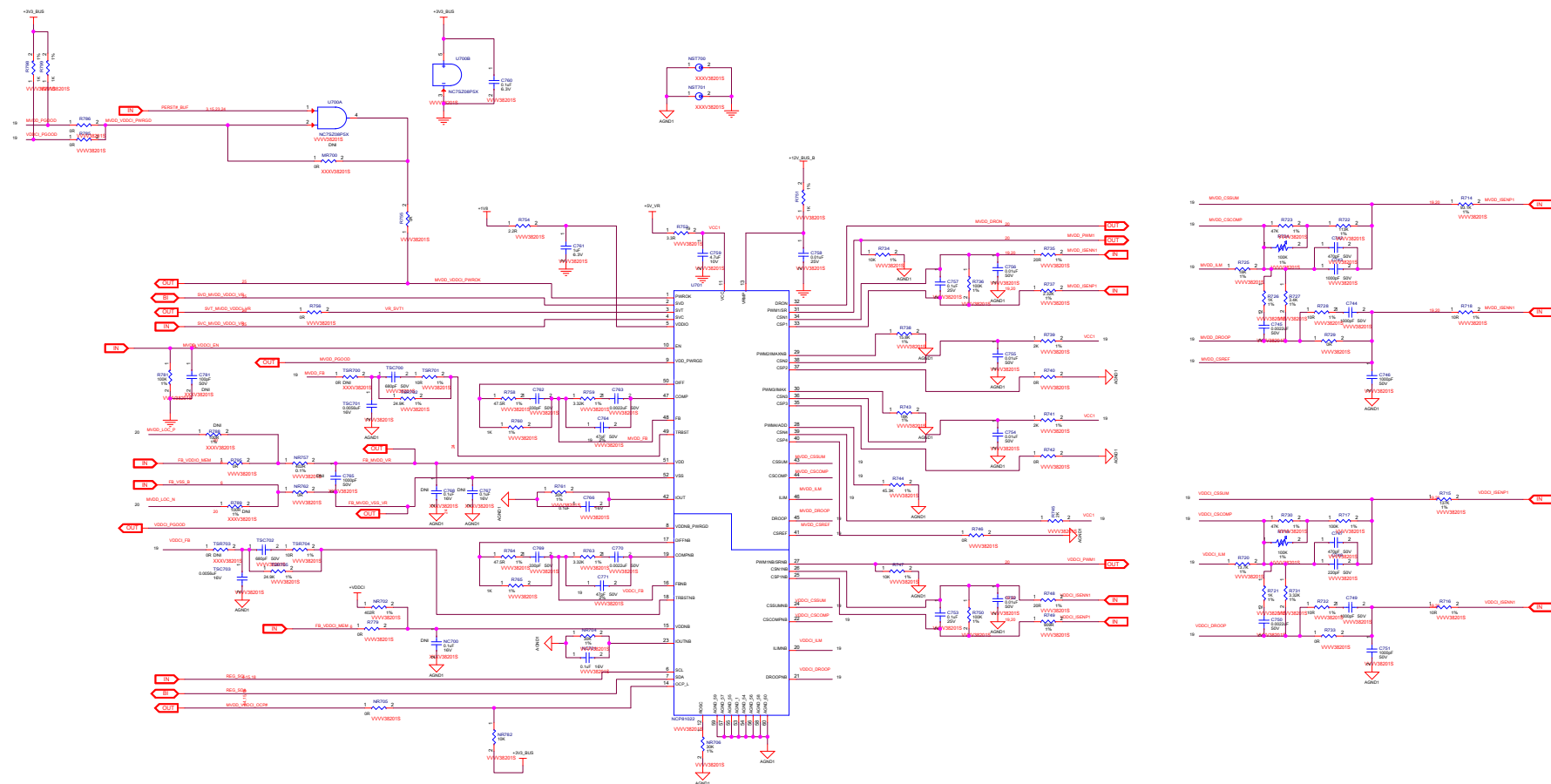


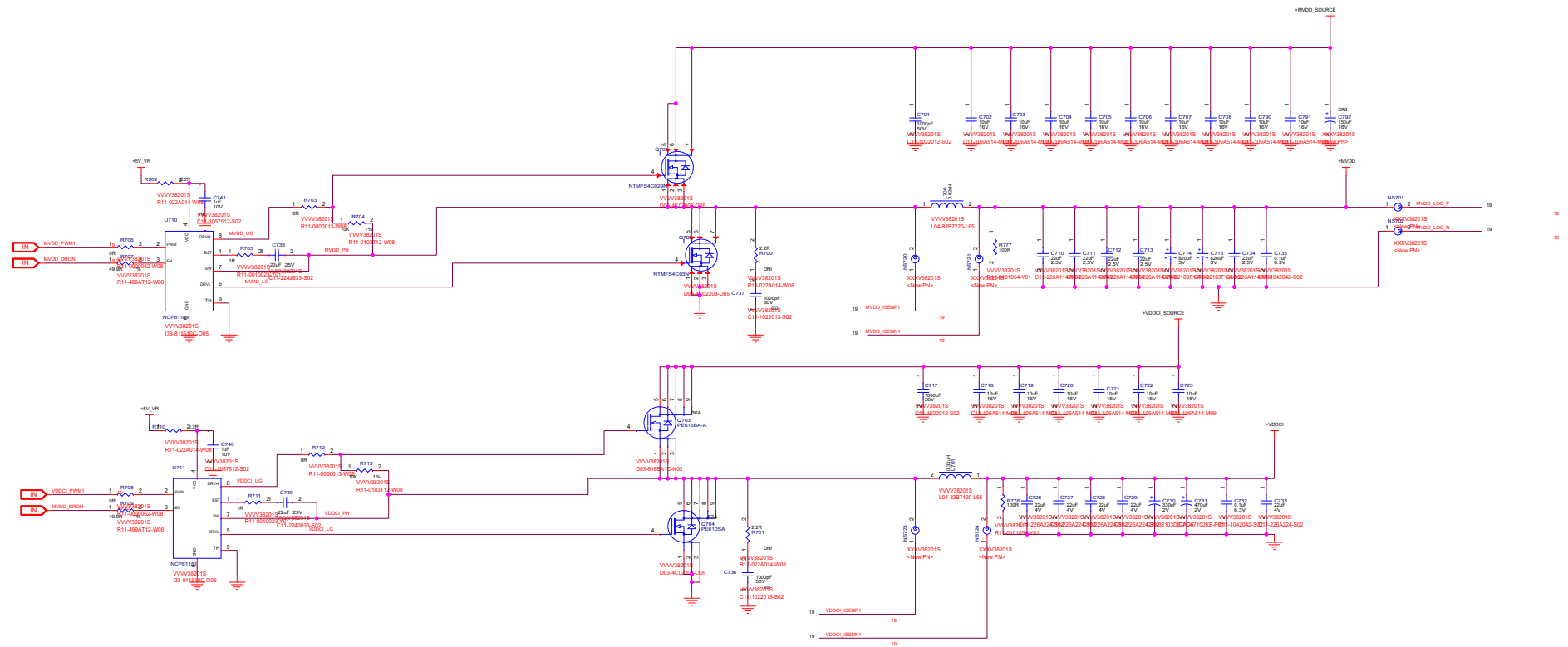
REG - VDDGFX/VDDSOC CTRL

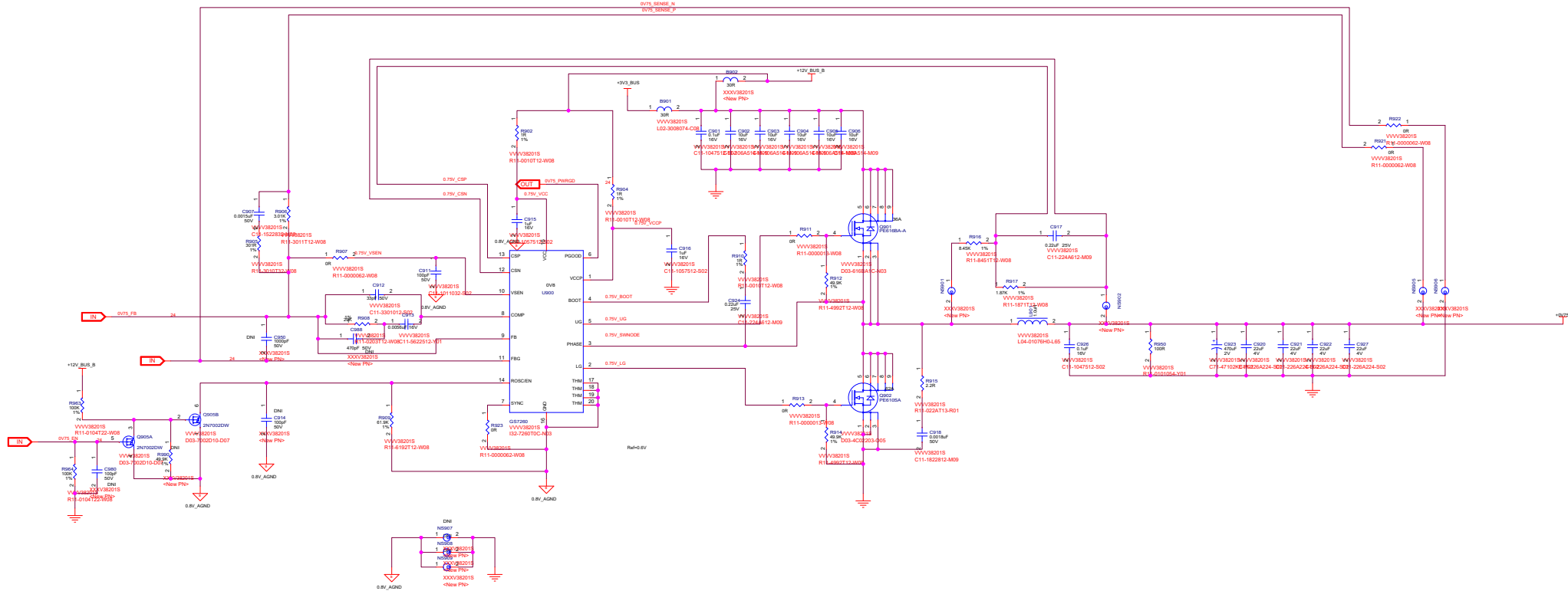




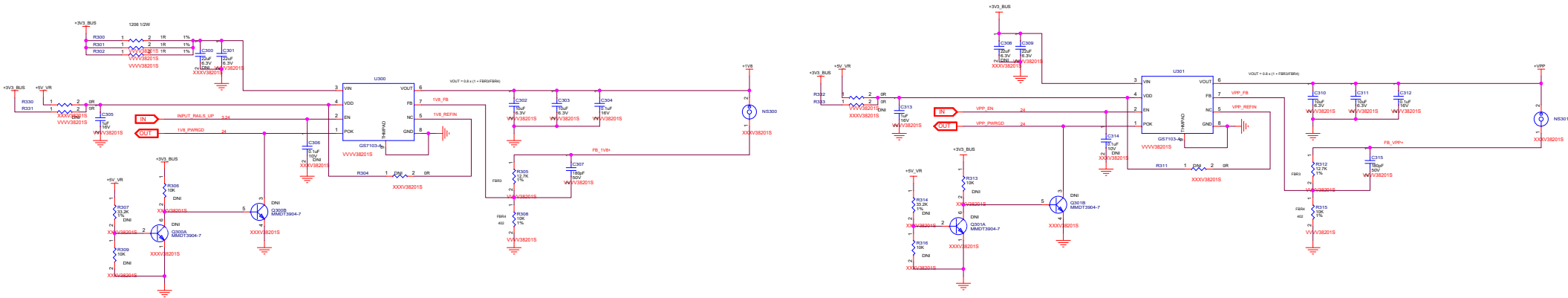




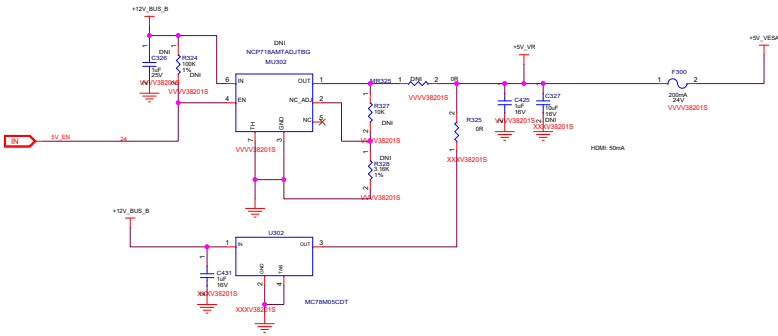


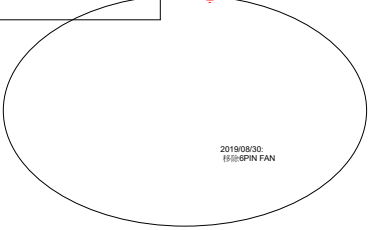
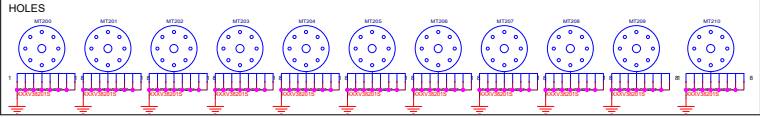
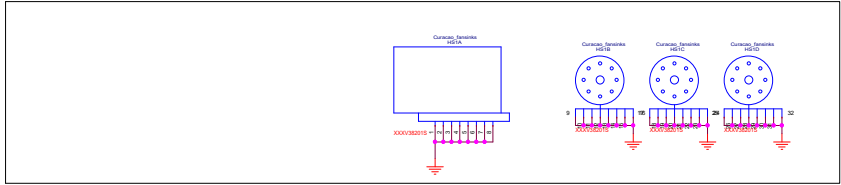
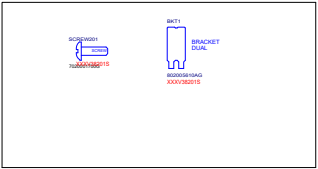
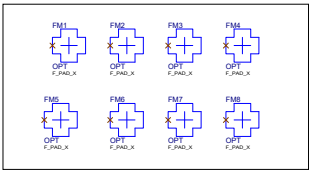
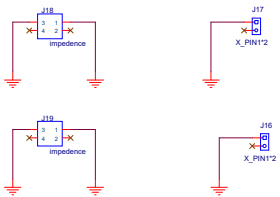
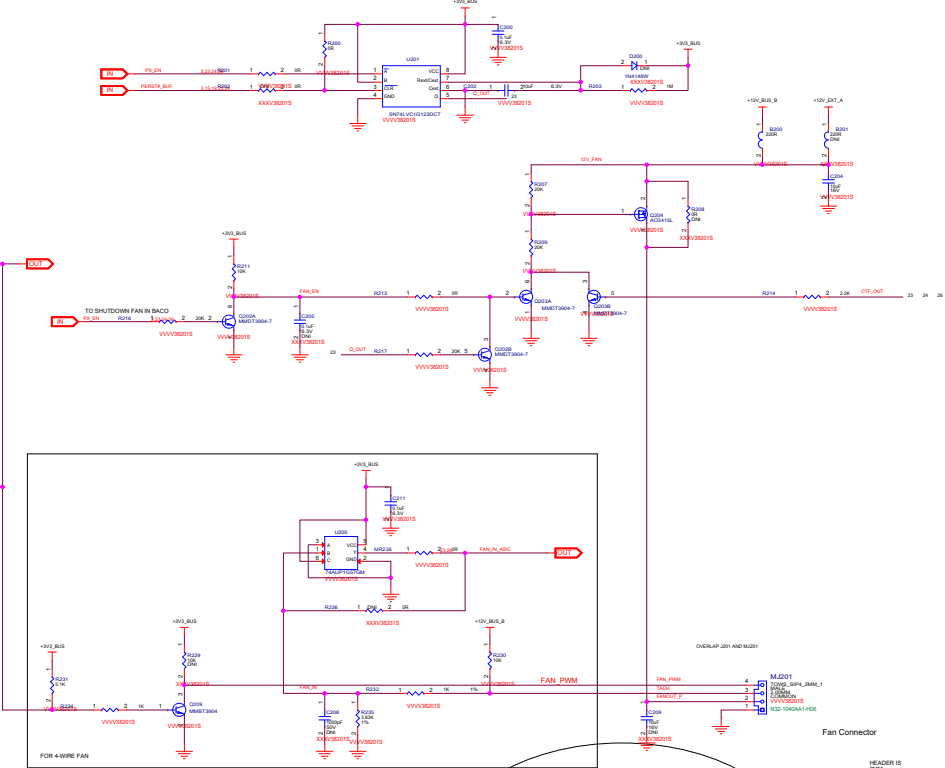
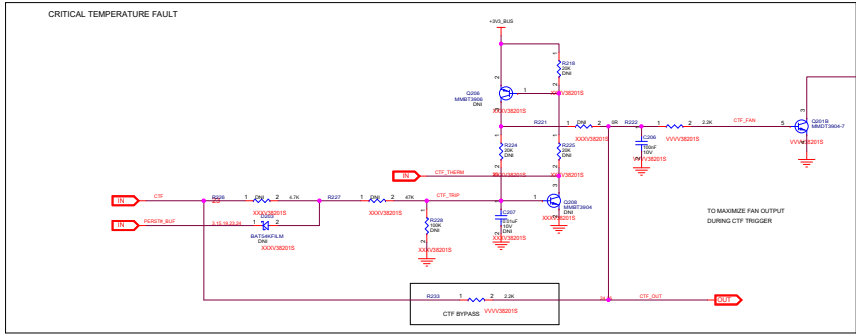
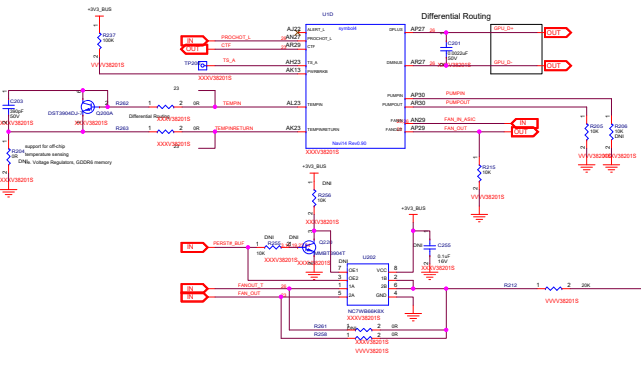


LDO - 1V8/VPP/5V



5V REGULATOR

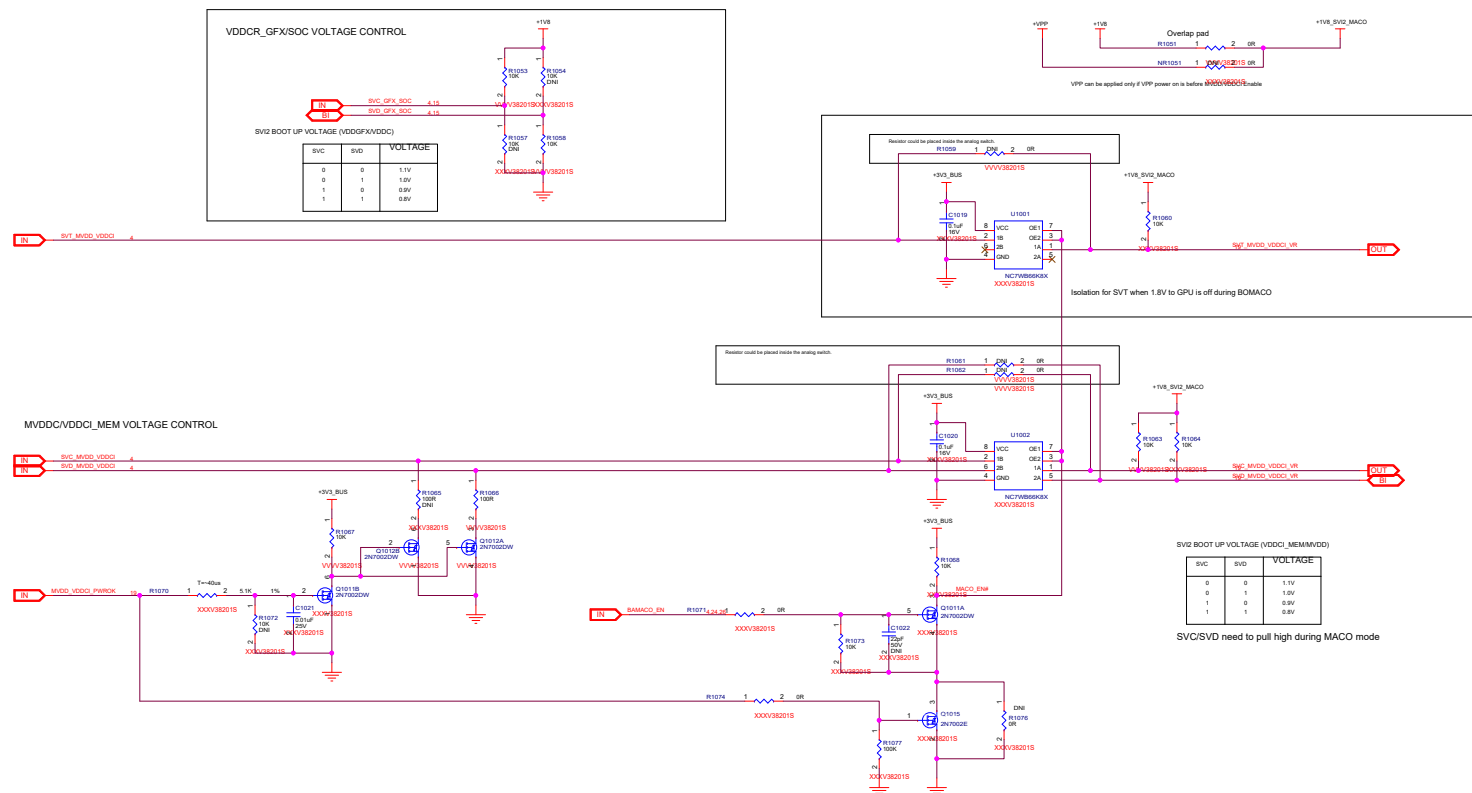




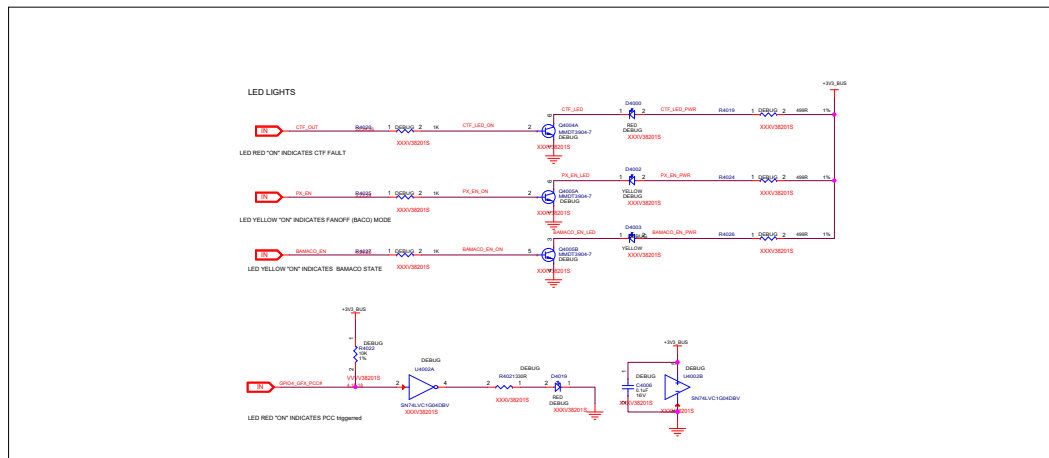
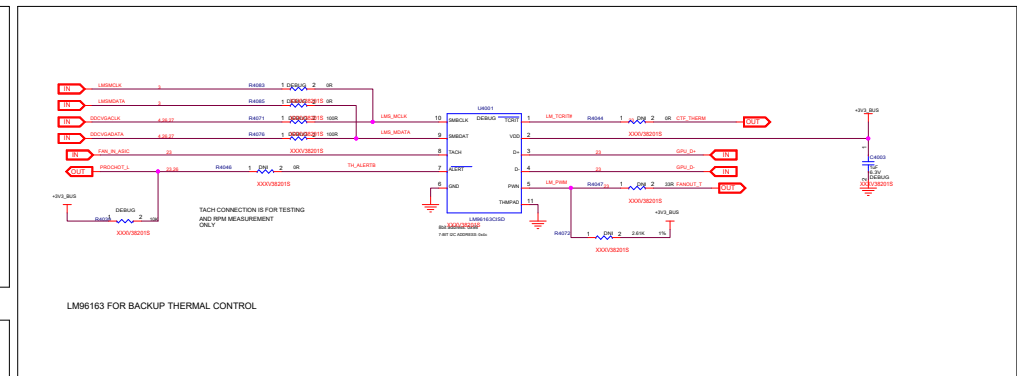
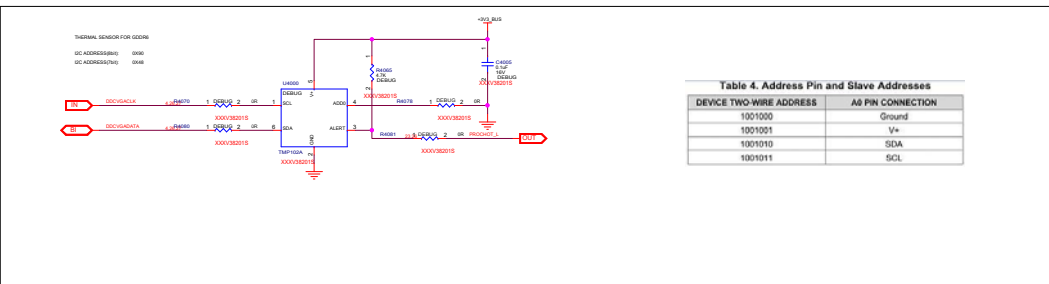
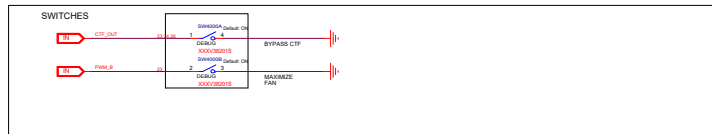
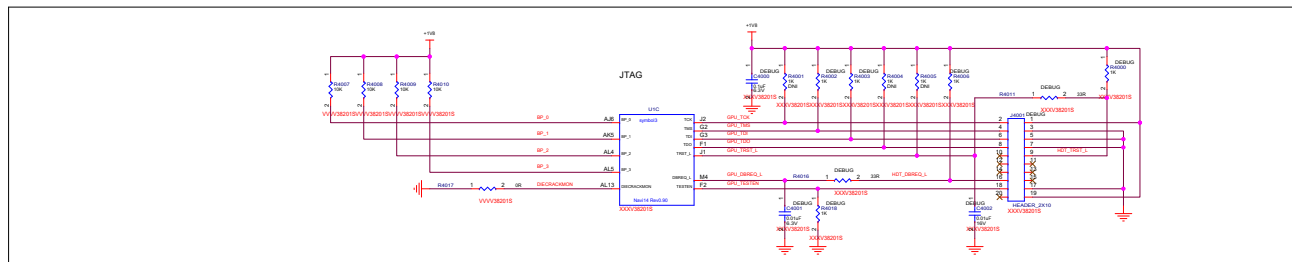
Fan Connector

HEADER IS
PINS NOT FOLLOW
2.54MM SPACING AS
FAN SPECIFICATION



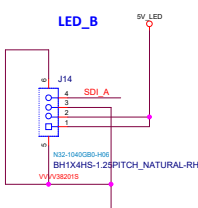
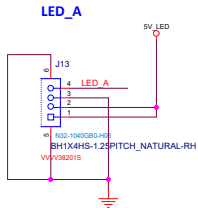
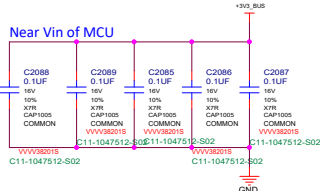
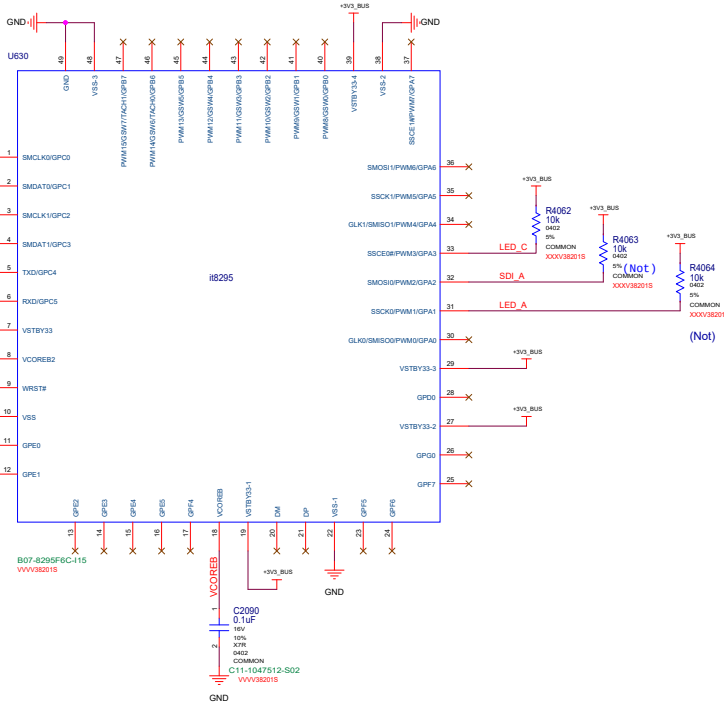
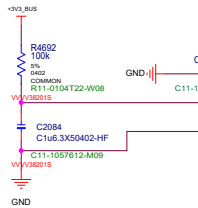
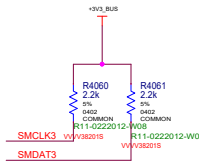
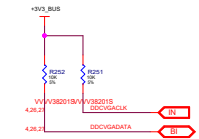
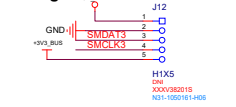


29) DEBUG CIRCUIT



Firmware Programming

Debug



5V_LED

