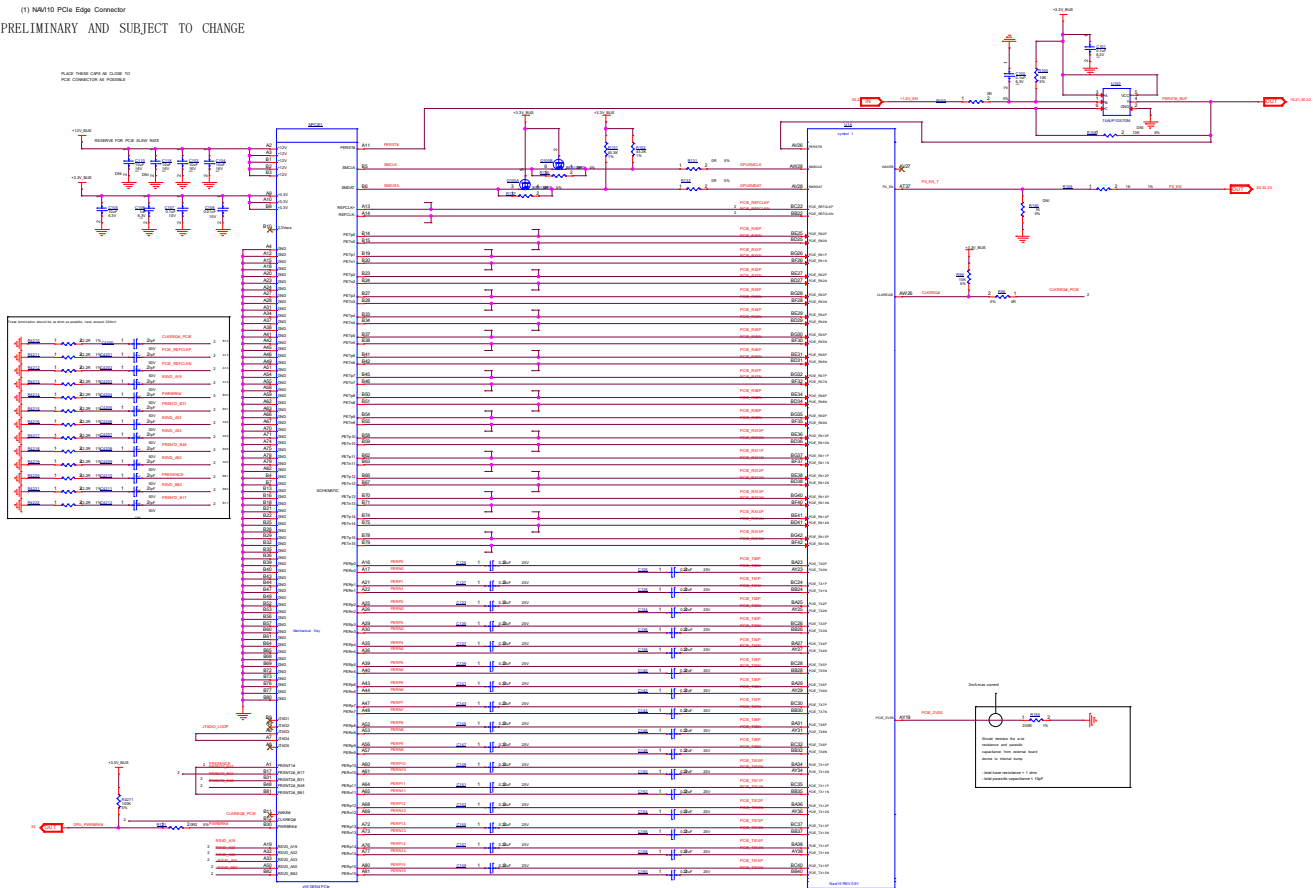
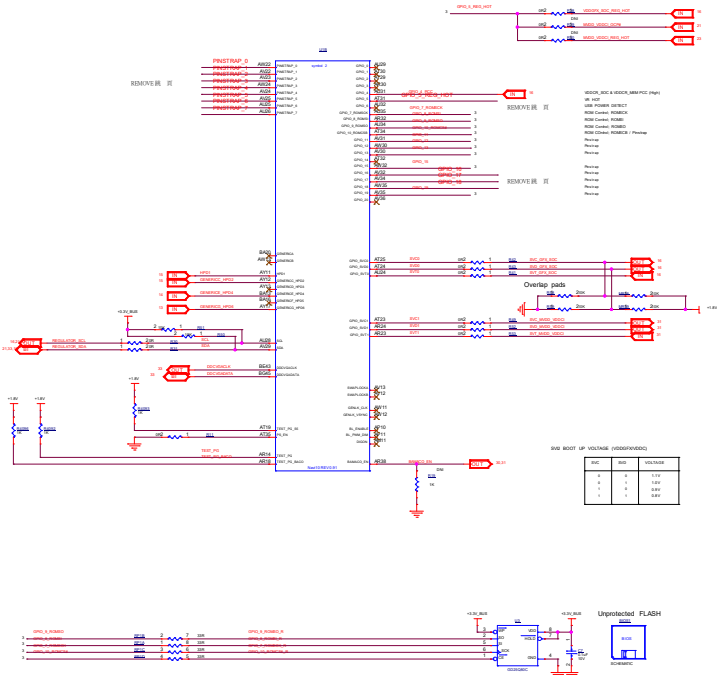


PRELIMINARY AND SUBJECT TO CHANGE

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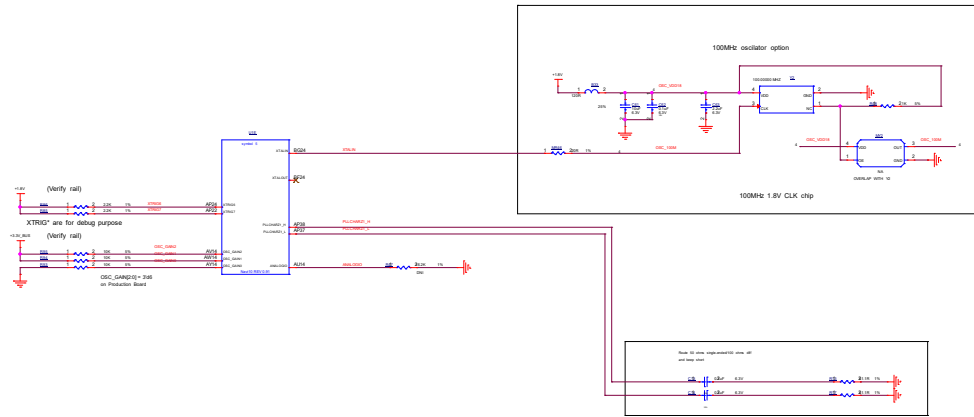
SHEET NO.	SHEET NAME	SHEET NO.	SHEET NAME
1	TOC	26	SMALL FAN REGULATORS
2	NAUTO PCB Interface	27	NAUTO DECAPS
3	NAUTO GPCH	28	NAUTO POWER
4	NAUTO STAL	29	NAUTO GND
5	NAUTO MEM CHAB	30	POWER MANAGEMENT
6	NAUTO MEM CHCD	31	SAG & SBRACO
7	NAUTO MEM CHEP	32	MECHANICAL & THERMAL
8	NAUTO MEM CHSH	33	DEBUG
9	GDORM MEM CHAB	34	BLOCK DIAGRAM
10	GDORM MEM CHCD	35	REVISION HISTORY
11	GDORM MEM CHEP		
12	GDORM MEM CHSH		
13	NAUTO TMCPA - GP		
14	NAUTO TMCPA - HDMI		
15	NAUTO TMCPA - DP DP		
16	GPX & SOC CONTROLLER		
17	VEDOR_GFX PHASES 1 and 5		
18	VEDOR_GFX PHASES 1 and 4		
19	VEDOR_GFX PHASES 1 and 7		
20	VEDOR_GFX PHASES 6 and VEDOR_SOC		
21	MVED_VEDCO CONTROLLER		
22	REG VEDCO		
23	REG VEDCO		
24	REG 0.75V		
25	REG 1.8V		

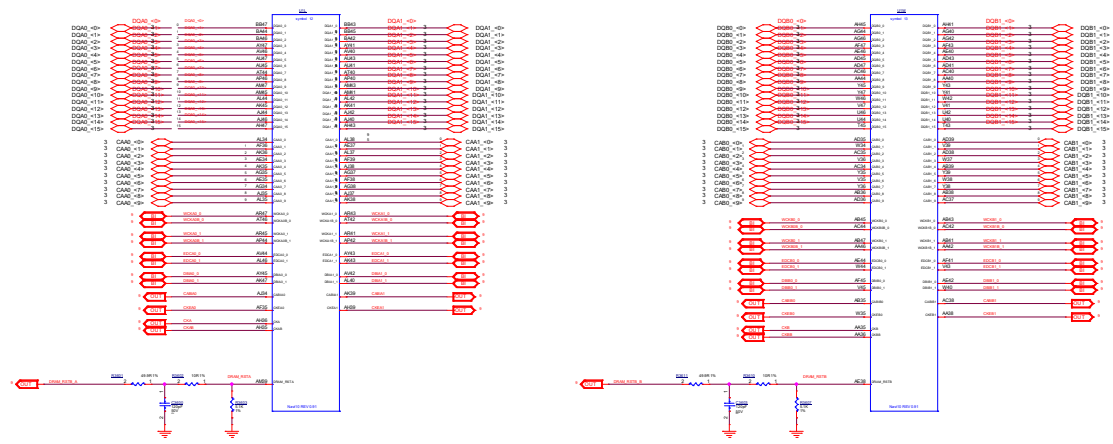


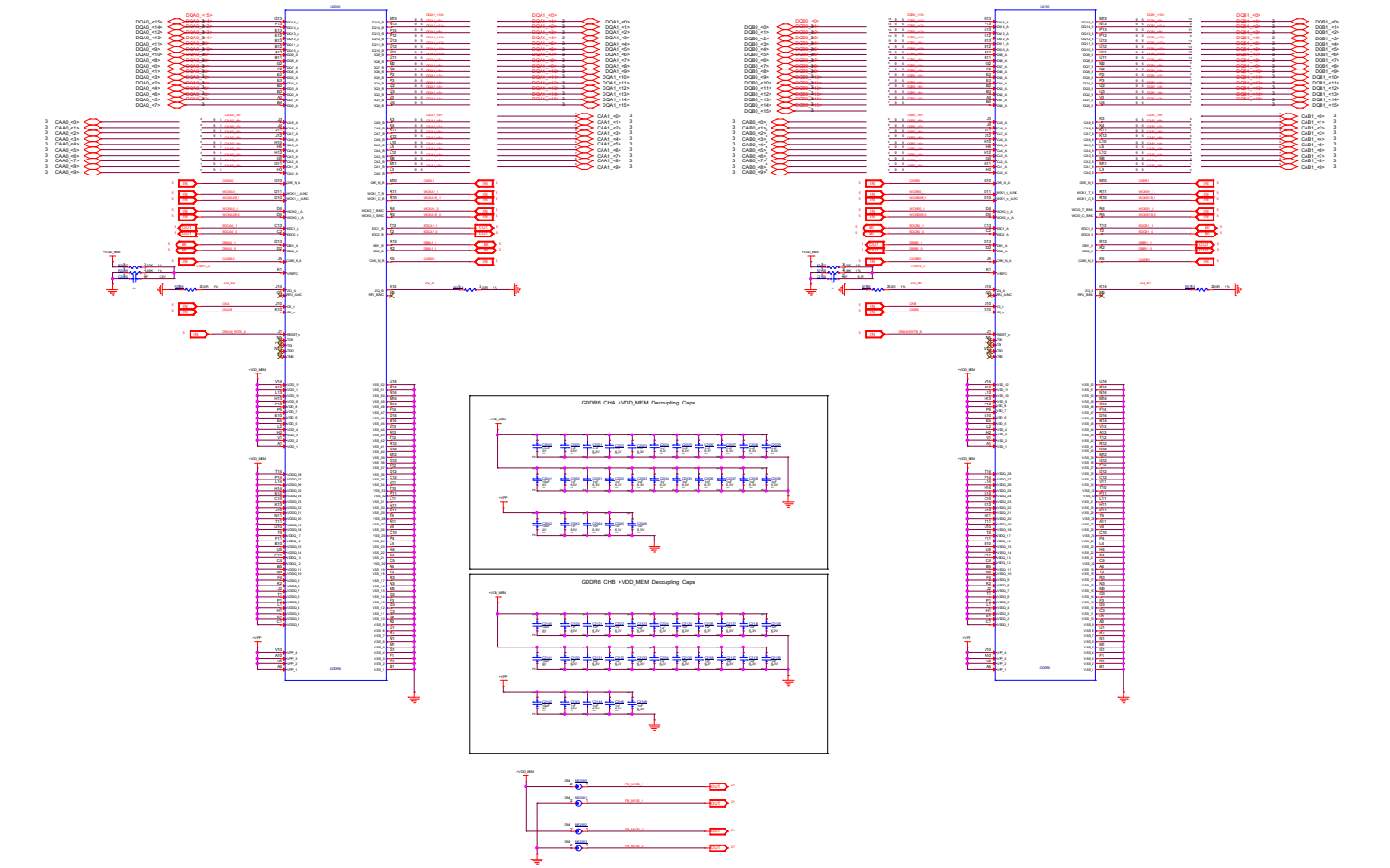


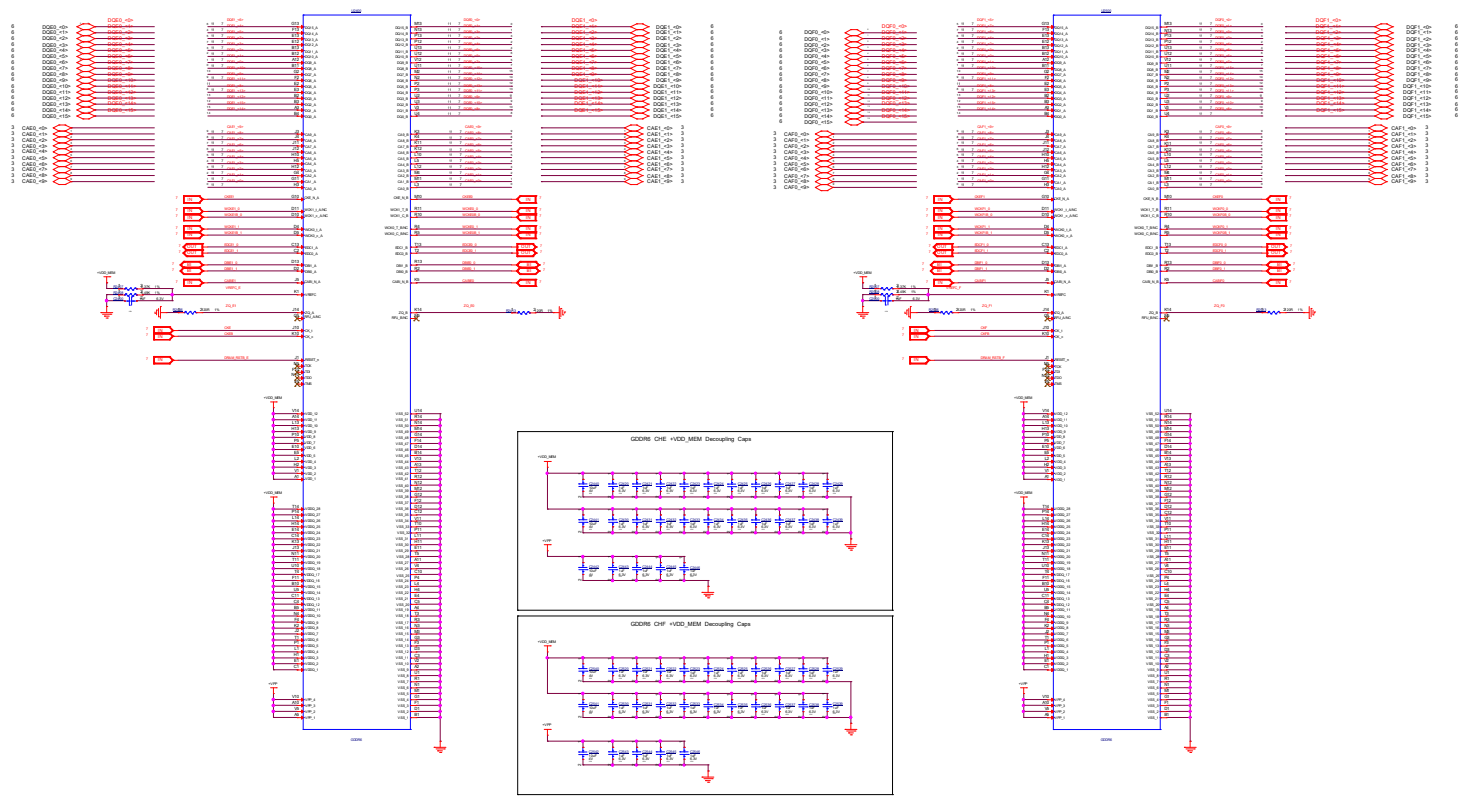
Note: Internal FPGAs at QFP pin 14-15K are not in this design.

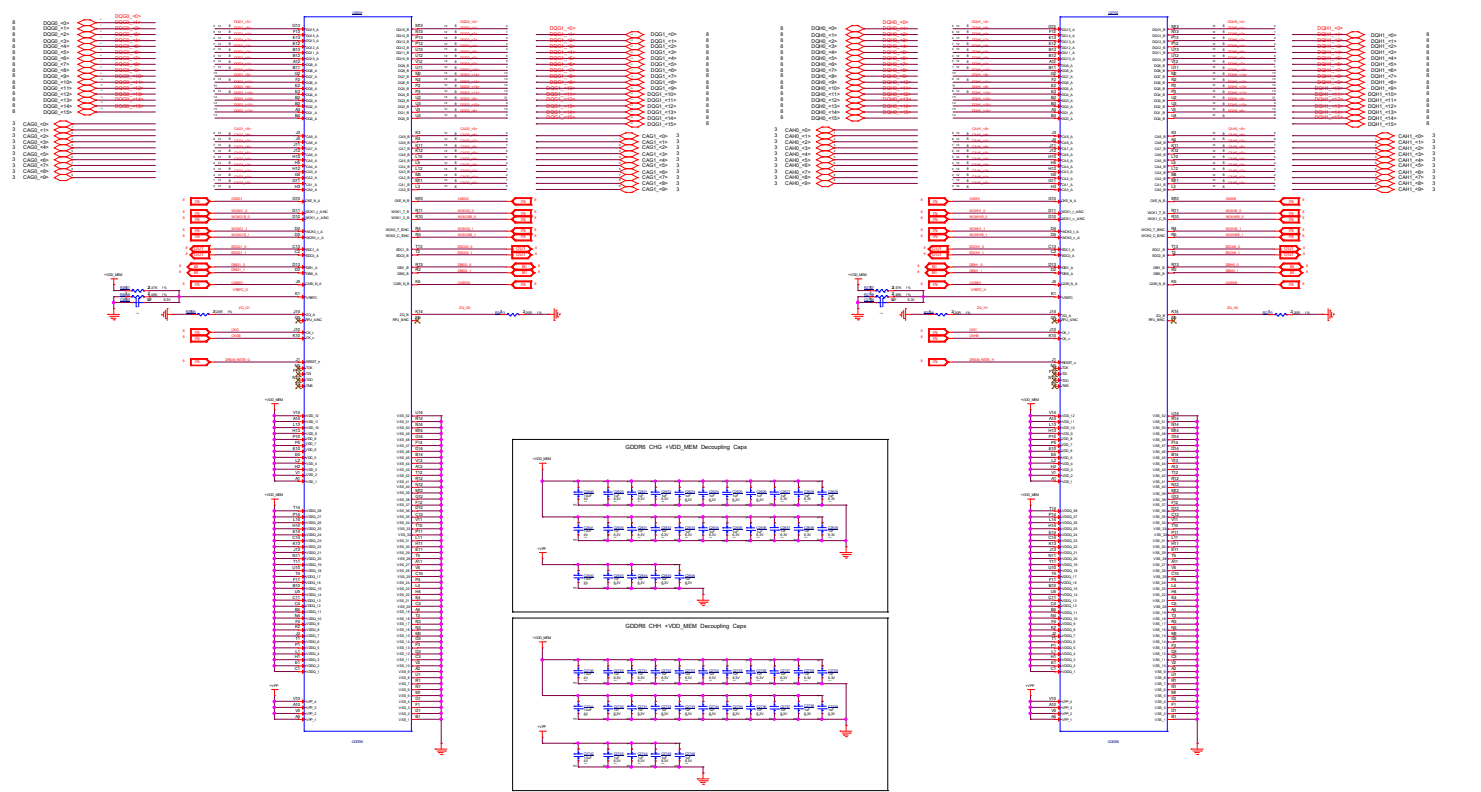
User	Internal Default Value	Definition
BIF	STRAP_BIF_GEN4_DIS_A	0 1. Pin 140A is not used. 2. Pin 140A is not used.
	PINSTRAP_BIF_CLK_PM_EN	0 1. Pin 140A is not used.
	PINSTRAP_BIF_LC_TX_SWING	0 1. Pin 140A is not used.
	PINSTRAP_BIF_VGA_DIS	0 1. Pin 140A is not used.
DCE	PINSTRAP_AUD_PORT_CONN2[0]	0 1. Pin 140A is not used.
	PINSTRAP_AUD1[0]	0 1. Pin 140A is not used.
	PINSTRAP_AUD2[0]	0 1. Pin 140A is not used.
	PINSTRAP_AUD3[0]	0 1. Pin 140A is not used.
Platform	PINSTRAP_AUD4[0]	0 1. Pin 140A is not used.
	PINSTRAP_AUD5[0]	0 1. Pin 140A is not used.
	PINSTRAP_AUD6[0]	0 1. Pin 140A is not used.
	PINSTRAP_AUD7[0]	0 1. Pin 140A is not used.
SMU	PINSTRAP_SMBUS_ADDR	0 1. Pin 140A is not used.
	PINSTRAP_BIOS_ROM_EN	1 1. Pin 140A is not used.

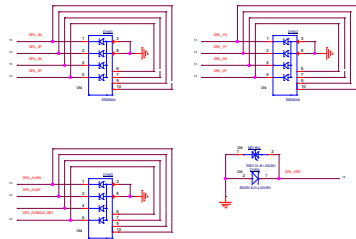




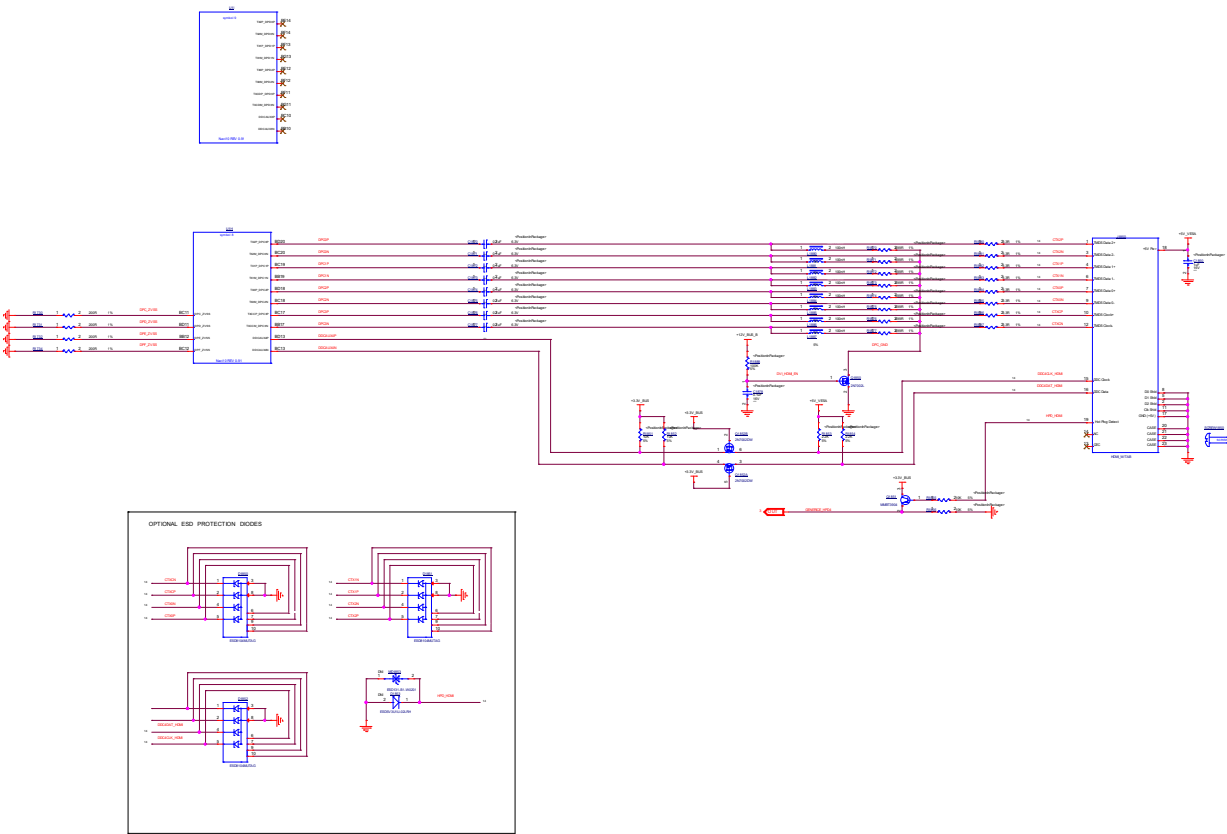




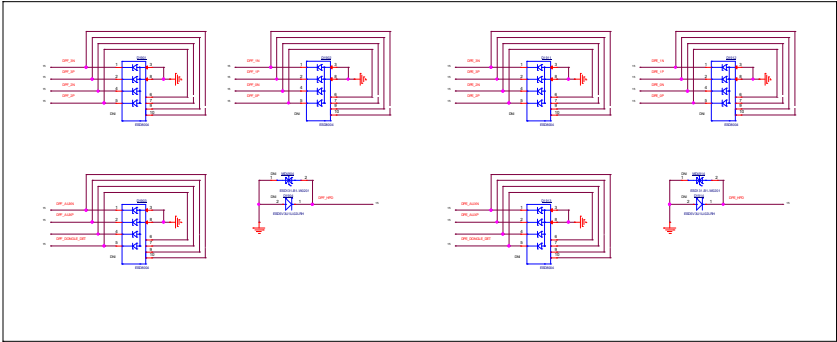
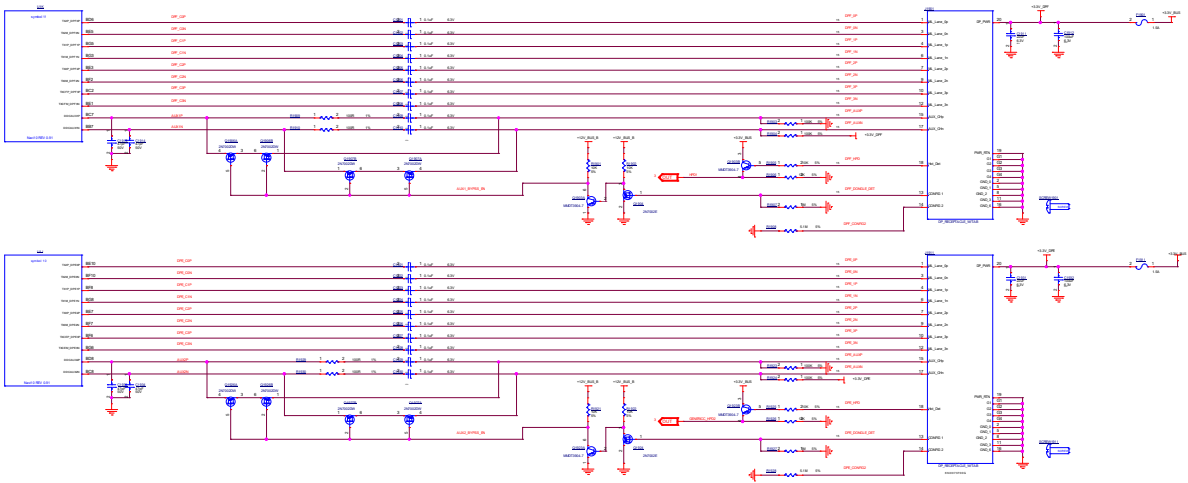


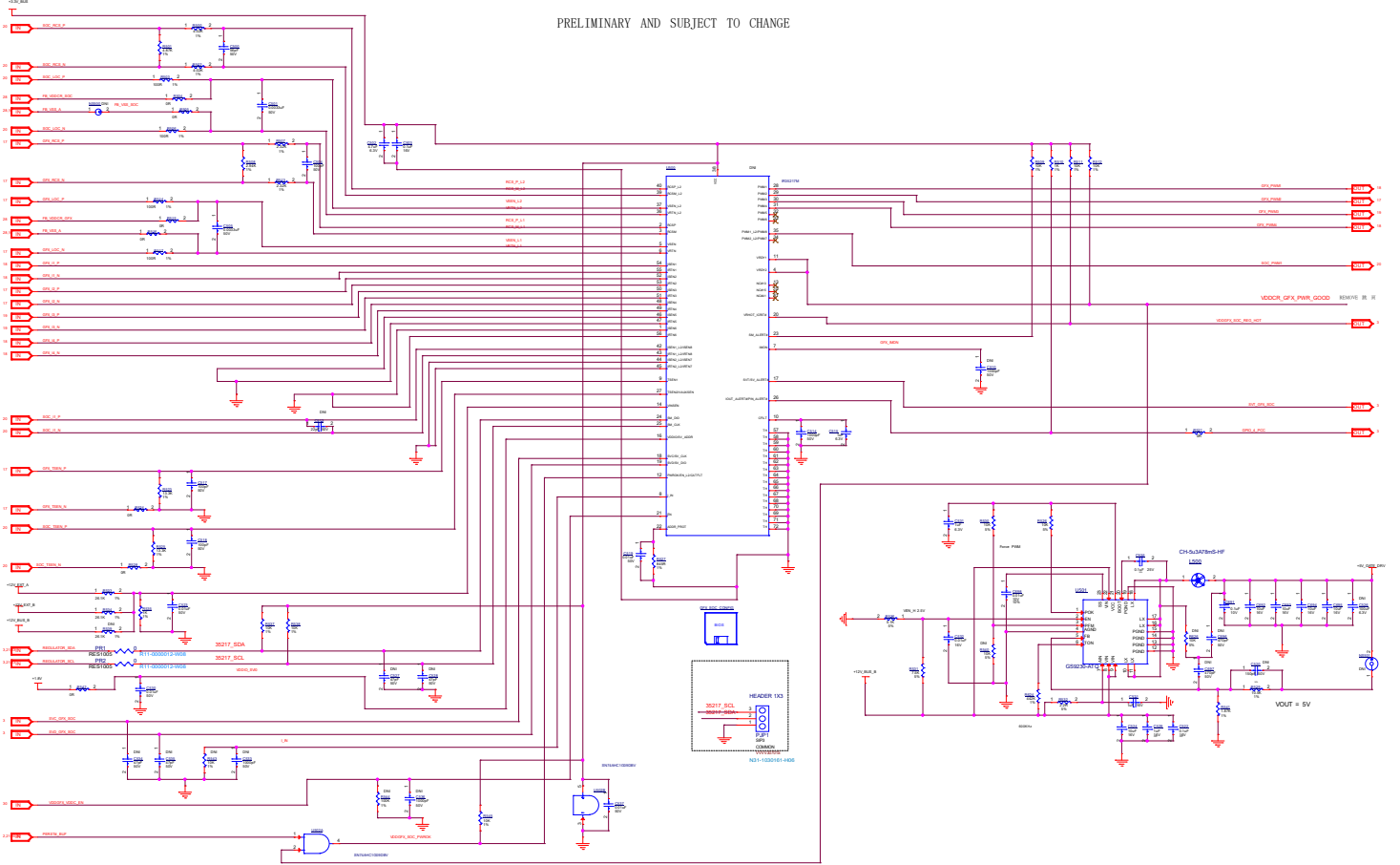


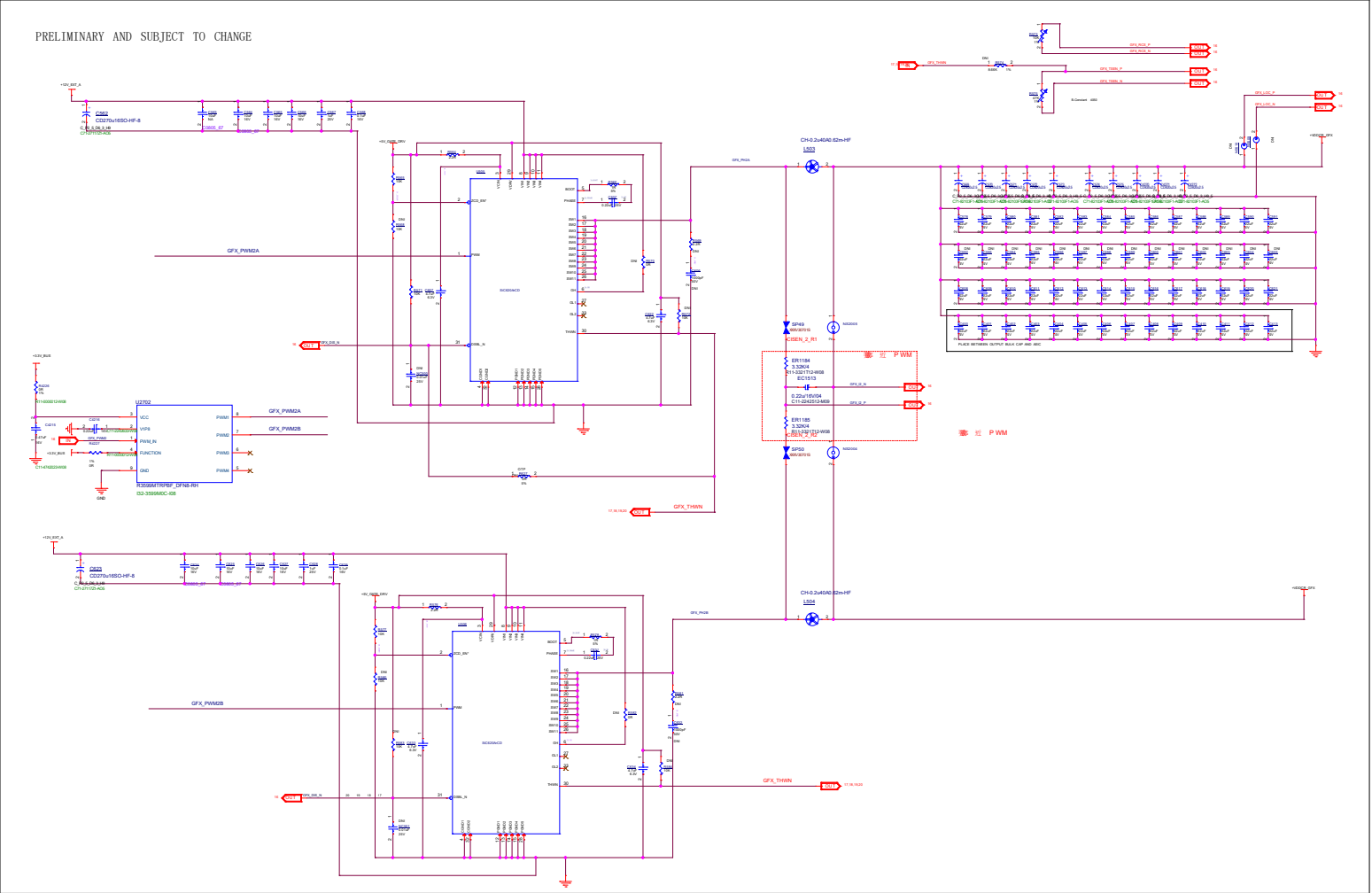
NAV110 TMDP C/D

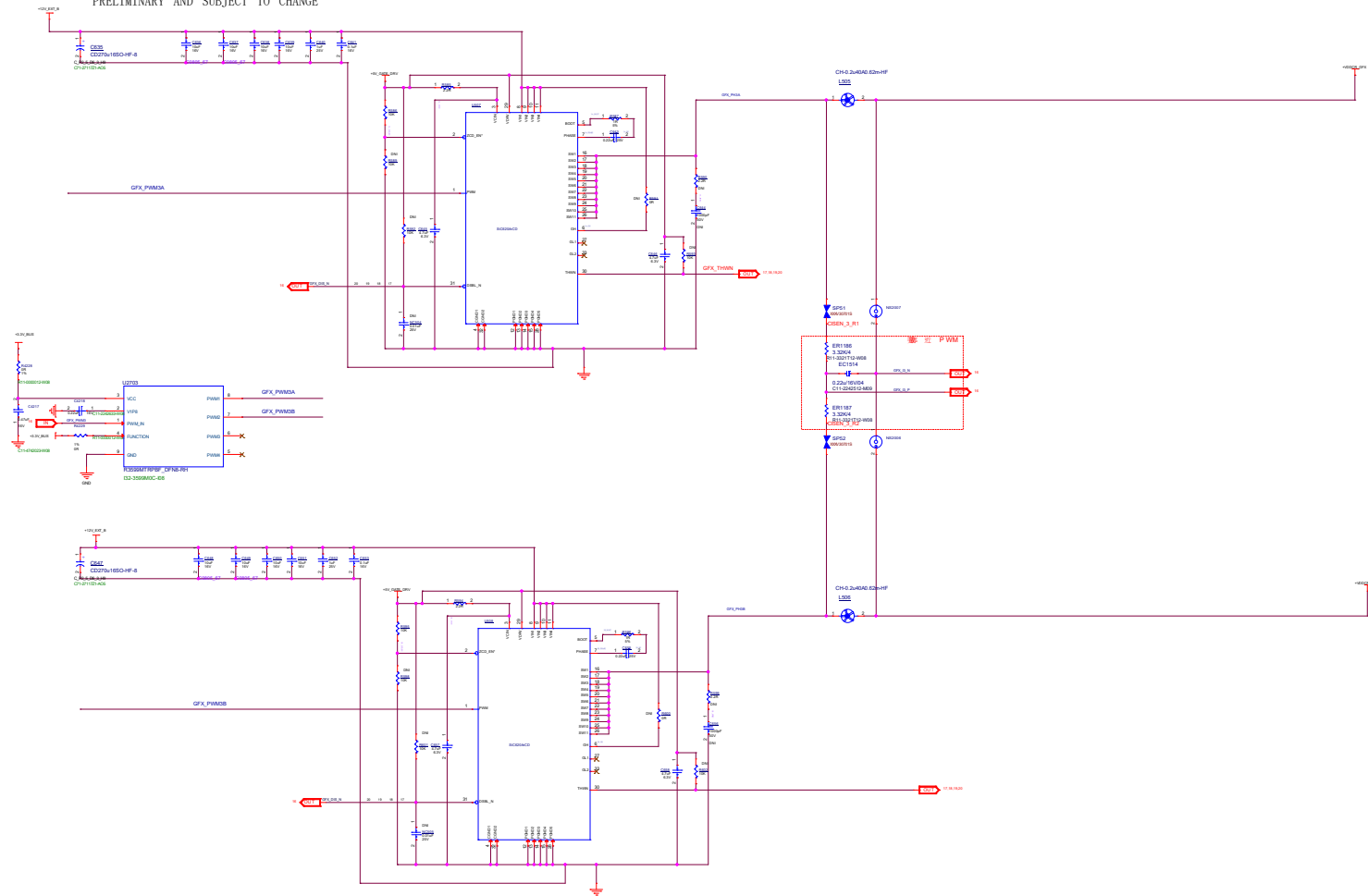


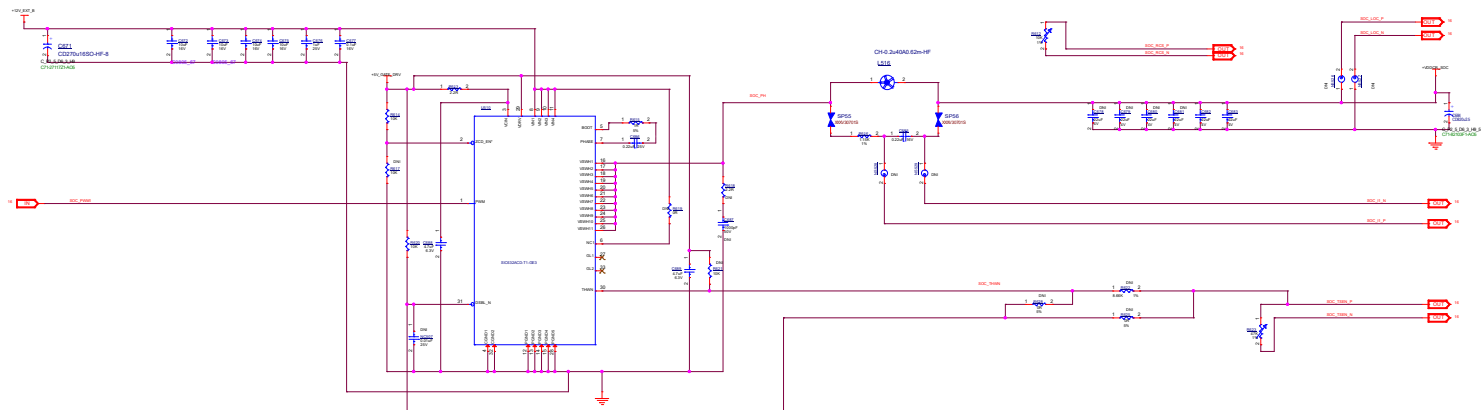
(9) NAVI10 TMDP E/F

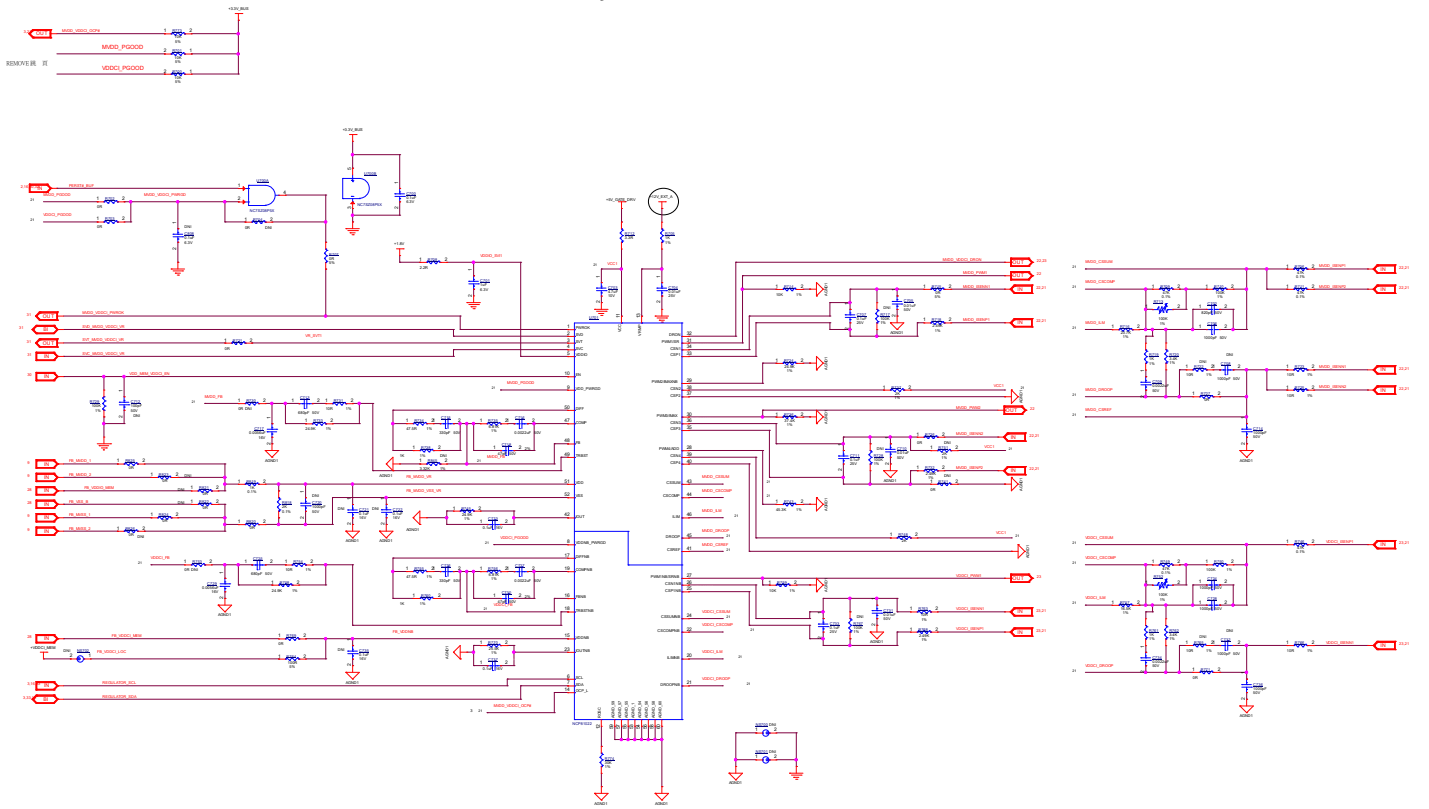


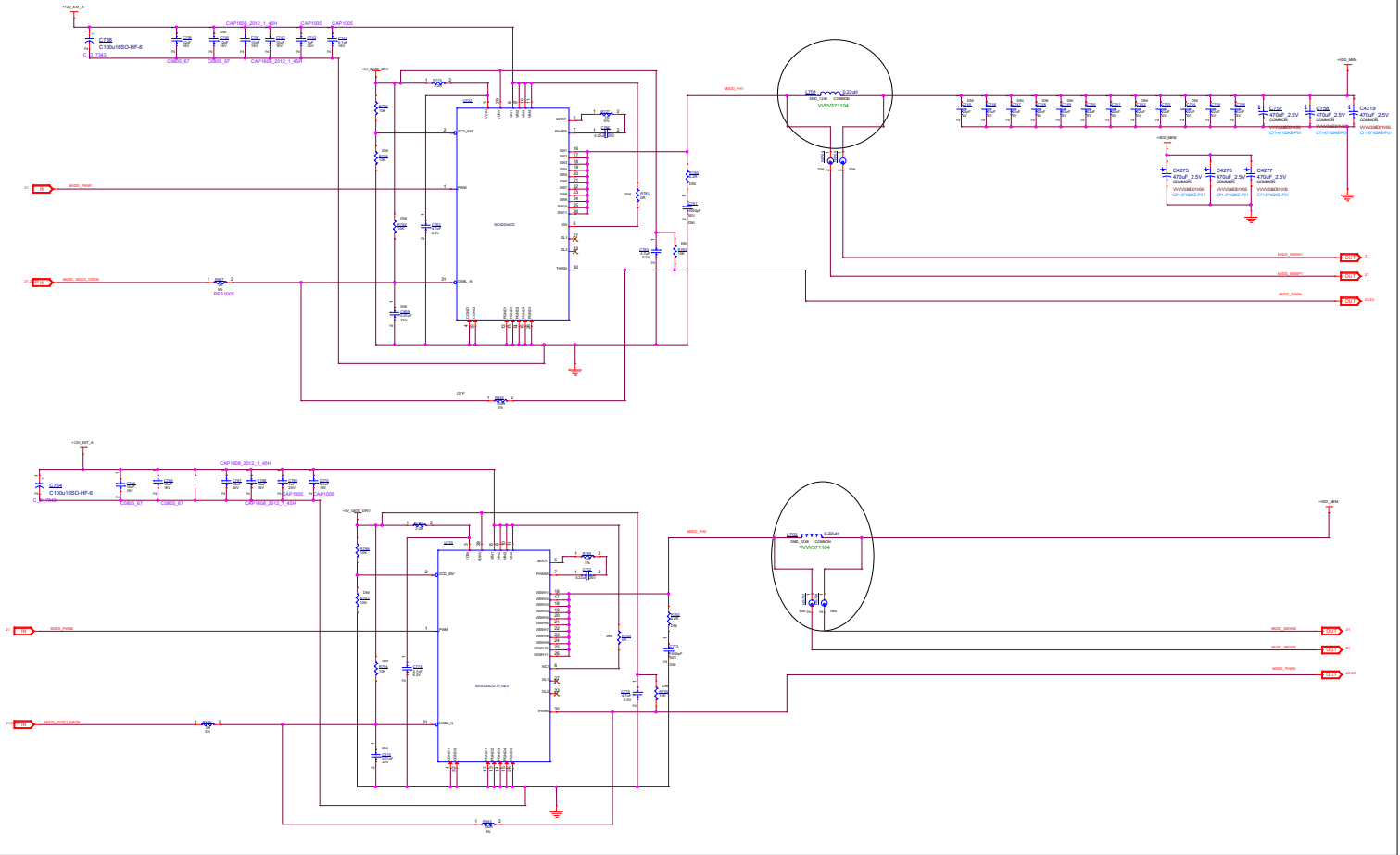


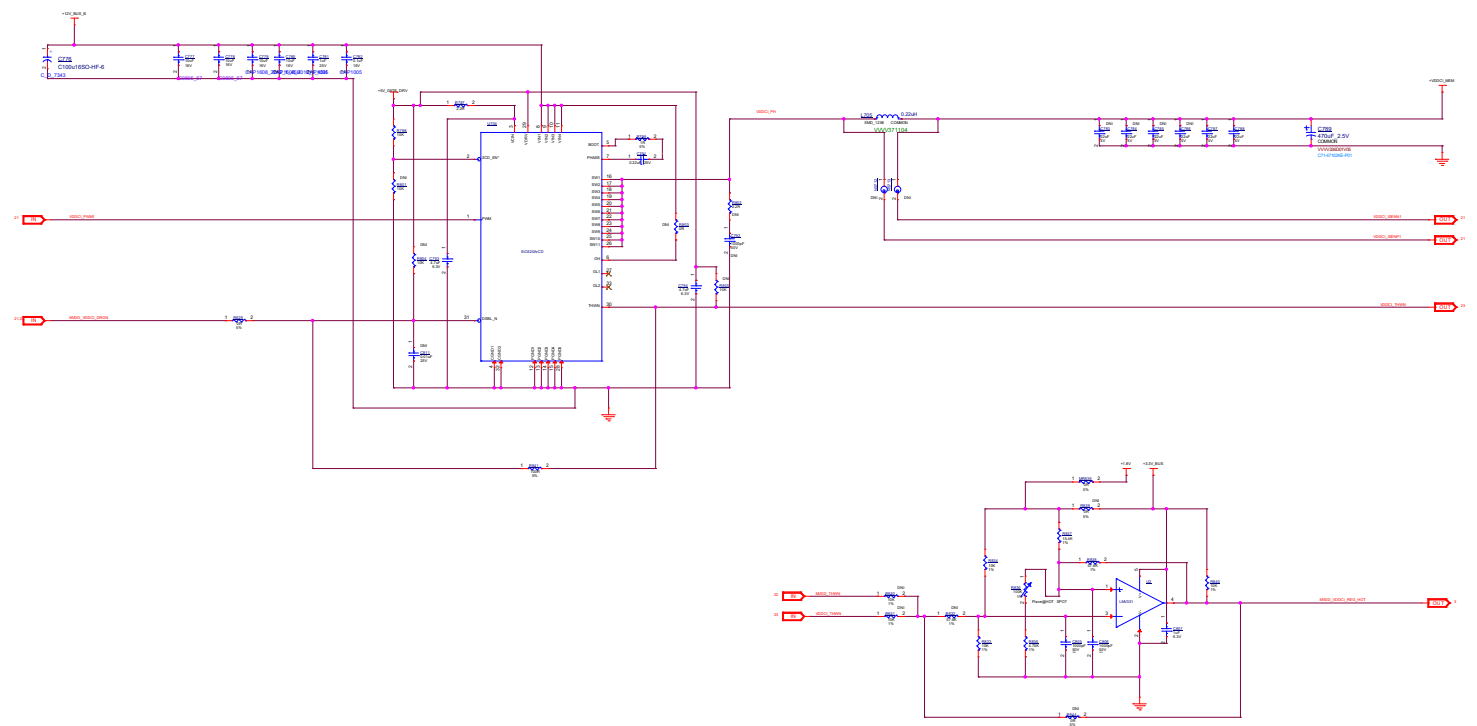


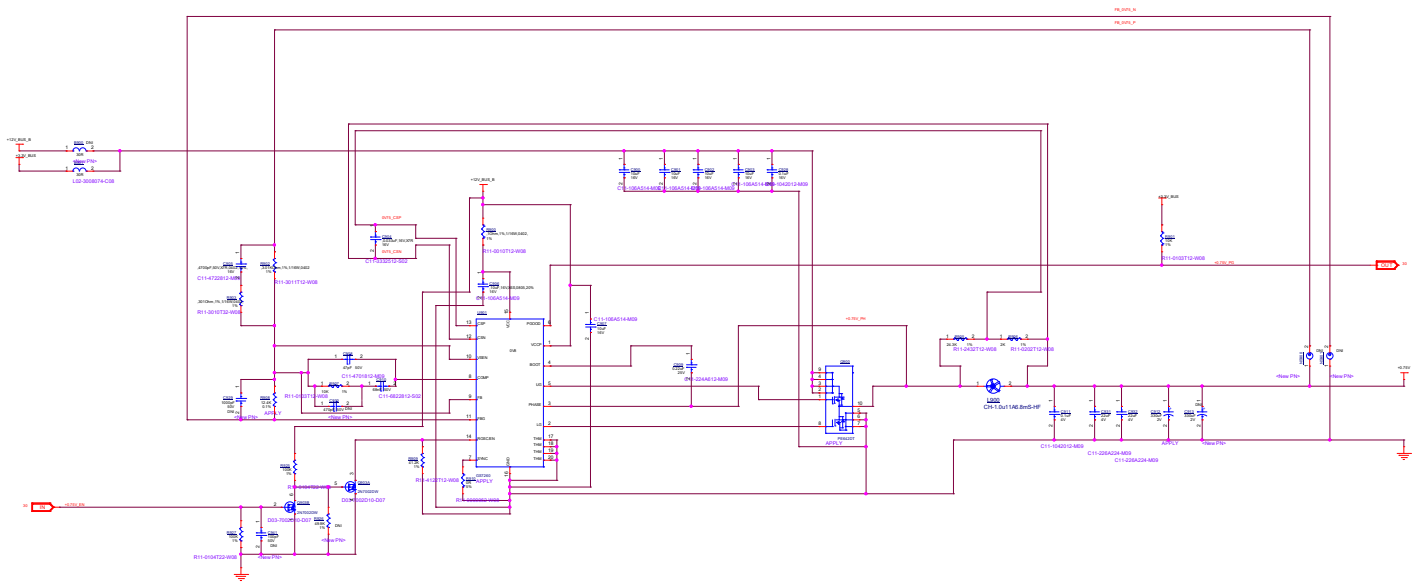


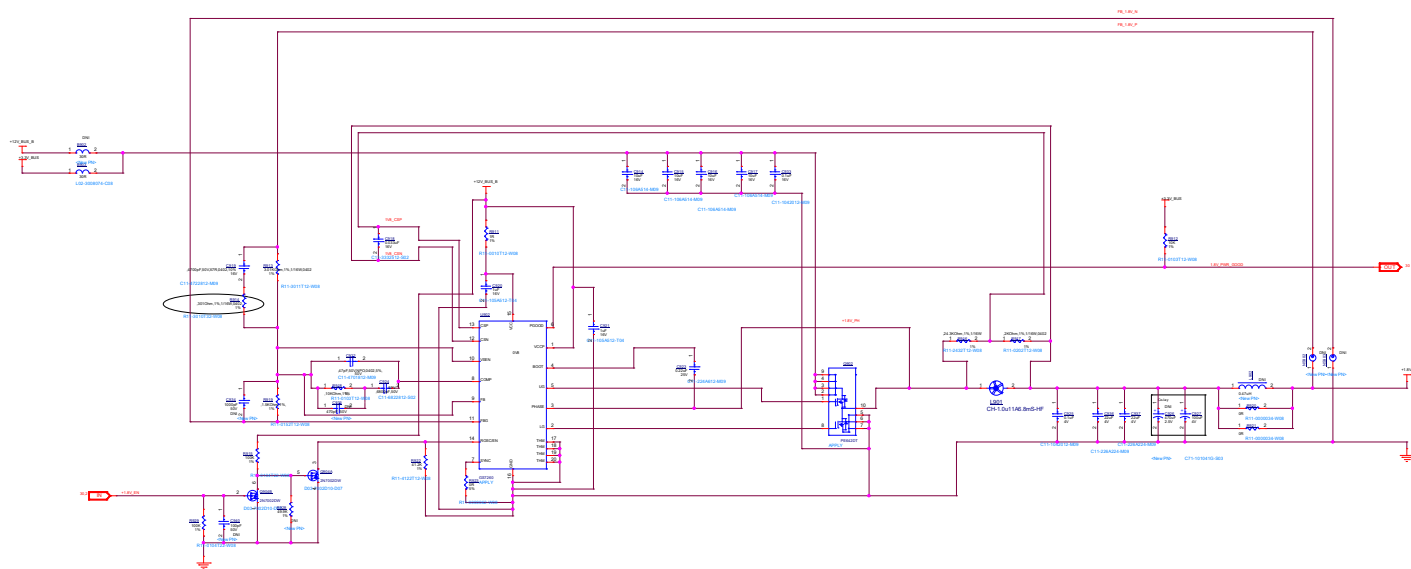




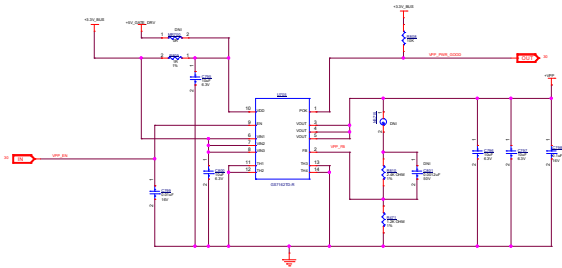
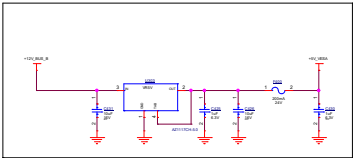


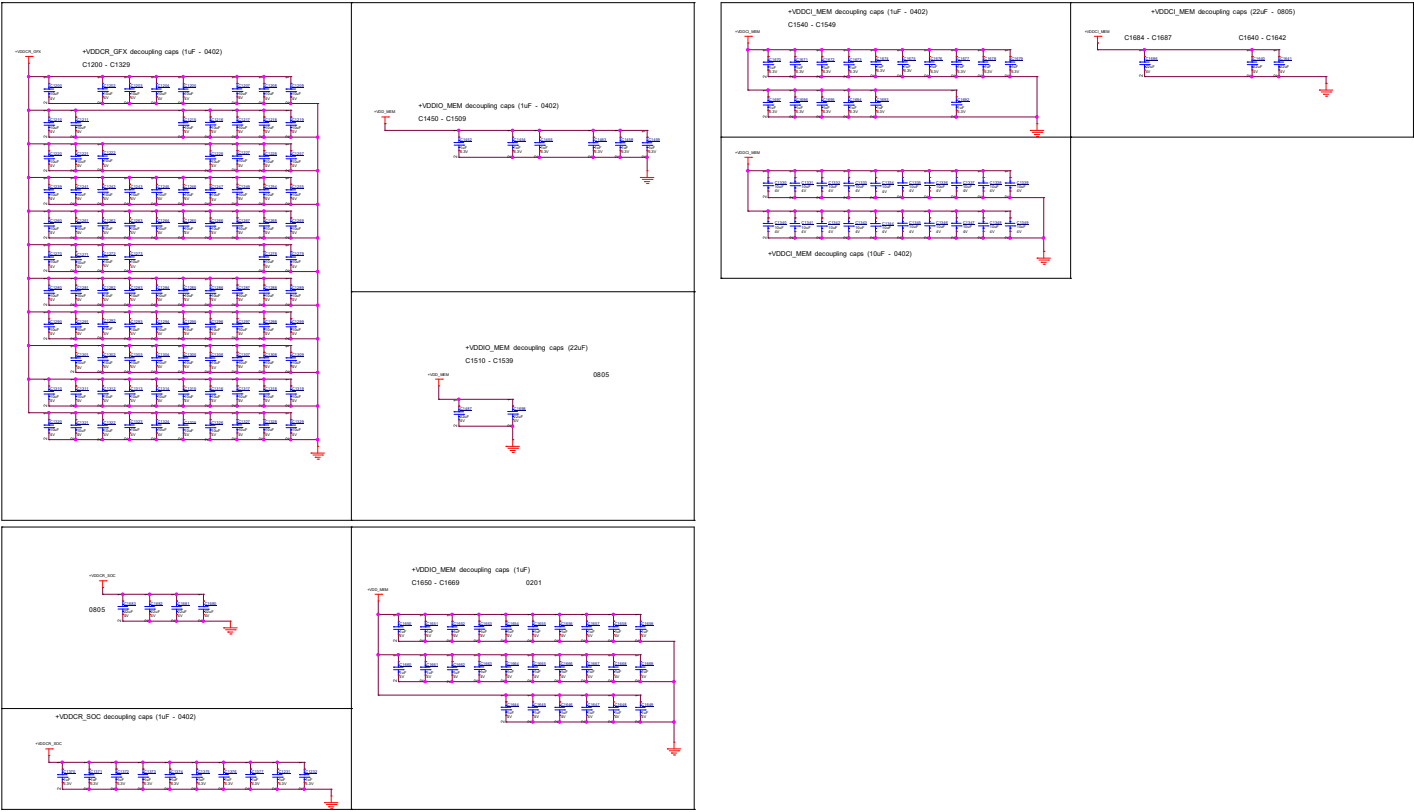


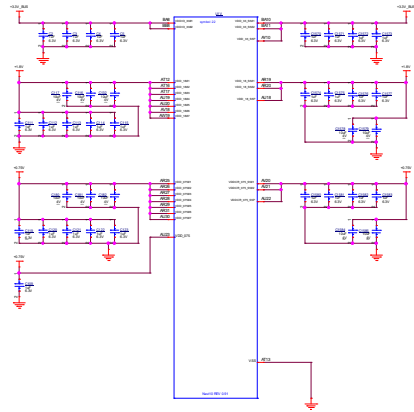
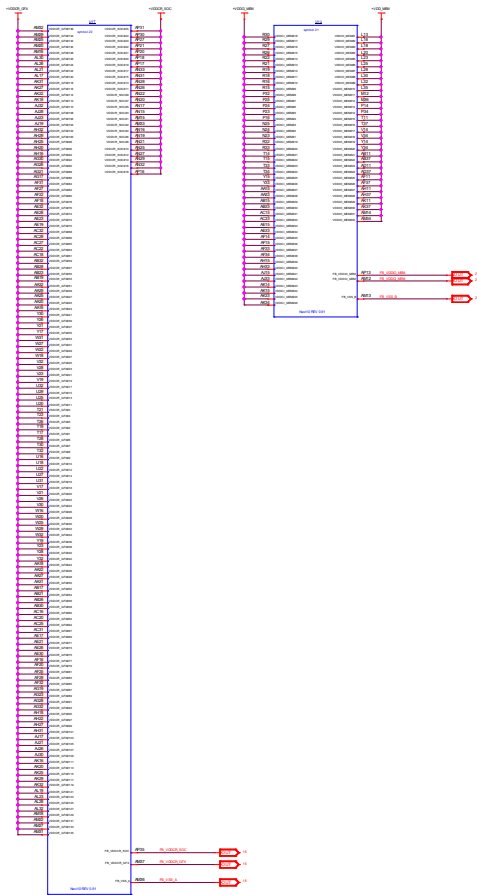


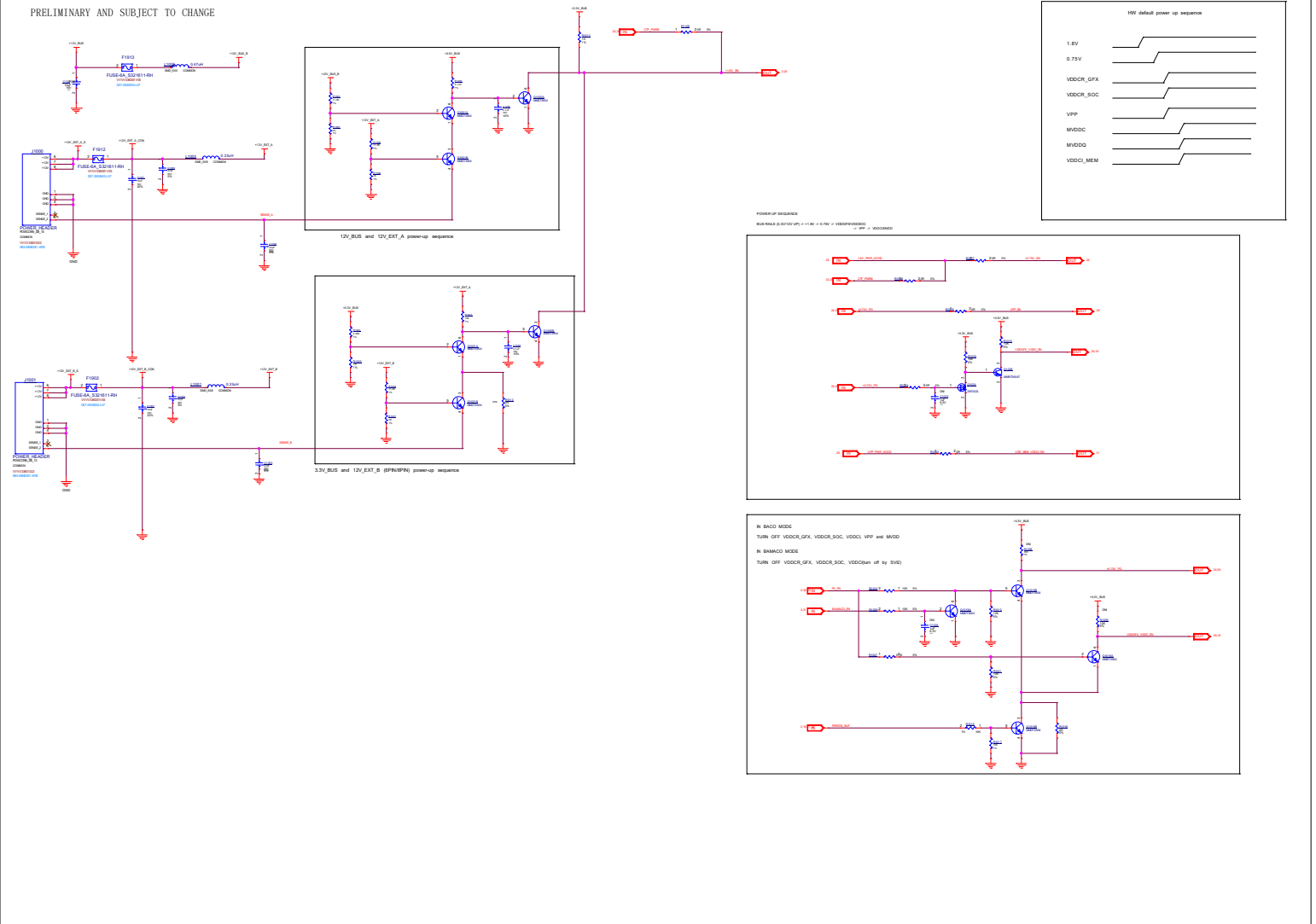


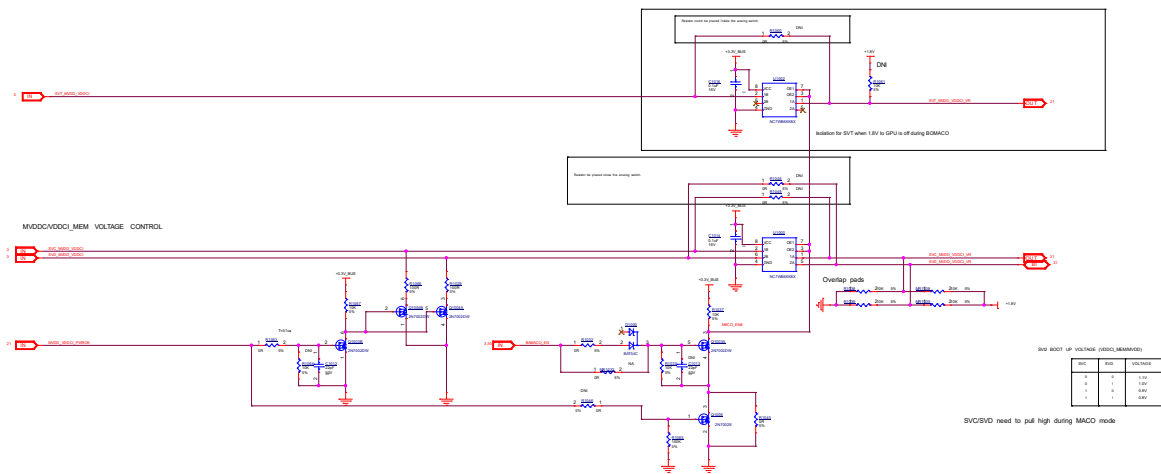
REGULATOR FOR -5V RAILS
I_{OUT} = 50mA

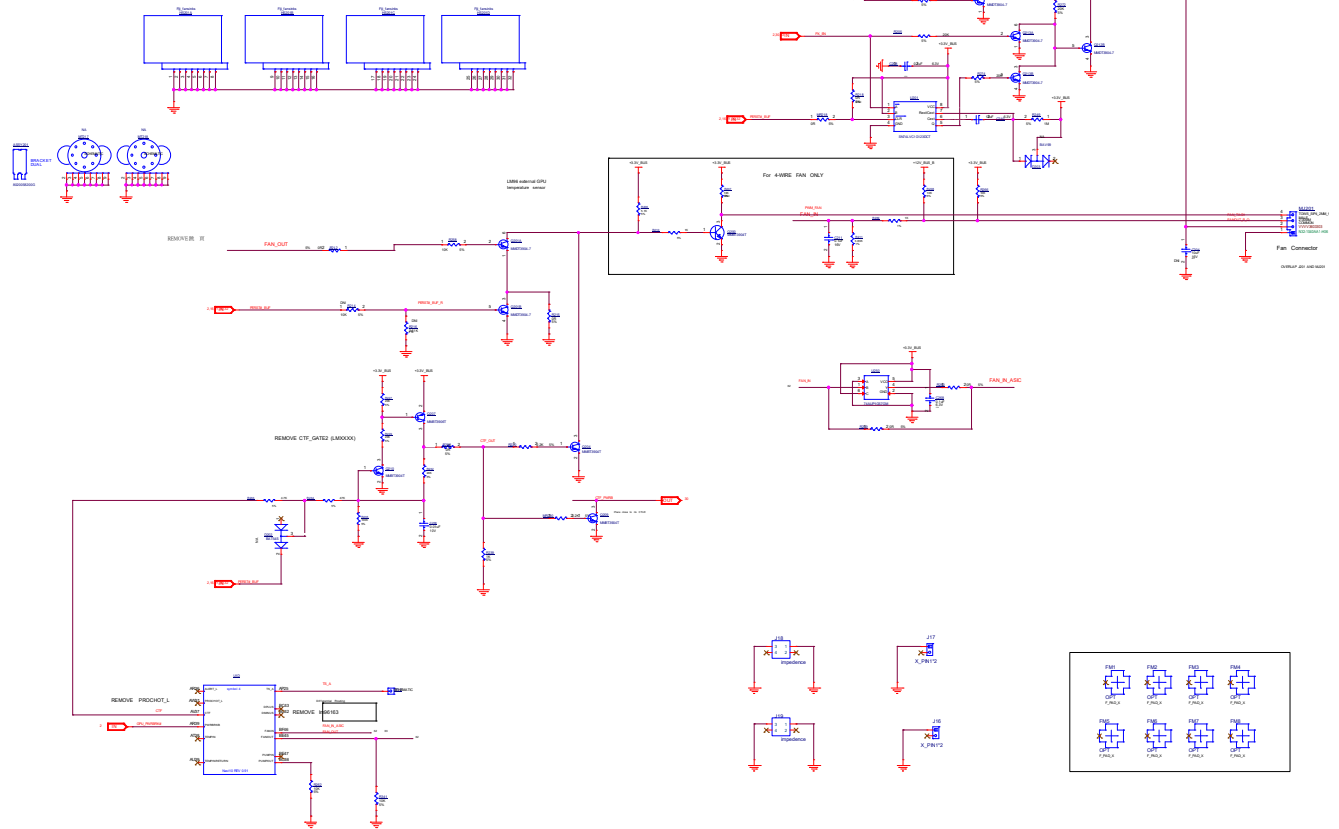


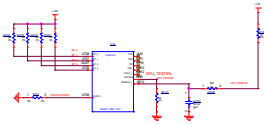




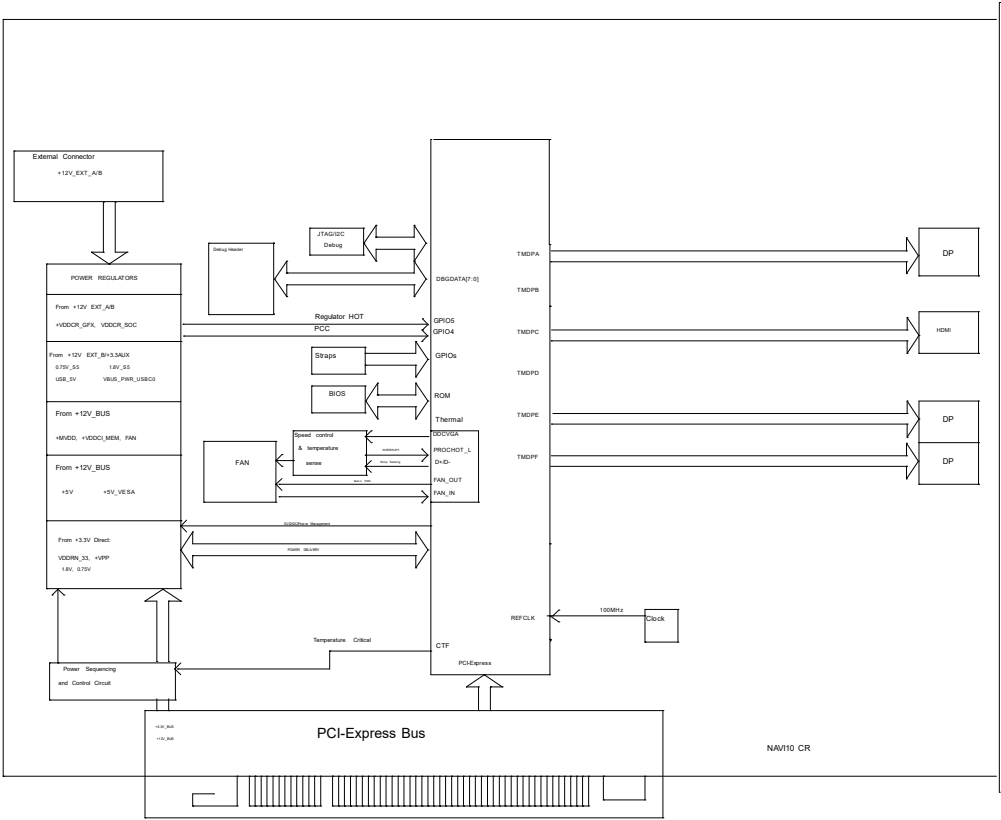






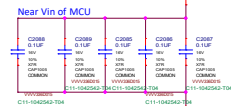
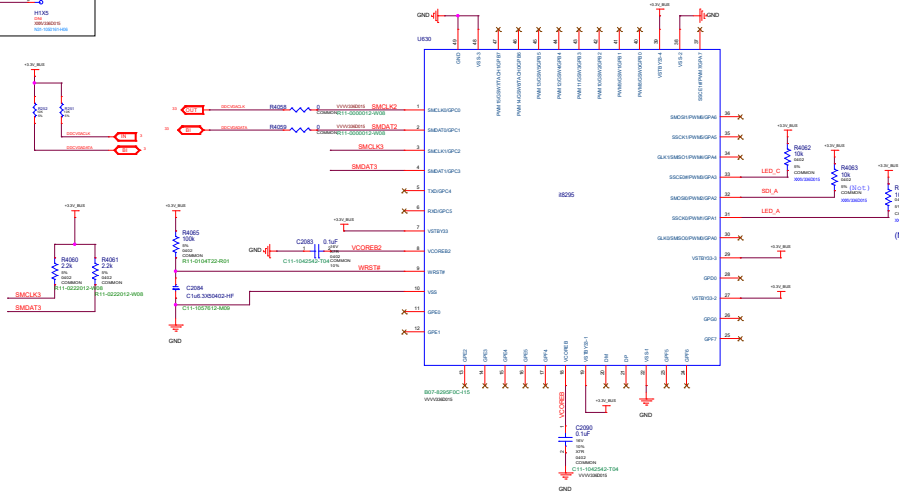
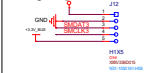


PROCON Lightbar Header

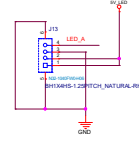


Firmware Programming

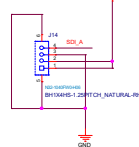
Debug



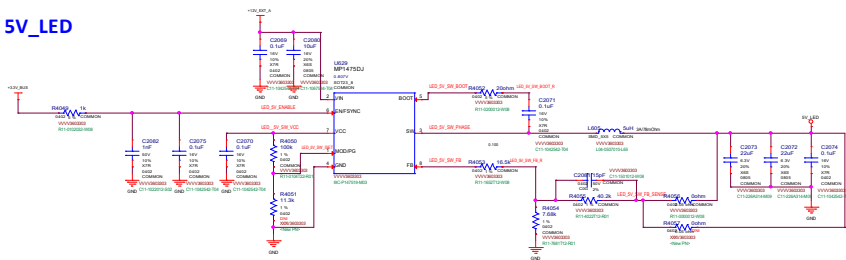
LED_A

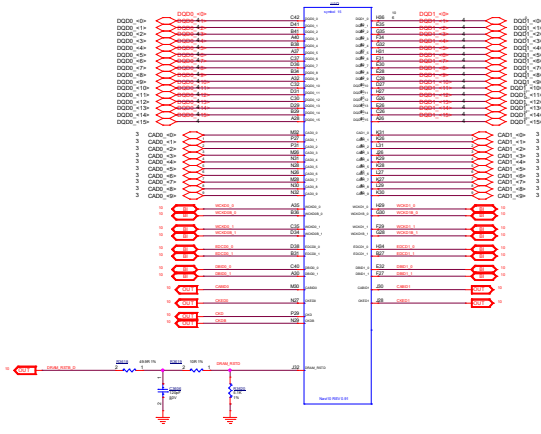
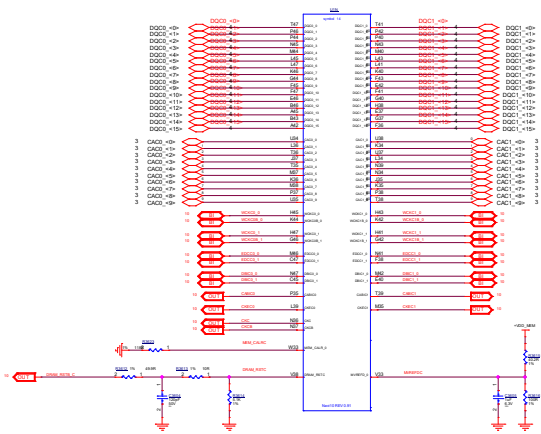


LED_B

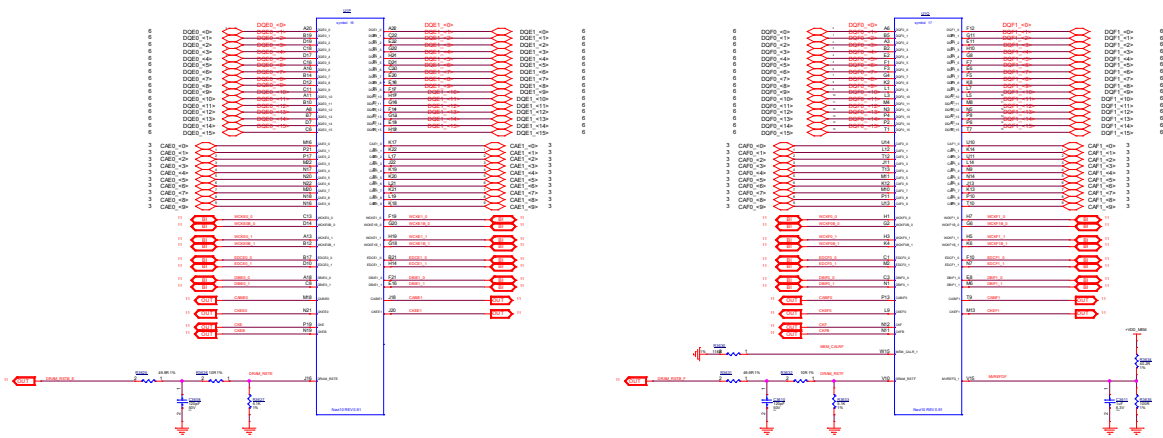


5V_LED

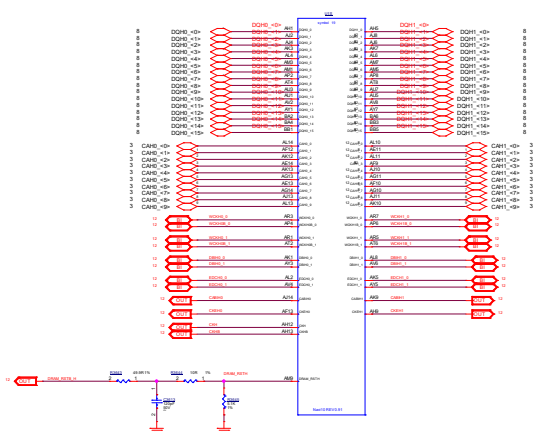
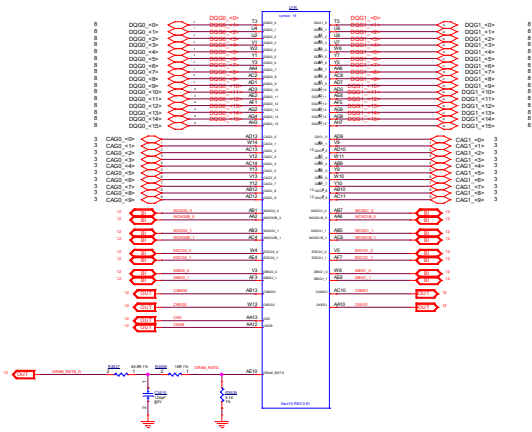




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SHEET: NAVI10-MEM002					
DATE: Mon Apr 15 09:57:57 2019		REV: 1.0			
SHEET NUMBER: 6 OF 35		TITLE: NAVI10 G6X18Mode DP DP HDMI DP			
DOCUMENT NUMBER: 10L_018m_00A					
NOTES: NOTE					



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SHEET: NAVI10 MEM CH E/F					
DATE: Mon Apr 15 09:57:57 2019		REV: 1.0			
SHEET NUMBER: 7		OF: 35		TITLE: NAVI10 G6X10 Mode DP DP HDMI DP	
DOCUMENT NUMBER: 100_010000_000					
NOTES: NOTE					



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