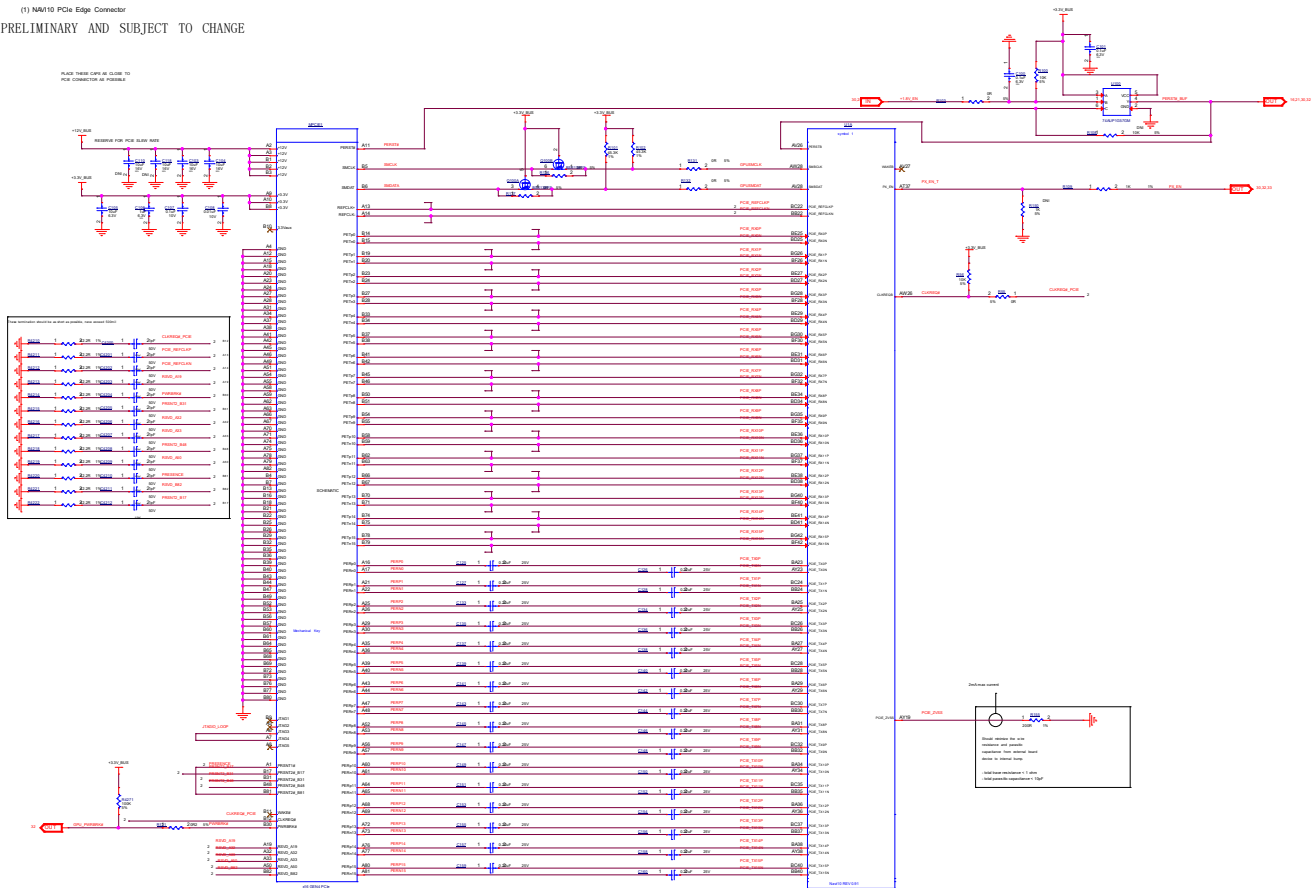
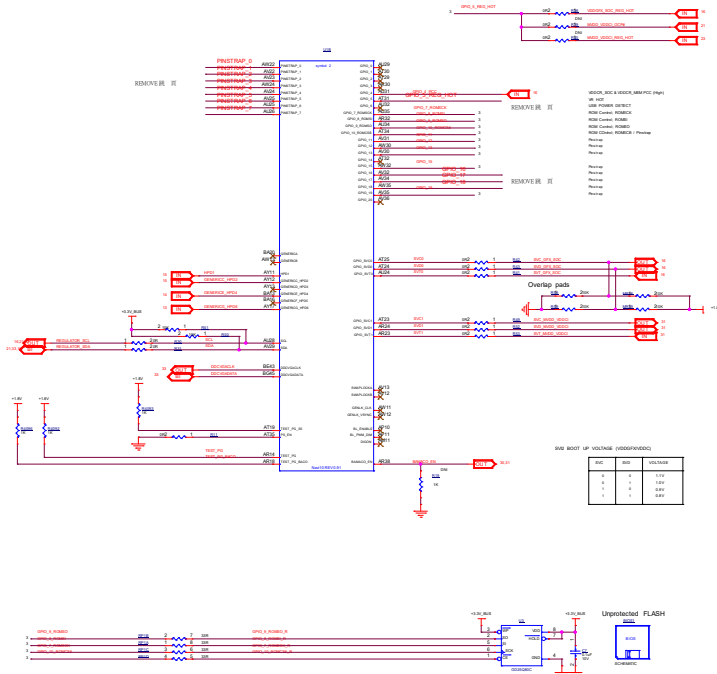


PRELIMINARY AND SUBJECT TO CHANGE

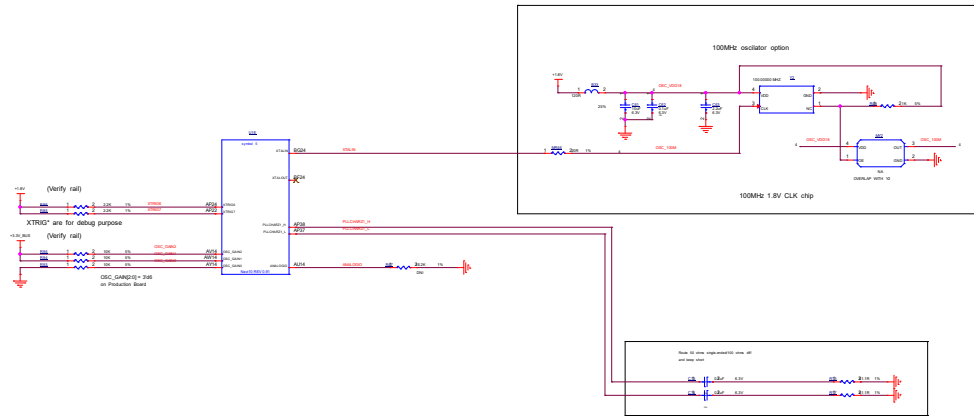
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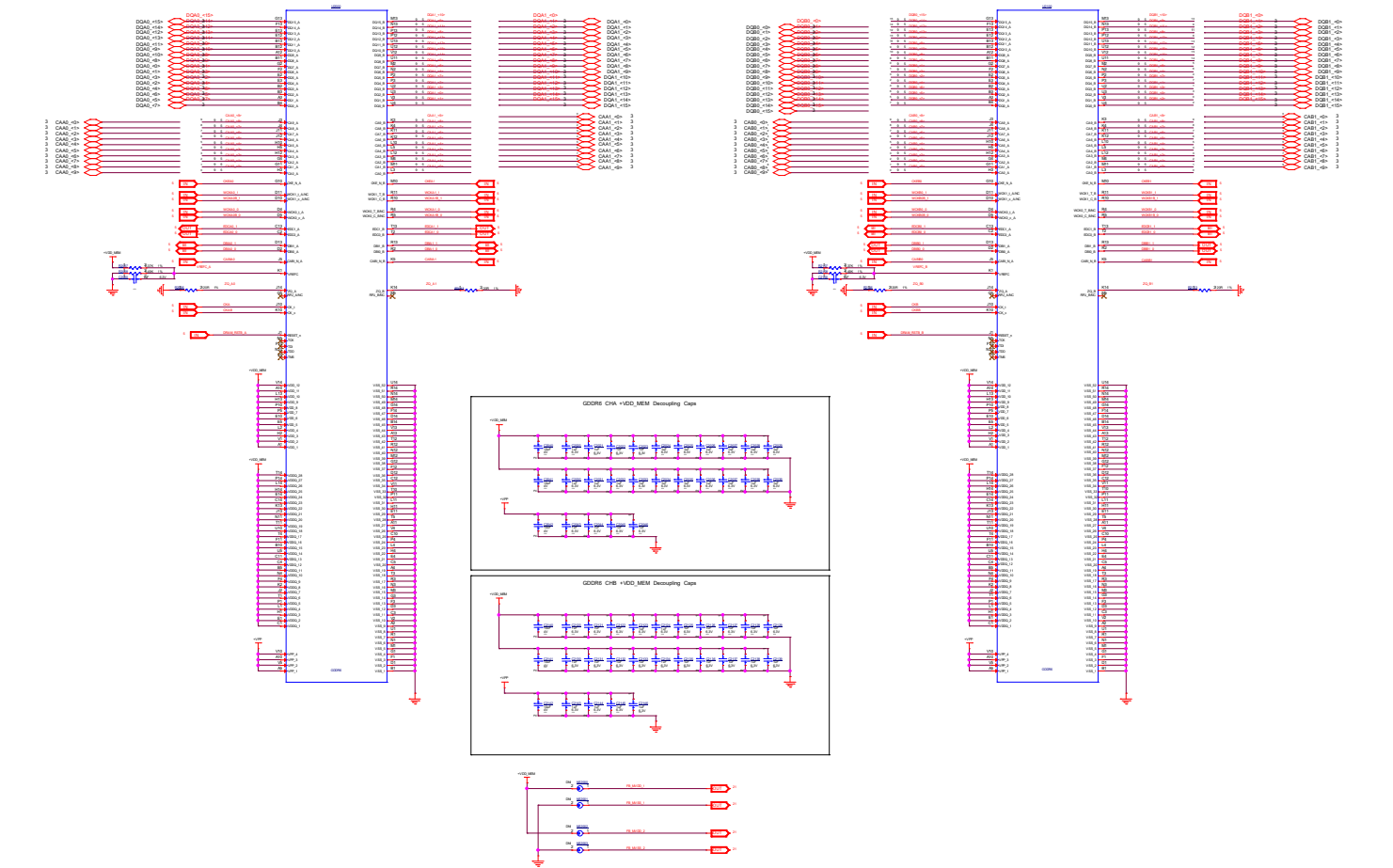
SHEET NO.	SHEET NAME	SHEET NO.	SHEET NAME
1	TOC	26	SMALL FAN REGULATORS
2	NAUTO PCB Interface	27	NAUTO DECAPS
3	NAUTO GPCH	28	NAUTO POWER
4	NAUTO STAL	29	NAUTO GND
5	NAUTO MEM CHAB	30	POWER MANAGEMENT
6	NAUTO MEM CHCD	31	SAG & SBRACO
7	NAUTO MEM CHEF	32	MECHANICAL & THERMAL
8	NAUTO MEM CHSH	33	DEBUG
9	GDORM MEM CHAB	34	BLOCK DIAGRAM
10	GDORM MEM CHCD	35	REVISION HISTORY
11	GDORM MEM CHEF		
12	GDORM MEM CHSH		
13	NAUTO TMCPA - GP		
14	NAUTO TMCPA - HDMI		
15	NAUTO TMCPA - DP DP		
16	GPX & SOC CONTROLLER		
17	VEDOR_GFX PHASES 1 and 5		
18	VEDOR_GFX PHASES 1 and 4		
19	VEDOR_GFX PHASES 1 and 7		
20	VEDOR_GFX PHASES 6 and VEDOR_SOC		
21	MVED_VEDCO CONTROLLER		
22	REG VEDCO		
23	REG VEDCO		
24	REG 0.75V		
25	REG 1.8V		

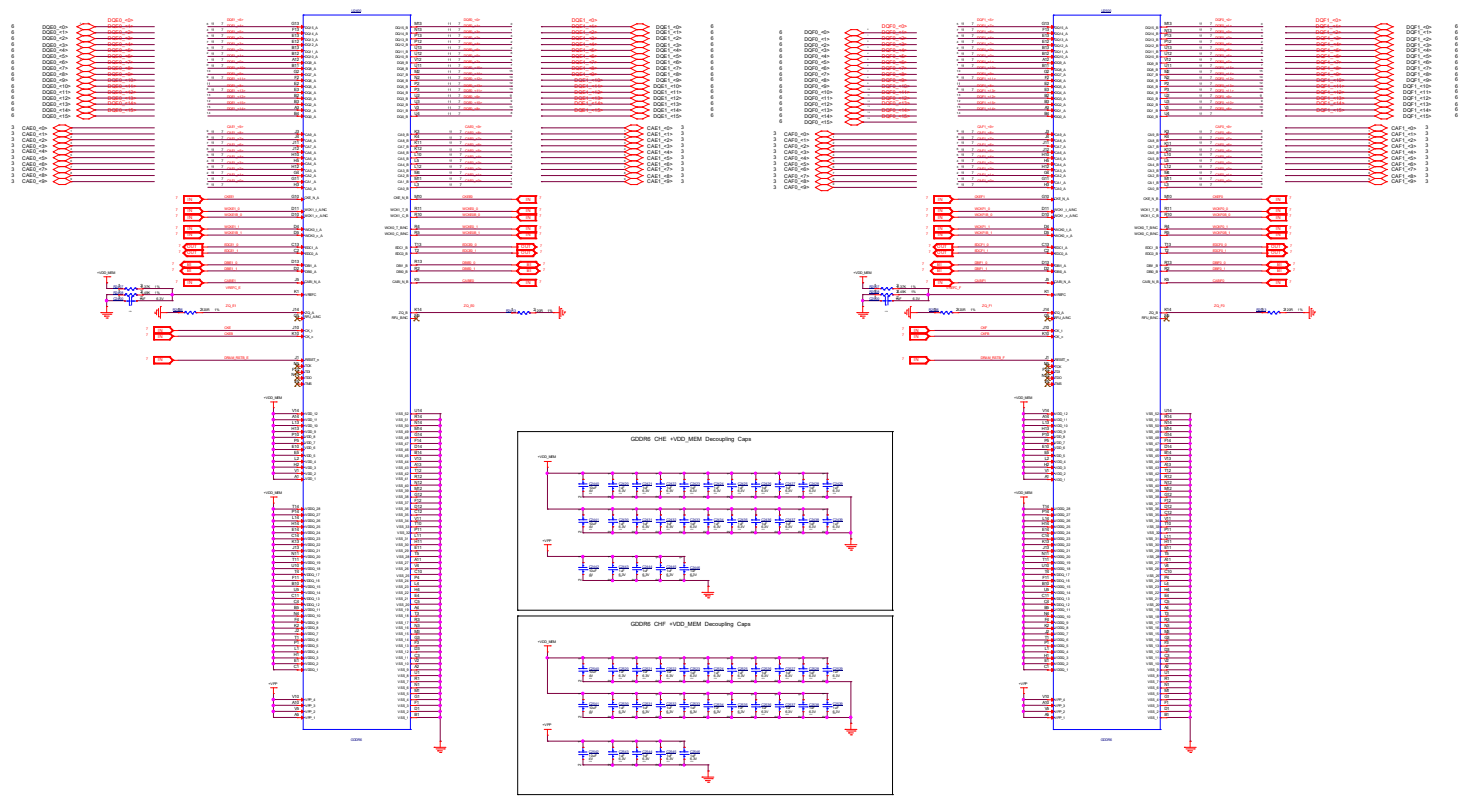


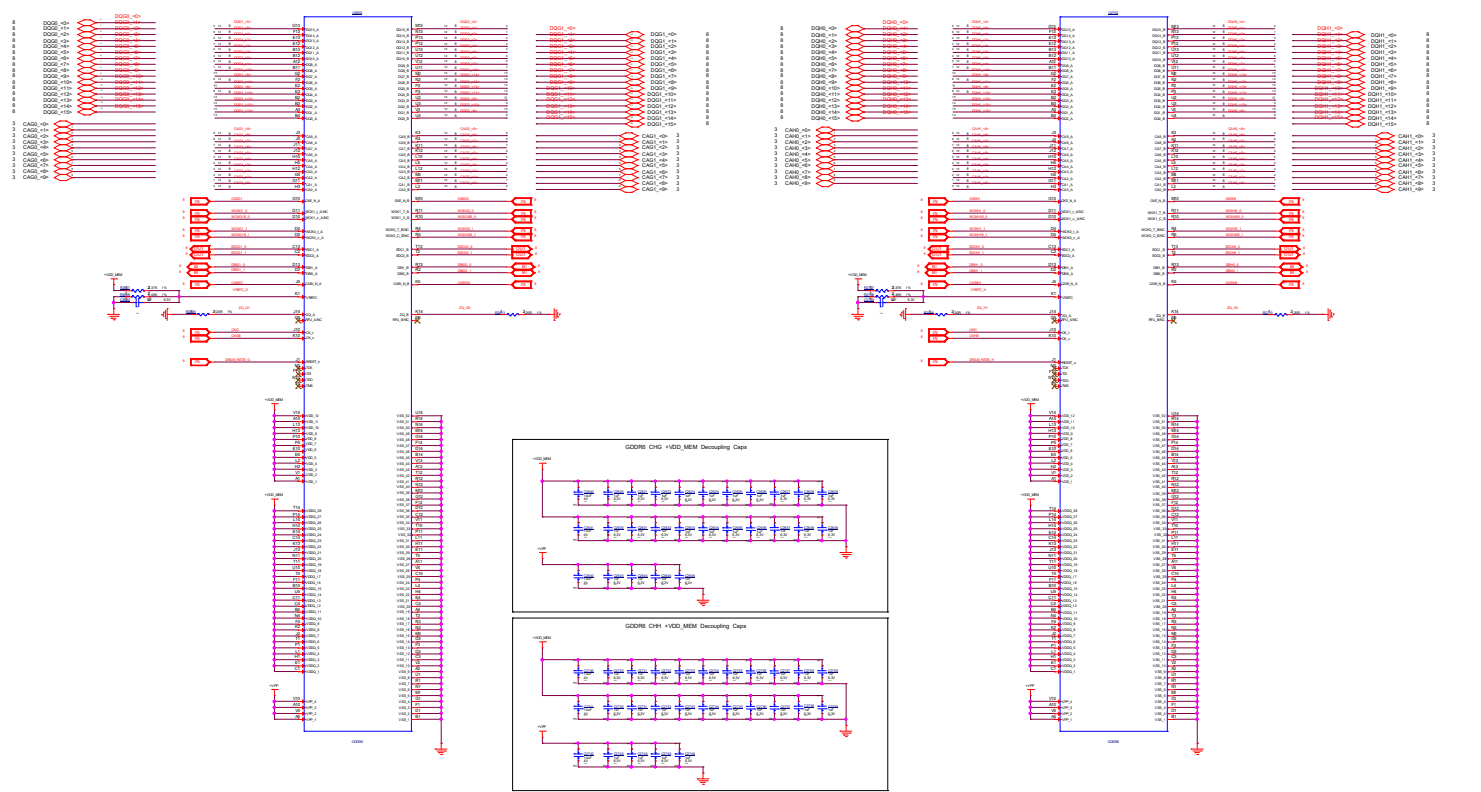


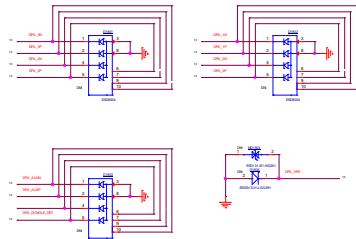
User	Internal Default Value	Definition
BIF	PINSTRAP_0	STRAP_BIF_GEN4_DIS_A
	PINSTRAP_1	STRAP_BIF_CLK_PM_EN
	PINSTRAP_2	STRAP_BIF_LC_TX_SWING
	PINSTRAP_3	STRAP_BIF_VGA_DIS
DCE	PINSTRAP_4	PINSTRAP_AUD_PORT_CONN2[0]
	PINSTRAP_5	PINSTRAP_AUD1[0]
	PINSTRAP_6	PINSTRAP_SMBUS_ADDR
	PINSTRAP_7	PINSTRAP_BIOS_ROM_EN
Platform	PINSTRAP_8	
	PINSTRAP_9	
	PINSTRAP_10	
	PINSTRAP_11	
SMU	PINSTRAP_12	
	PINSTRAP_13	
	PINSTRAP_14	
	PINSTRAP_15	



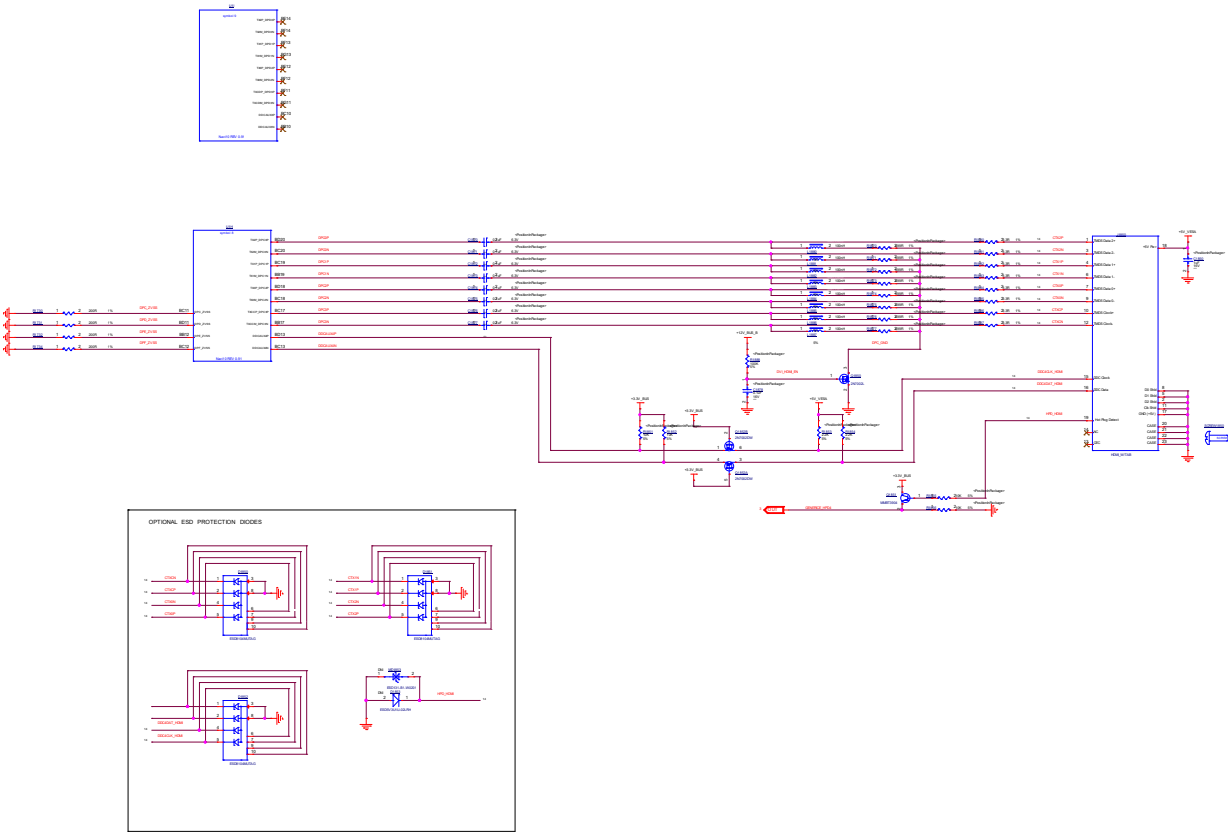




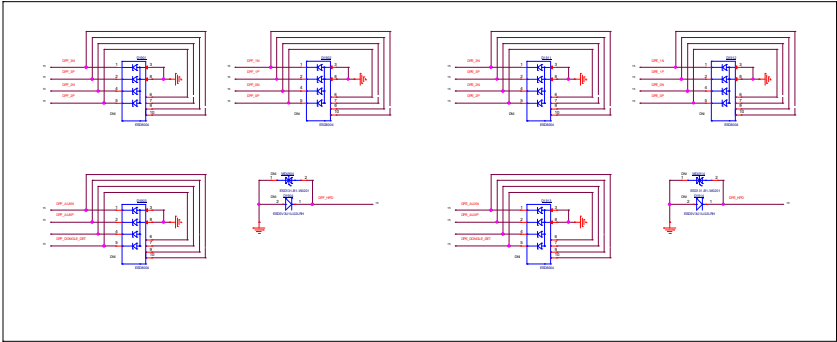
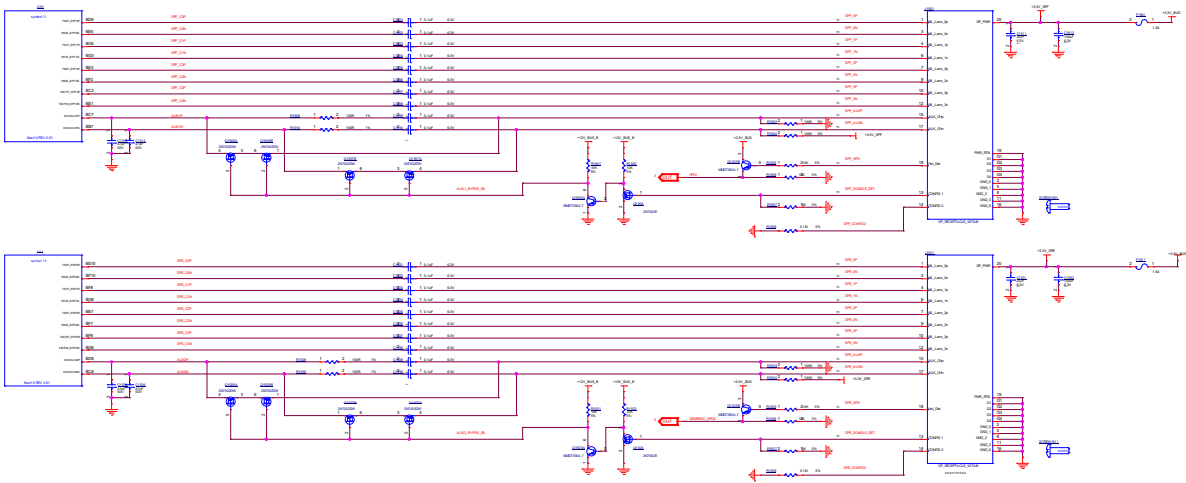


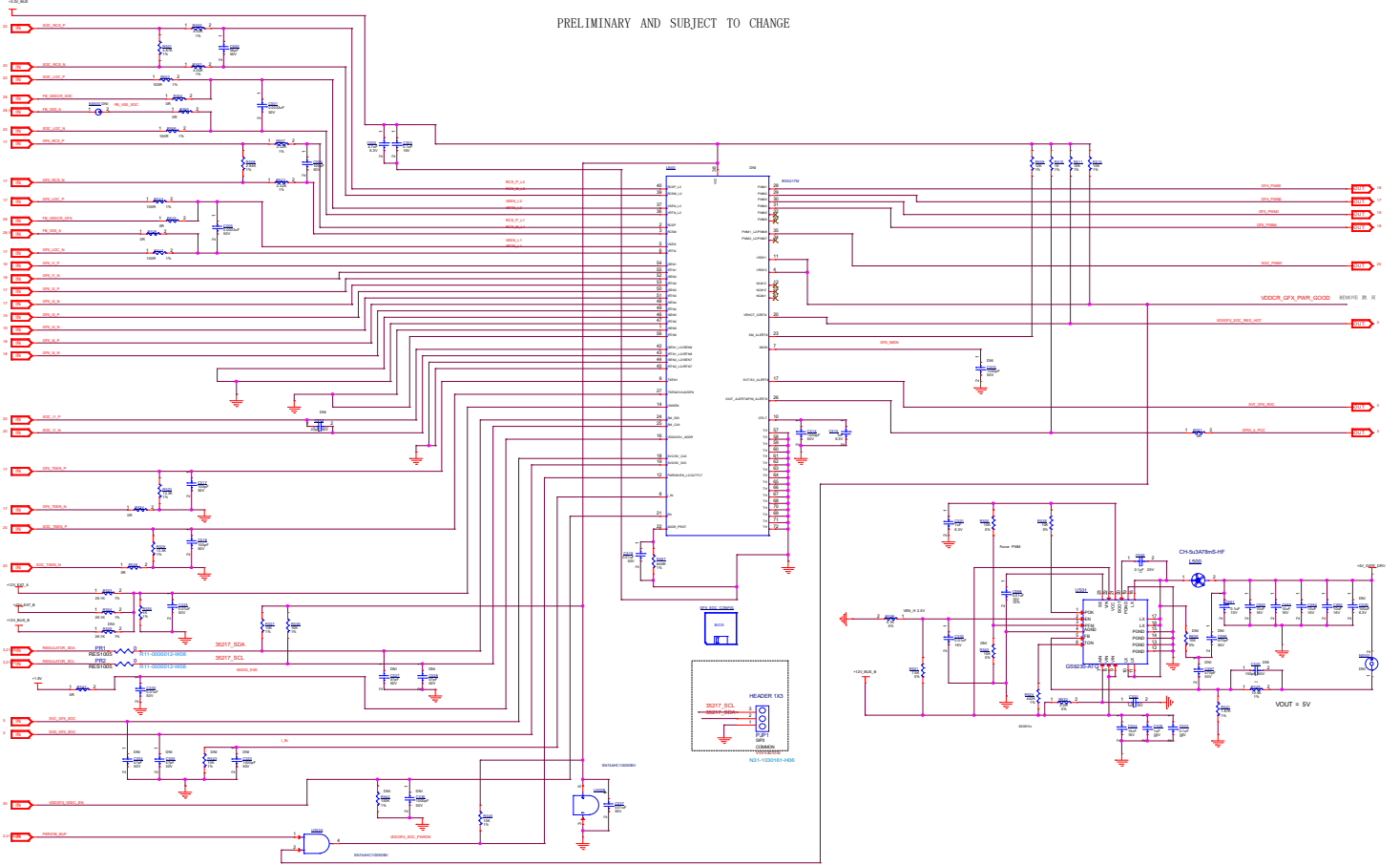


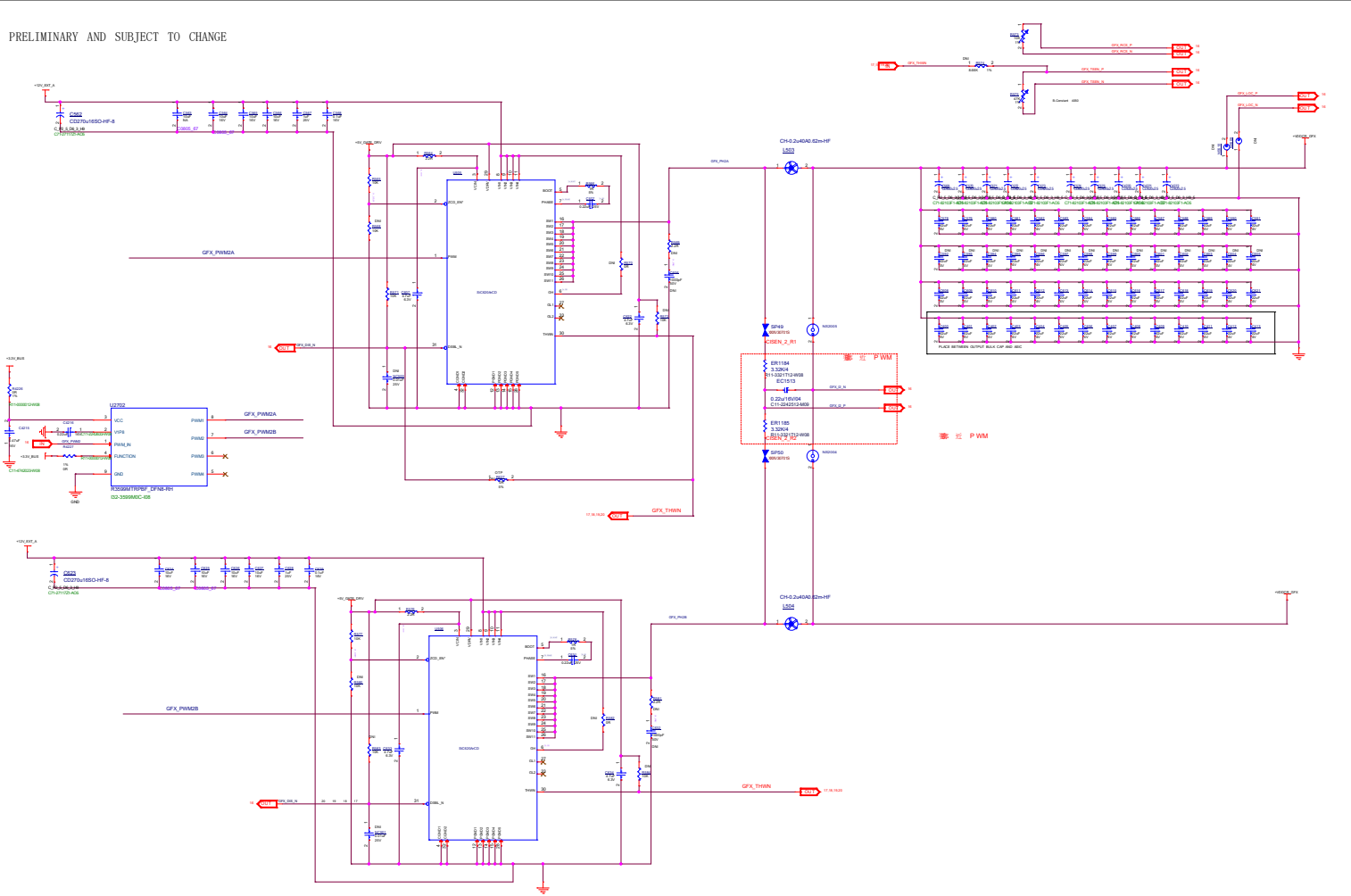
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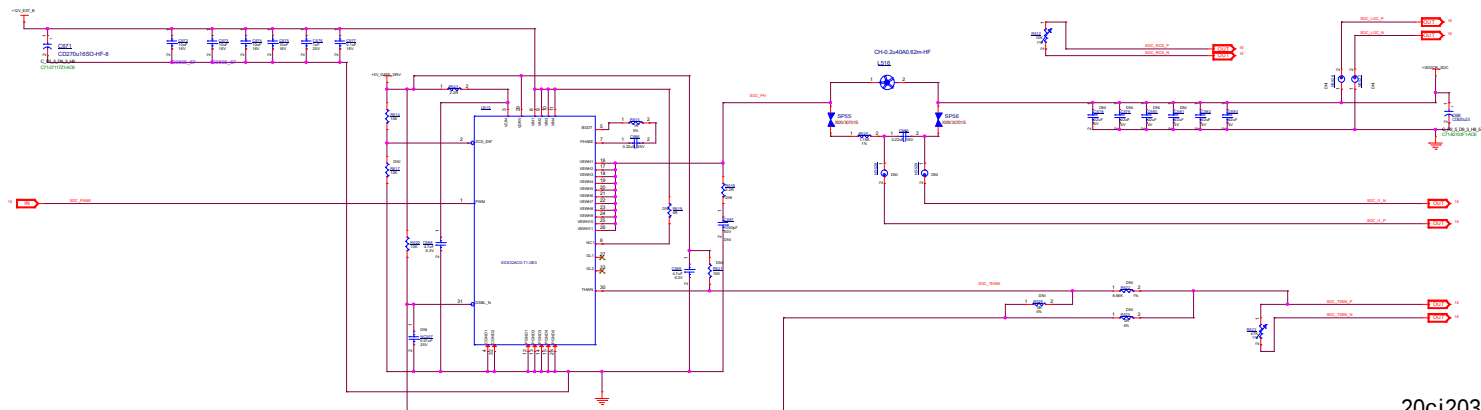


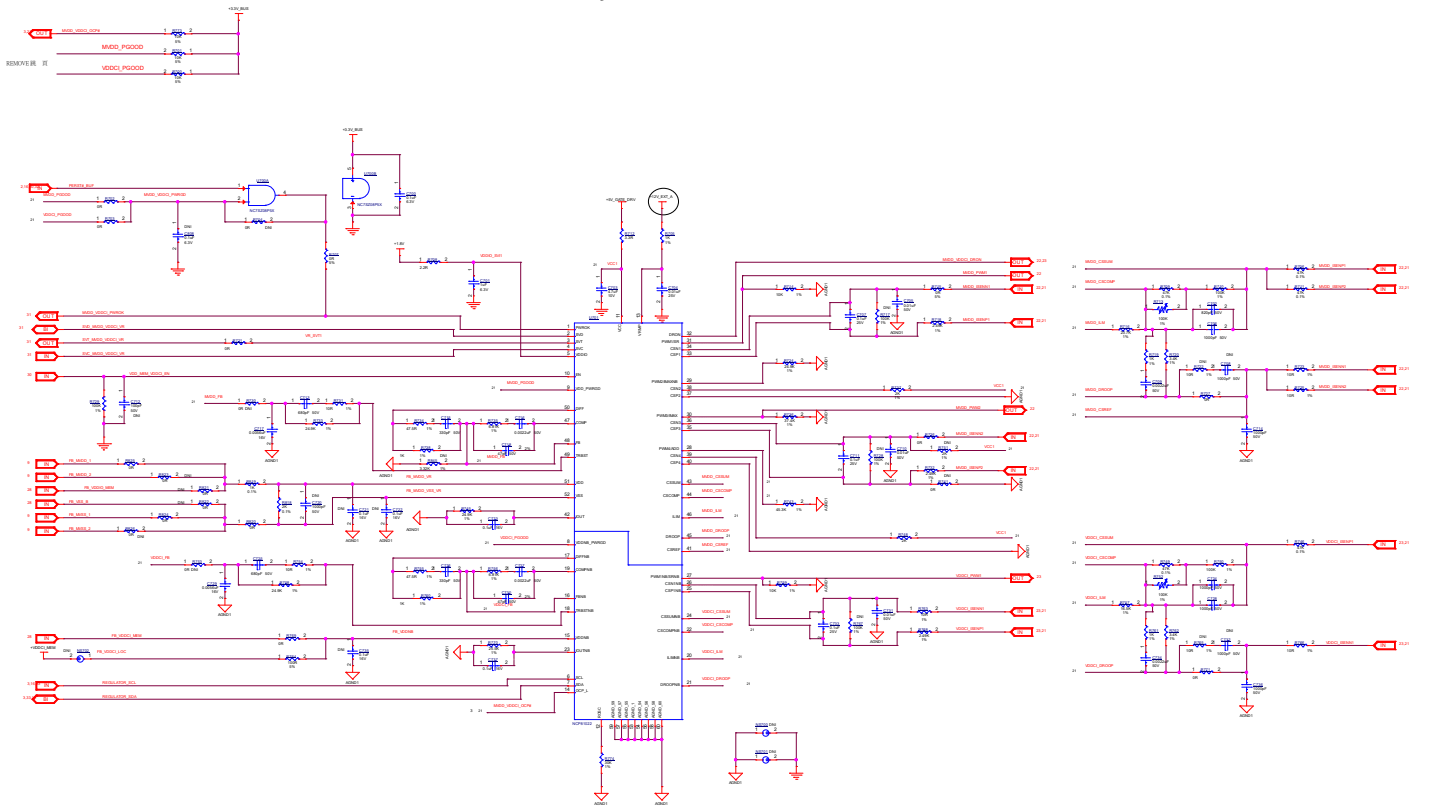
(9) NAVI10 TMDP E/F

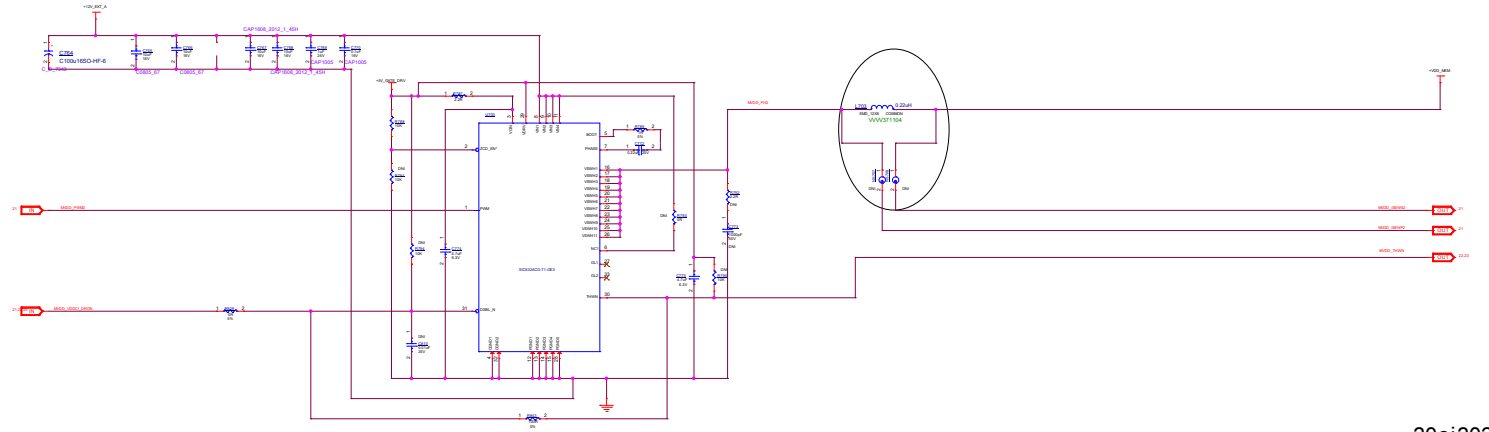
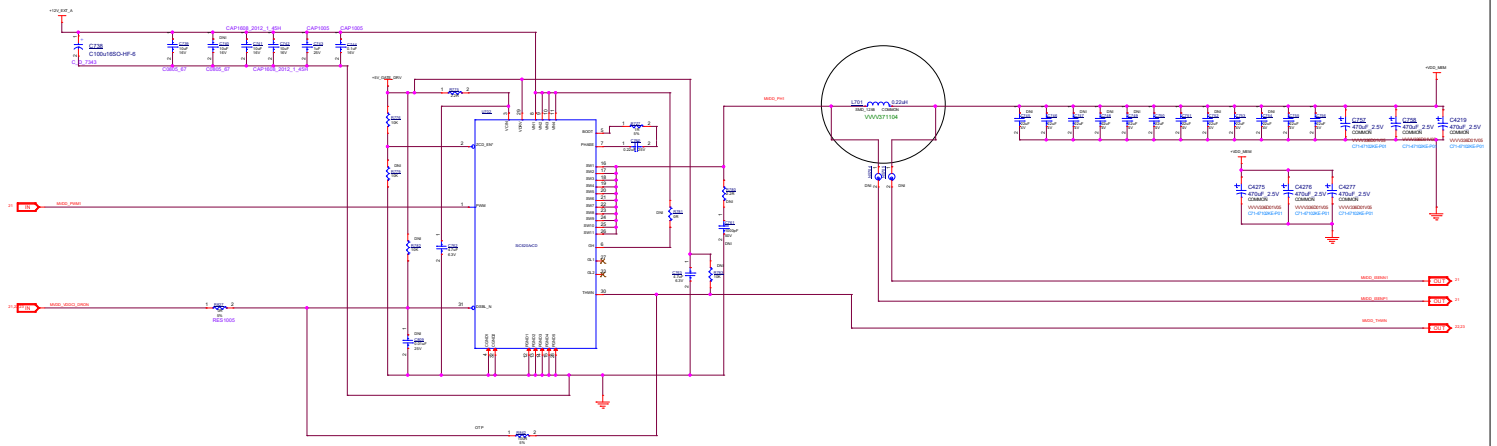


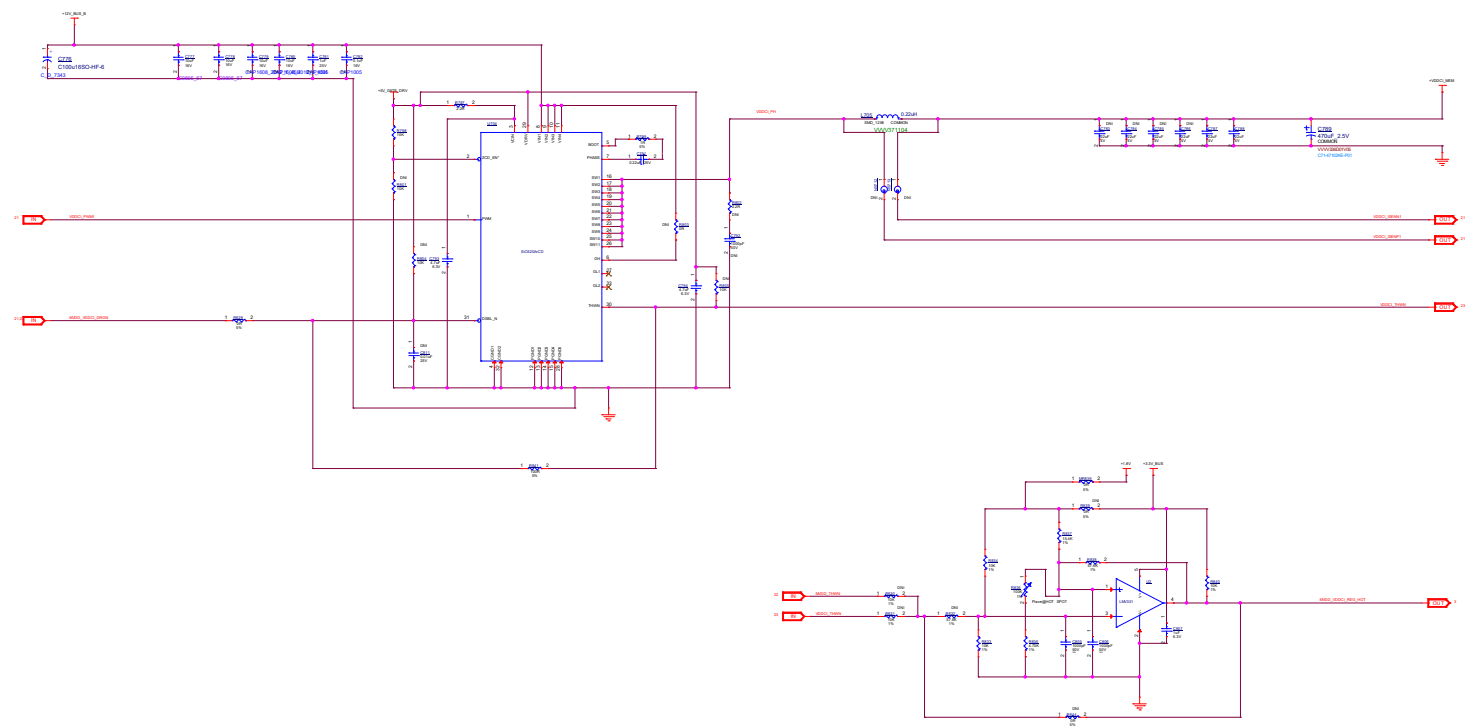




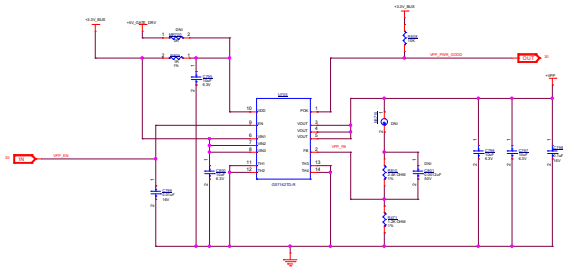
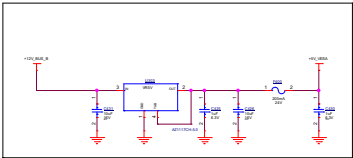


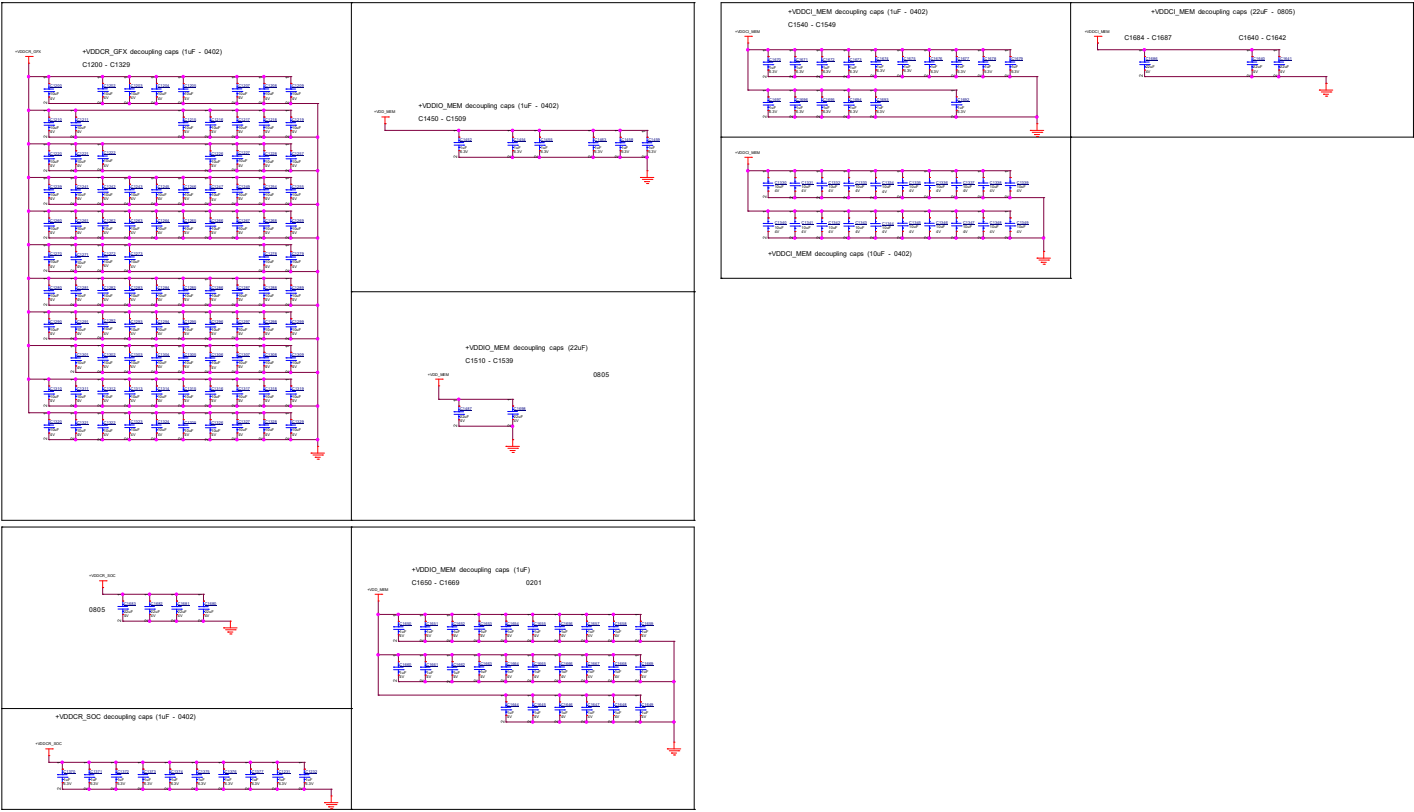


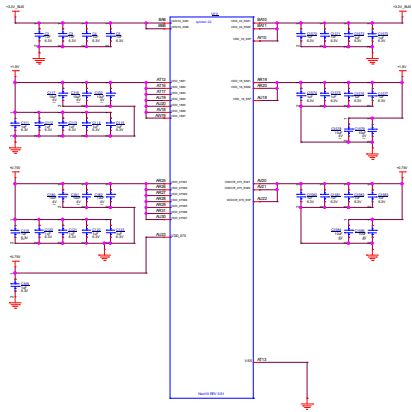
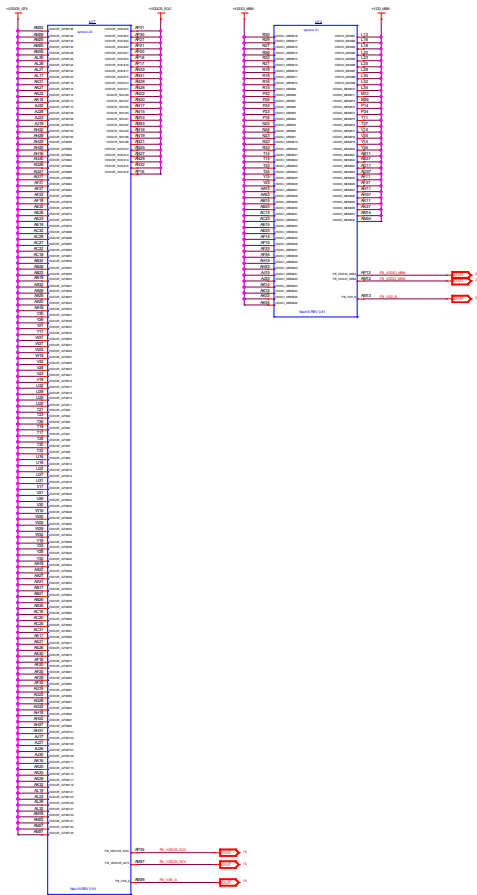


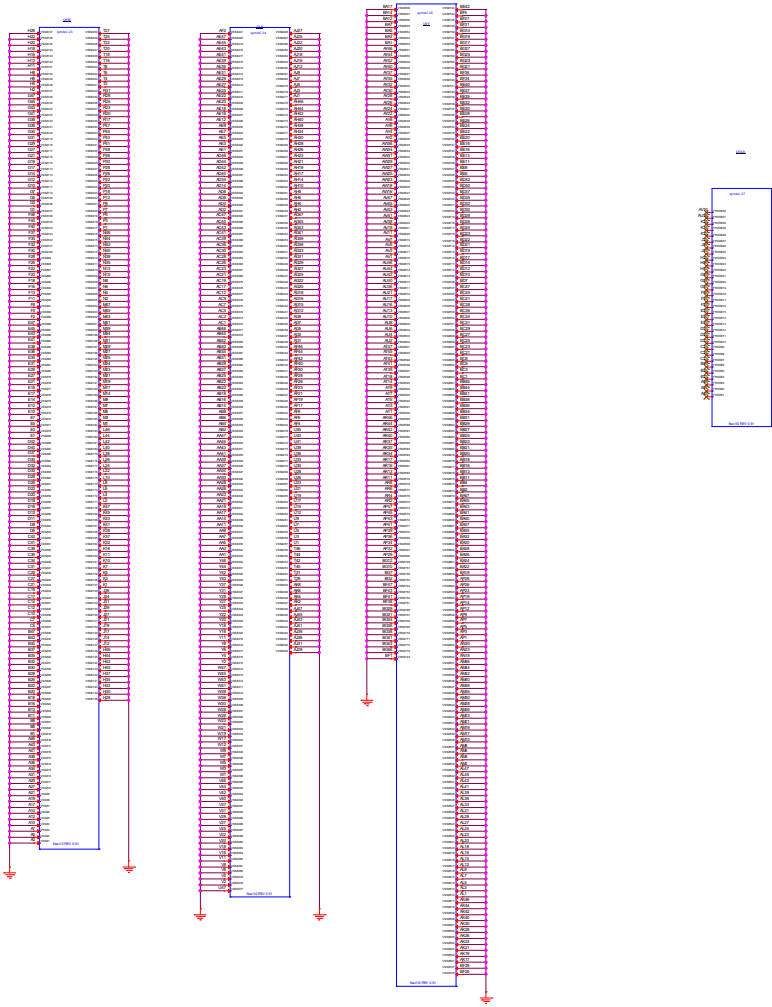


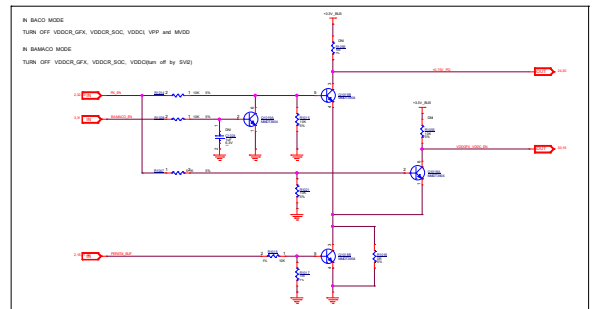
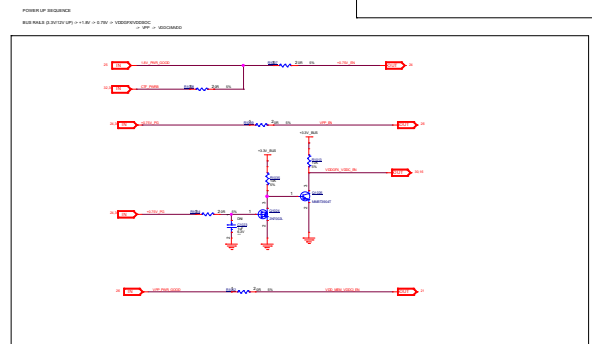
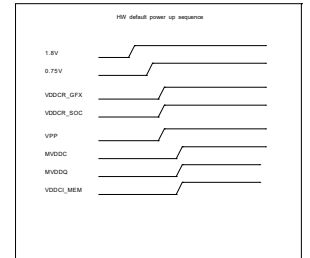
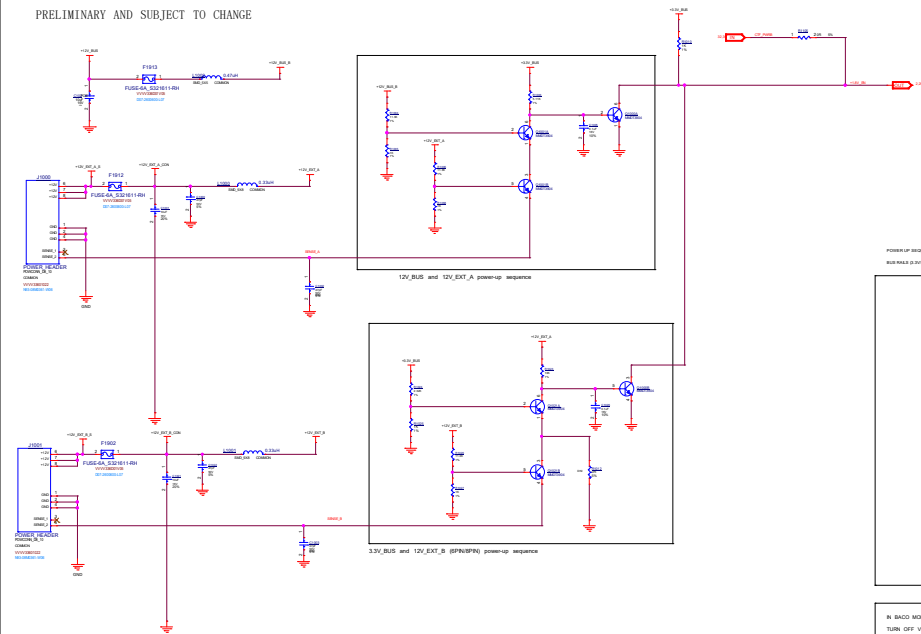
REGULATOR FOR -5V Rails
I_{OUT} = 50mA

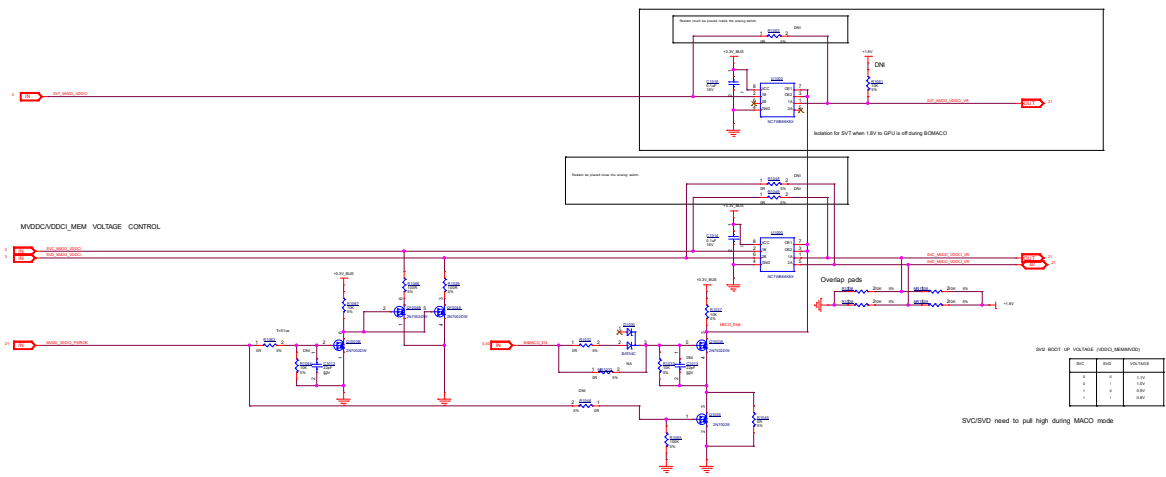


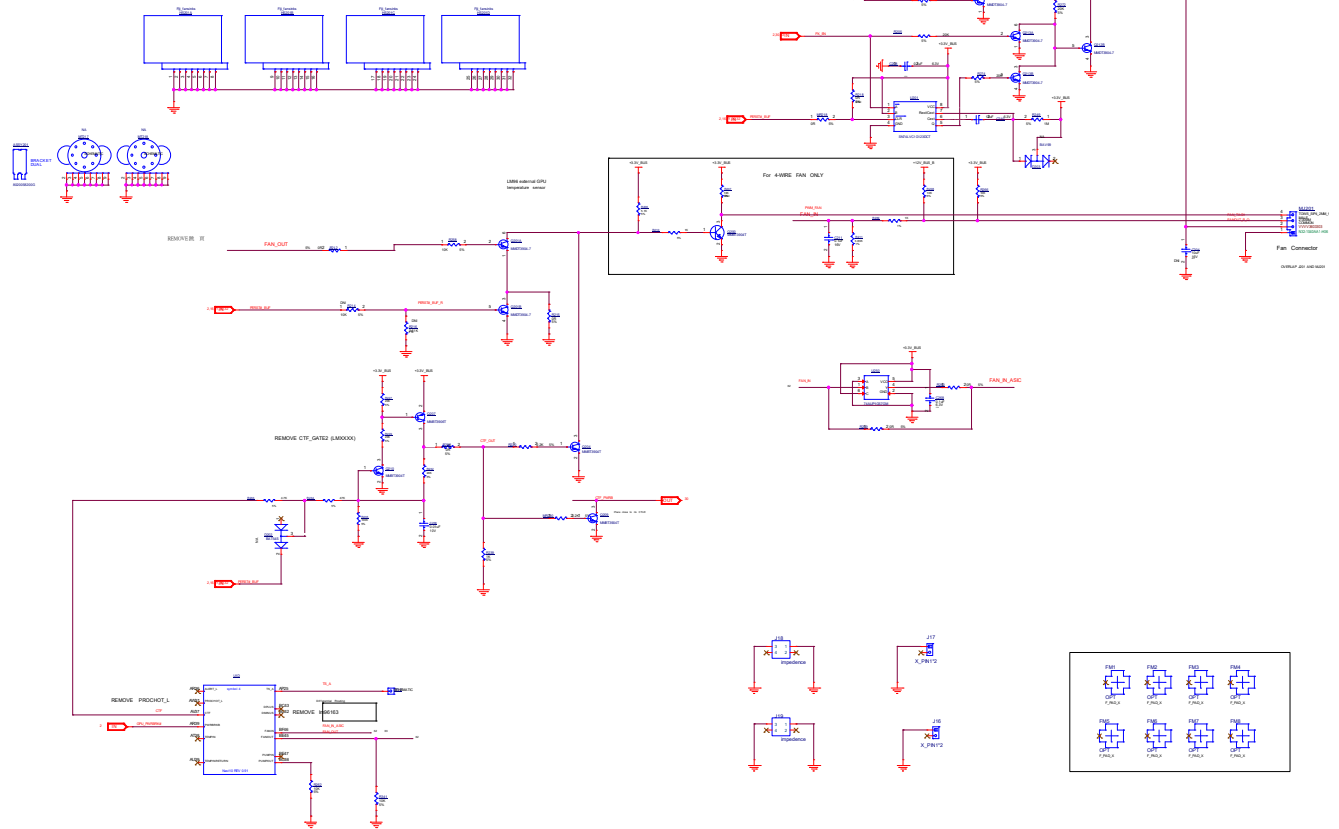


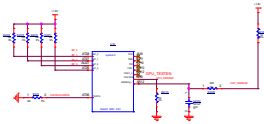






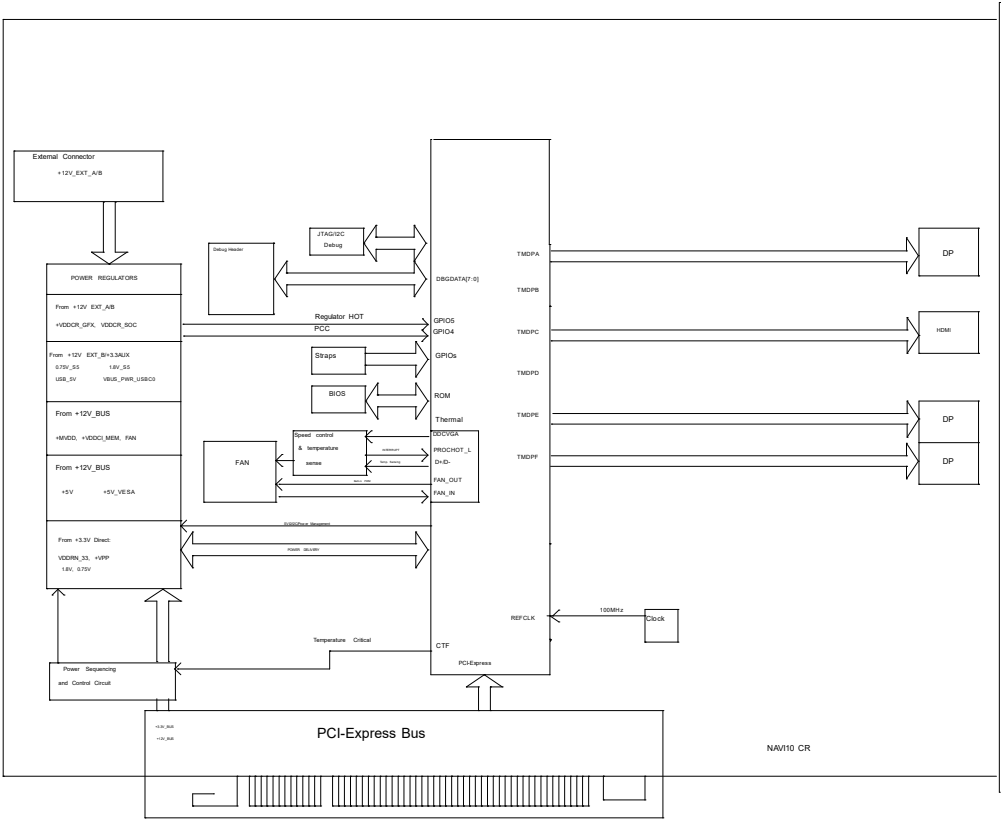






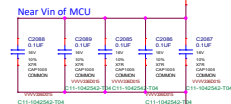
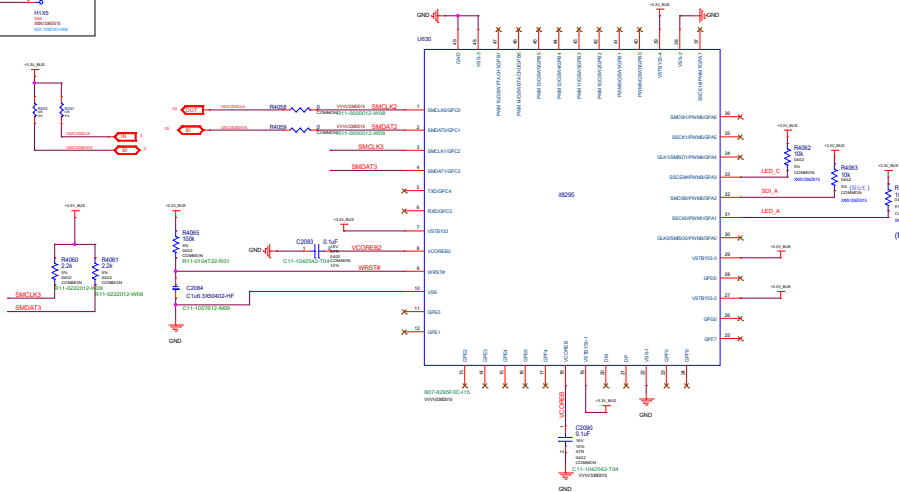
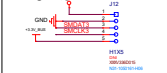
PROCON Lightbar Header



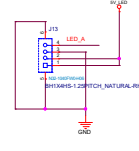


Firmware Programming

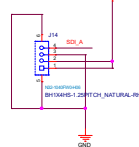
Debug



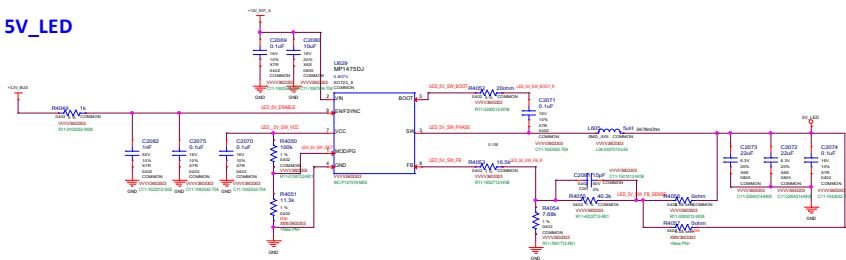
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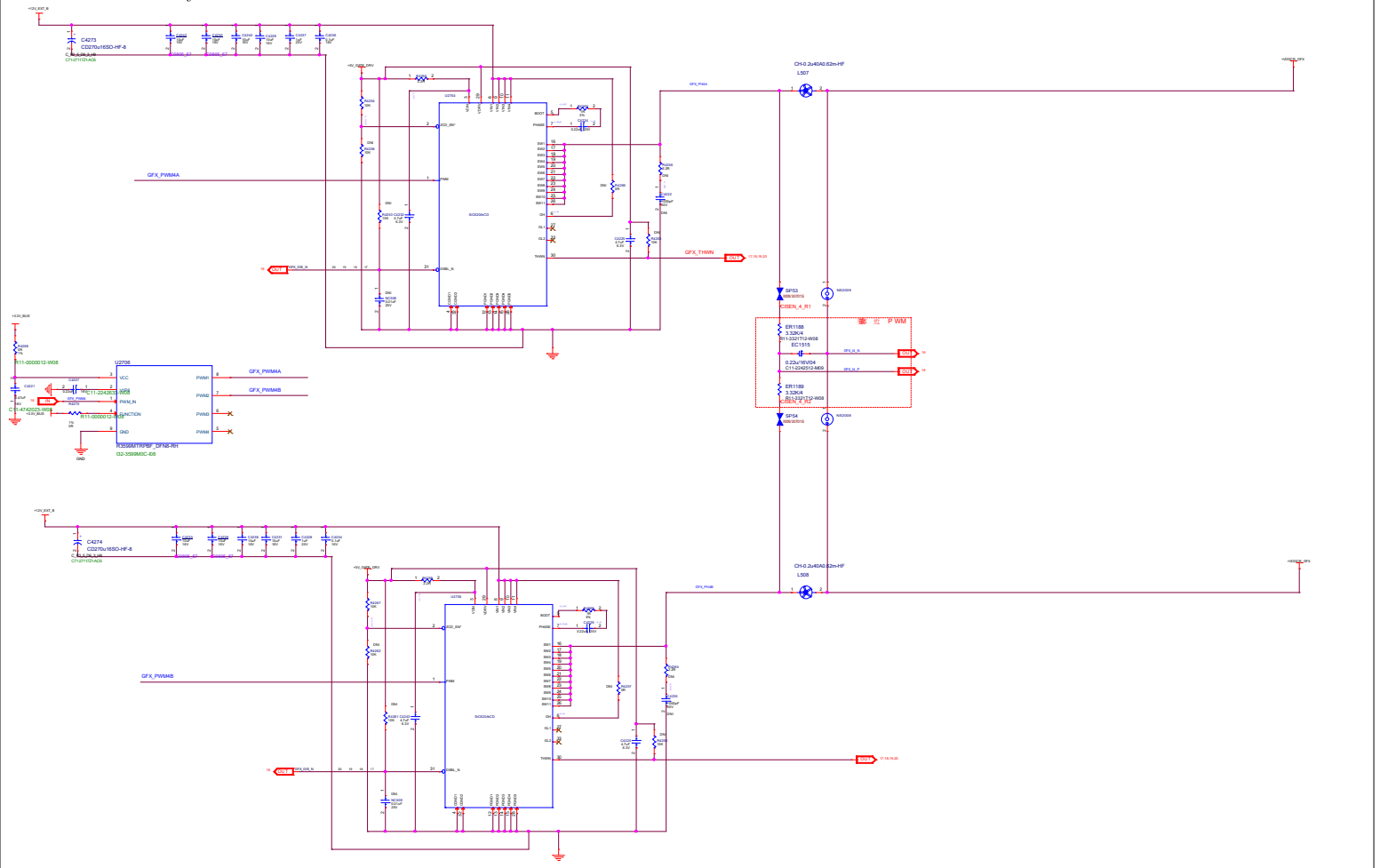


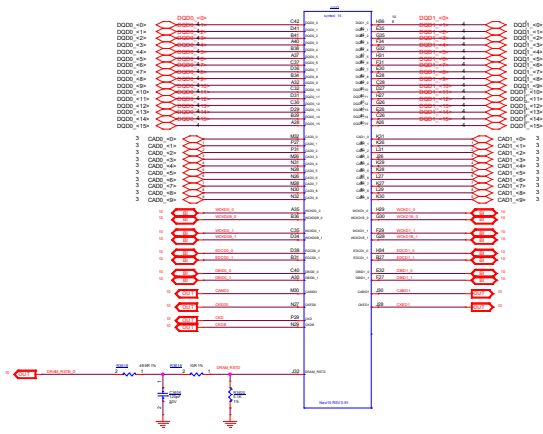
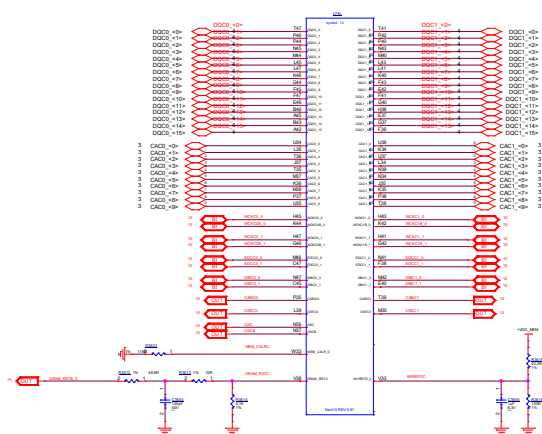
LED_B



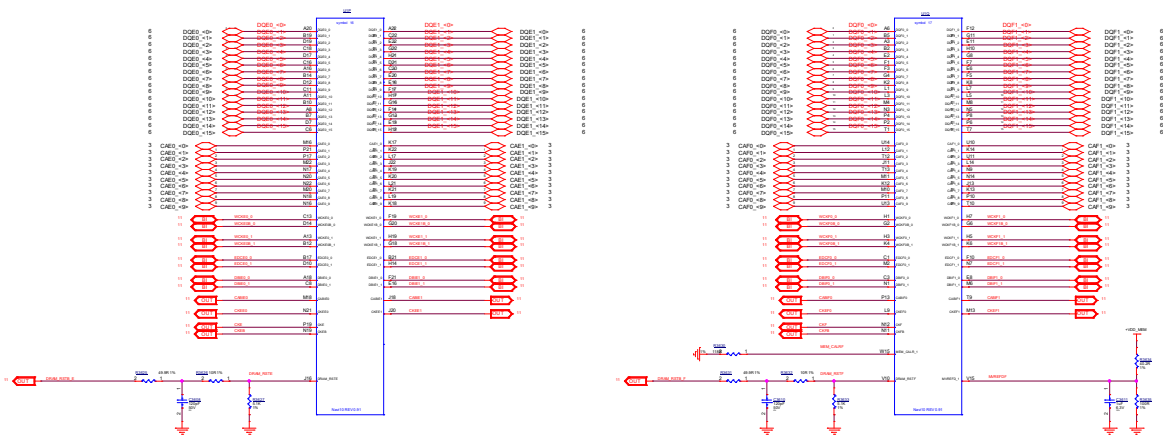
5V_LED



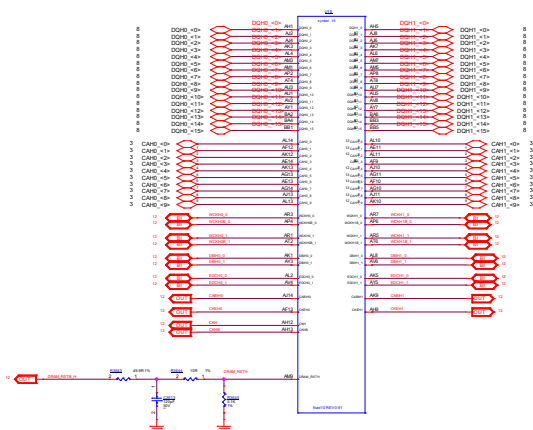
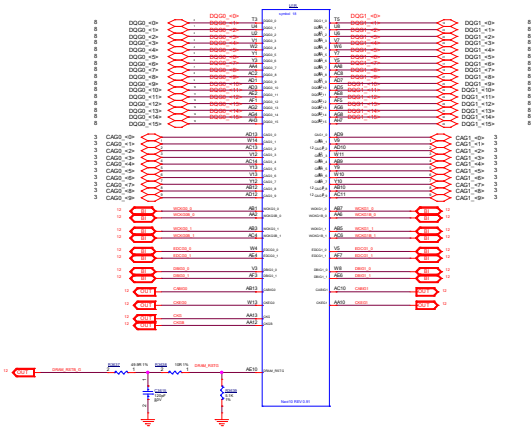




AMD - PLATFORM HARDWARE ENG #BLD. NO.1887, ZHANGJING ROAD SHANGHAI, CHINA 201203			
SHEET: NAVIO MEMO-C00			
DATE: Mon Sep 10 07:27:07 2010		REV: 1.00	
SHEET NUMBER: 0	OF 30		
DOCUMENT NUMBER:	YSL_0189A_BJA		
NOTES: NOTE			



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SHEET: NAVI10 MEM CH E/F					
DATE: Mar-Apr 18 10:57:57 2019		REV: 1.0			
SHEET NUMBER: 7		OF: 35		TITLE: NAVI10 G6X10 Mode DP DP HDMI DP	
DOCUMENT NUMBER: 101_010000_000					
NOTES: NOTE					



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SHEET: NAVI10 MEM G/H				
DATE: Mon Apr 15 10:37:08 2019		REV: 1.0		
SHEET NUMBER: 8 OF 35				
DOCUMENT NUMBER: 105_0184a_00A				
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