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REV: 2.0

TITLE: Oland G5 2GB VGA/sDVI-I+HDMI/DP

DOCUMENT NUMBER: 105-C869:xx-00B

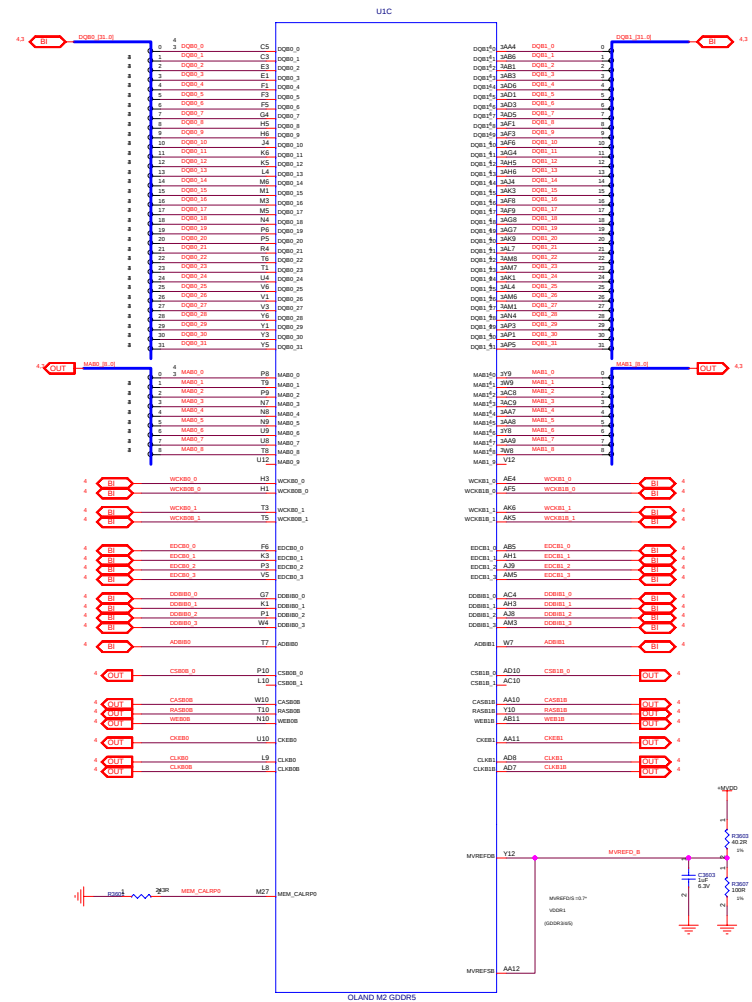
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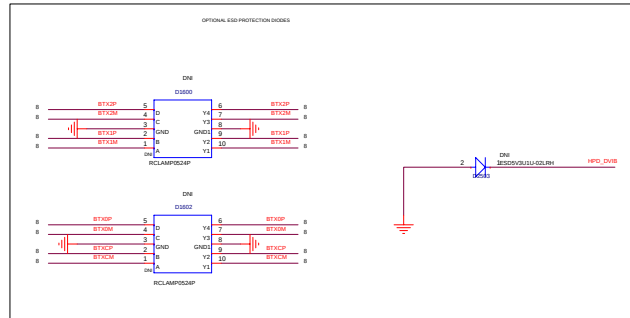
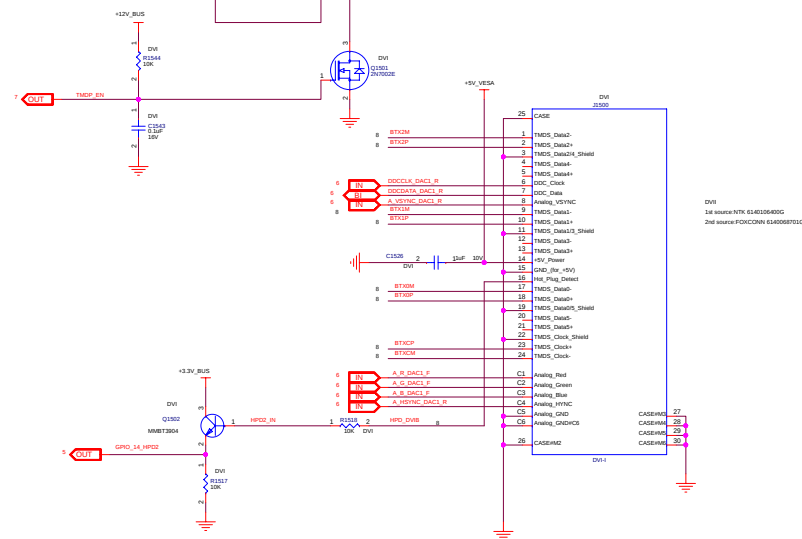
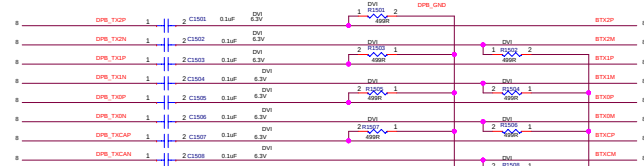
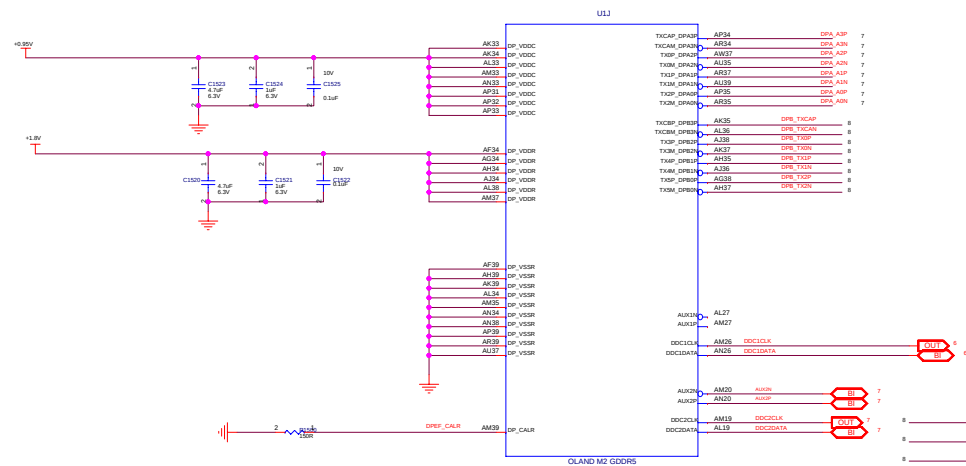
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TITLE: Oland G5 2GB VGA/sDVI-I+HDMI/DP



SHEET: Oland TMDP sDVI

DATE: Mon Sep 21 10:05:46 2015

SHEET NUMBER: 8 OF 20

DOCUMENT NUMBER: 105-C869xx-00B

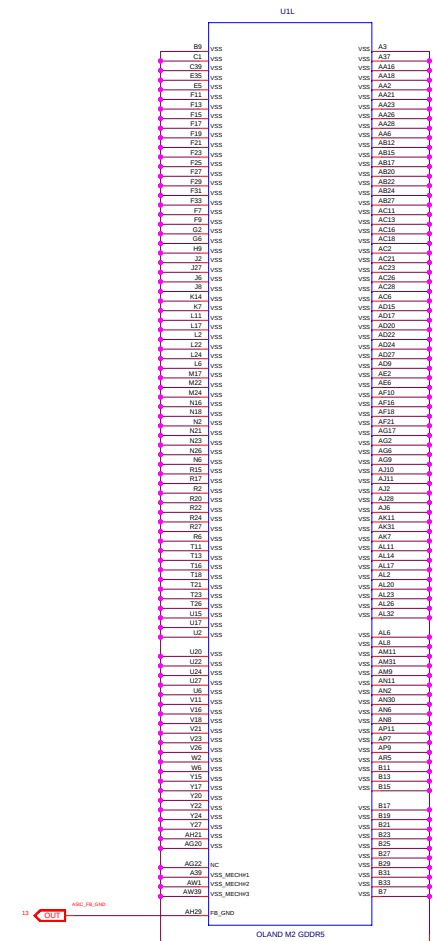
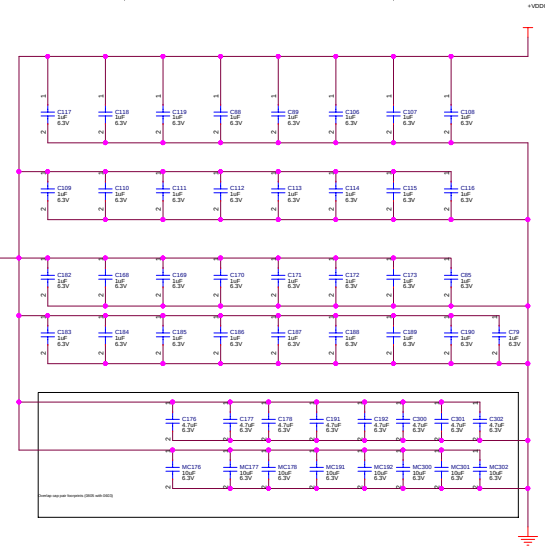
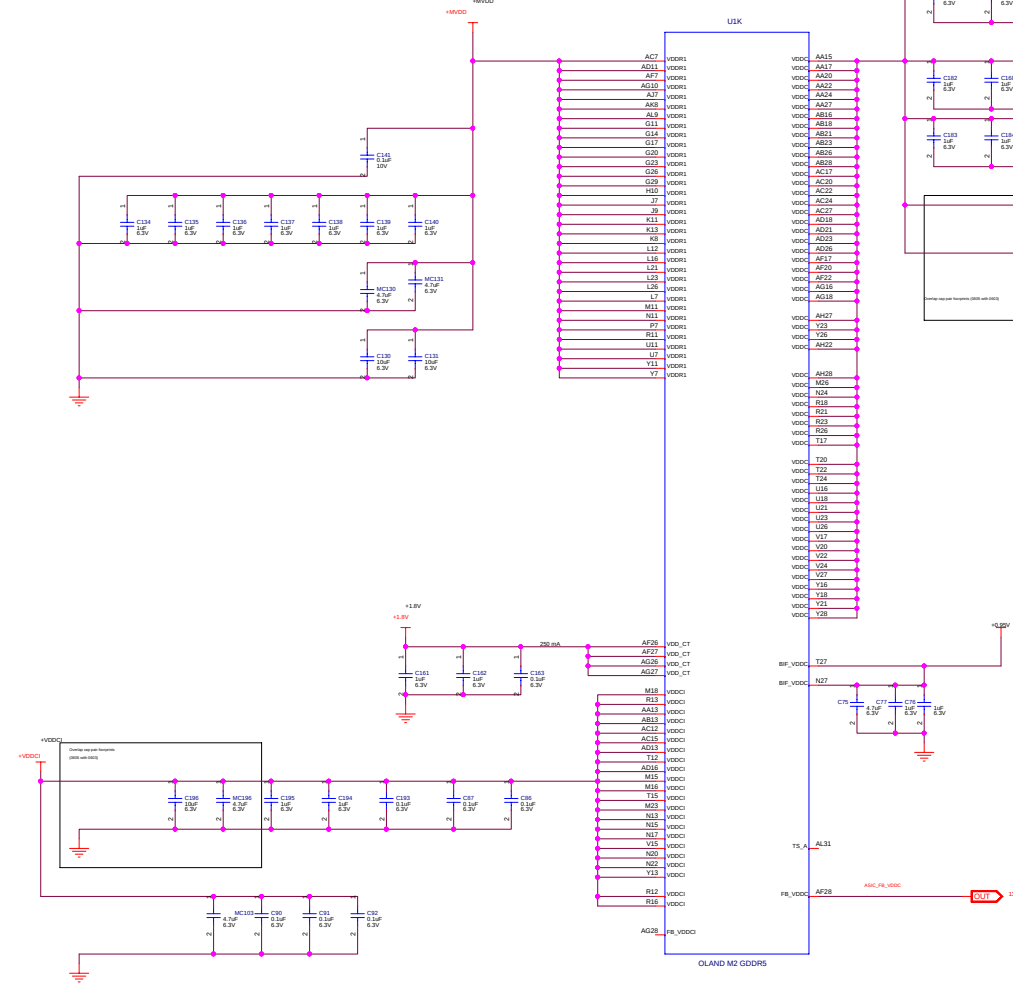
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TITLE: Oland G5 2GB VGA/sDVI-I+HDMI/DP

OLAND Power & GND



AMD - PLATFORM HARDWARE ENG
#48, No. 1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203



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SHEET: Oland Power&GND

DATE: Mon Sep 21 10:05:46 2015

REV: 2.0

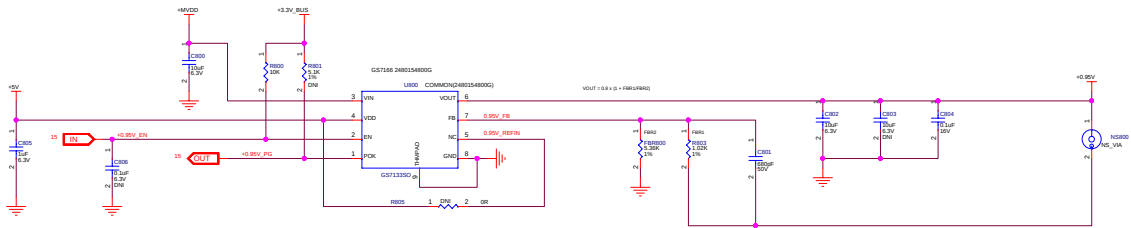
SHEET NUMBER: 9 OF 20

TITLE: Oland G5 2GB VGA/sDVI-I+HDMI/DP

DOCUMENT NUMBER: 105-C869:xx-00B

Linear Regulators

LDO #1: $V_{in} = +1.5V \pm 2\%$ $V_{out} = +0.95V \pm 2\%$ $I_{out} = 1.44A$ (TYP) RMS MAX
PCB: 50 to 70mm sq. copper area for cooling



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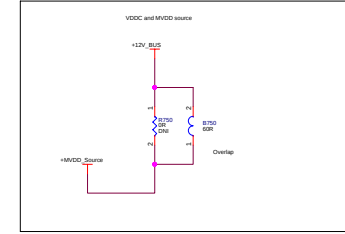
SHEET: 0.95V
DATE: Mon Sep 21 10:06:44 2015 REV: 2.0
SHEET NUMBER: 10 OF 20
DOCUMENT NUMBER: 105-C869xx-008

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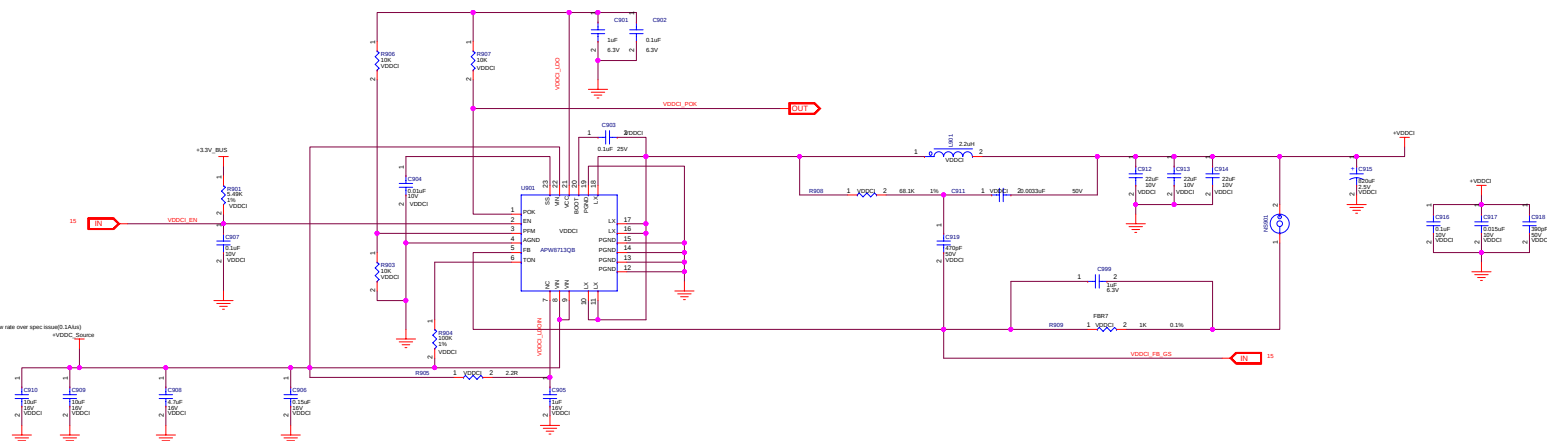
TITLE: Oland G5 2GB VGA/sDV/H+HDMI/DP

Regulator for MVDD

Use +VDDC_Source to share 1.65V to eliminate PCBs 12V current draw rate over spec (max0.345A)
+MVDDC_Source



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SHEET: MVDD				TITLE: Oland G5 2GB VGA/sDVI+HDMI/DP	
DATE: Tue Sep 22 06:32:59 2015		REV: 2.0			
SHEET NUMBER: 11 OF 20					
DOCUMENT NUMBER: 105-C865xx-008					



Tue Sep 22 06:32:59 2015

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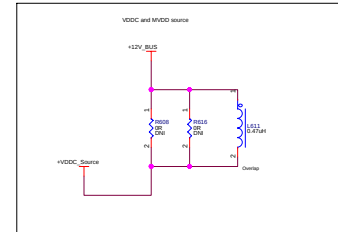
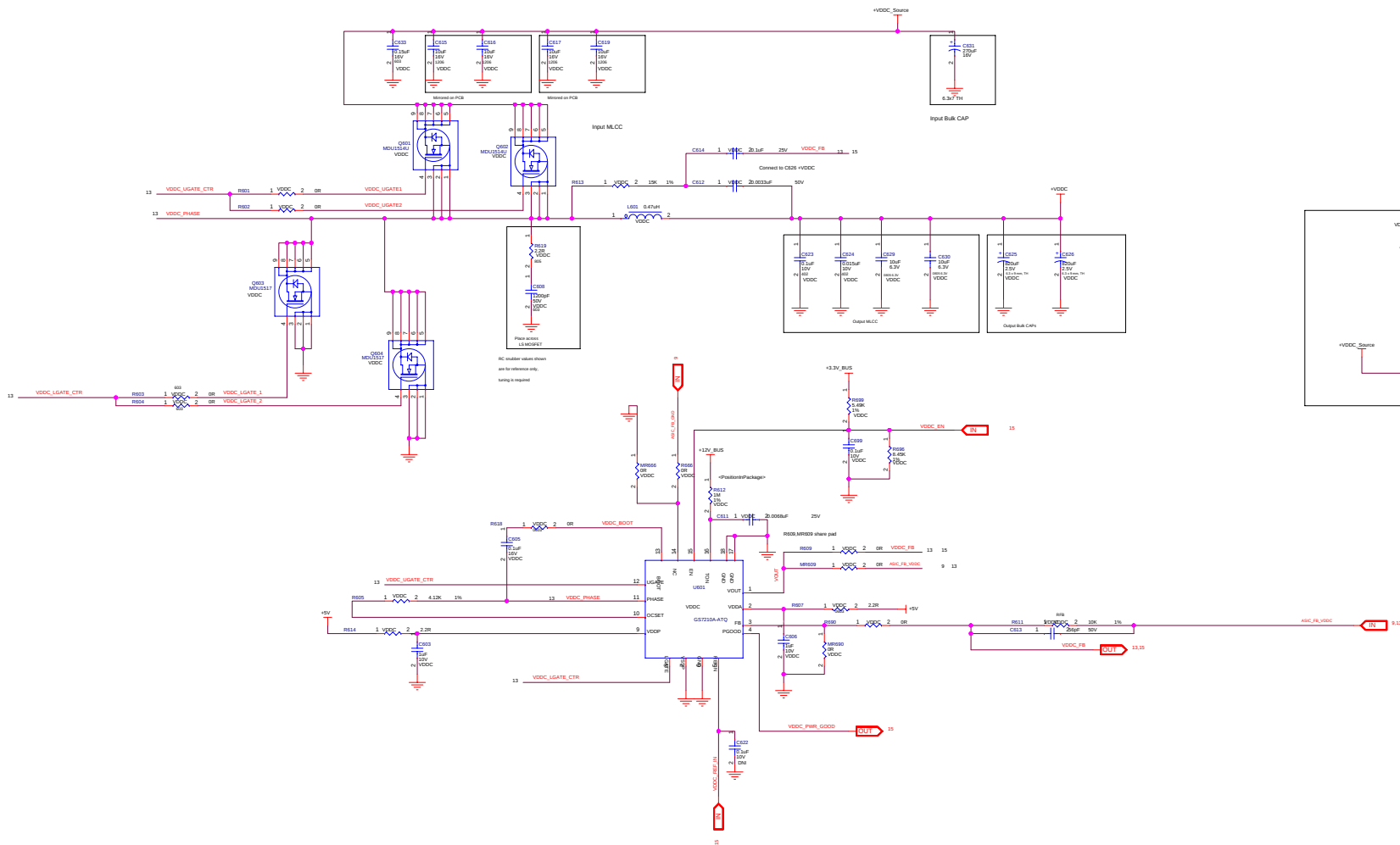
105-C869xx-00B

NOTE

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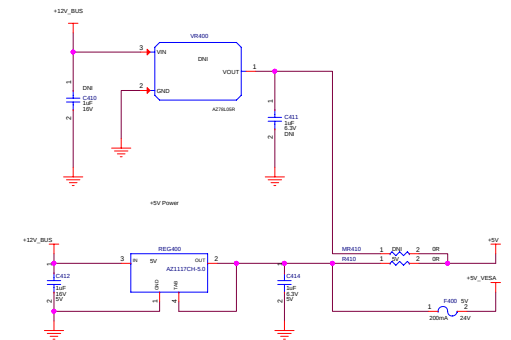
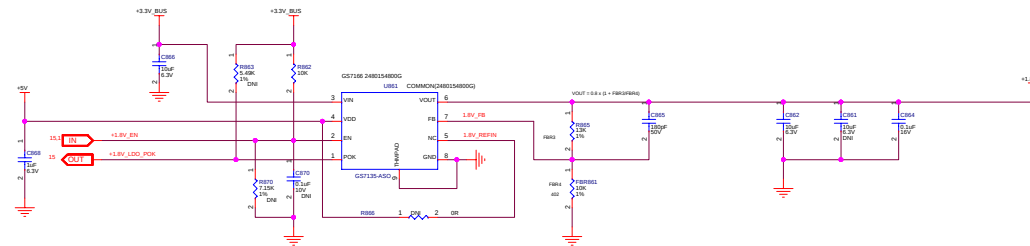
Oland G5 2GB VGA/sDVI-I+HDMI/DP



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SHEET: VDDC								
DATE: Mon Sep 21 10:06:41 2015			REV: 2.0					
SHEET NUMBER: 13			OF 20			TITLE: Oland G5 2GB VGA/sDVI+HDMI/DP		
DOCUMENT NUMBER: 105-C869xx-008								

Linear Regulators

LDO #2: Vin = 3.00V to 3.60V (3.3V +/- 9%) Vout = +1.8V +/- 2%; Iout = 1.6A (TbV) RMS MAX
PCB: 50 to 70mm sq. copper area for cooling



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SHEET: Linear Regulators

DATE: Mon Sep 21 10:05:44 2015

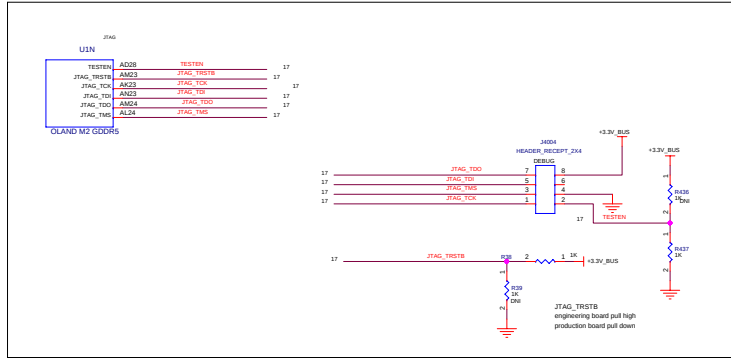
REV: 2.0

SHEET NUMBER: 14 14 OF 20 20

DOCUMENT NUMBER: 105-C869:xx-00B

TITLE: Oland G5 2GB VGA/sDVI-I+HDMI/DP

(19) Debug Circuits



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SHEET: Debug Circuit		
DATE: Mon Sep 21 10:05:47 2015		REV: 2.0
SHEET NUMBER: 17 OF 20		
DOCUMENT NUMBER: 105-C869xx-00B		

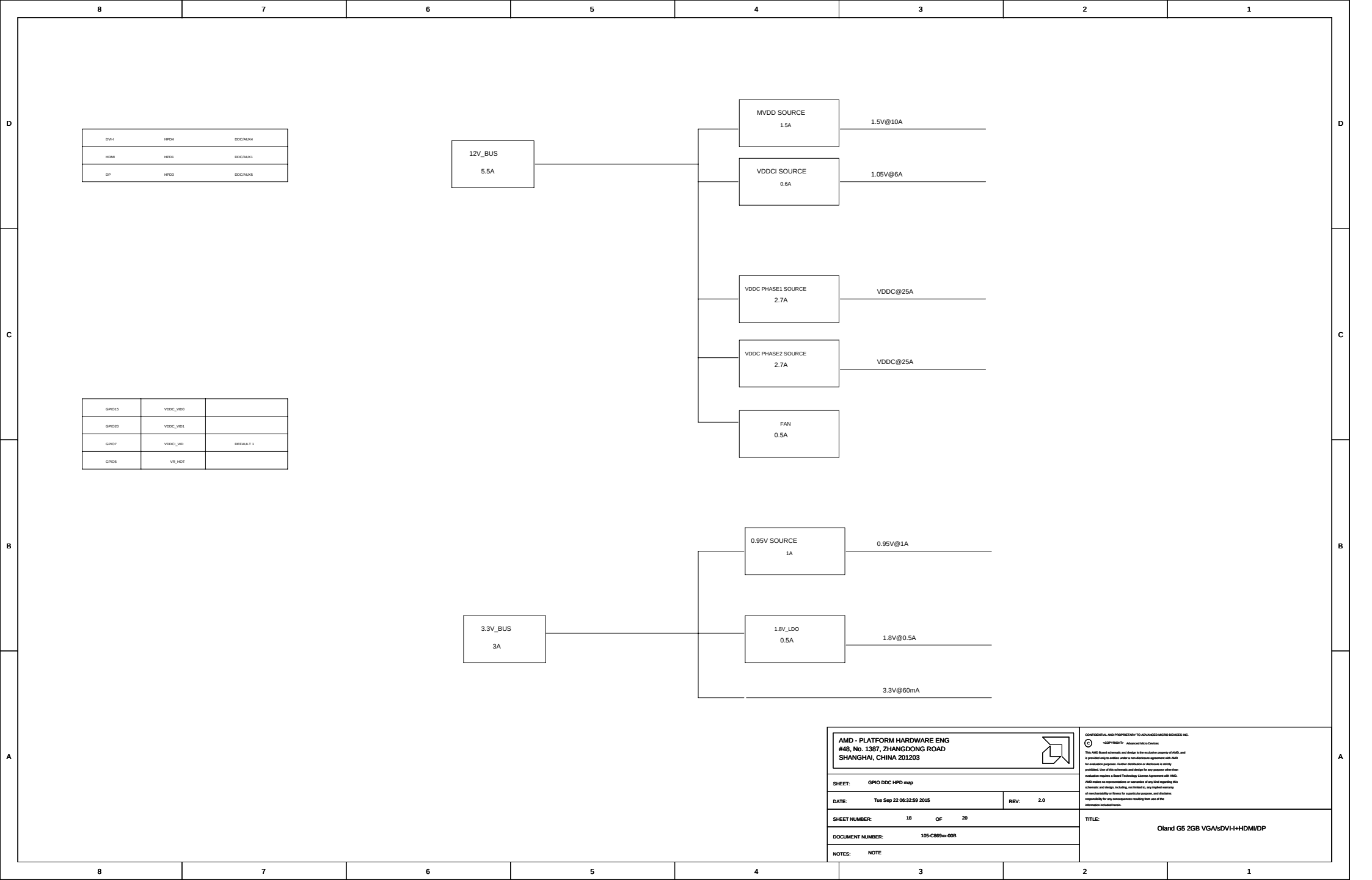
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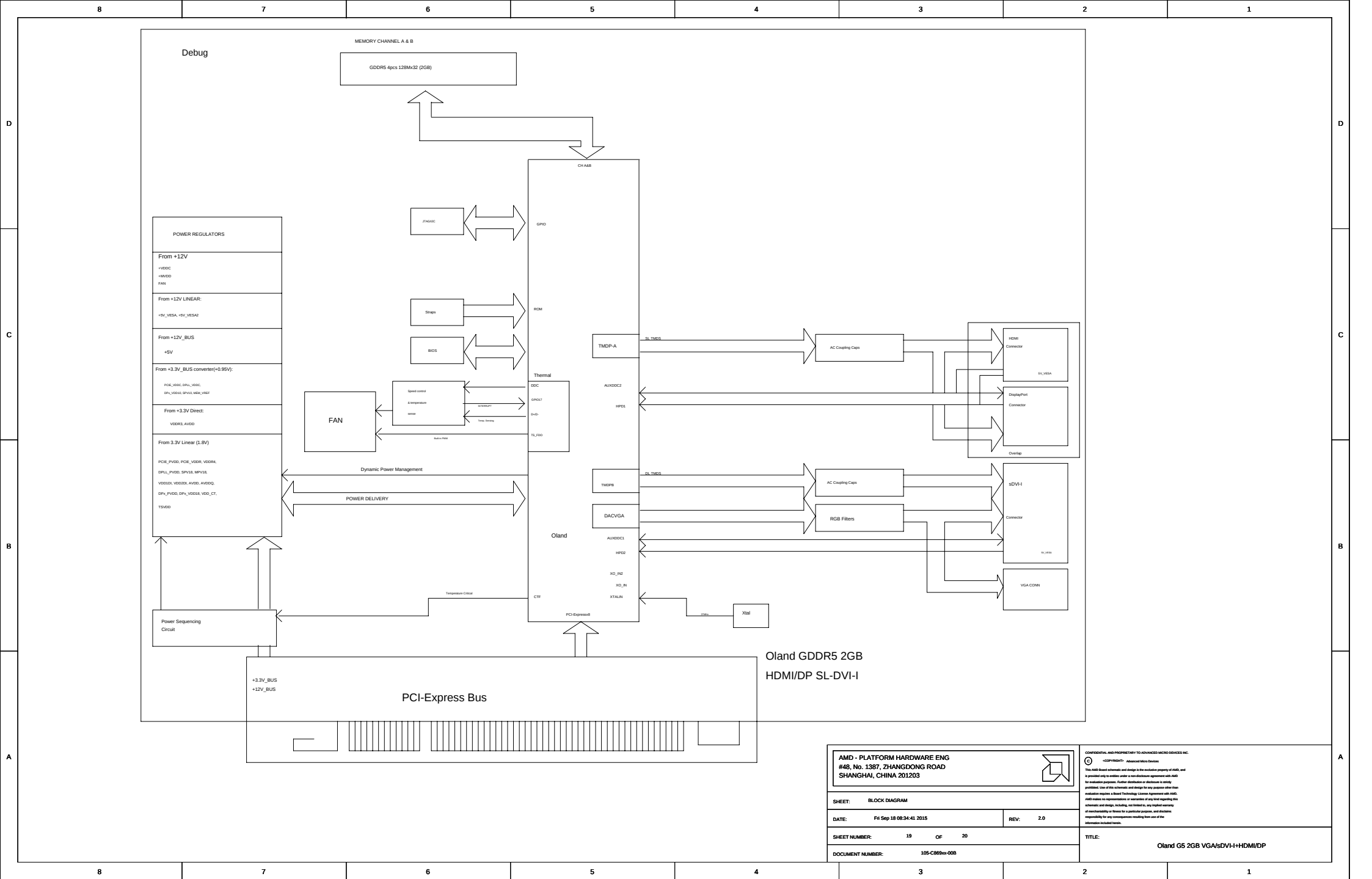
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TITLE:

Olant GS 2GB VGA/SDV/H+HDMI/DP





8		7		6		5		4		3		2		1		
AMD 		TITLE: Oland G5 2GB VGA/SDVI-I+HDMI/DP				DOCUMENT NUMBER: 105-C809v0-006			DATE: Tue Sep 22 02:20:17 2015			SHEET NUMBER: 20 OF 20			REV: 2.0	
REVISION HISTORY				ENGINEER: Tony Yang		NOTES: NOTE		CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRO DEVICES INC. This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for evaluation purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representation or warranty of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of this information included herein. C -COPYRIGHT Advanced Micro Devices				AMD - PLATFORM HARDWARE ENG #48, No. 1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203				
SCH Rev		PCB Rev		Date		REVISION DESCRIPTION										
1.00		000		04/23/14		1 Initial release										
2.00		000		09/22/15		2 Add VGA, HD										

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