

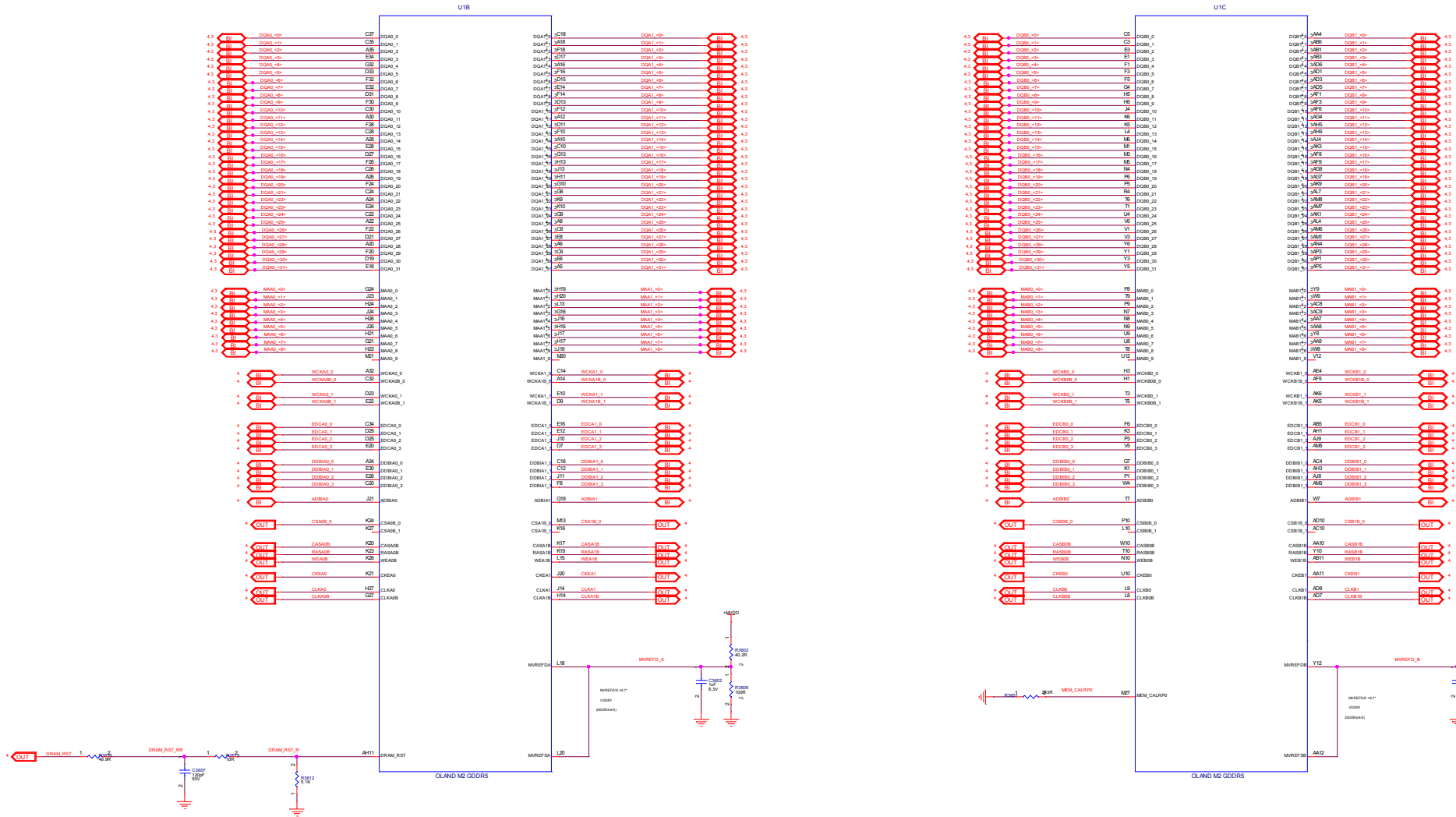
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AMD - PLATFORM HARDWARE ENG
#49, No. 1367, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

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SHEET:	Oland MEM HW ENG	
DATE:	Mon Sep 21 10:06:45 2016	REV: 2.0
SHEET NUMBER:	3	OF 20
DOCUMENT NUMBER:	105-C869rev-008	

TITLE:

Oland G5 2GB VGA&DVI-HDMI&DP

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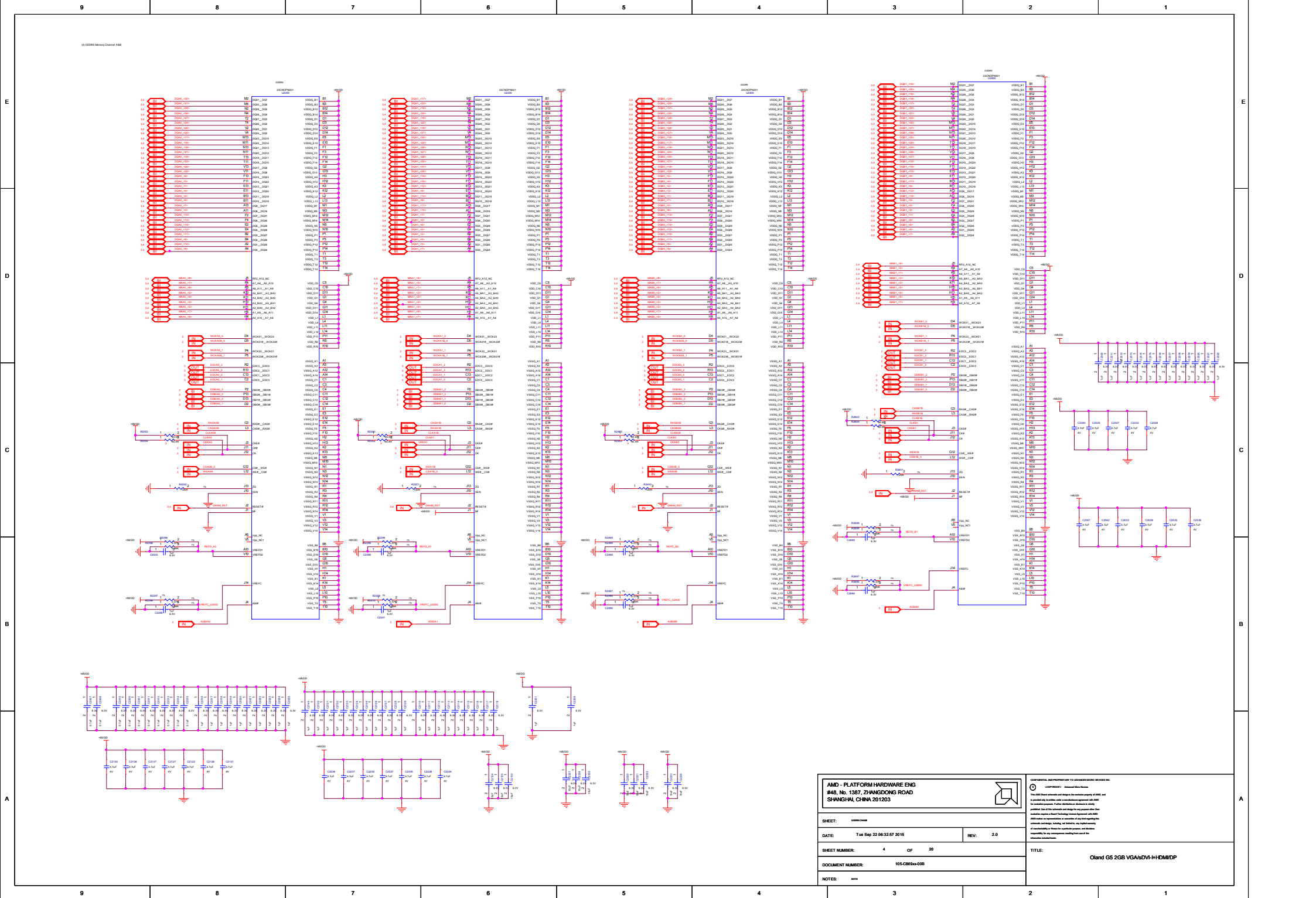
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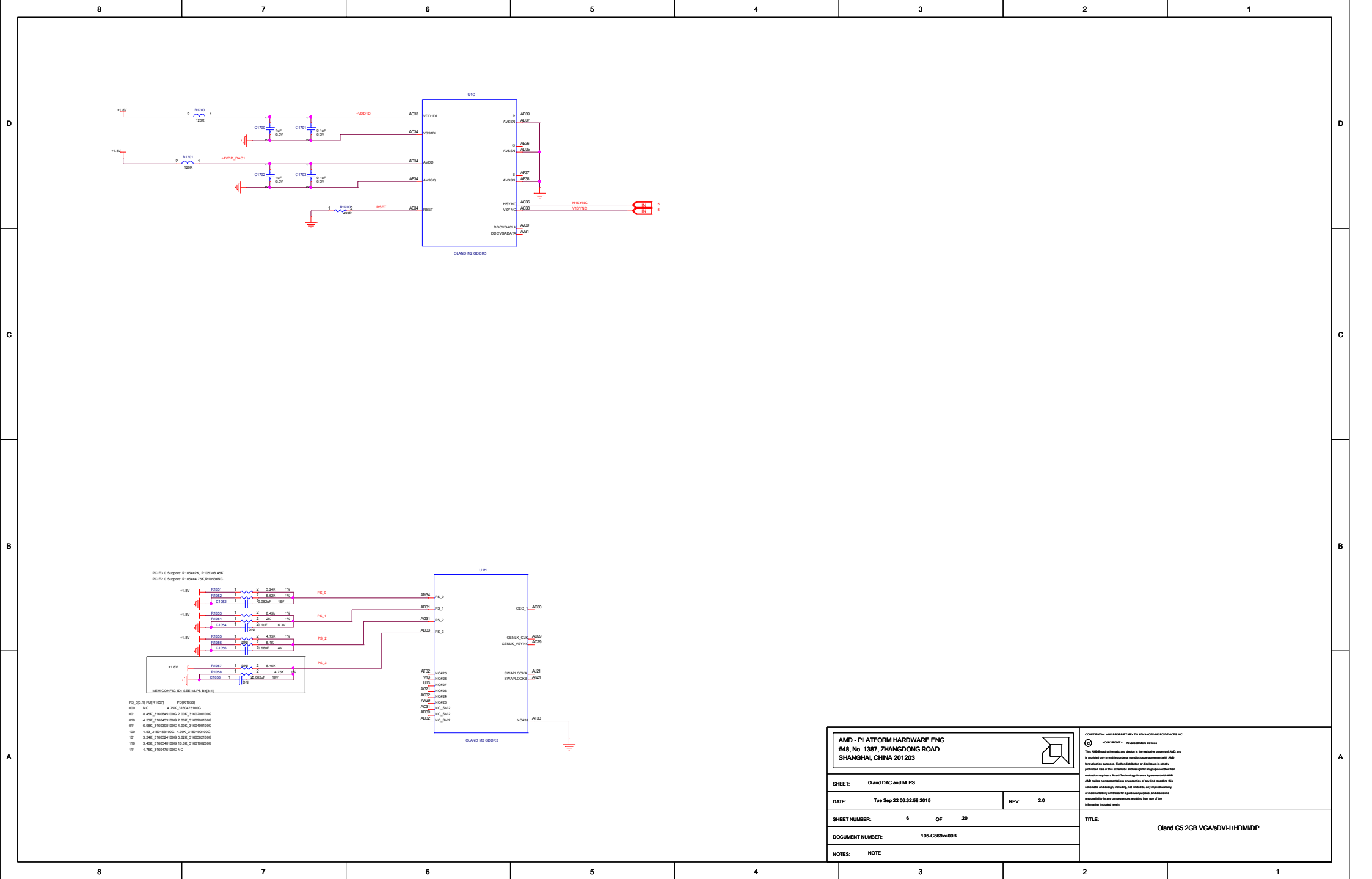
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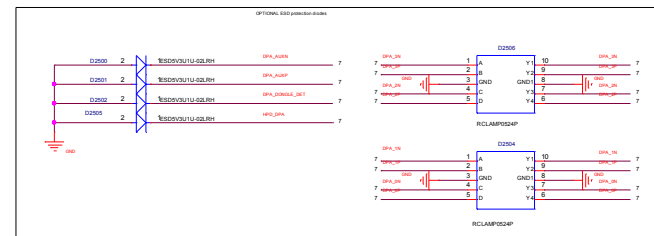
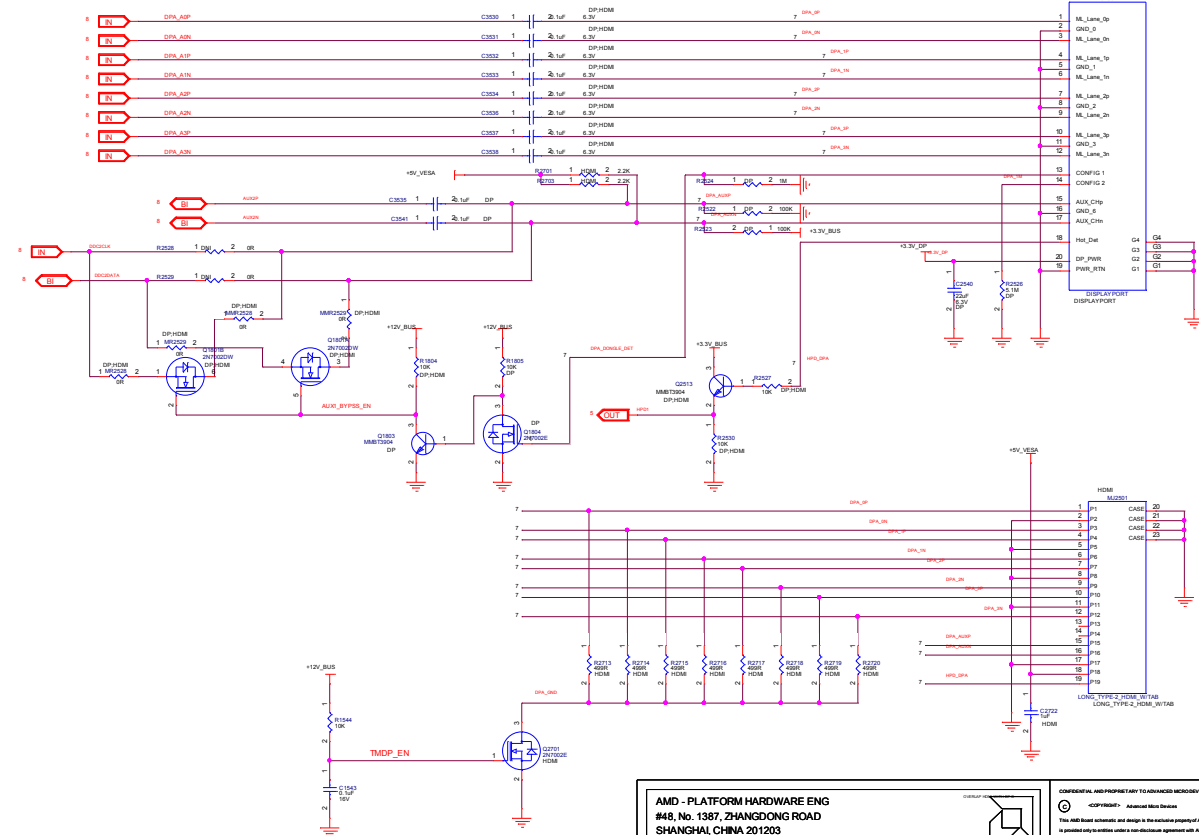
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#48, No. 1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

SHEET: Oland Connector HDMI/DP

DATE: Mon Sep 21 10:05:38 2015

SHEET NUMBER: 8 OF 20

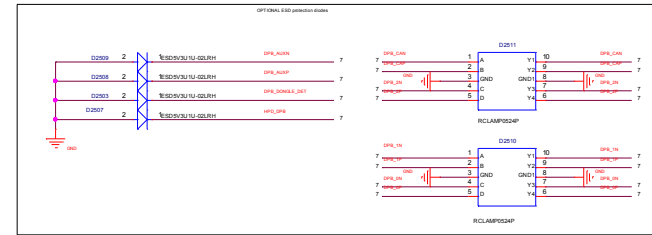
DOCUMENT NUMBER: 105-C869xx-00B

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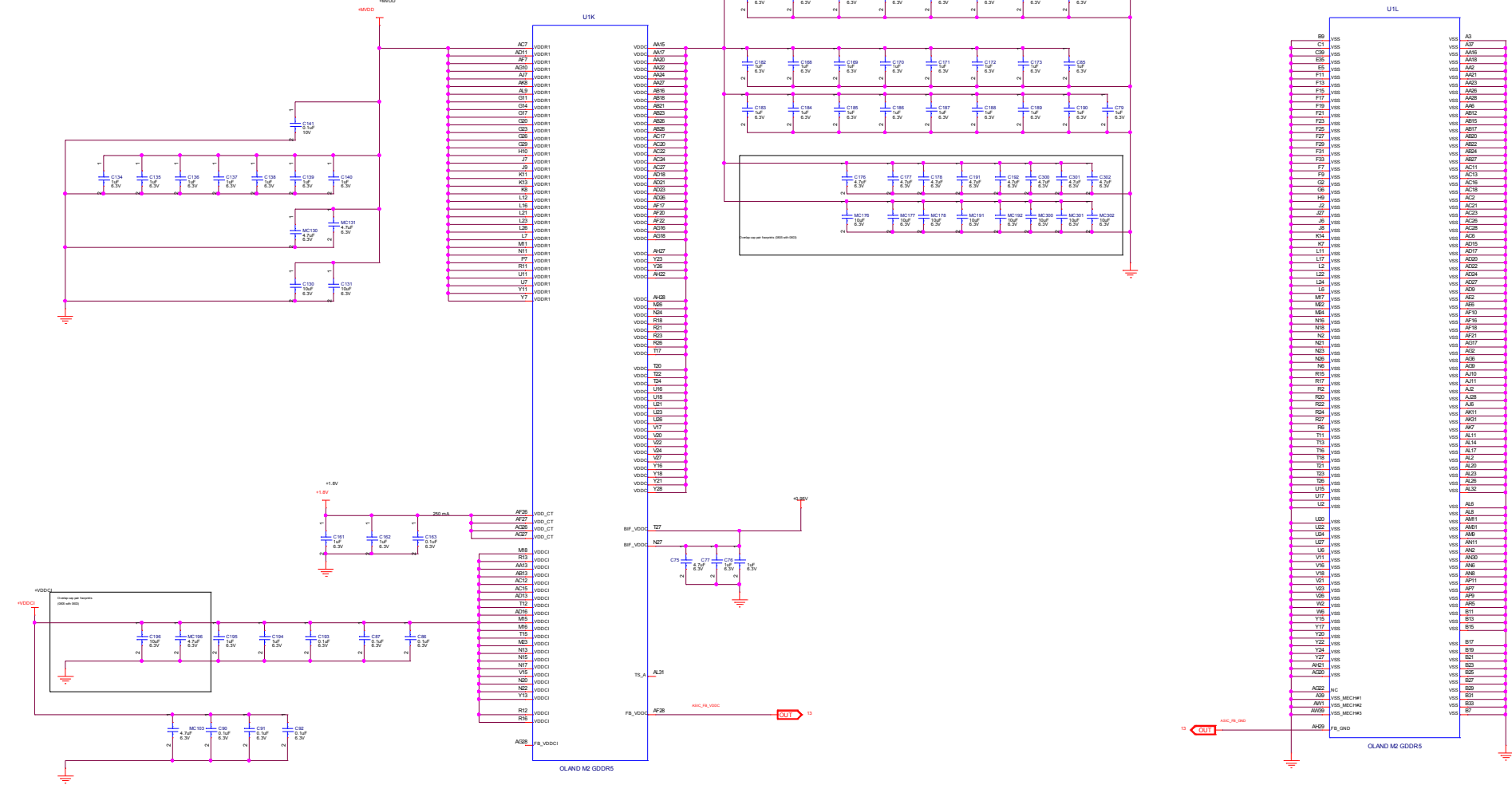
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TITLE: Oland G5 2GB VGA/sDVI+HDMI/DP



OLAND Power & GND



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SHEET: 01and Power&GND

DATE: Mon Sep 21 10:05:46 2015

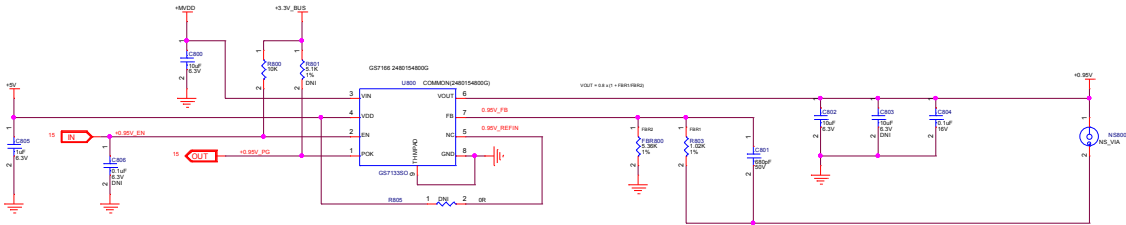
REV: 2.0

SHEET NUMBER: 9 OF 20

TITLE: Oland G5 2GB VGA/sDVI+HDMI/DP

Linear Regulators

LDO #1: $V_{in} = +1.5V \pm 2\%$ $V_{out} = +0.95V \pm 2\%$ $I_{out} = 1.44A (T_{BV})_{RMS\ MAX}$
PCB: 50 to 70mm sq. copper area for cooling



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SHEET: 0.95V

DATE: Mon Sep 21 10:06:44 2015

REV: 2.0

SHEET NUMBER: 10 OF 20

DOCUMENT NUMBER: 105-C869a-008

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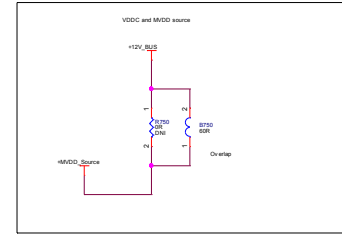
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TITLE: Oland G5 2GB VGA+DVI+HDMI+DP

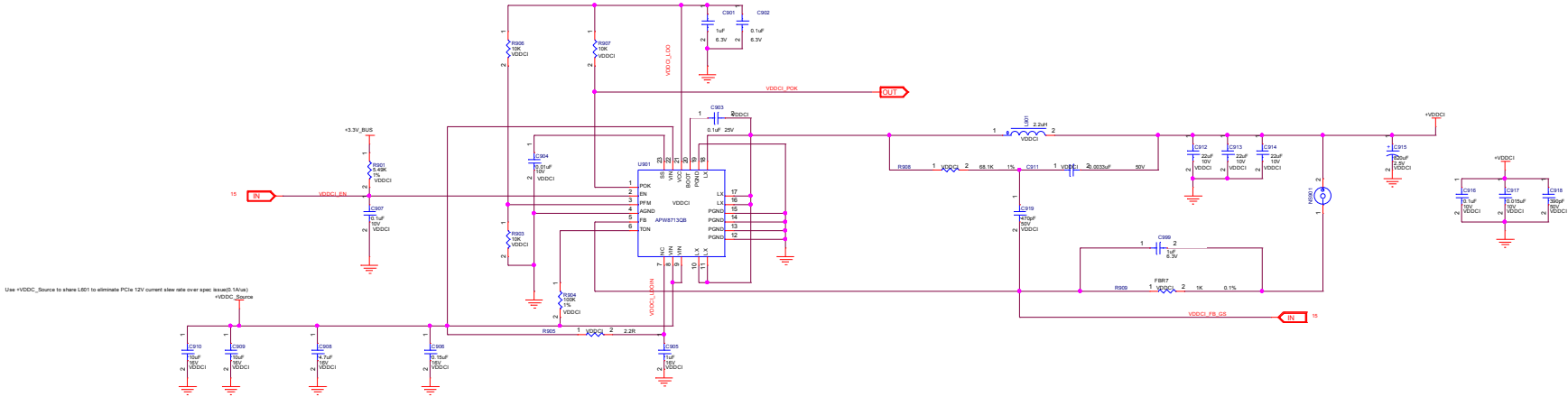
Regulator for MVD0

Use +VDDC_Source to share L601 to eliminate PCB's 12V current slew rate over spec (max) 1A/us
+MVD0_Source



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SHEET: MVD0			
DATE: Tue Sep 22 06:32:59 2015	REV: 2.0	TITLE: Oland G5 2GB VGA+DVI+HDMI+DP	
SHEET NUMBER: 11 OF 20			
DOCUMENT NUMBER: 105-C869xx-008			

Regulator for MIDD

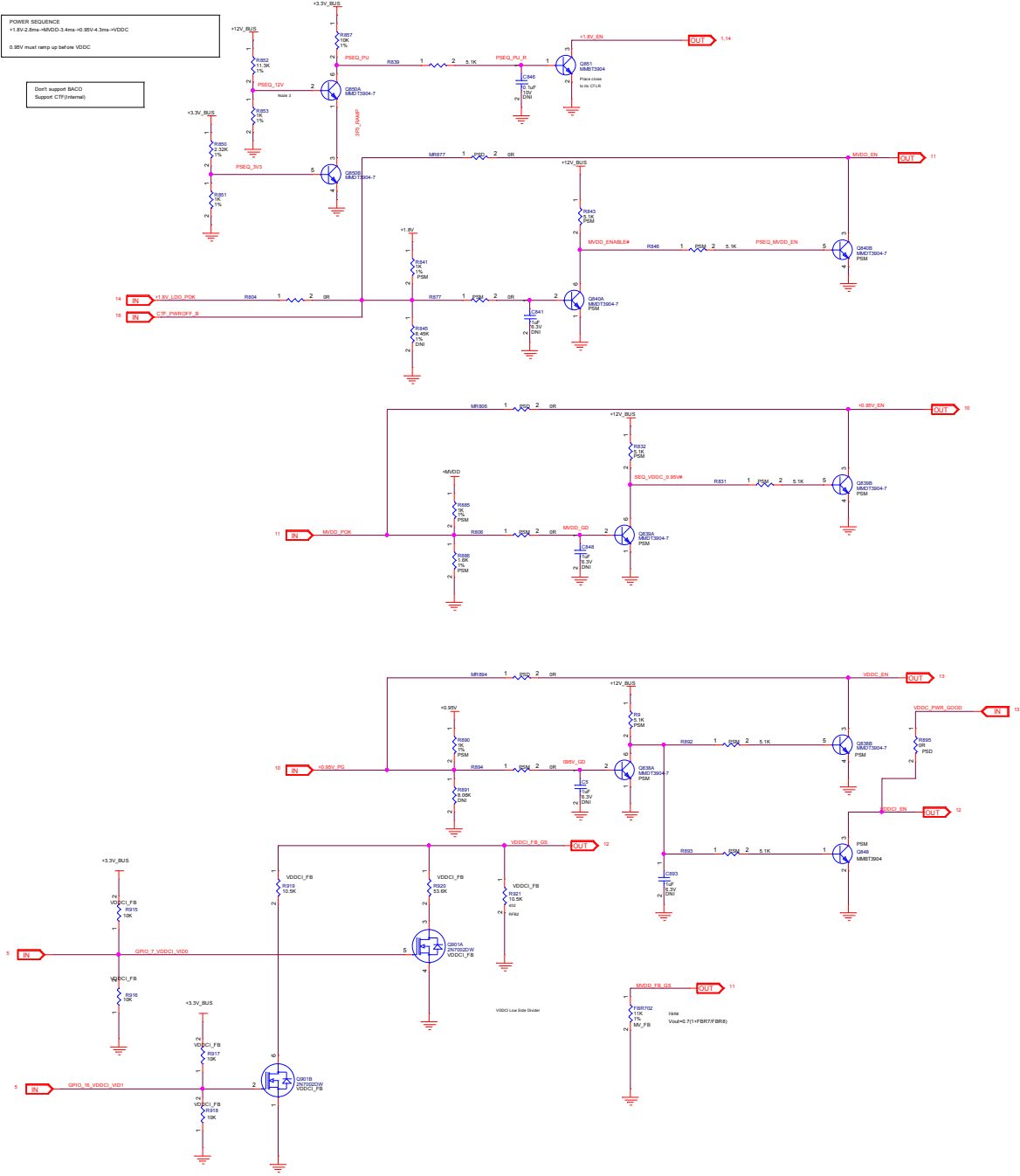


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SHEET: VDDCI		DATE: Tue Sep 22 06:32:59 2015	REV: 2.0	TITLE: Oland G5 2GB VGA/sDVI-HDMI/DP
SHEET NUMBER: 12 OF 20				
DOCUMENT NUMBER: 105-C8690-008				
NOTES: NOTE				

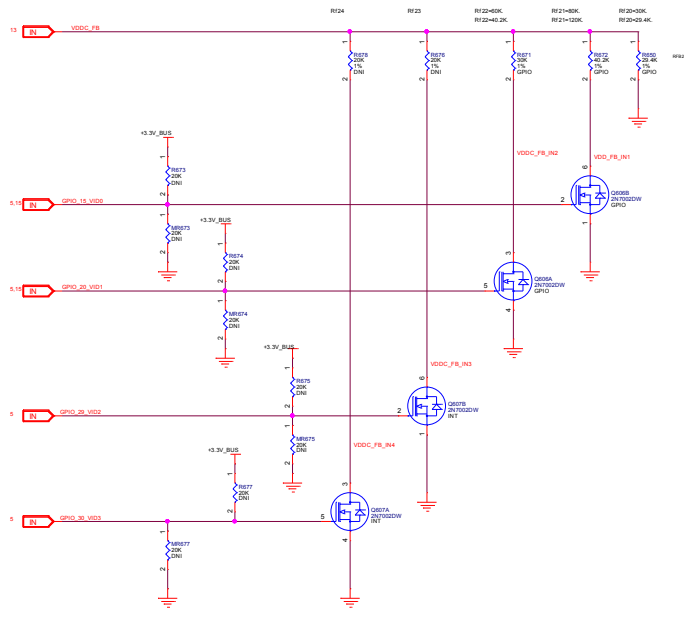
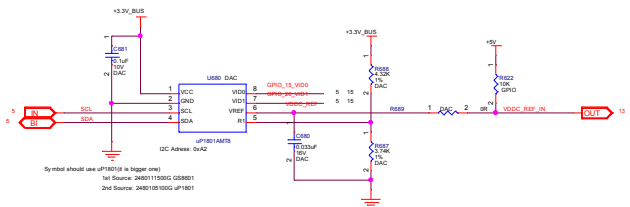
Power Management - Power Gating and Dynamic Voltage Control

POWER SEQUENCE
+1.8V, 2.5ms-HVDDIO, 3.4ms-HVDDIO, 4.3ms-HVDDIO
0.85V must ramp up before VDDIO

Don't support SACD
(Support CTS/Interact)



VDDIO Low Side Divider

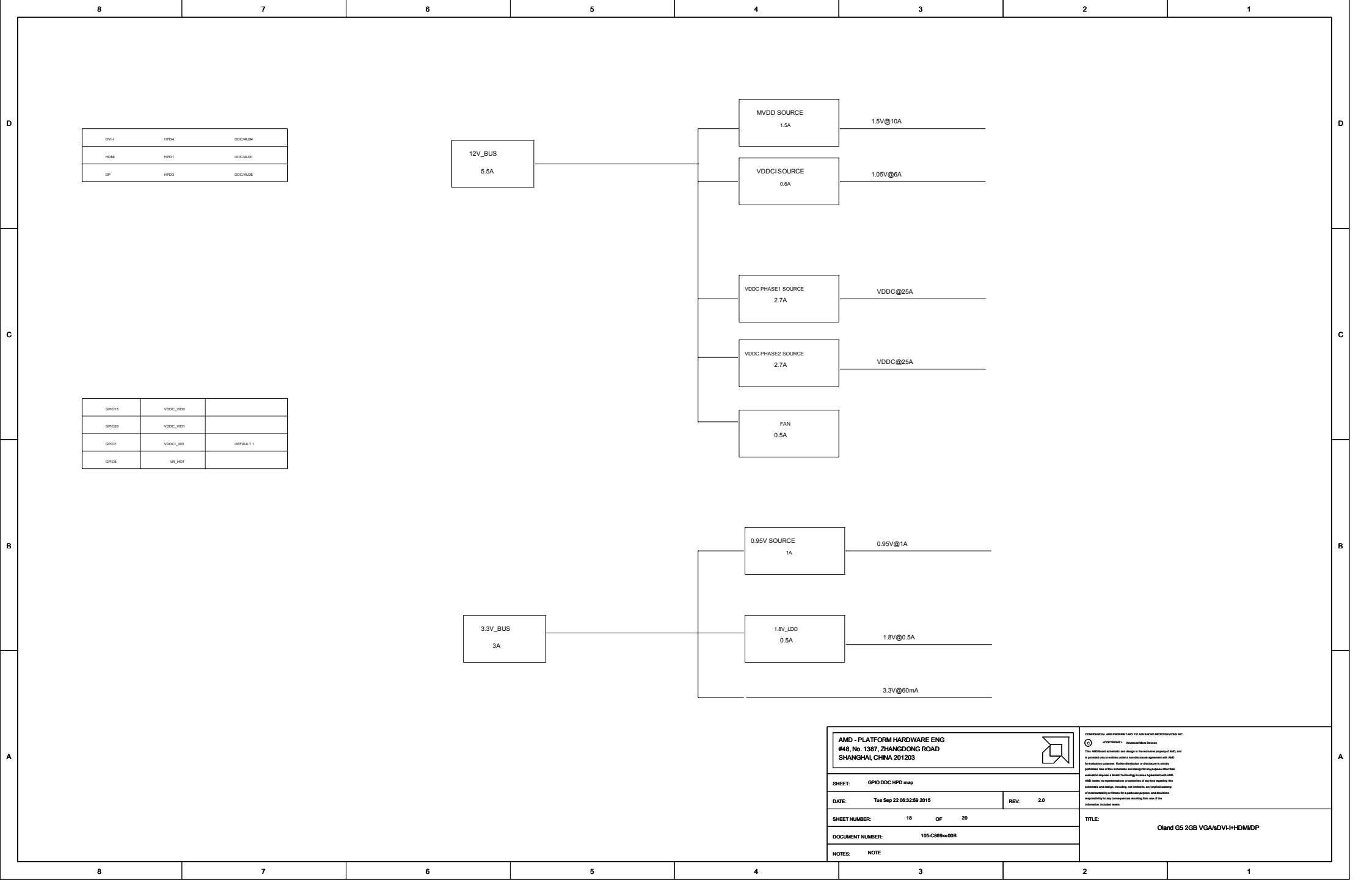


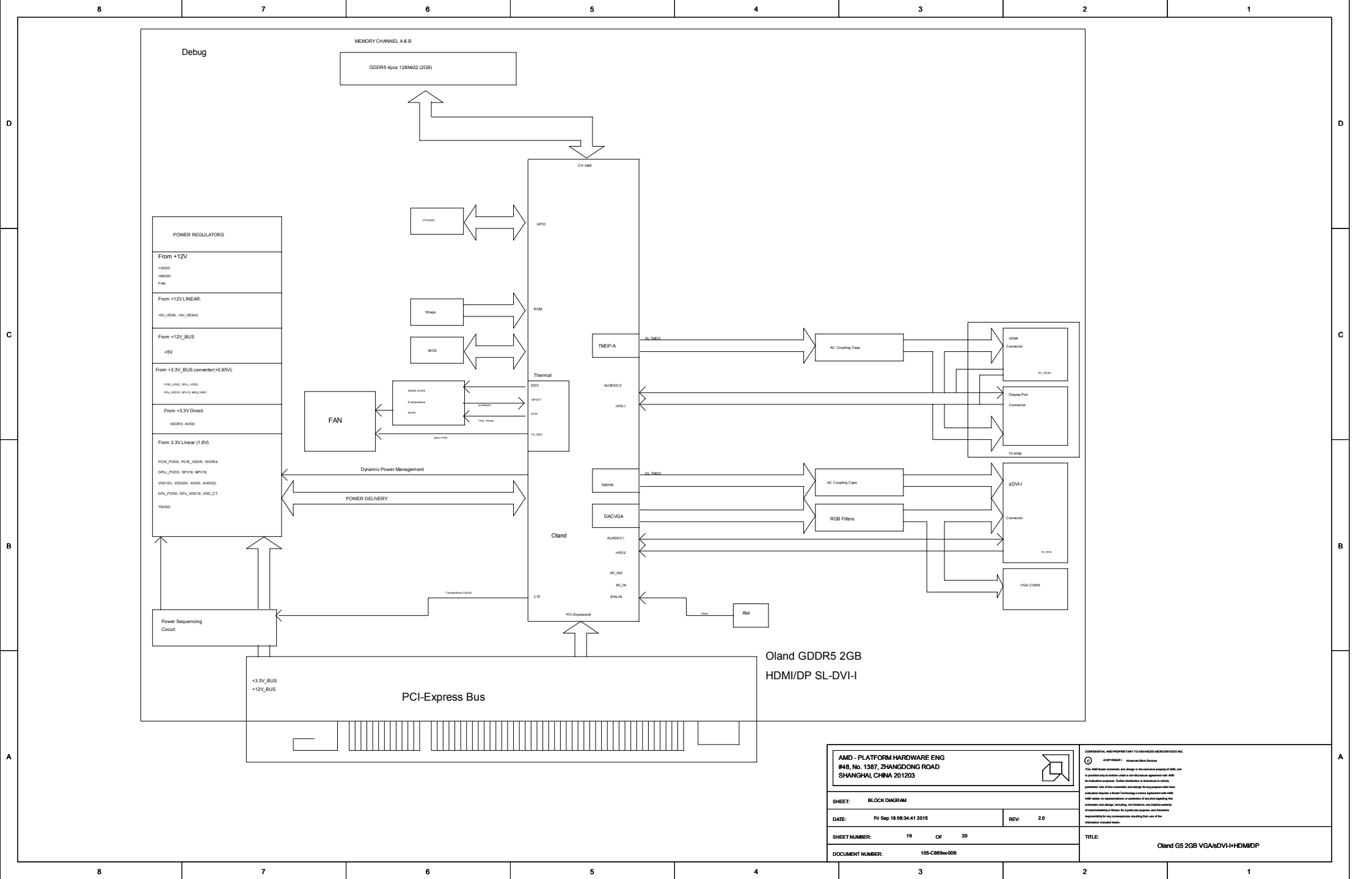
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SHANGHAI, CHINA 201203

SHEET:	PWR MGMT & SEQ	
DATE:	Tue Sep 22 02:06:31 2016	REV: 2.0
SHEET NUMBER:	15	OF 20
DOCUMENT NUMBER:	105-C86900-008	

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TITLE:
Oland G5 2GB VGA/SDVI-HDMI/DP





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AMD 		TITLE: Oland G6 2GB VGA/sDV/H+HDMI/DP		DOCUMENT NUMBER: 105-C889wa-008		DATE: Tue Sep 22 02:20:17 2015		SHEET NUMBER: 20 OF 20		REV: 2.0			
REVISION HISTORY				ENGINEER: Tony Yang		NOTES: NOTE		CONFIDENTIAL AND PROPRIETARY TO ADVANCED MICRODEVICES INC. This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD for customer purposes. Further distribution or disclosure is strictly prohibited. Use of this schematic and design for any purpose other than evaluation requires a Board Technology License Agreement with AMD. AMD makes no representation or warranty of any kind regarding this schematic and design, including, not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of this information included herein.		C ©COPYRIGHT Advanced MicroDevices		AMD - PLATFORM HARDWARE ENG #48, No. 1367, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SCH Rev		PCB Rev		Date		REVISION DESCRIPTION							
1.00		000		08/19/14		1 Initial release							
2.00		000		09/22/15		2 Add VGA, etc							

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