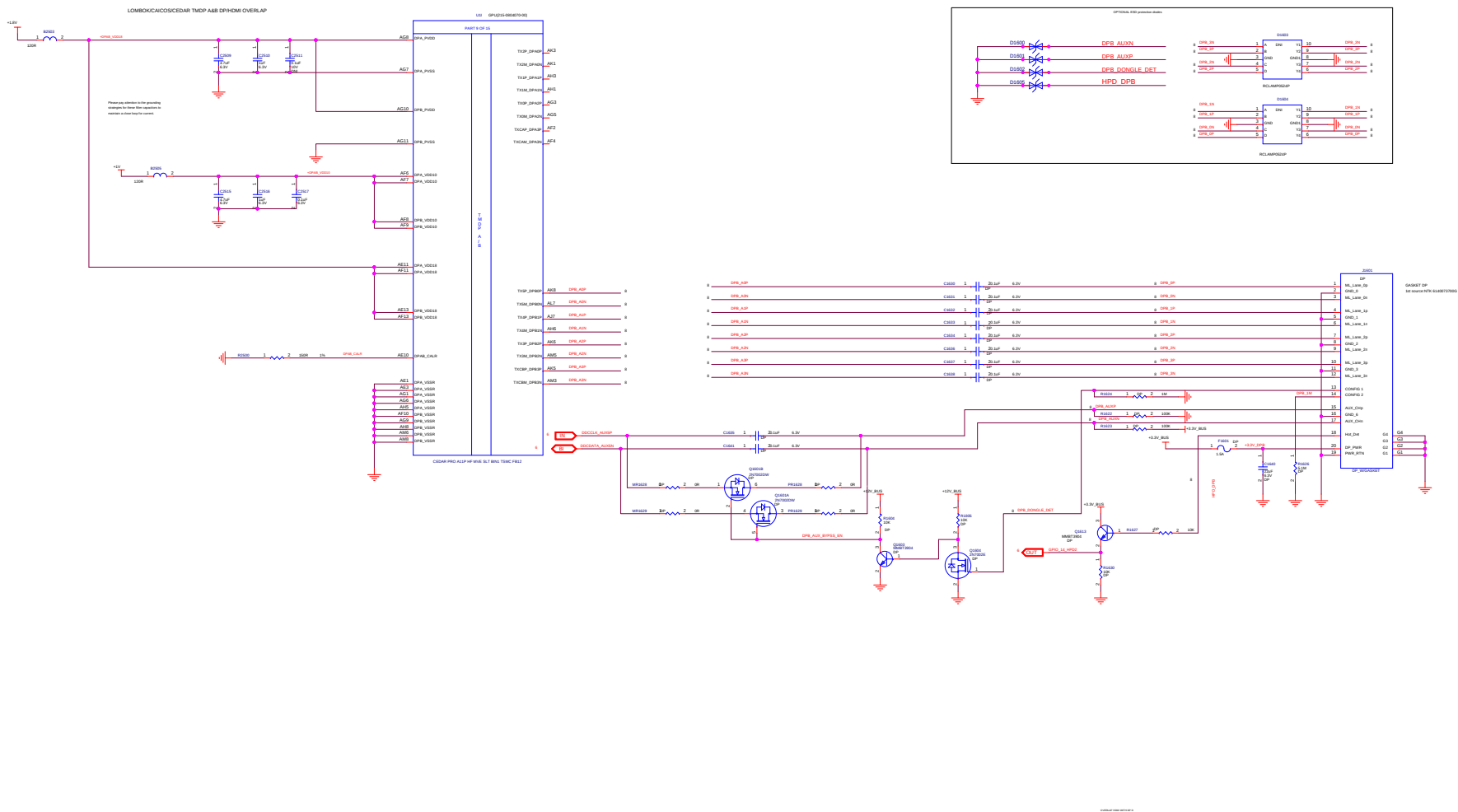
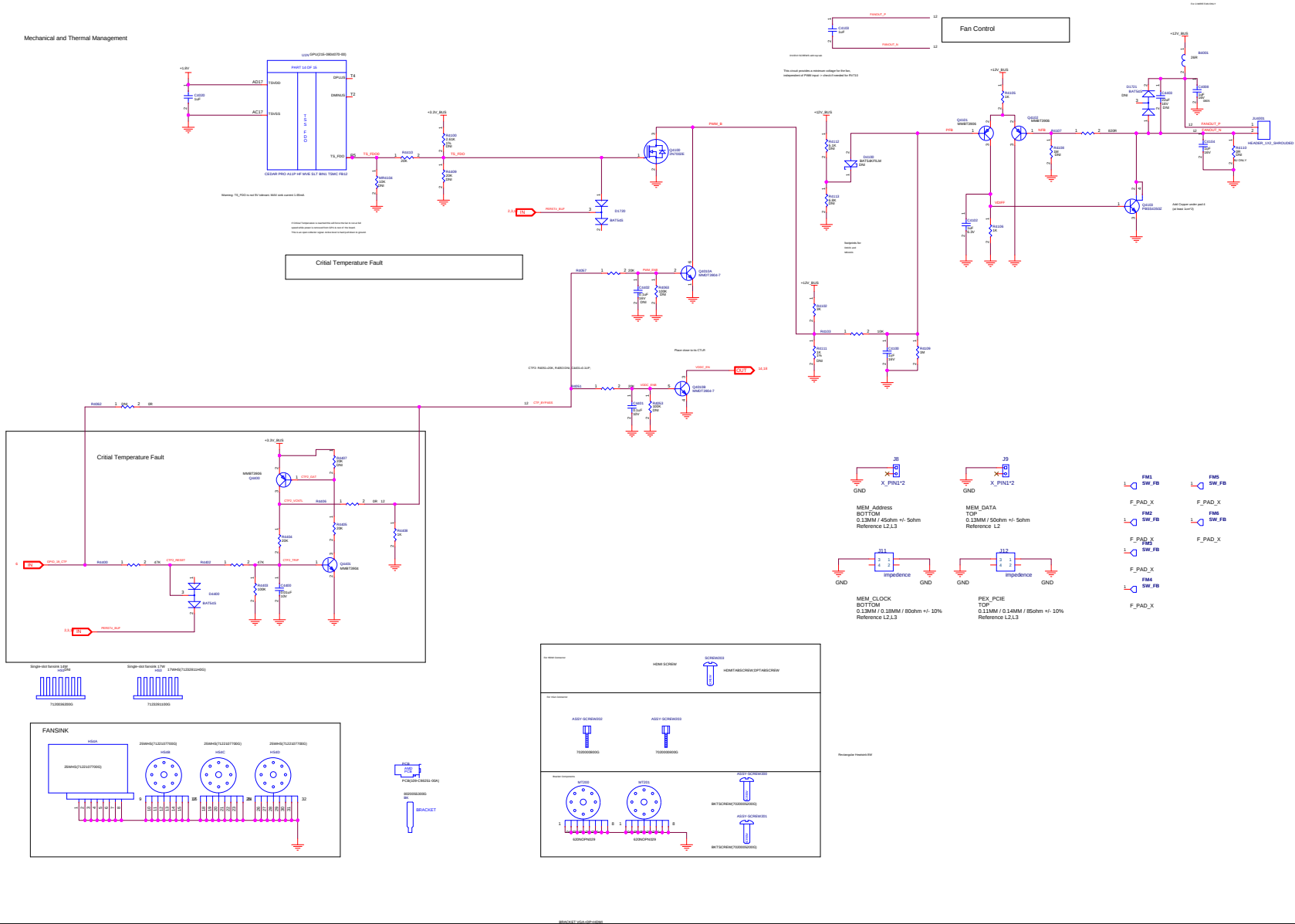


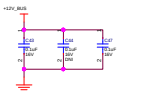
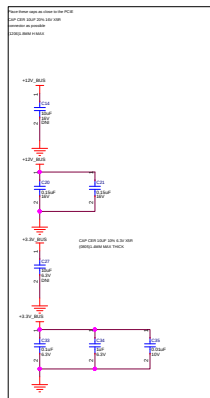




[illegible]

GROUP NAME DESCRIPTION	
COMMON	INSTALLED IN MAIN ROOM
SWP	NO PPL NOTED IN RM
ENT	ENTRY INSTAL
INT	INTERNAL ROOM WONT BE IN PRODUCTION ROOM
GPU	GPU PM
HW	BRACKET
HS	HEATING OR FAN/GR
WATER	WATER/COOL
SUBROOM	SUBROOM PM
SUB ROOM	
WATER	WATER/COOL SUB ROOM
DRUGS	DRUGS SUB ROOM
POWER SUB ROOM	
VSDC	VSDC SUB ROOM
VSDC	VSDC SUB ROOM
WSDC	WSDC SUB ROOM

[illegible]



SYMBOL LEGEND	
DN	DO NOT INSTALL
A	ACTIVE LOW
	DIGITAL GROUND
	ANALOG GROUND
BLD	BIND-UP ONLY

AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203



SHEET: PCIe Edge Connector

DATE: Thu Jun 04 08:11:34 2015

REV:	1.0
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SHEET NUMBER: 2 OF 21

DOCUMENT NUMBER: 105-C982xx-00A

NOTES:	NOTE
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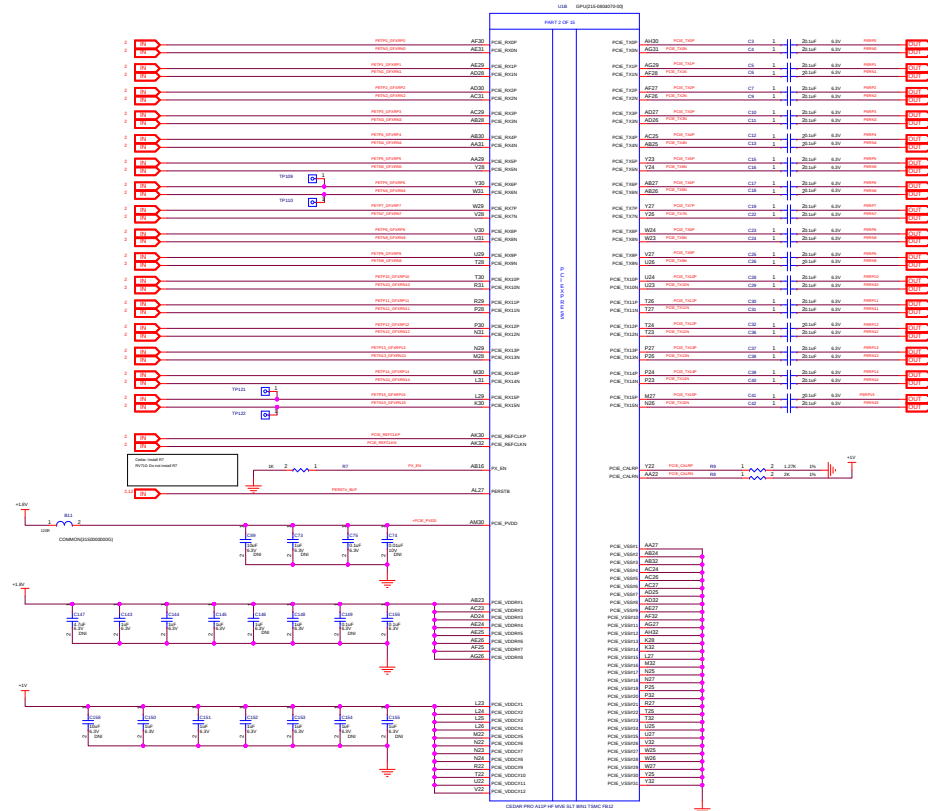
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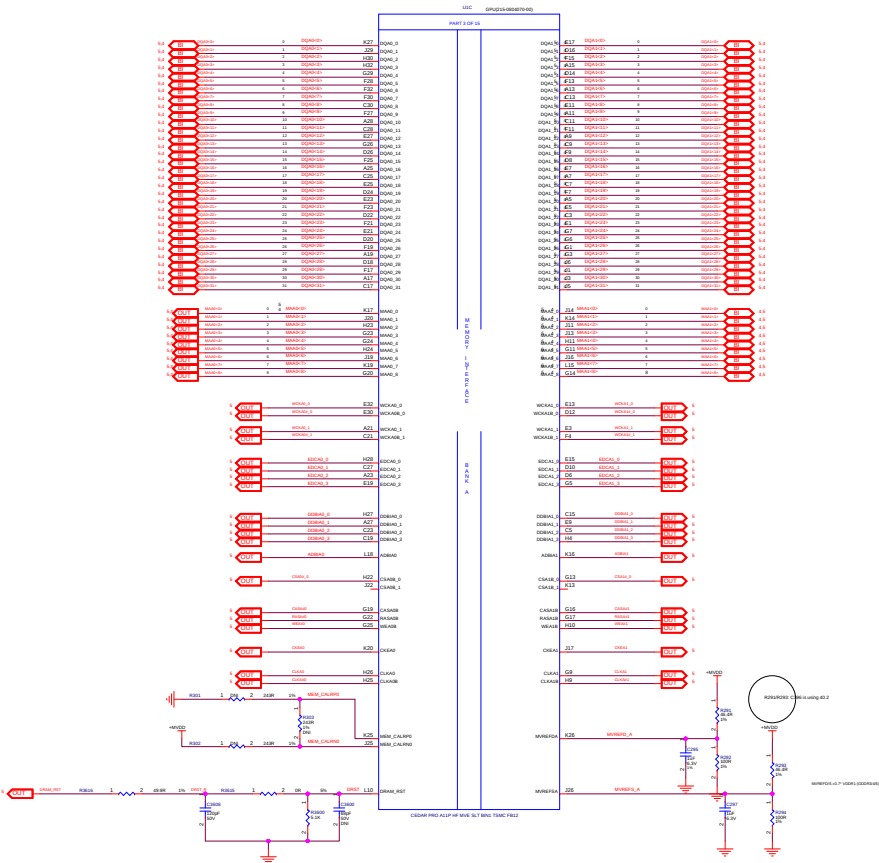
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TITLE: Calicos G5 VGA+DP+HDMI 25W ASIC, 5.7"

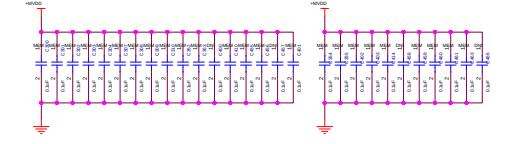
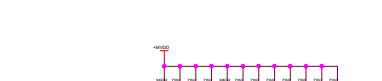
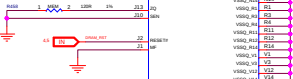
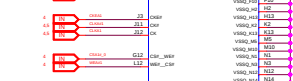
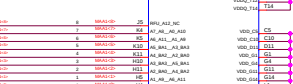
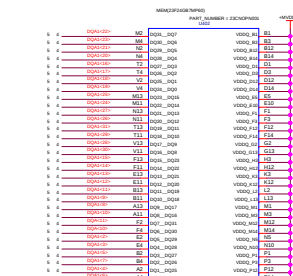
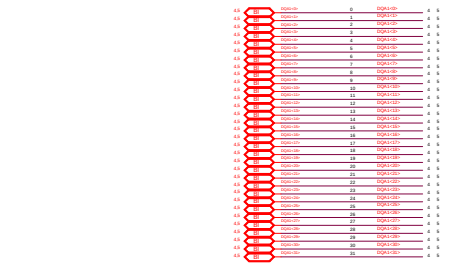
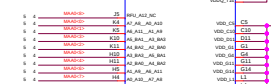
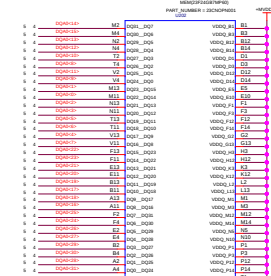
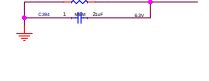
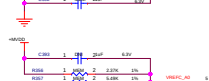
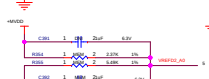
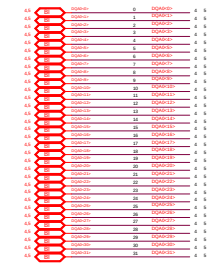
LOMBOK/CAICOS/CEDAR PCIe Interface

NOTE: Some of the PCIe signals will
be available through vias on traces.

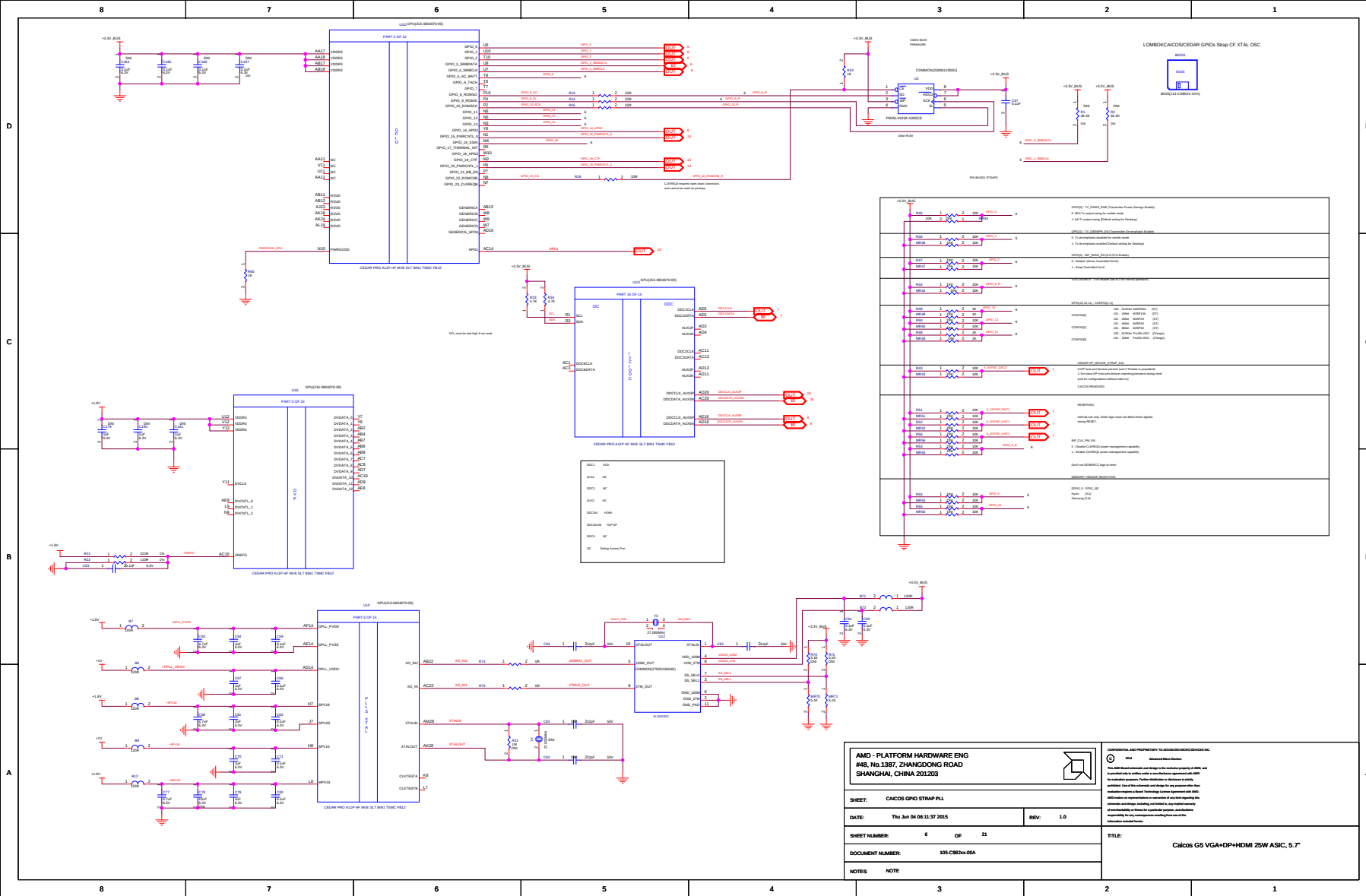


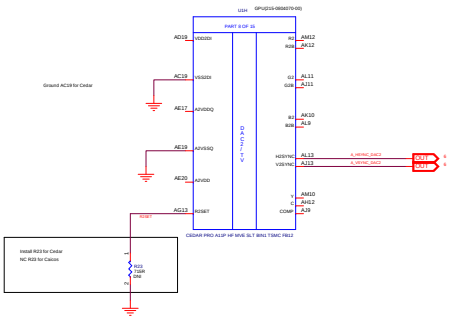
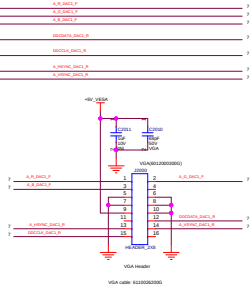
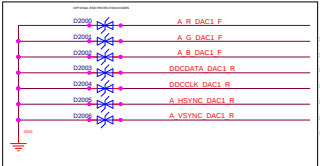
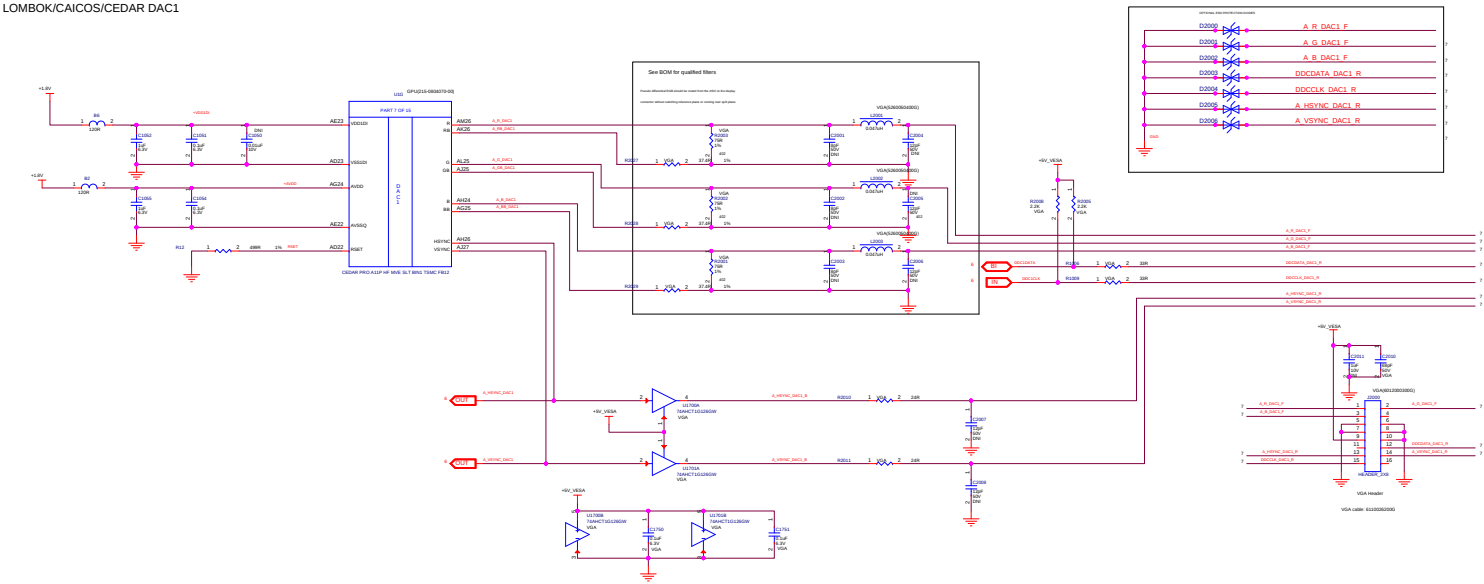


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SHEET:	CAICOS MEM			
DATE:	Thu Jun 04 06:11:35 2015	REV:	1.0	
SHEET NUMBER:	4	OF	21	
DOCUMENT NUMBER:	305-CB20a-00A			
NOTES:	NOTE			
TITLE:		Calicos G5 VGA+DP+HDMI 25W ASIC, 5.7"		



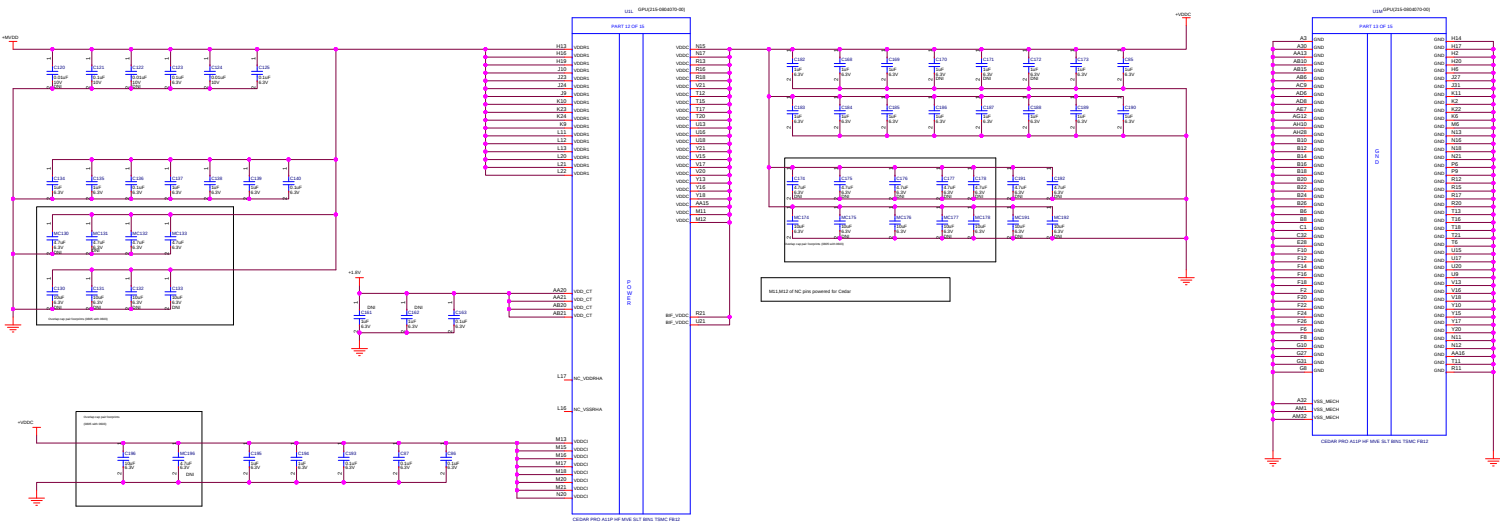
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DATE: Thu Jun 04 06:11:36 2015		REV: 1.0		TITLE: Calcos G5 VGA+DP+HDMI 25W ASIC, 5.7"	
SHEET NUMBER: 5 OF 21		DOCUMENT NUMBER: 105-CR026-00A		NOTES: NOTE	





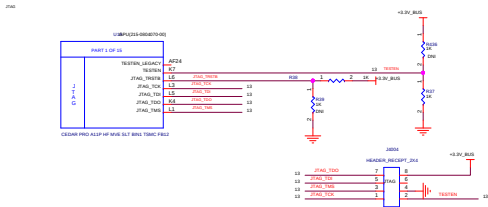
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SHEET: CAICOS DAC		DATE: Thu Jun 04 06:11:37 2015		REV: 1.0	
SHEET NUMBER: 7		OF 21		TITLE: Calicos G5 VGA+DP+HDMI 25W ASIC, 5.7"	
DOCUMENT NUMBER: 105-0822a-00A					
NOTES: NOTE					

LOMBOK/CAICOS/CEDAR Power & GND

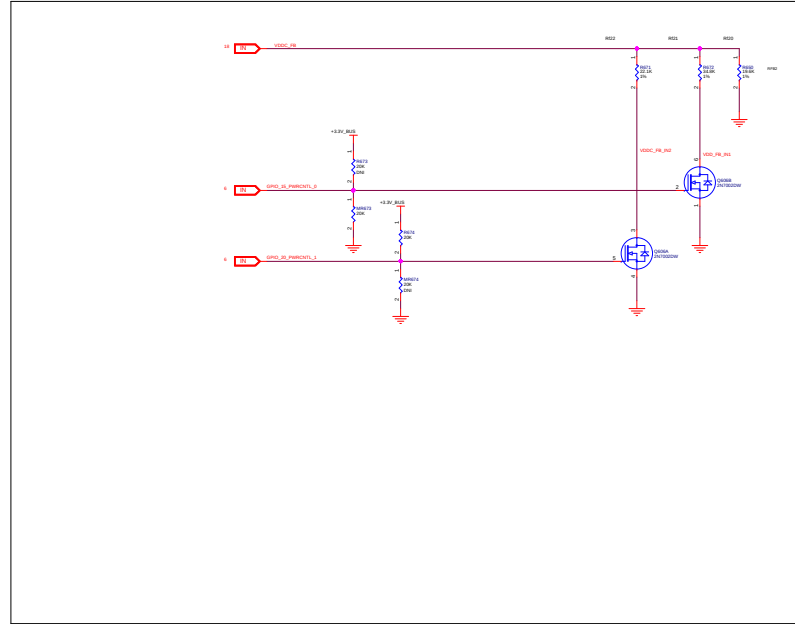
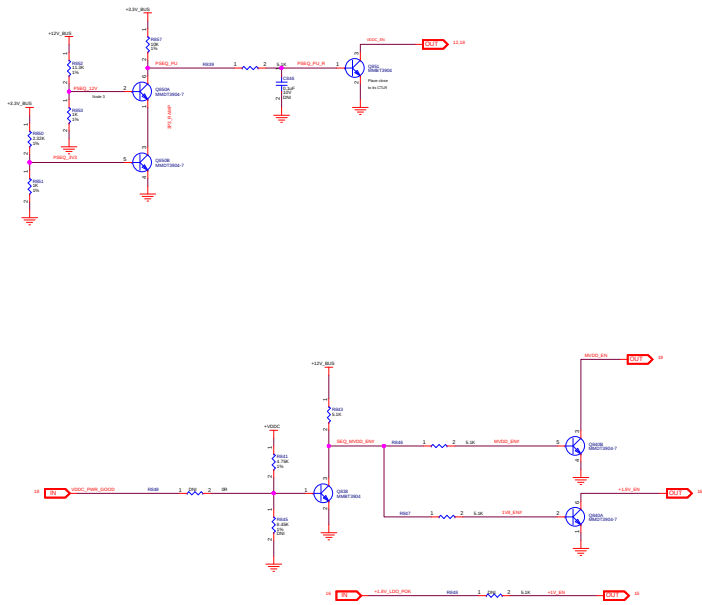


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SHEET: CAICOS Power&GND			REV: 1.0	
DATE: Thu May 26 09:09:00 2015		REV: 1.0		
SHEET NUMBER: 11 OF 21		TITLE: Calcos G5 VGA+DP+HDMI 25W ASIC, 5.7"		
DOCUMENT NUMBER: 915-0823x-05A				

(19) Debug Circuits



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SHEET: Debug Circuit			
DATE: Thu May 28 09:09:00 2015		REV: 1.0	
SHEET NUMBER: 13 OF 21		TITLE: Calcos G5 VGA+DP+HDMI 25W ASIC, 5.7"	
DOCUMENT NUMBER: SIS-0823x-05A			



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SHEET: PWR SEQ	
DATE: Thu May 26 09:58:2015	REV: 1.0
SHEET NUMBER: 14	OF 21
DOCUMENT NUMBER: SIS-CH230-05A	

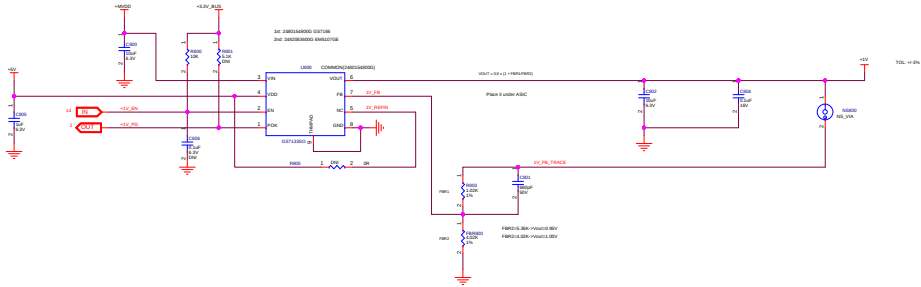


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TITLE: Calicos G5 VGA+DP+HDMI 25W ASIC, 5.7"

Linear Regulators

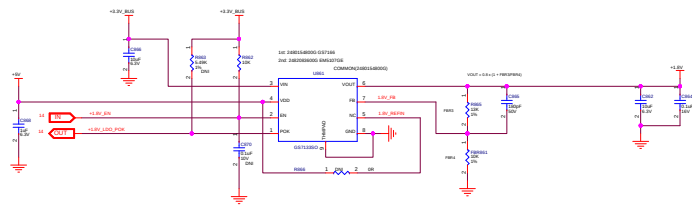
LDO IC: Vin = +1.5V +/- 2% Vout = +1.5V +/- 2% Iout = 3A MAX
PCB: 50 to 70mm sq. copper area for cooling



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SHEET: 1V		REV: 1.0		TITLE: Calix G5 VGA+DP+HDMI 25W ASIC, 5.7"	
DATE: Thu Jun 04 06:11:37 2015					
SHEET NUMBER: 15 OF 21					
DOCUMENT NUMBER: 105-08220-00A					
NOTES: NOTE					

Linear Regulators

LDO1 V1	Vin = 3.00V to 3.60V (3.3V +/- 5%)	Vout = +3.3V +/- 2%	Iout = 1A MAX
PCB: 50 to 70mm sq. copper area for cooling			



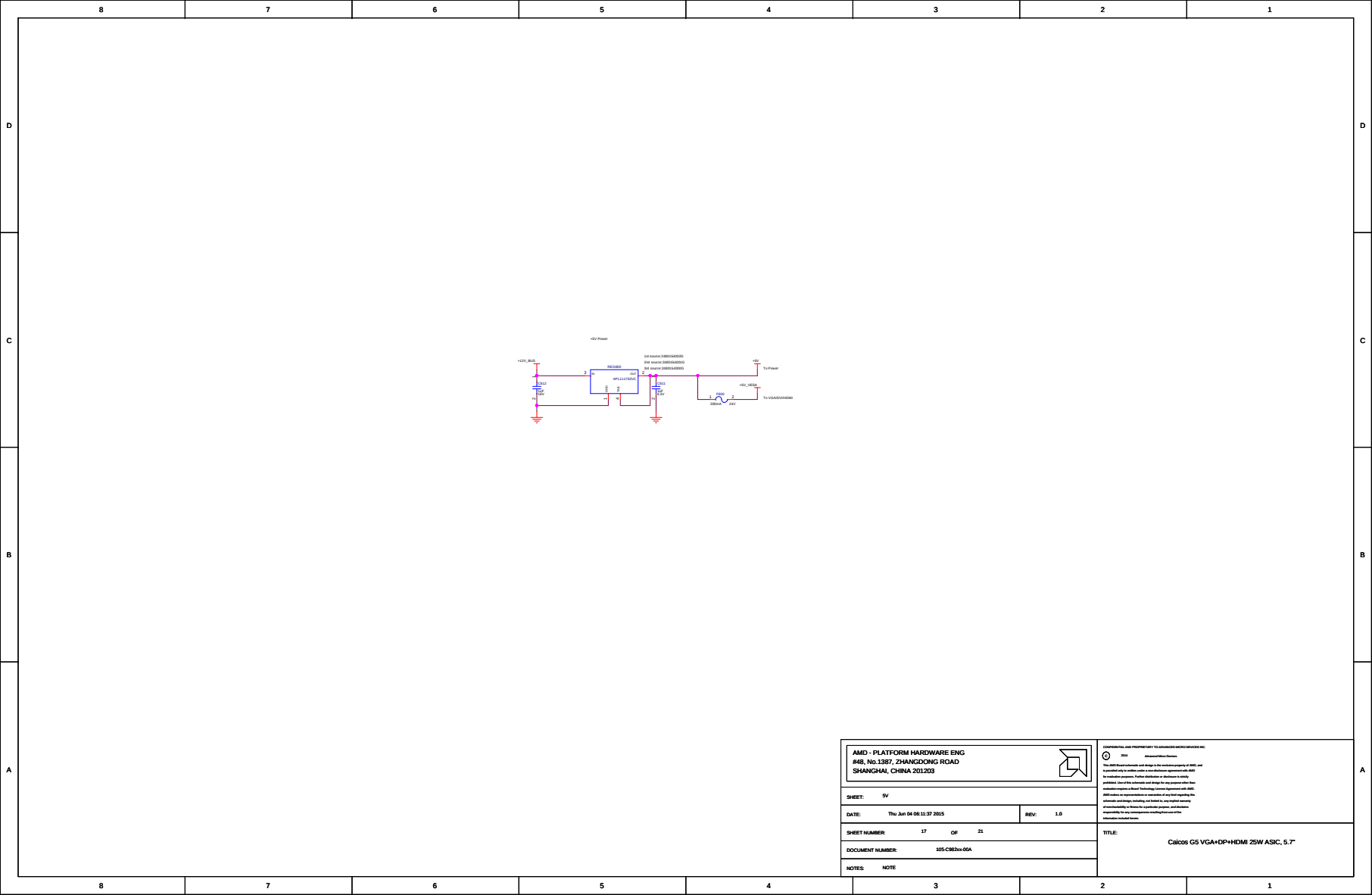
AMD - PLATFORM HARDWARE ENG
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

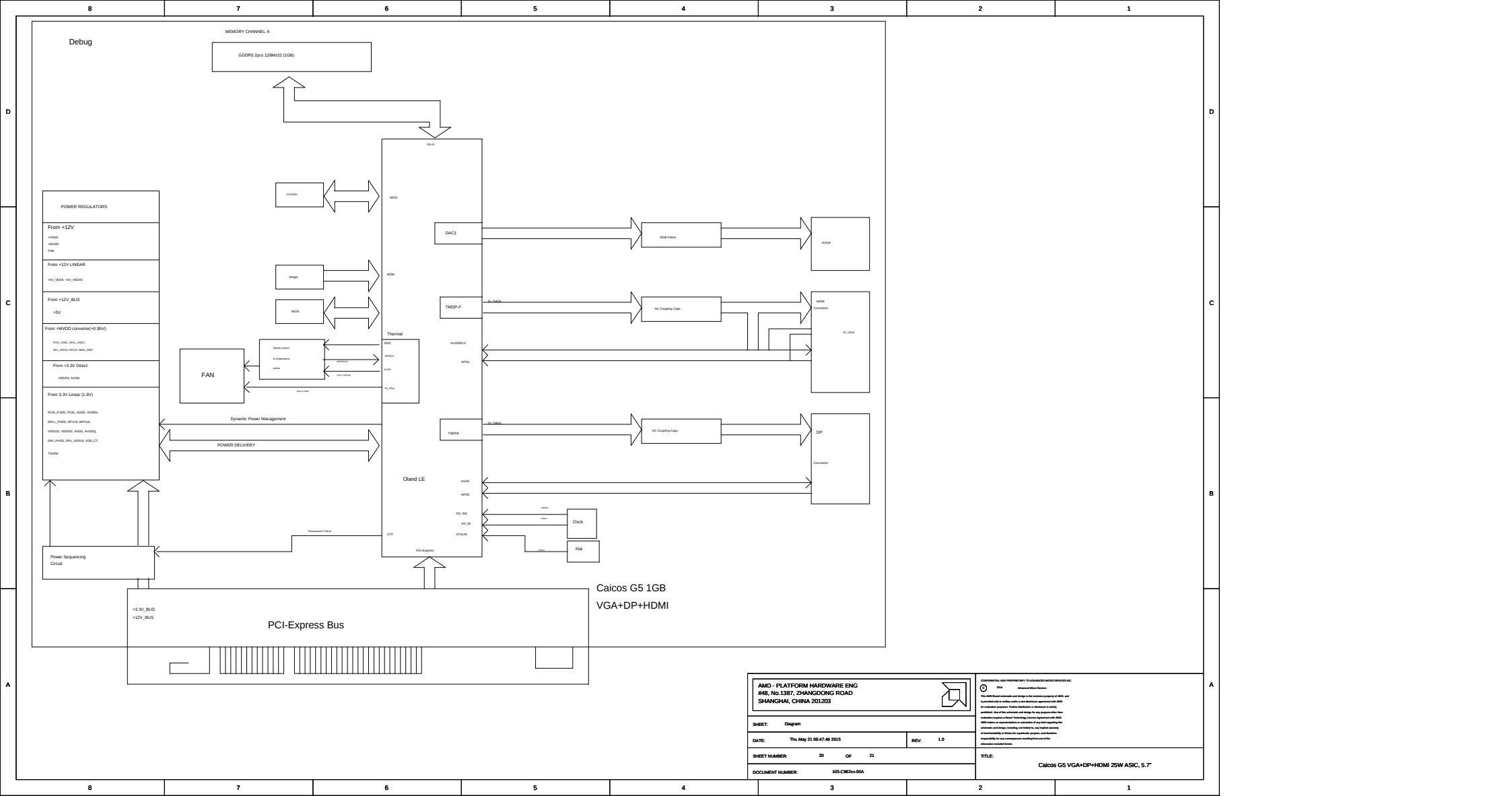


SHEET:		1/1	
DATE:		Thu May 26 09:08:58 2015	REV: 1.0
SHEET NUMBER:		16	OF 21
DOCUMENT NUMBER:		105-CR23a-00A	

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TITLE: Calcos G5 VGA+DP+HDMI 25W ASIC, 5.7"







TITLE:

Calcos G5 VGA+DP+HDMI 25W ASIC, 5.7

DOCUMENT NUMBER: 105-C9820x-00A

DATE: Thu May 28 02:44:26 2015

SHEET NUMBER: 21 OF 2

REV: 1.0

REVISION HISTORY

ENGINEER:

Tony Yang

NOTES:

NOTE

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#48, No.1387, ZHANGDONG

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8	7	6	5	4	3	2	1
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