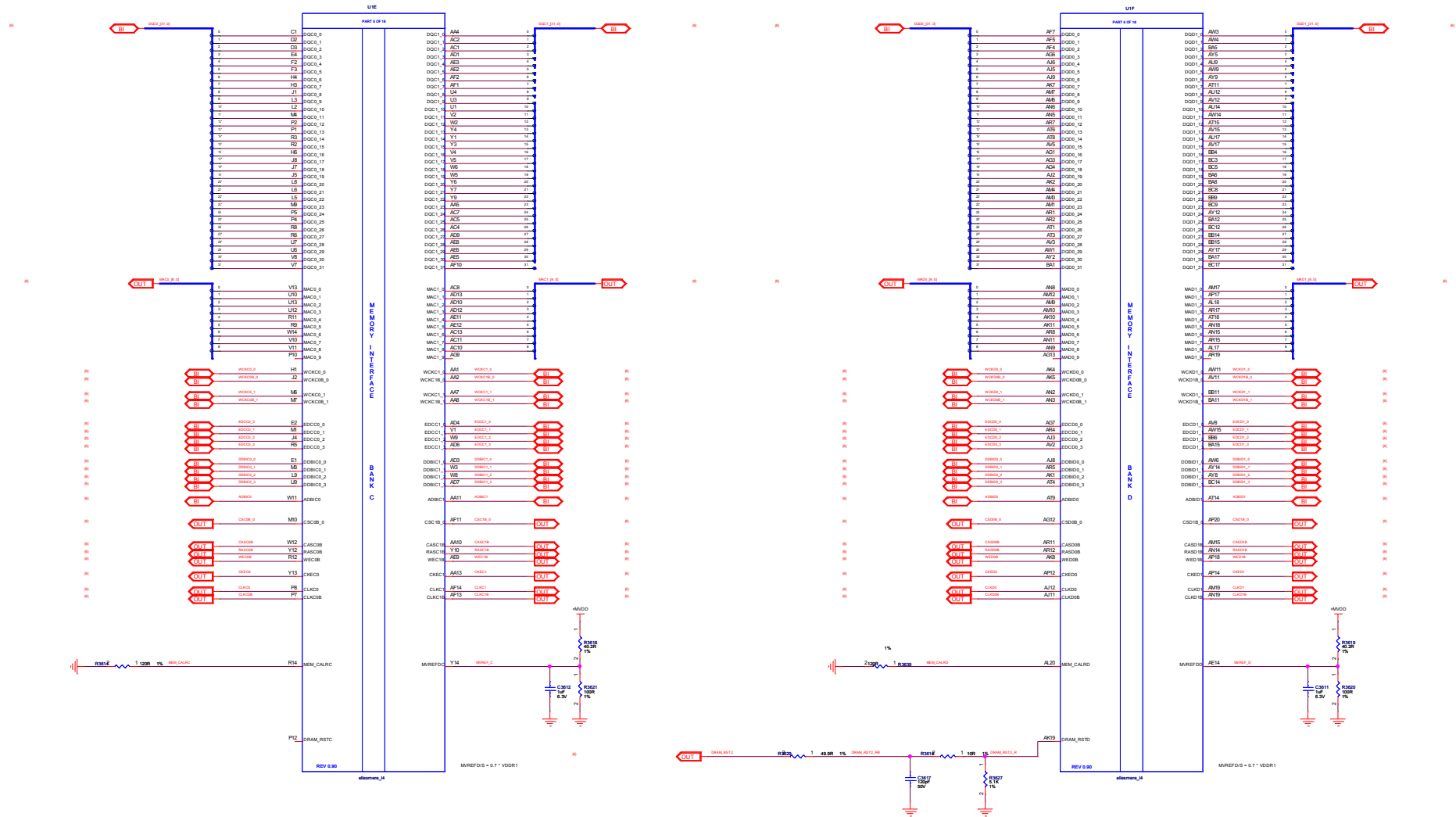
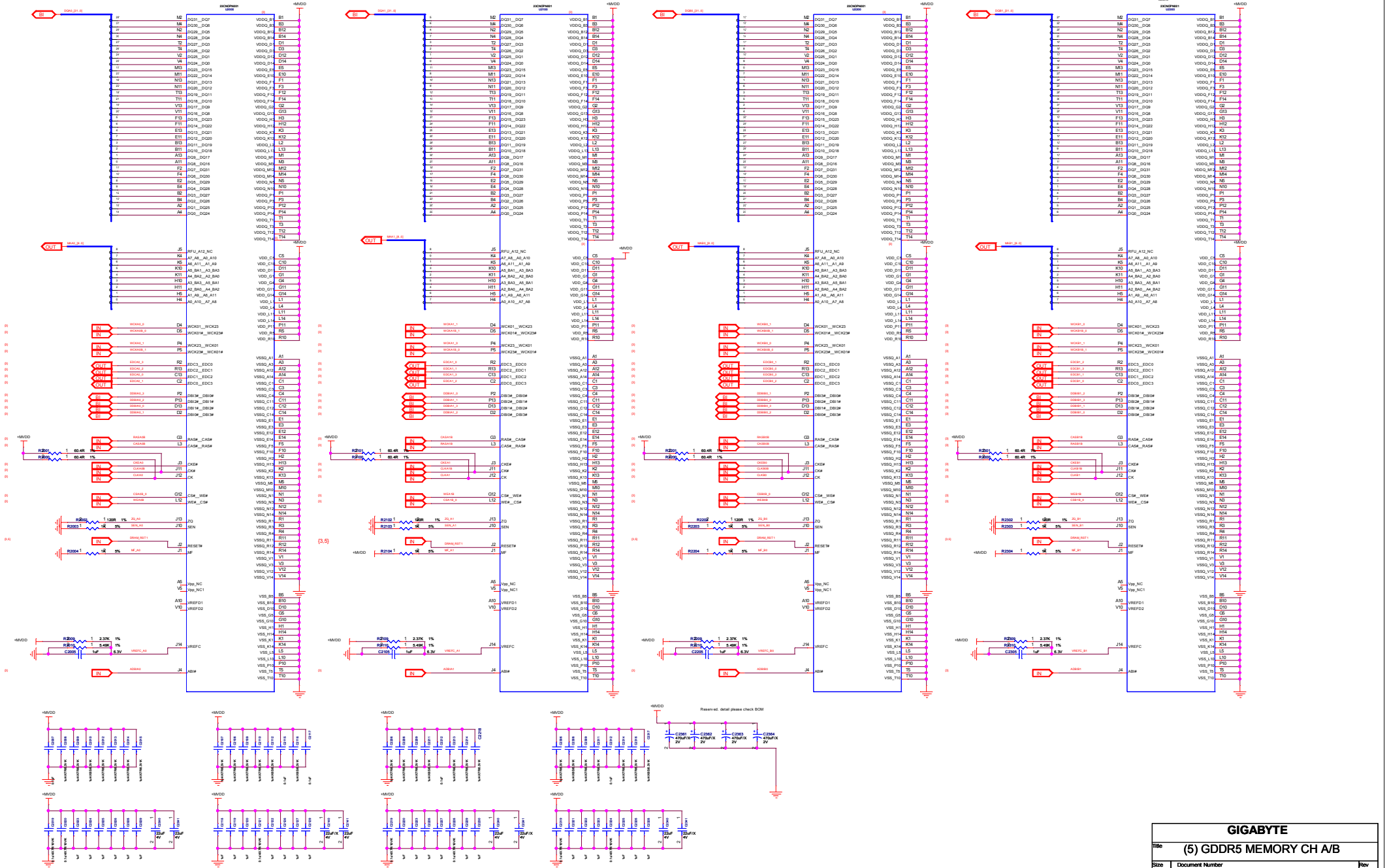
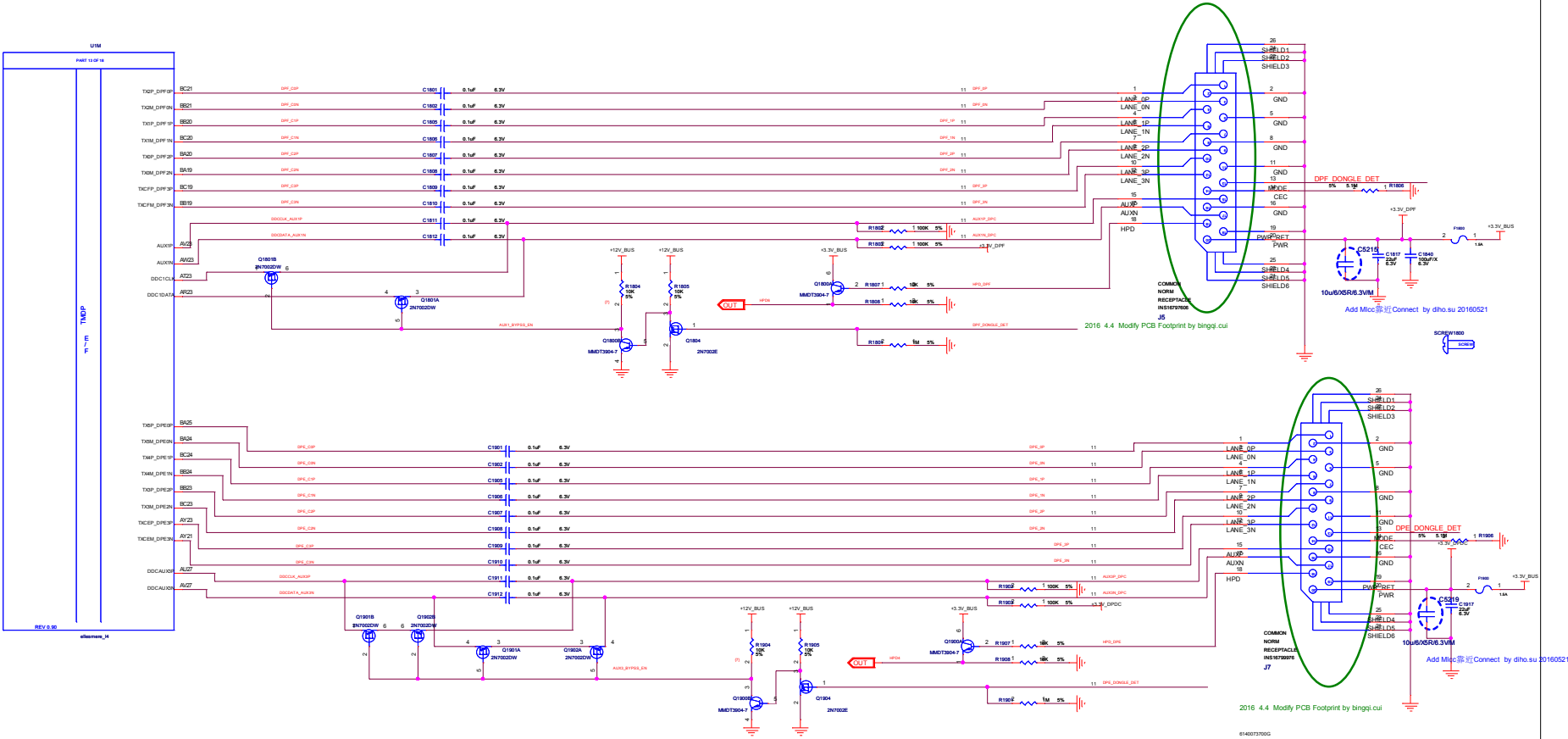


(4) ELLESMERE MEM INTERFACE CH C/D

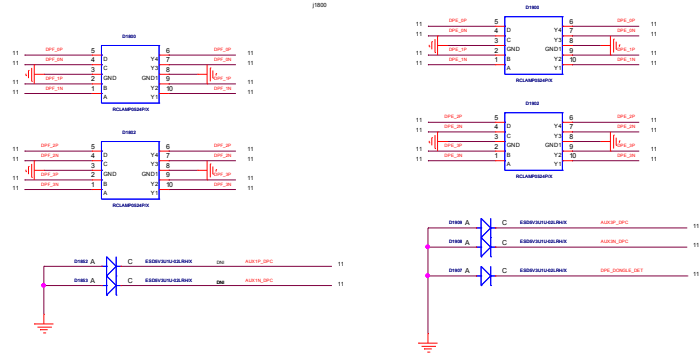


(5) GDDR5 MEMORY CH A/B



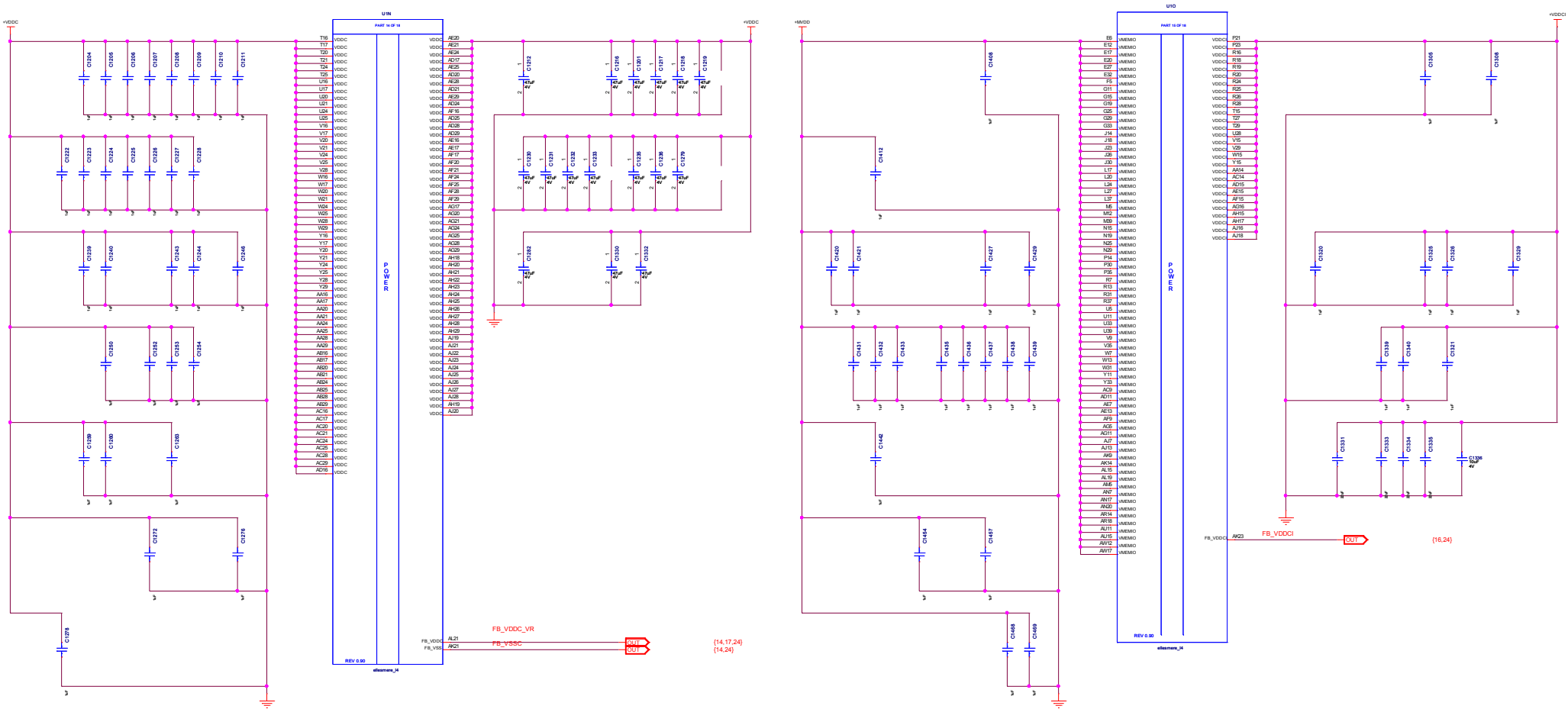


OPTIONAL ESD PROTECTION DIODES

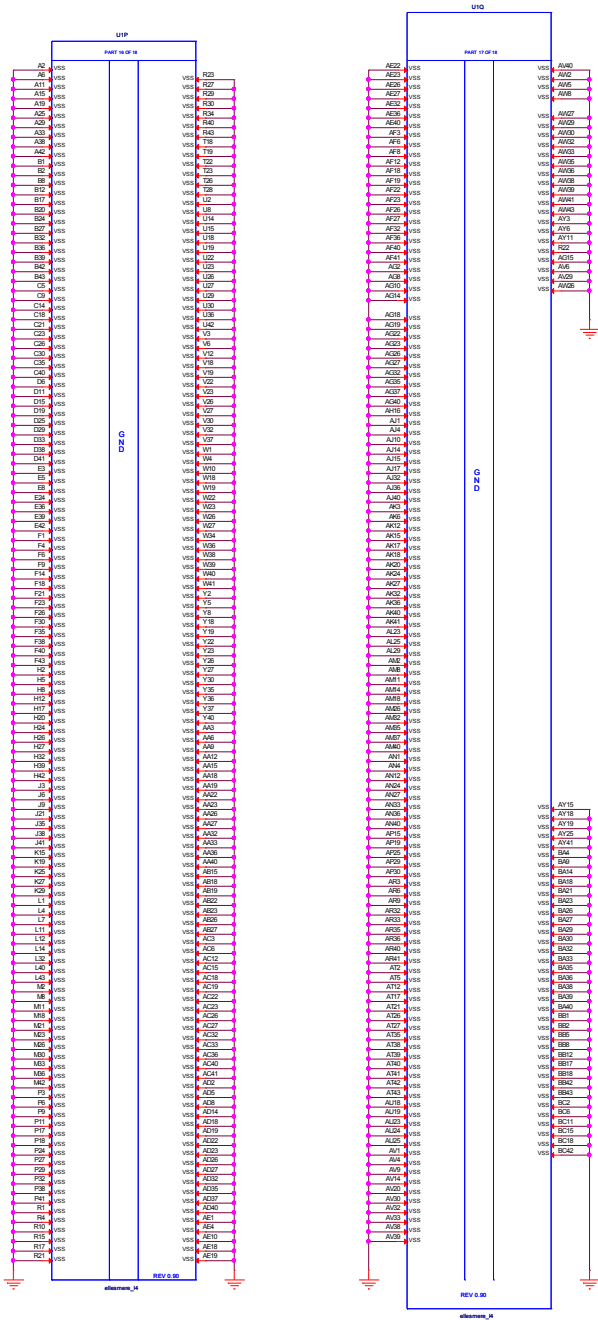


GIGABYTE			
Title ELLESMERE LVTMDP E/F			
Size	Document Number	Rev	
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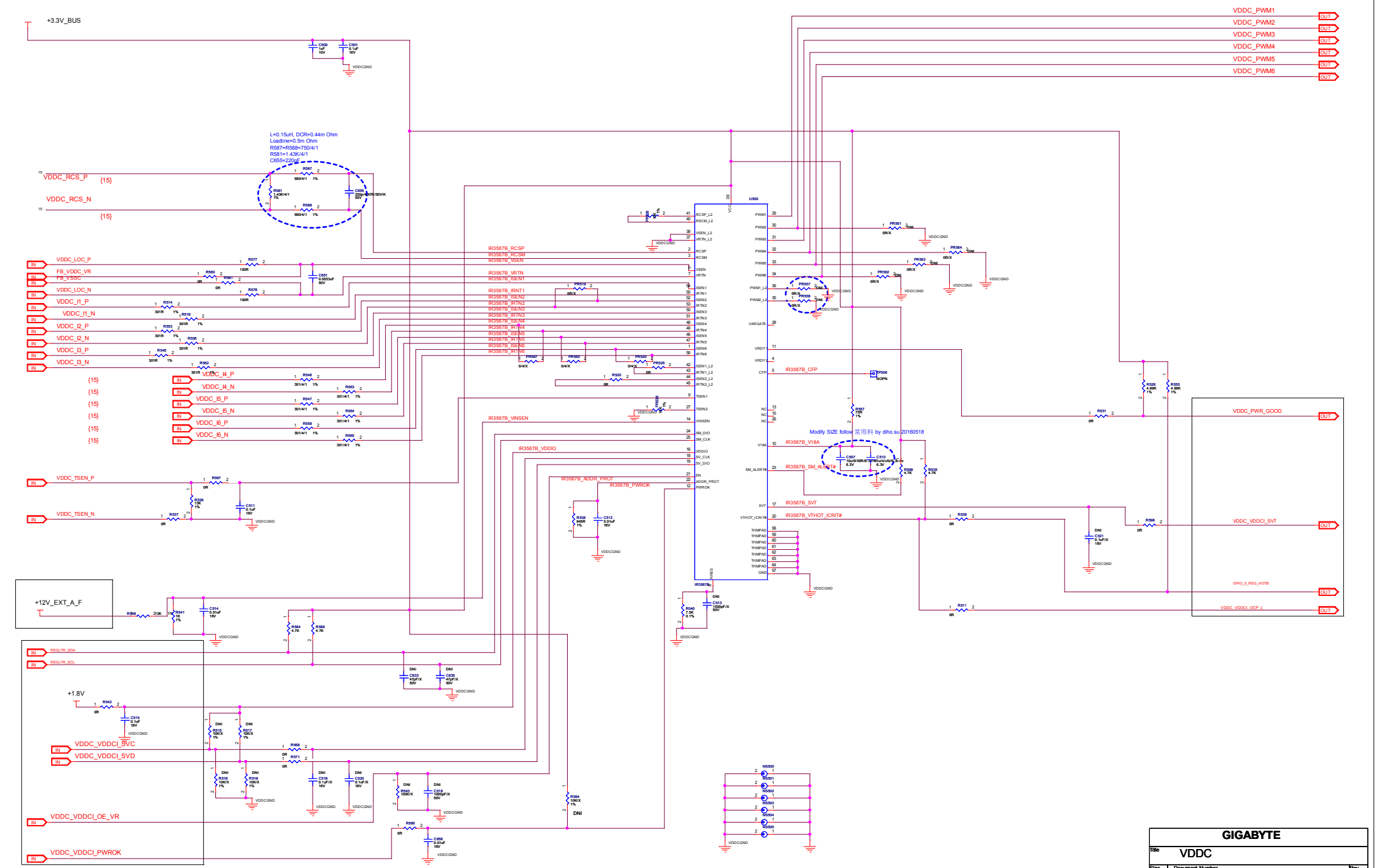
(12) ELLESMERE POWER



(13) ELLESMERE GROUND

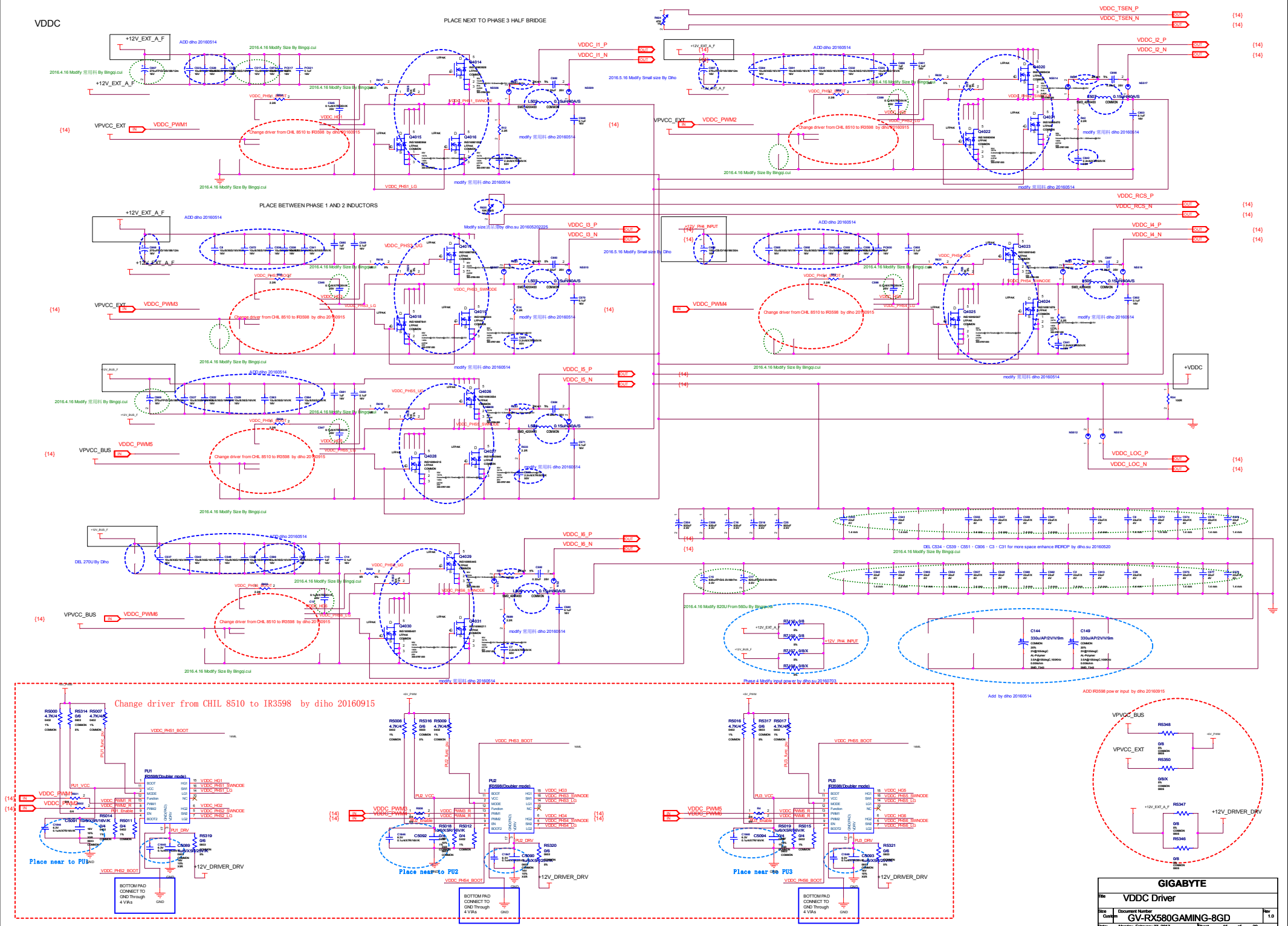


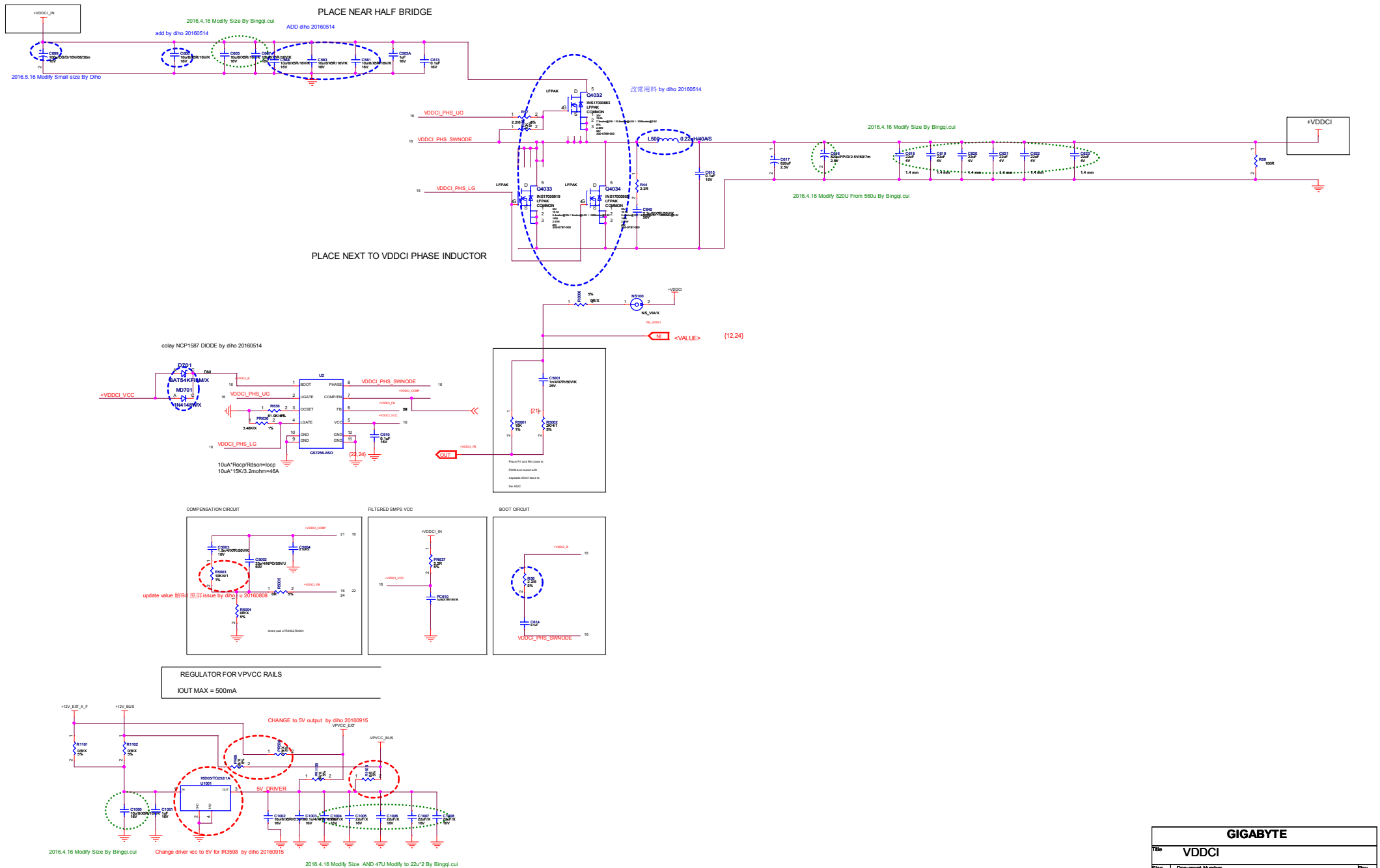
GIGABYTE			
Title ELLESMERE GND			
Size	Document Number		Rev
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VDDC

PLACE NEXT TO PHASE 3 HALF BRIDGE

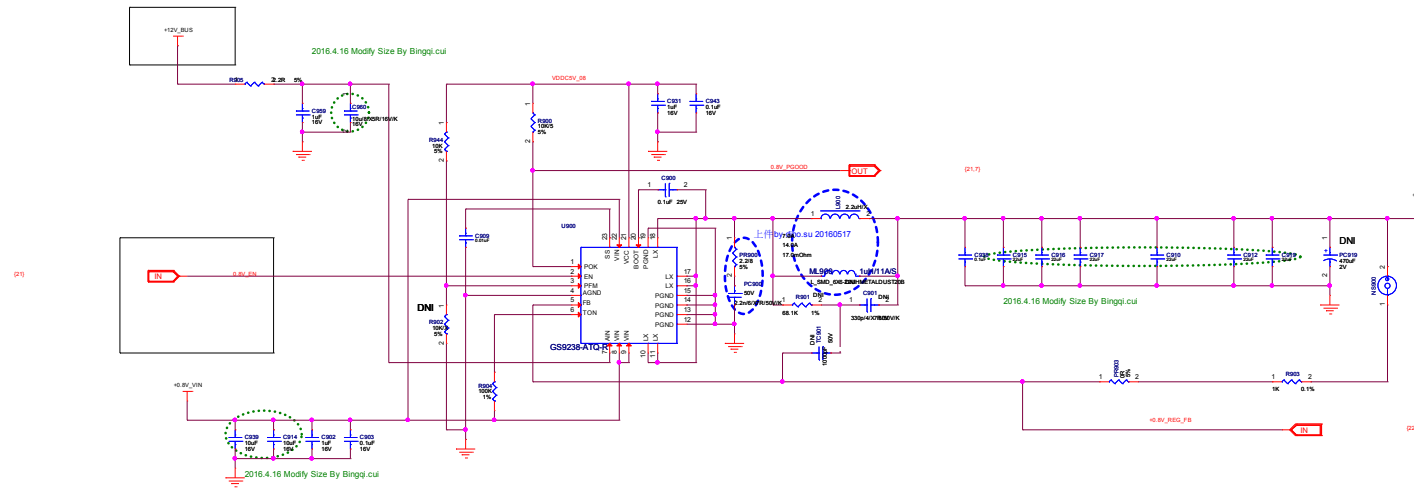




GIGABYTE			
File	VDDCI		
Size	Document Number	Rev	
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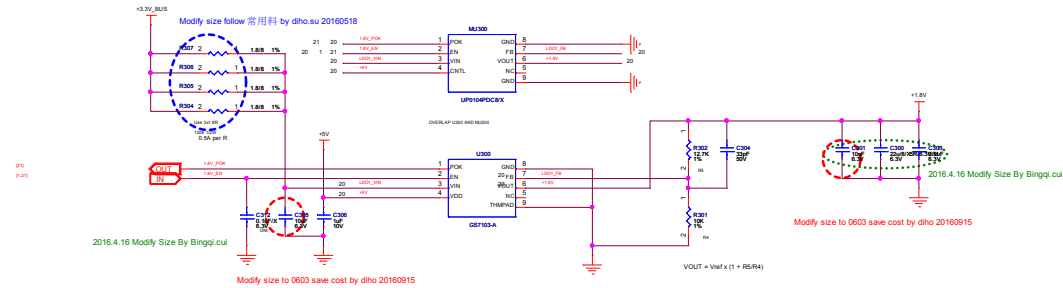


(17) 0.95V

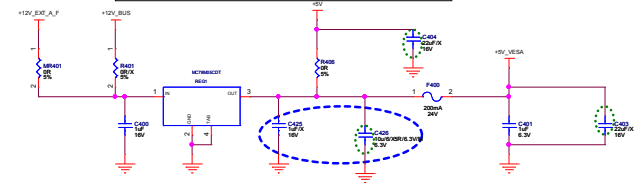


(18) SMALL RAIL REGULATORS

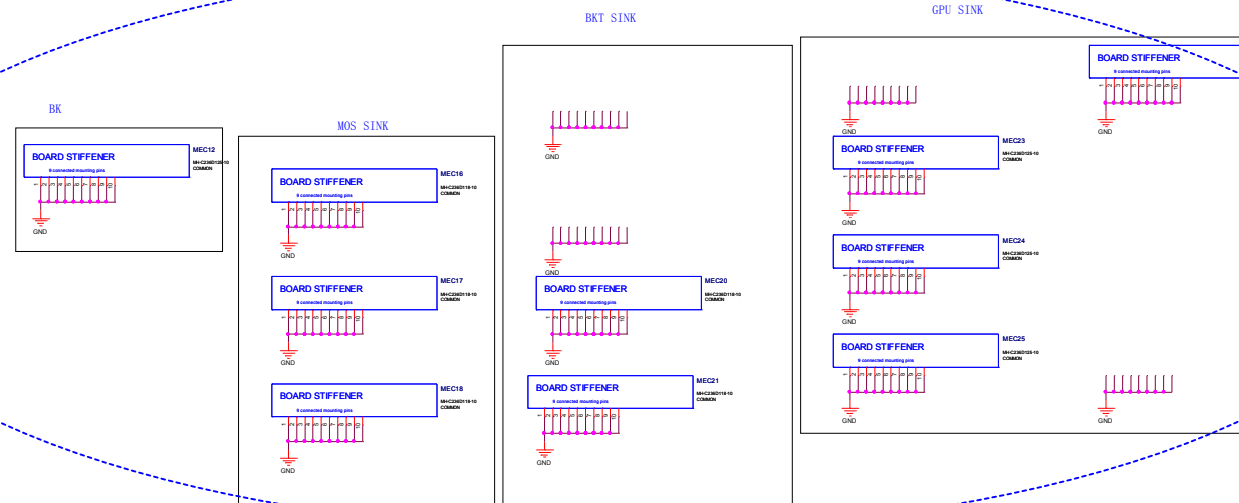
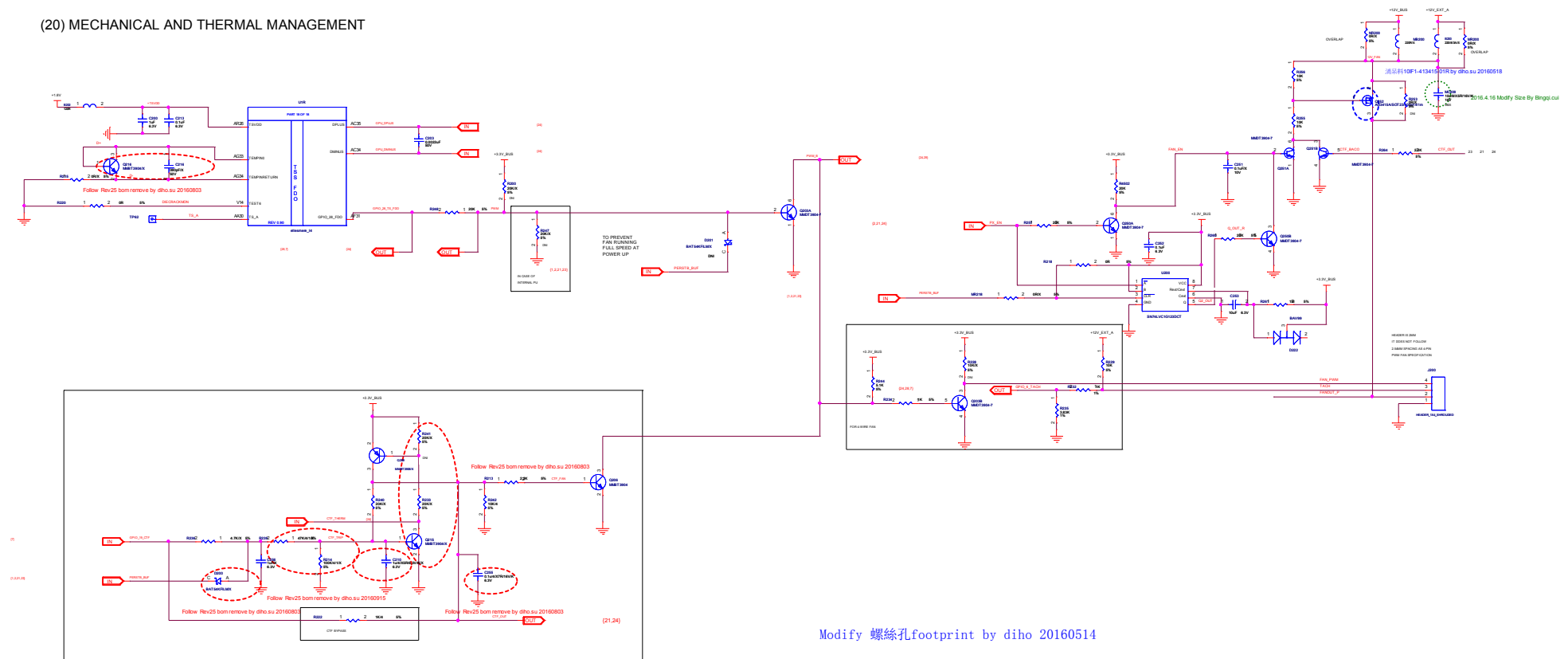
LDO #1: VIN = 3.0V TO 3.6V MAX
VOUT = +1.8V +/- 2%
IOUT = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING



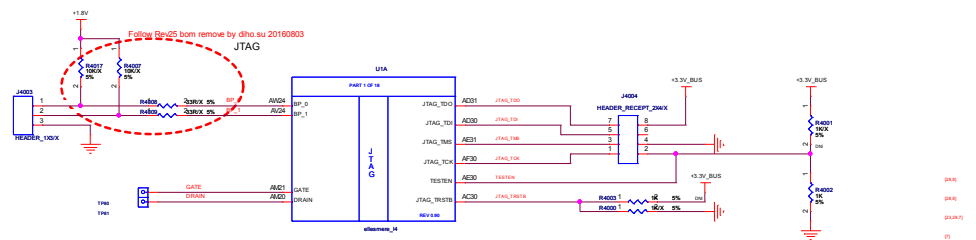
REGULATOR FOR +5V RAILS
IOUT MAX = 150mA



(20) MECHANICAL AND THERMAL MANAGEMENT

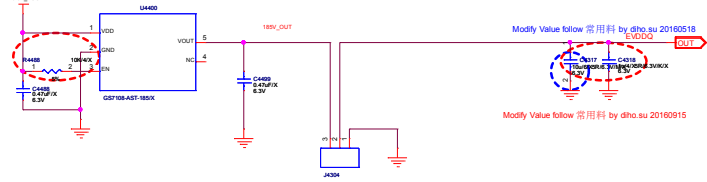


(21) DEBUG CIRCUITS



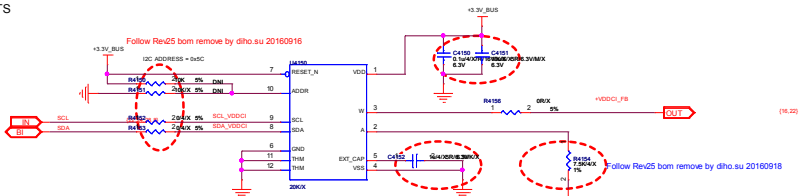
Follow Rev25 bom remove by dho.su 20160915

E-FUSE CAPABILITY
DEFAULT = GPIO-CONTROLLED
(MANUAL OPTION AS BACK-UP)



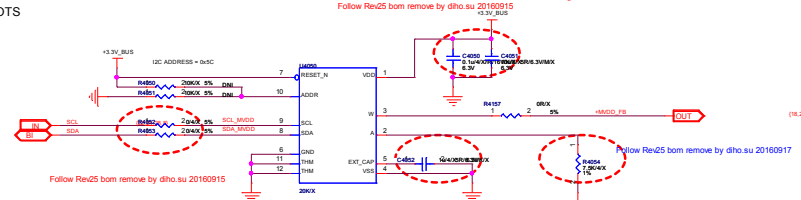
Follow Rev25 bom remove by dho.su 20160915

DIGITAL POTS



Follow Rev25 bom remove by dho.su 20160915

DIGITAL POTS



Follow Rev25 bom remove by dho.su 20160916

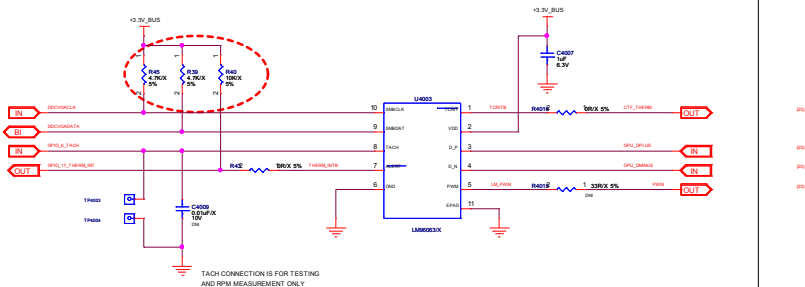
(1, 28, 6)

(1, 28, 6)

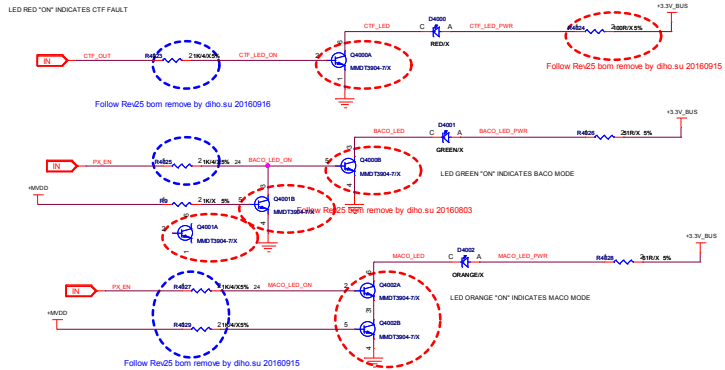
SWITCHES



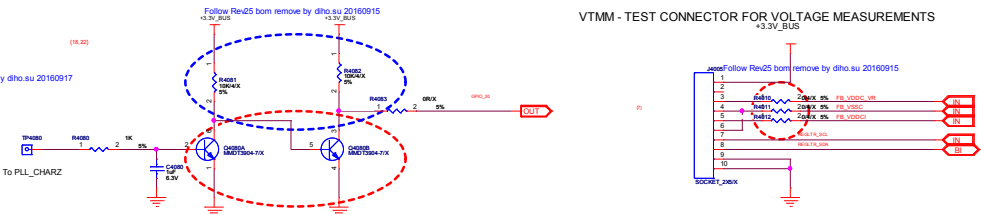
LM96163 FOR BACKUP THERMAL CONTROL



LED LIGHTS



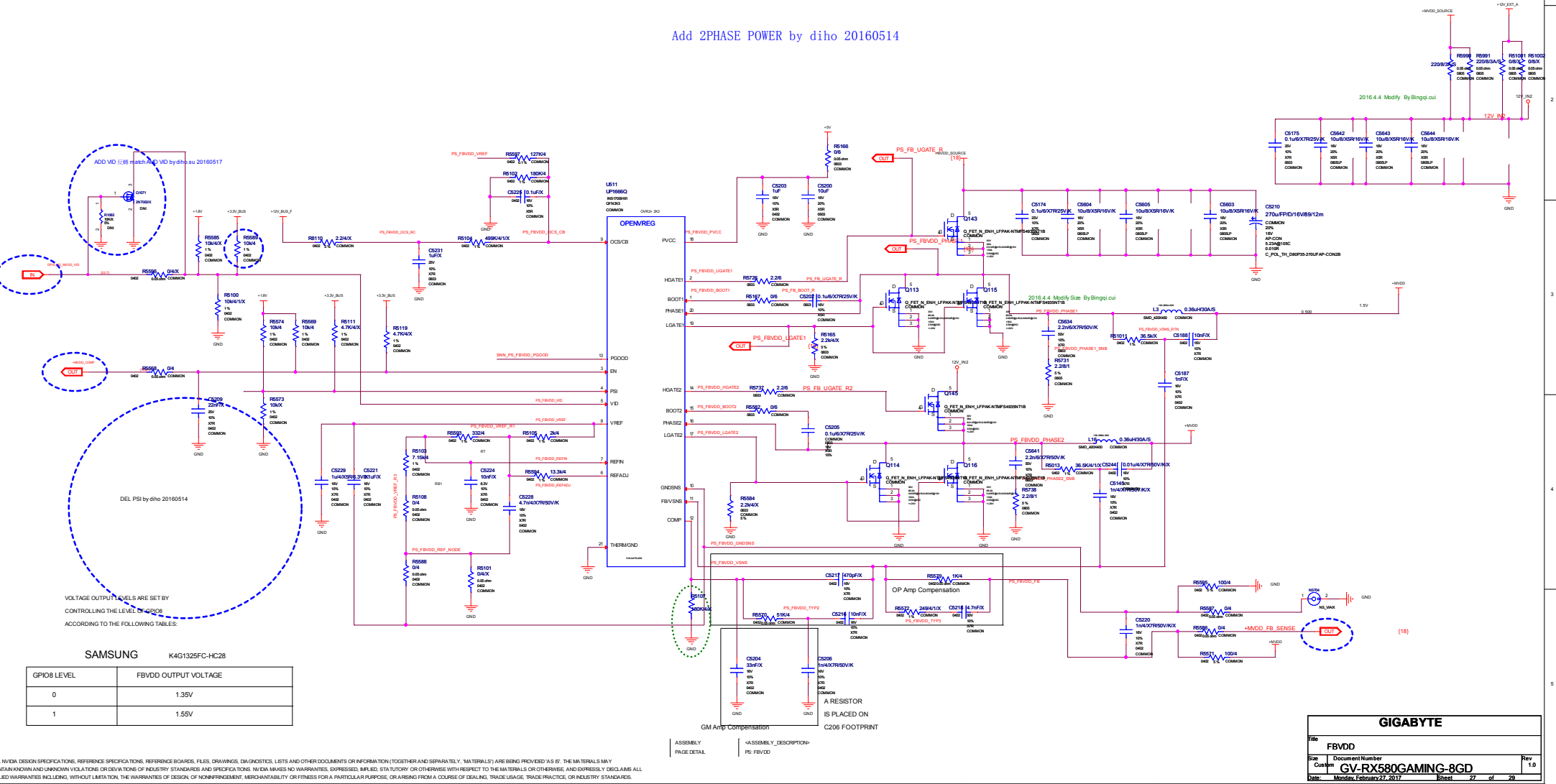
VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS

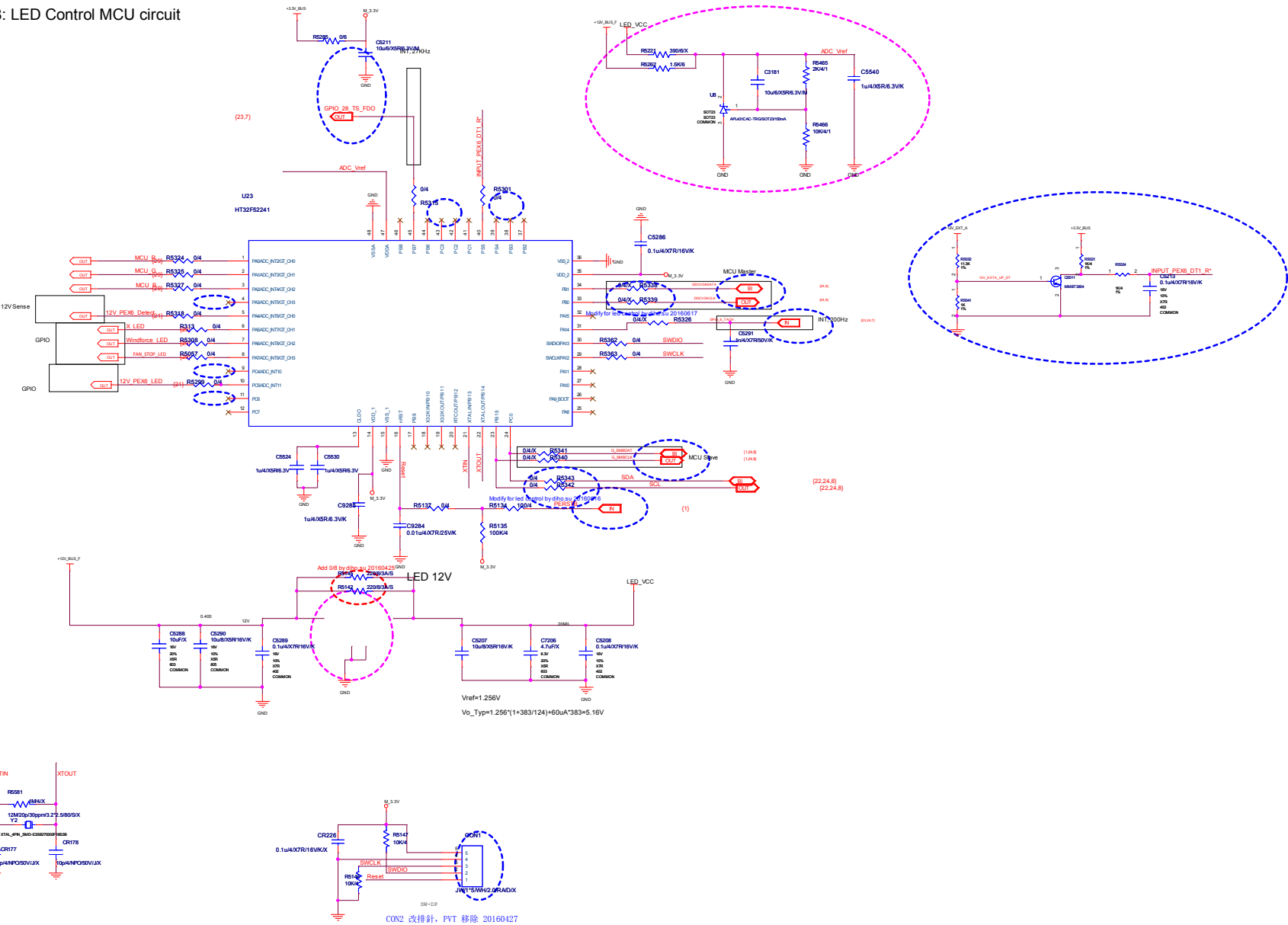


GIGABYTE

Title			DEBUG CIRCUITS
Site	Document Number	Rev	
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0	00A	00000000	
1	00B	00000000	1. Add SP017 to operation 10000 2. Update 10000 with response
2	00C	00000000	PC00A: - add serial resistor R11880 - R11887 - add pull-down inductor L11880 - L11887 Remove C405, C403, VR405, C410, C41A, R405
3	00D	00000000	Modify "00F Design"





Vout = 8 ~ 7.5V

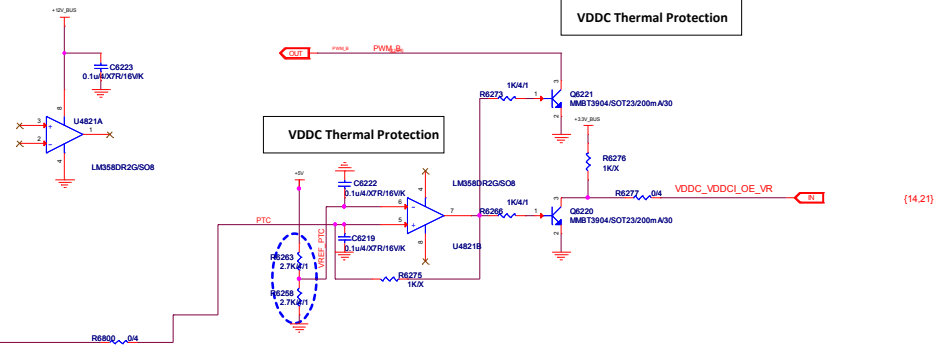
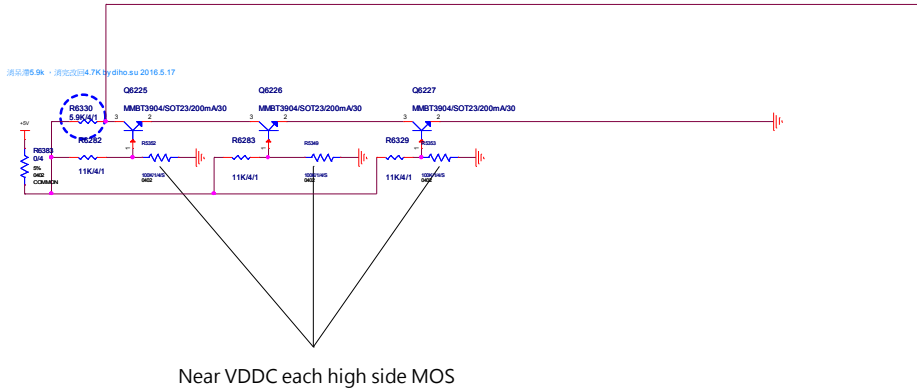
ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY
PAGE DETAIL

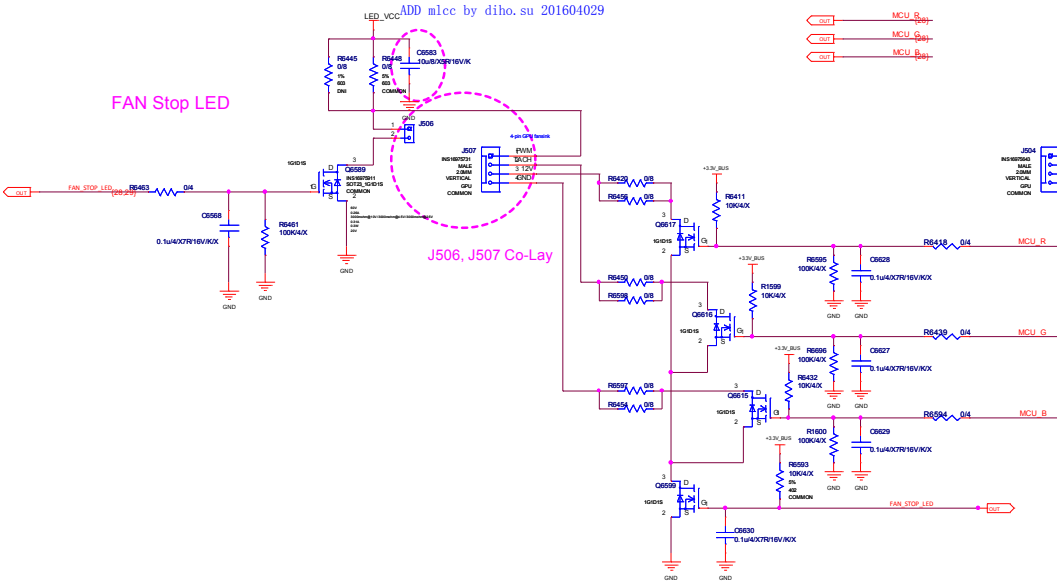
ASSEMBLY DESCRIPTION
MECH Bracket Thermal

File			
Size	Document Number	Rev	
Client	GV-3080GAMING-RGD	1.0	
Date:	Monday, February 27, 2017	Sheet	28 of 29

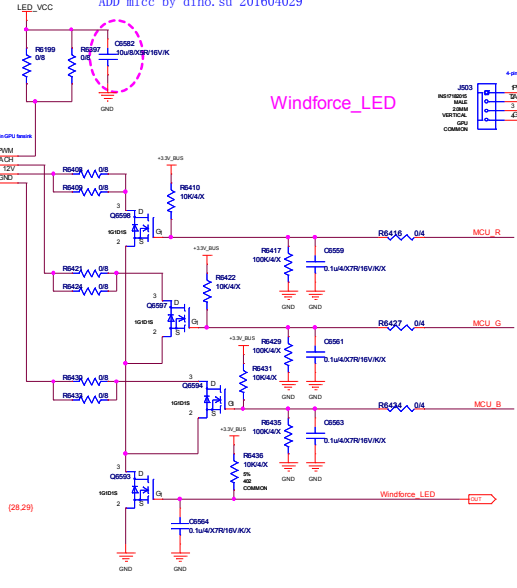
Add MOS VRHOT CIRCUIT



FAN Stop LED



Windforce_LED



X1_LED

