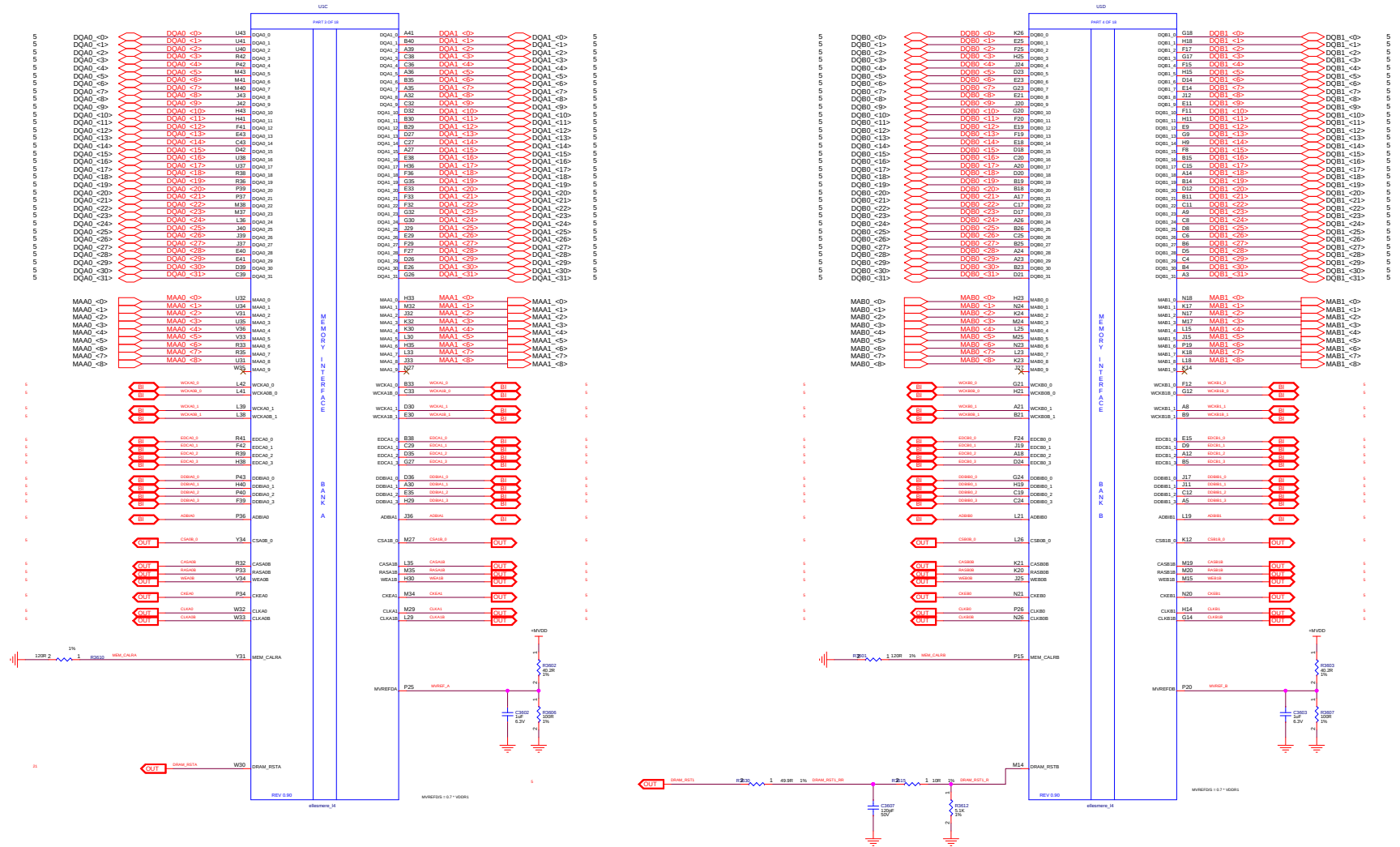


(3) ELLESMERE MEM INTERFACE CH A/B

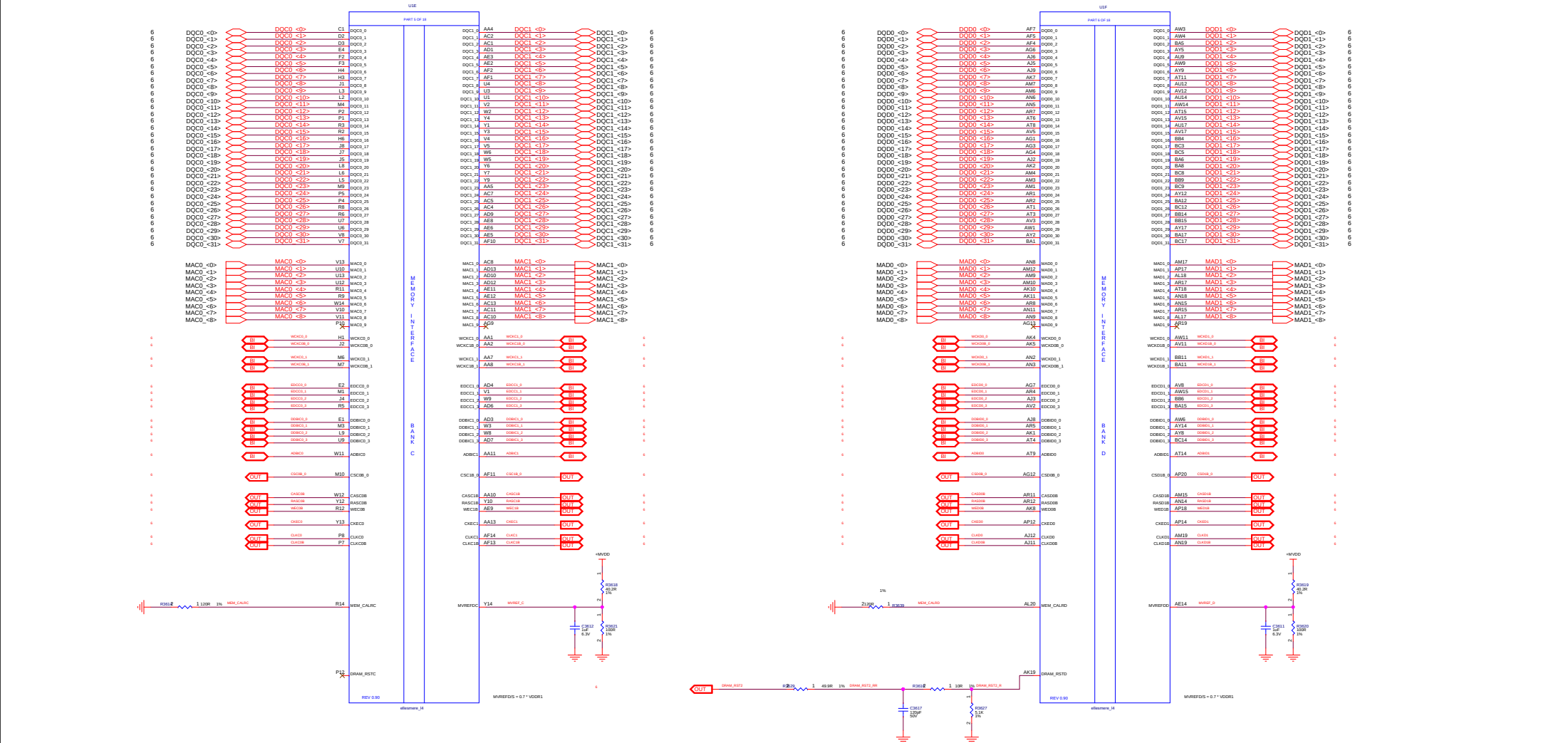


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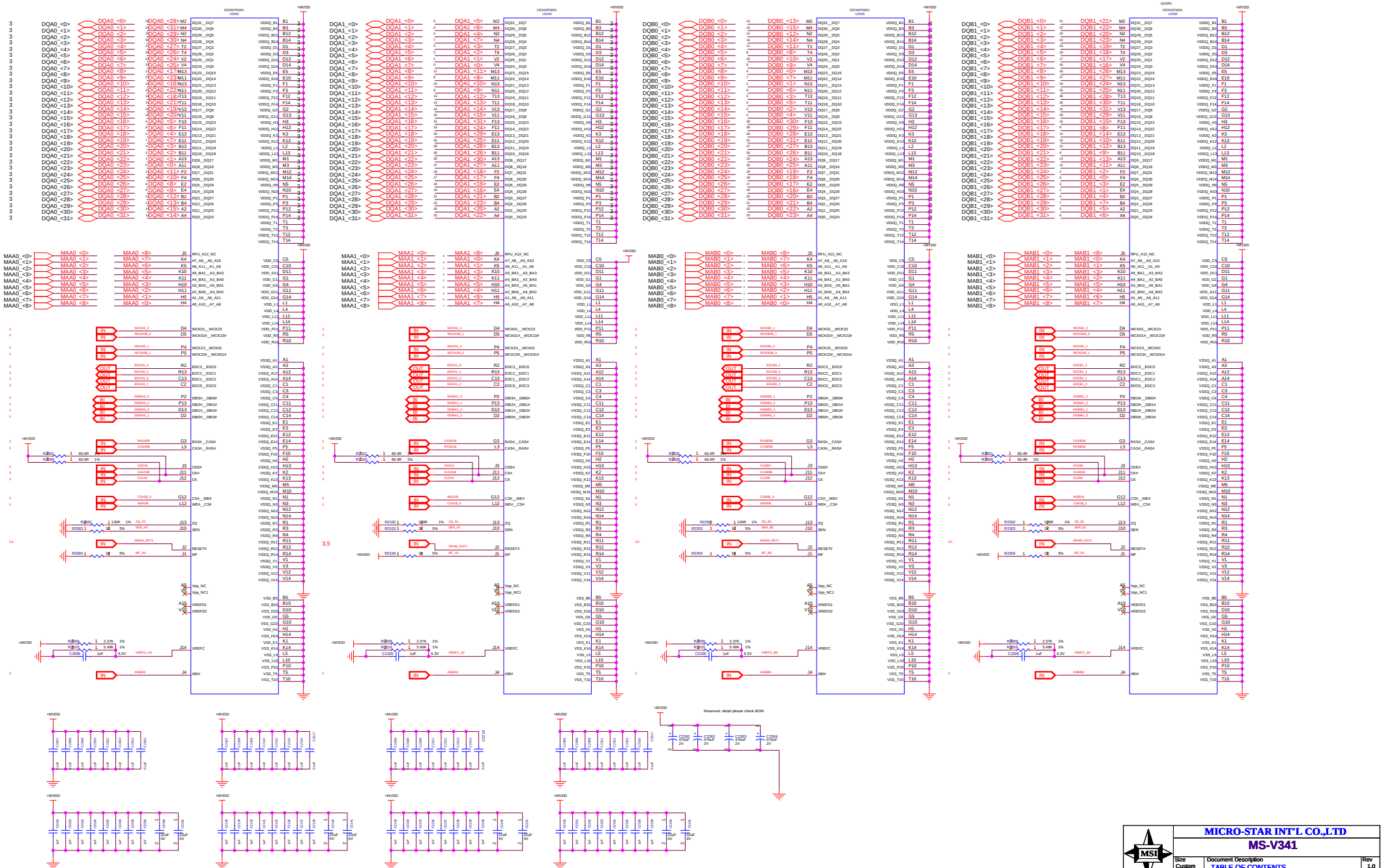
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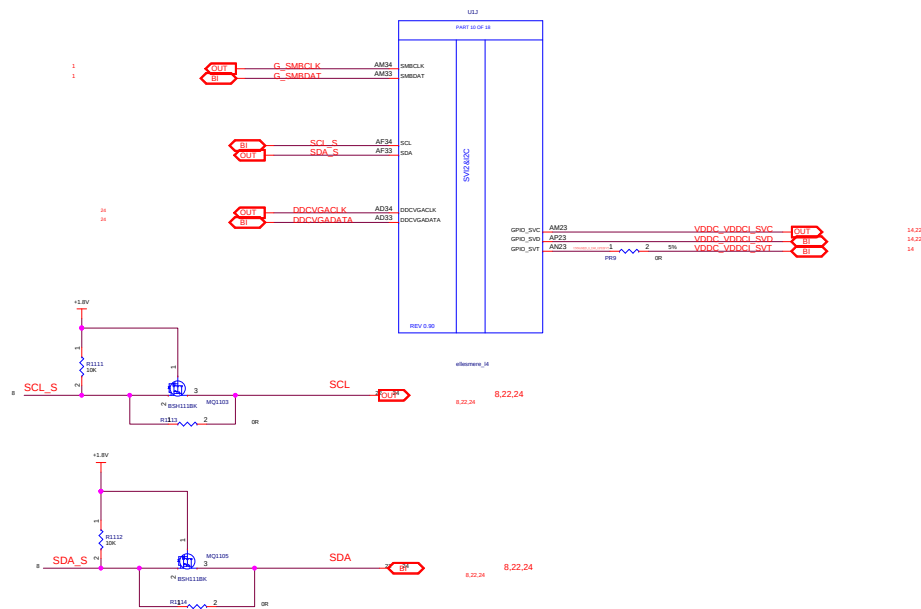
(4) ELLESMERE MEM INTERFACE CH C/D

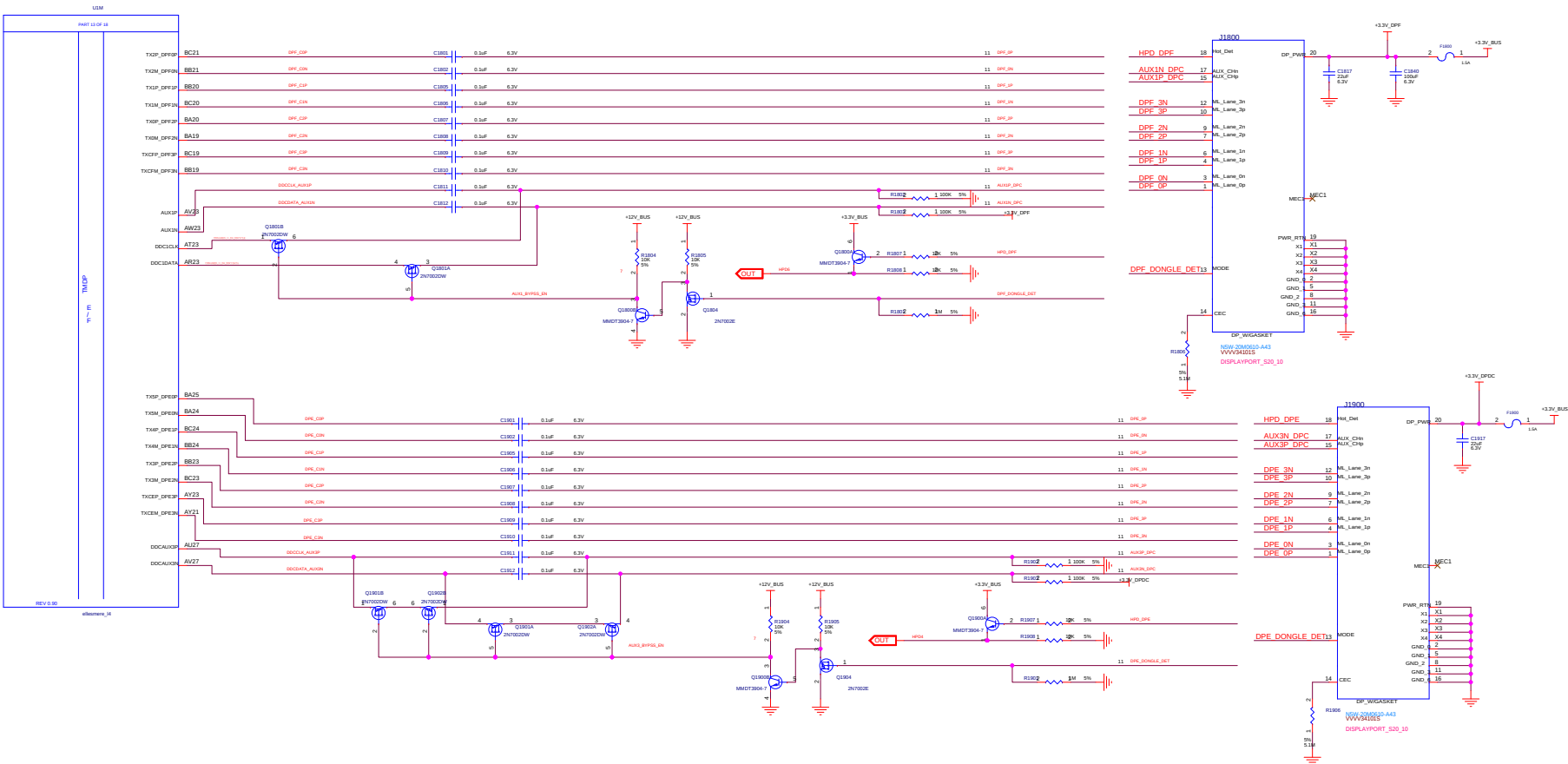


(5) GDDR5 MEMORY CH A/B

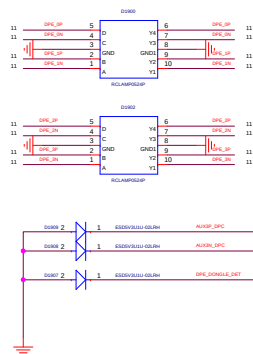
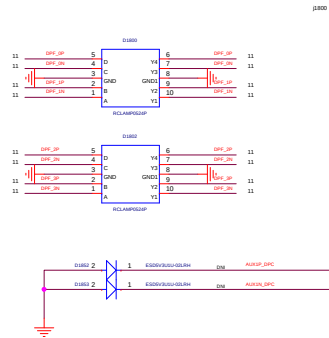


(8) ELLESMERE DAC1 LOCK

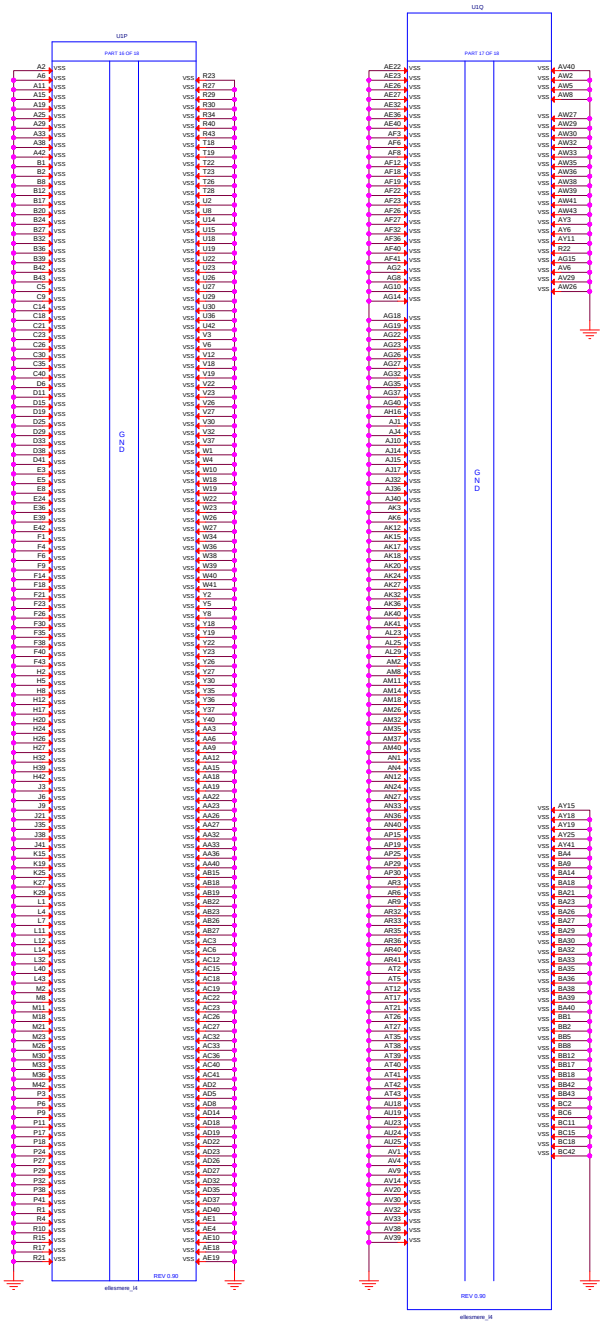




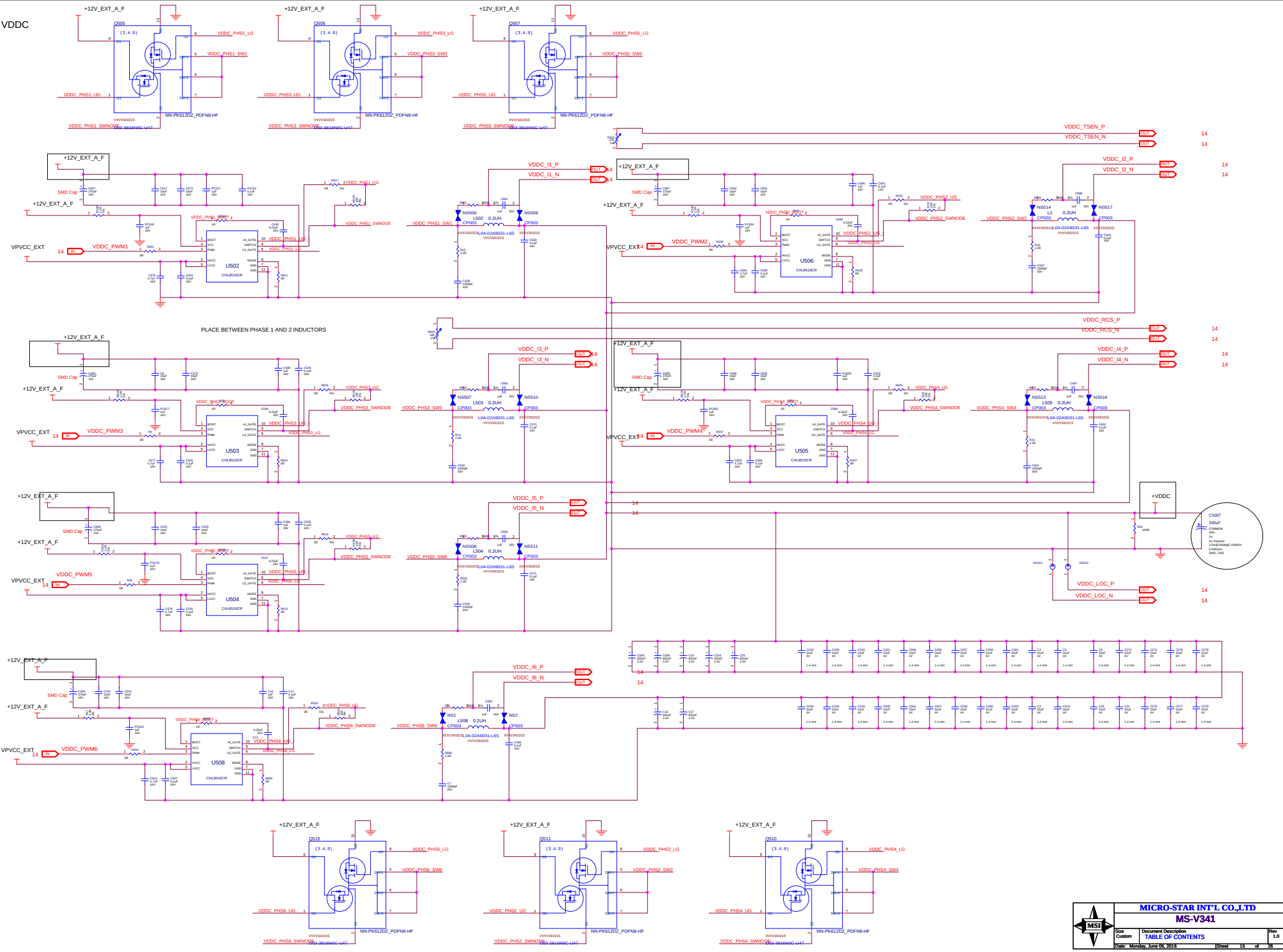
OPTIONAL ESD PROTECTION DIODES

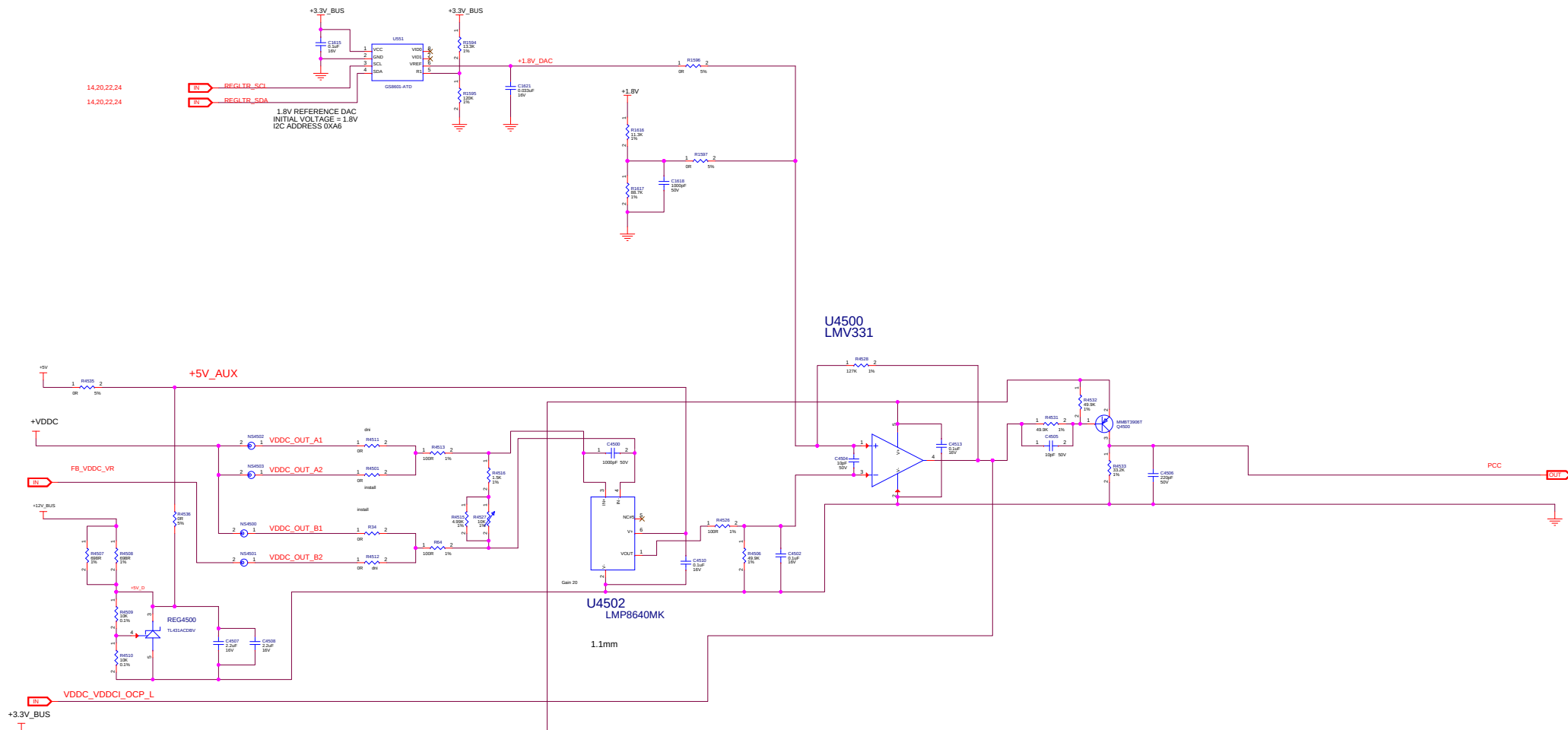


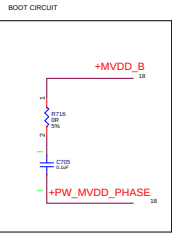
(13) ELLESMERE GROUND



VDDC







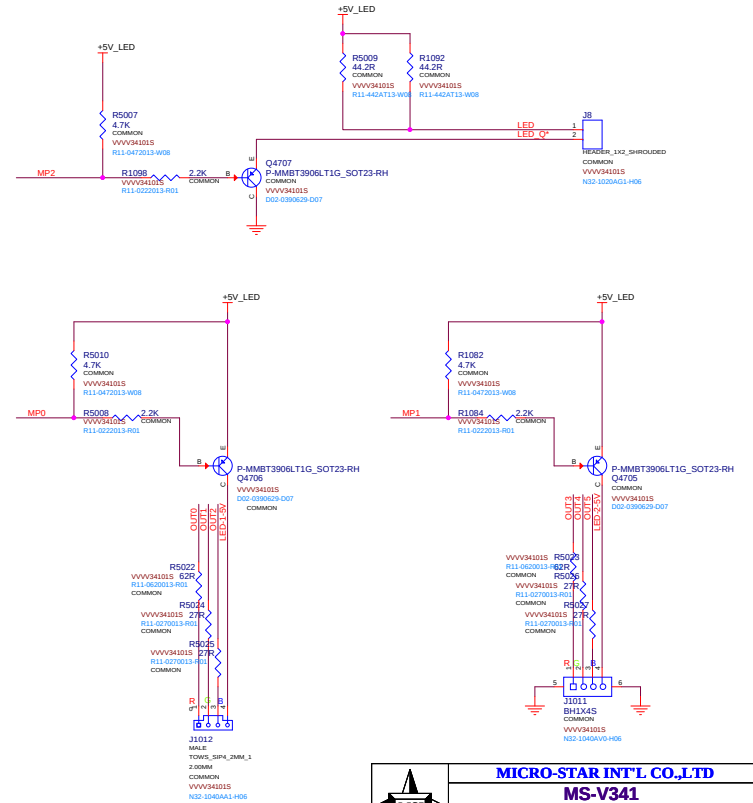
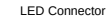
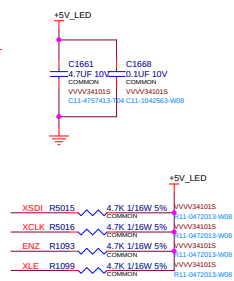
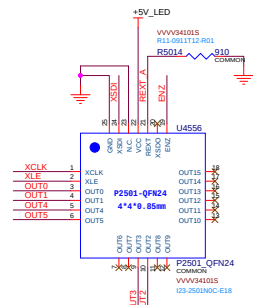
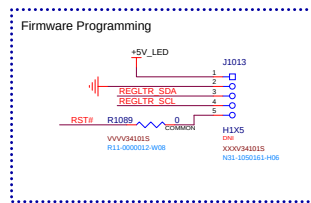
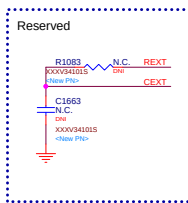
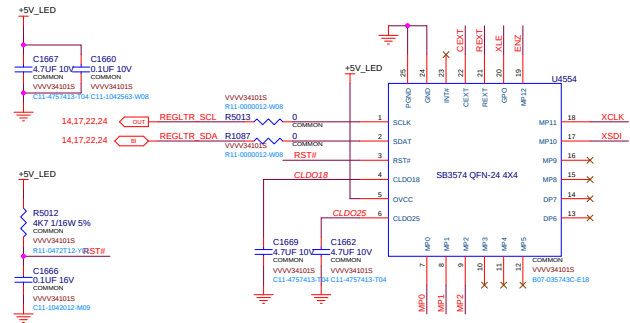
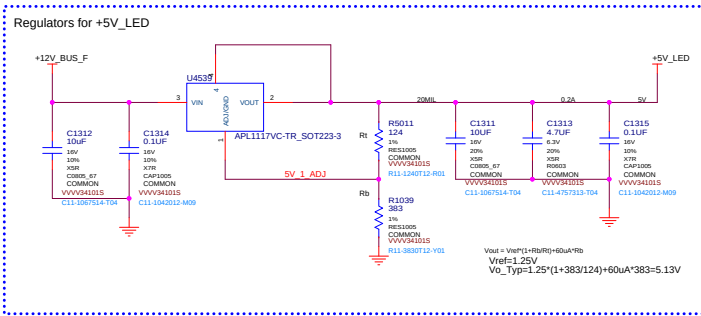
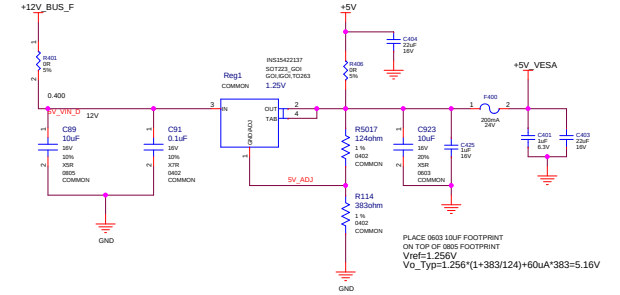
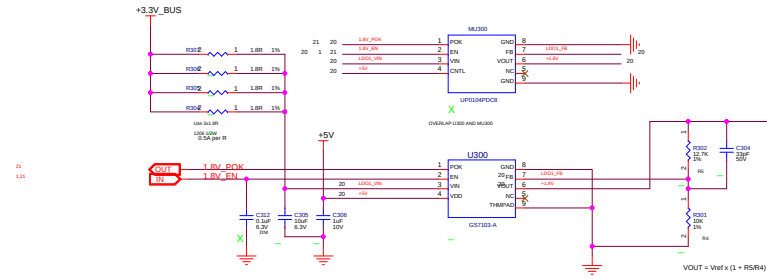
21



(18) SMALL RAIL REGULATORS , LED_CONTROLLER_ENE3574

LDO #1: VIN = 3.0V TO 3.6V MAX VOUT = +1.8V +/- 2% IOUT = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

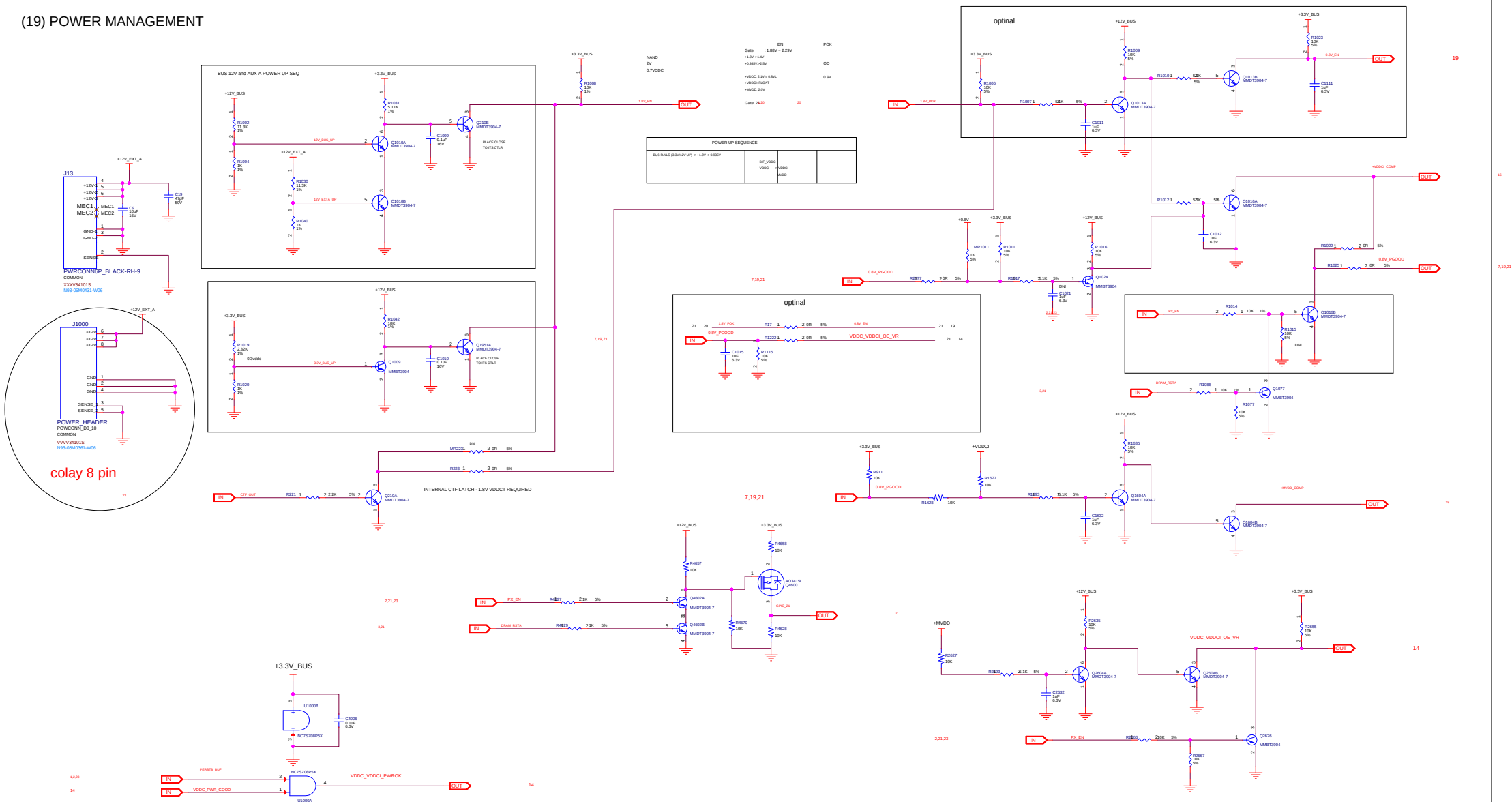
REGULATOR FOR +5V RAILS
IOUT MAX = 150mA

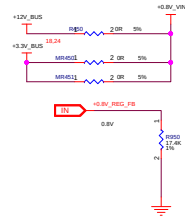
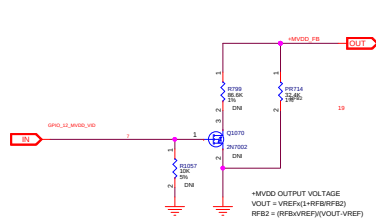
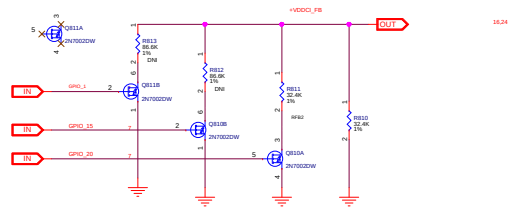
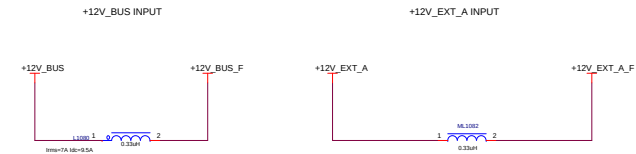
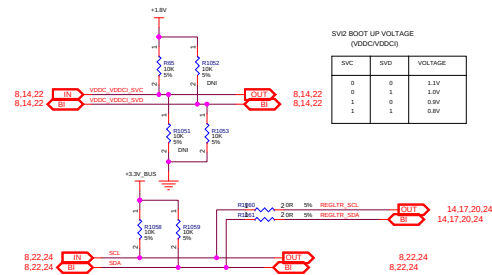


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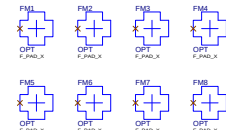
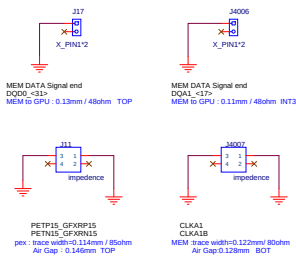
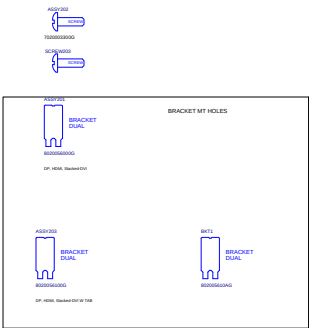
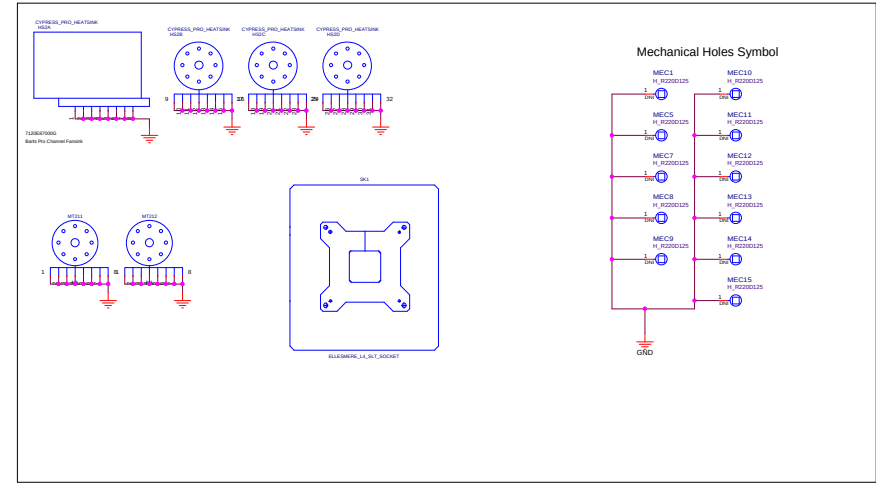
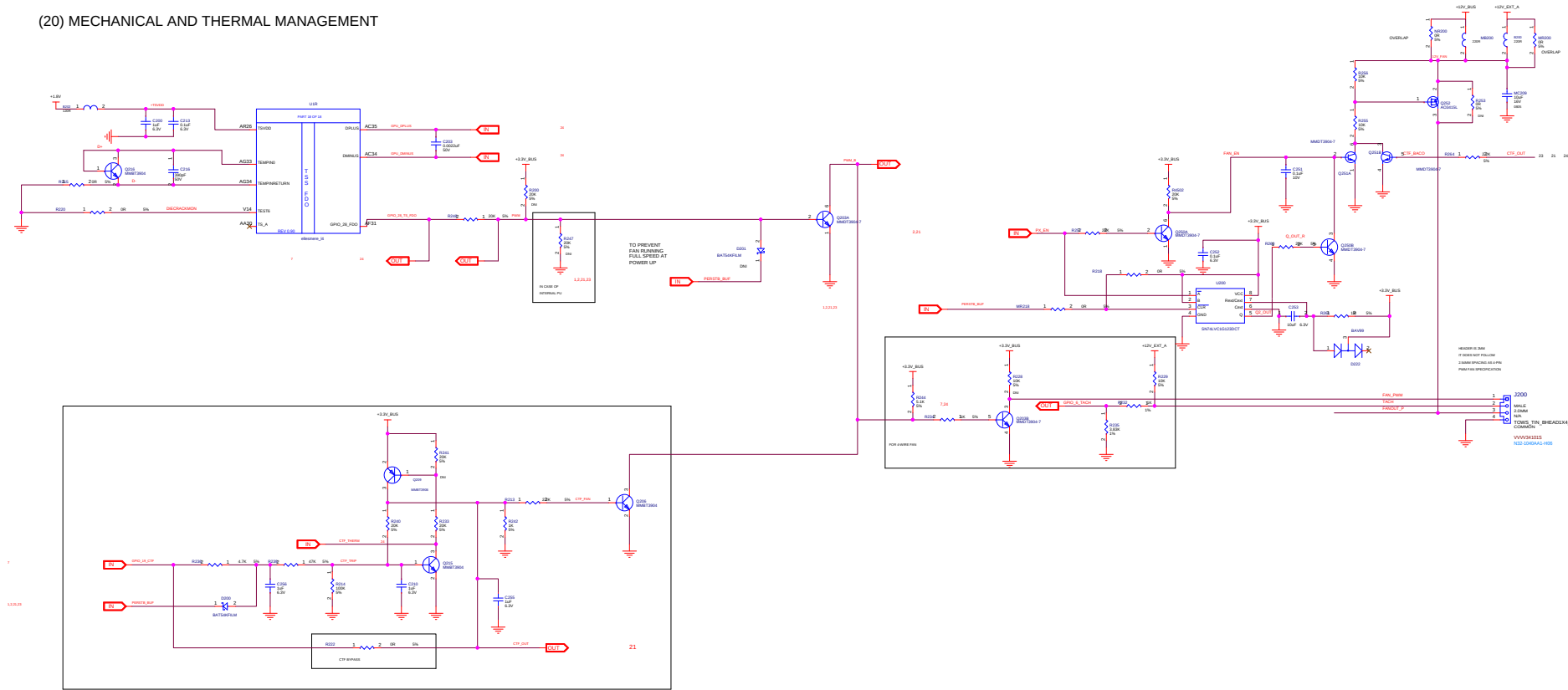
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(19) POWER MANAGEMENT

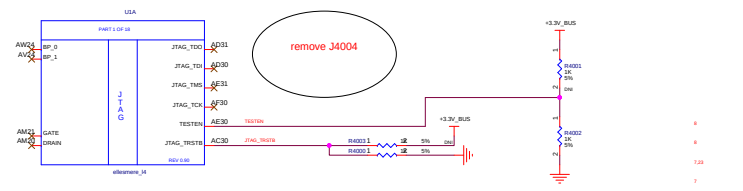




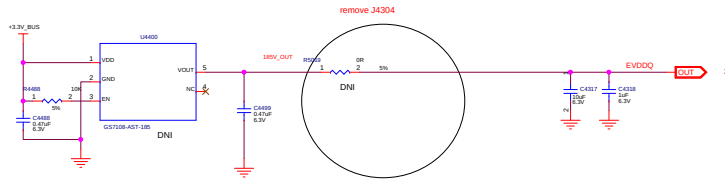
(20) MECHANICAL AND THERMAL MANAGEMENT



JTAG

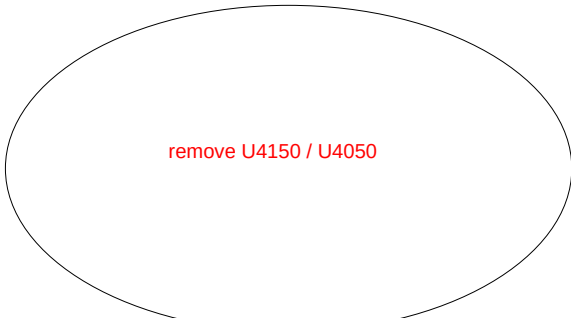


E-FUSE CAPABILITY
DEFAULT = GPIO-CONTROLLED
(MANUAL OPTION AS BACK-UP)



DIGITAL POTS

DIGITAL POTS



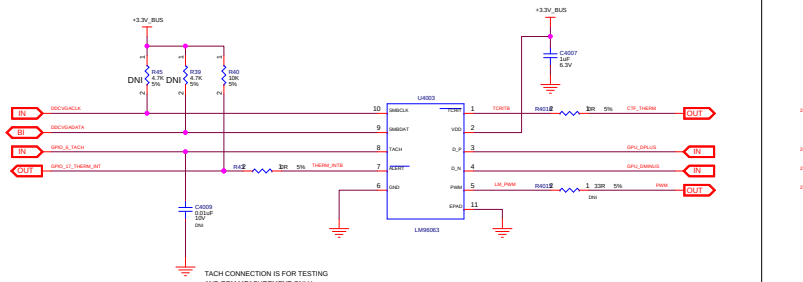
remove J4001/J4002

SWITCHES

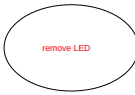
remove SW

BYPASS/DISABLE CTF
MAXIMIZE FAN

LM96163 FOR BACKUP THERMAL CONTROL



LED LIGHTS

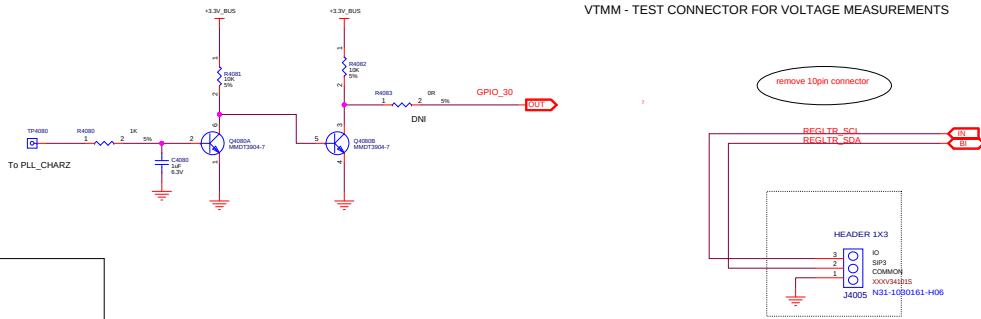


LED RED "ON" INDICATES CTF FAULT

LED GREEN "ON" INDICATES BACO MODE

LED ORANGE "ON" INDICATES MACO MODE

VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS



0	00A	00000000	
1	00B	00000000	1. Add EPIC3 for Aquatics, MSDC 2. Update MSDC driver signature
2	00C	00000000	MSDCH: -- add standby regulator R13B00 ~ R13B07 -- add pull-down inductor L13B00 ~ L13B07 Remove C405, C403, V19A00, C402, C414, R406

- Page7: remove J2,J3
- Page9: enable DVI
- Page10: DP change to HDMI
- Page15: change dual-N MOS
- Page16: change dual-N MOS
- Page18: change dual-N MOS
- Page20: Add LED circuit
- Page21: colay 8pin power connecter
- Page24: remove debug circuit