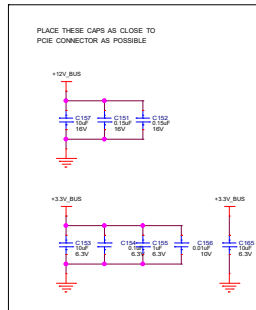
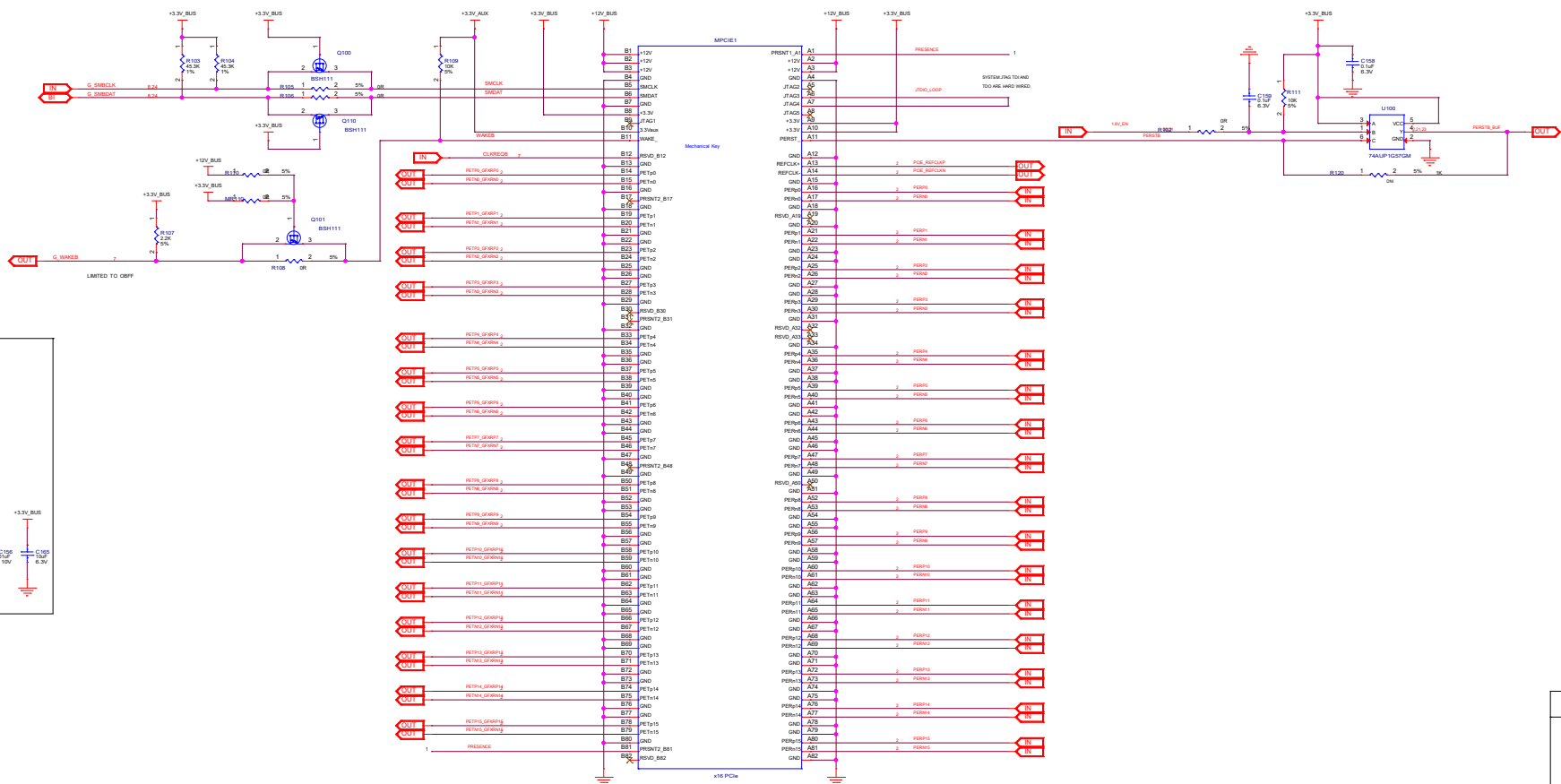
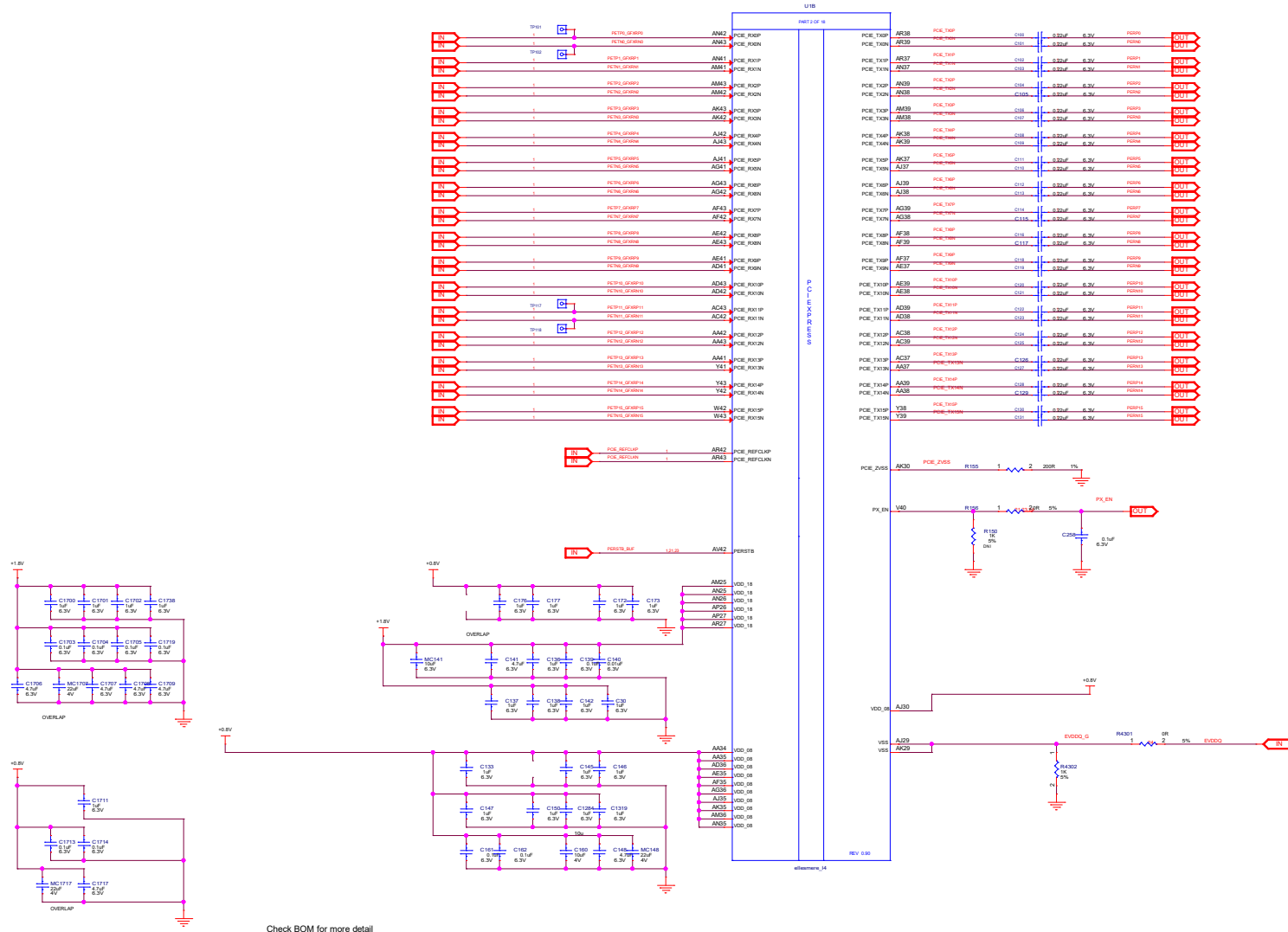


(1) PCI-EXPRESS EDGE CONNECTOR

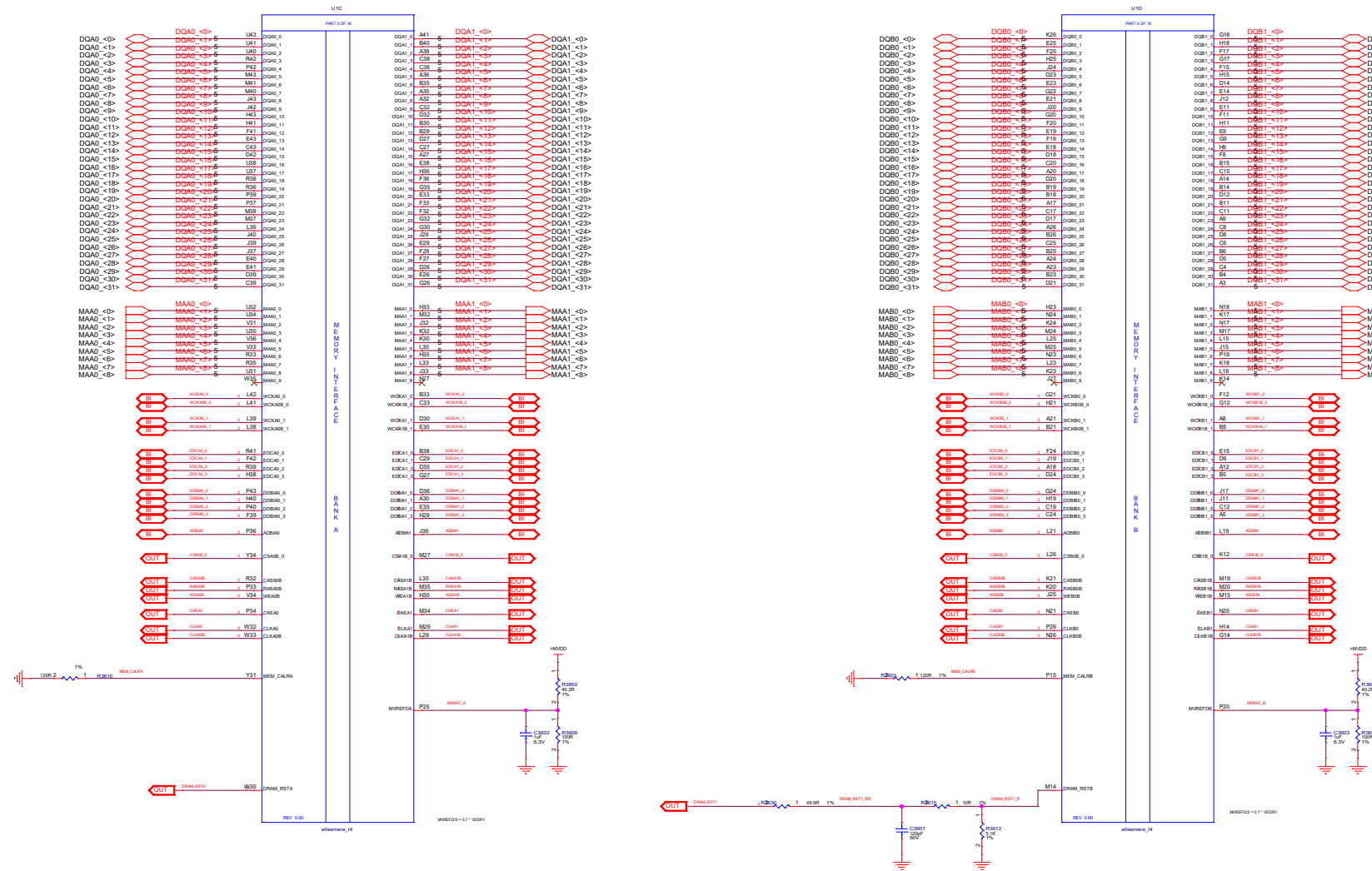


SYMBOL LEGEND	
DNI	DO NOT INSTALL
S or #	ACTIVE LOW
BUO	BRING UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

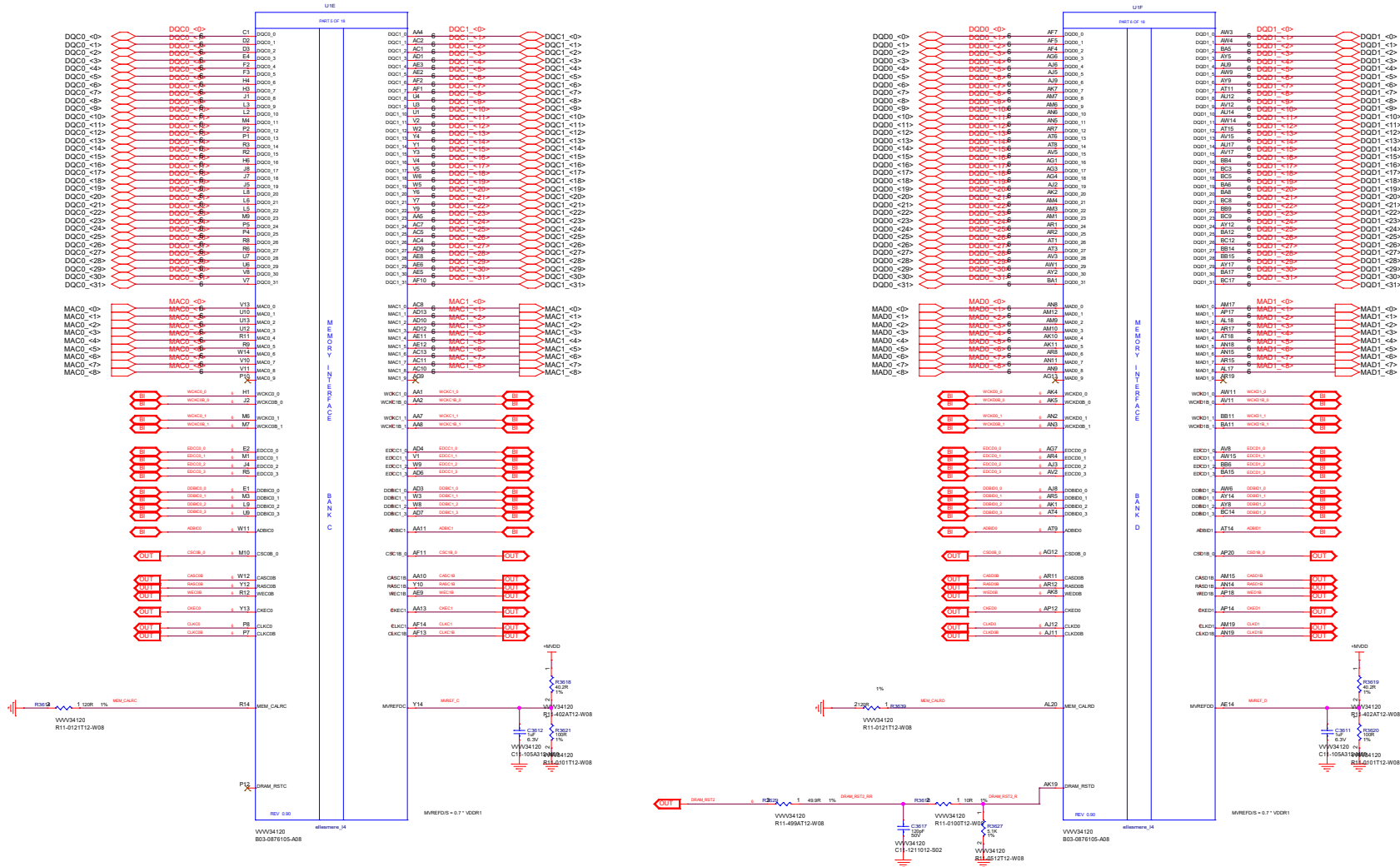
(2) ELLESMERE PCIE INTERFACE



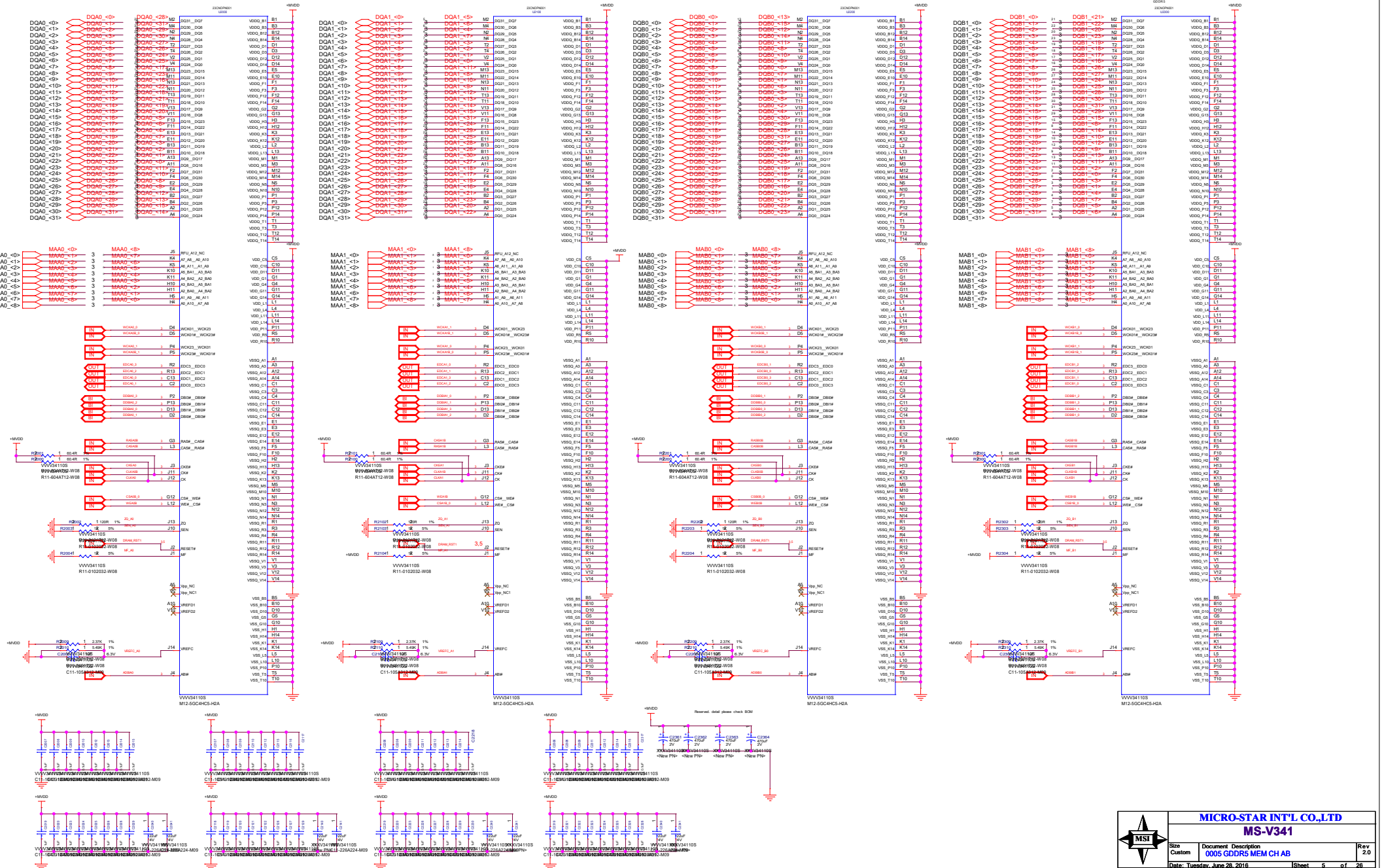
(3) ELLESMERE MEM INTERFACE CH A/B



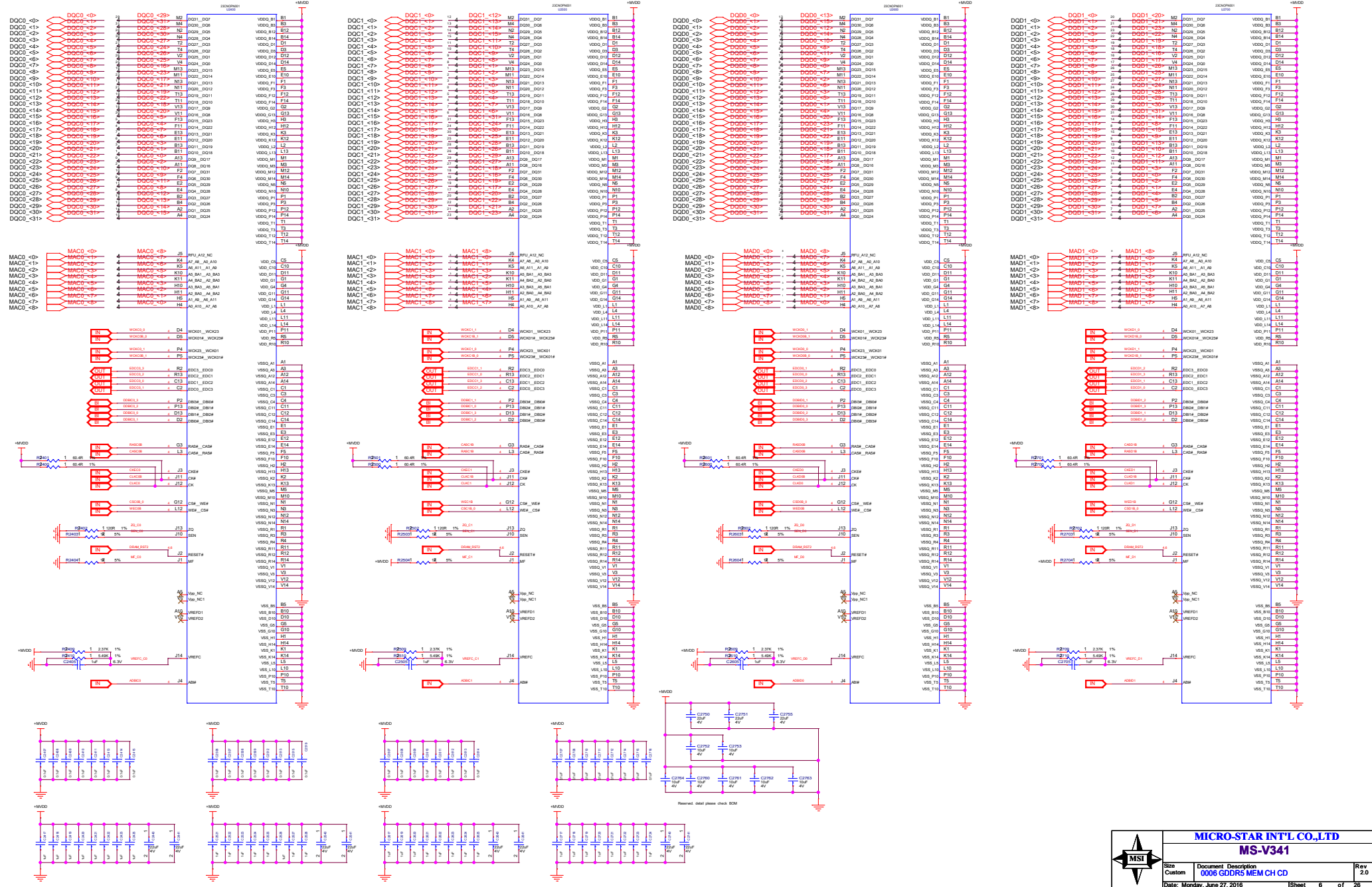
(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B



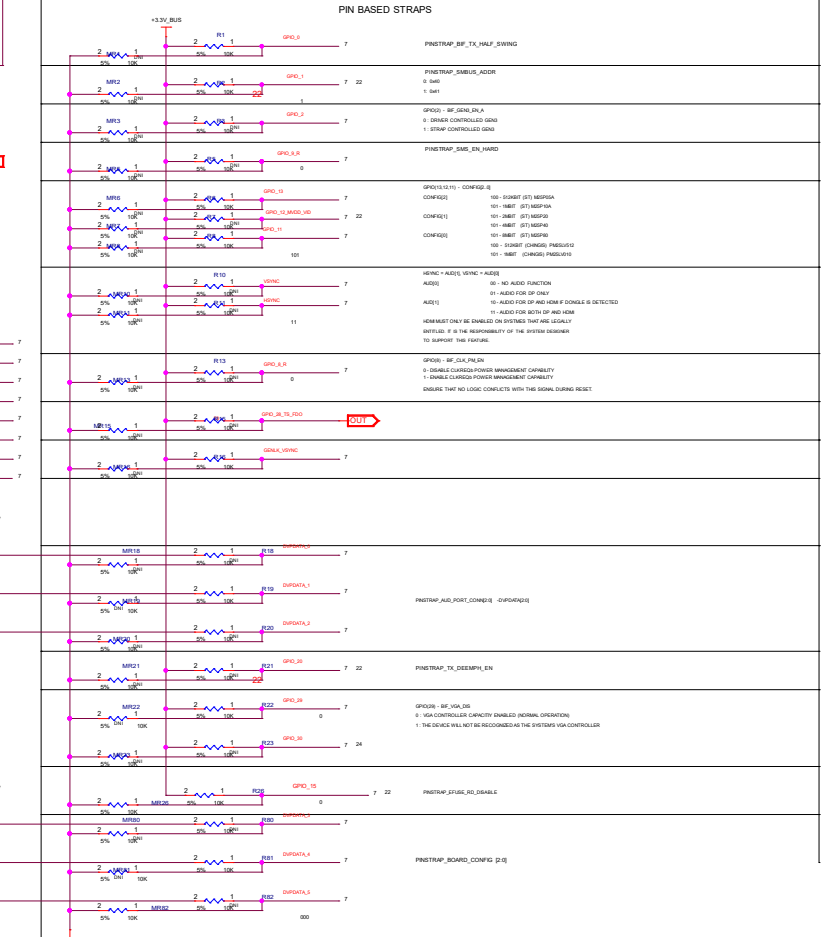
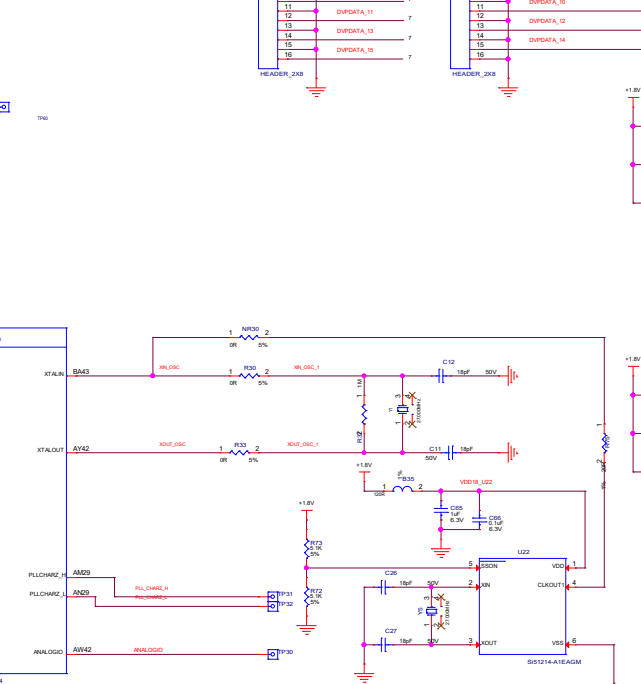
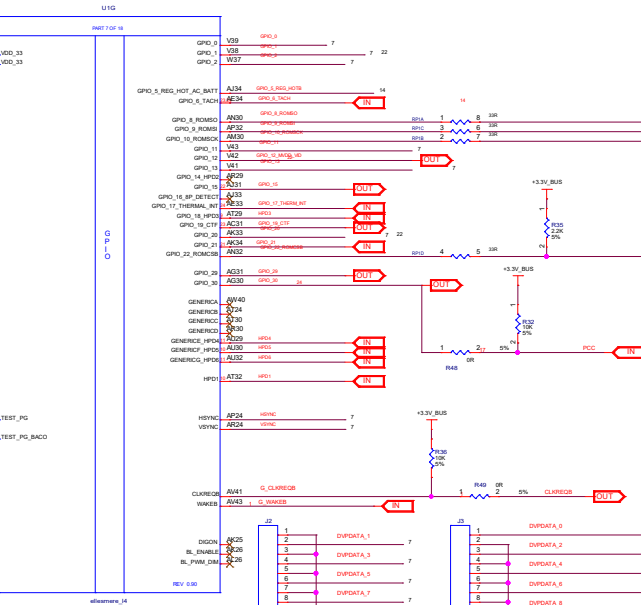
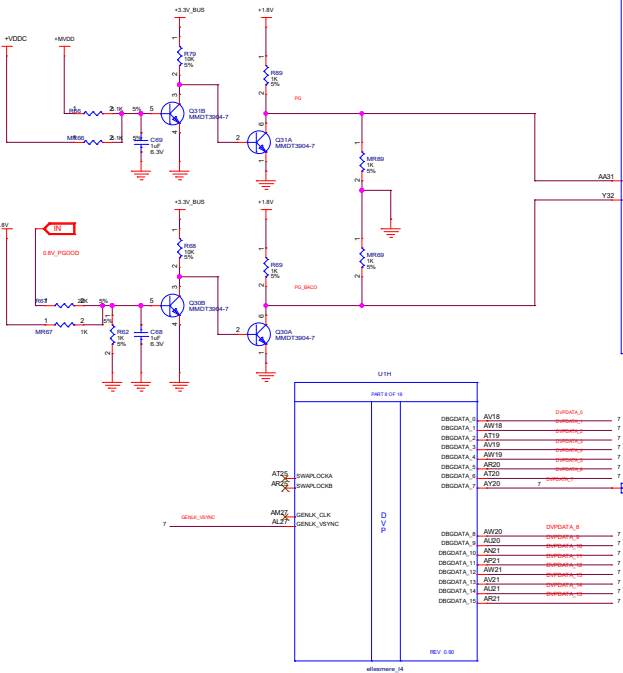
(6) GDDR5 MEMORY CH C/D



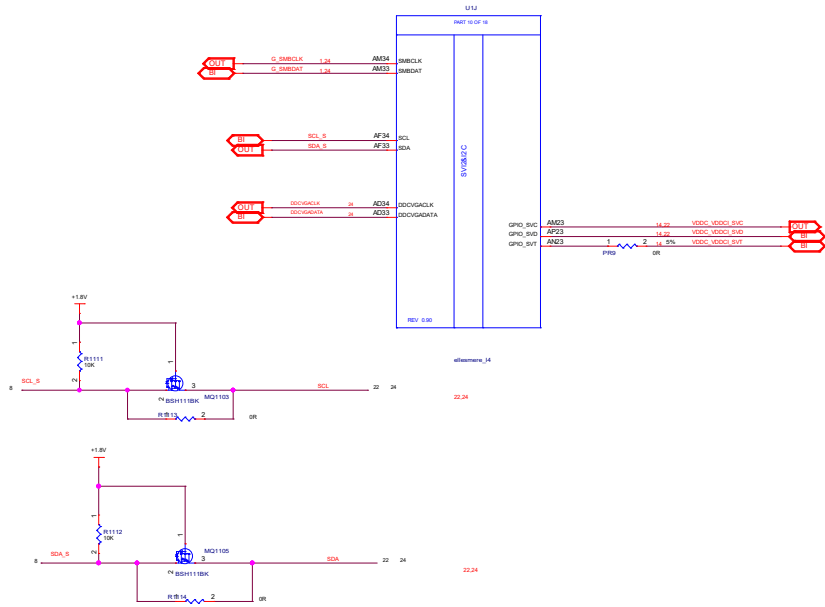
(7) ELLESMERE GPIO STRAP CF XTAL

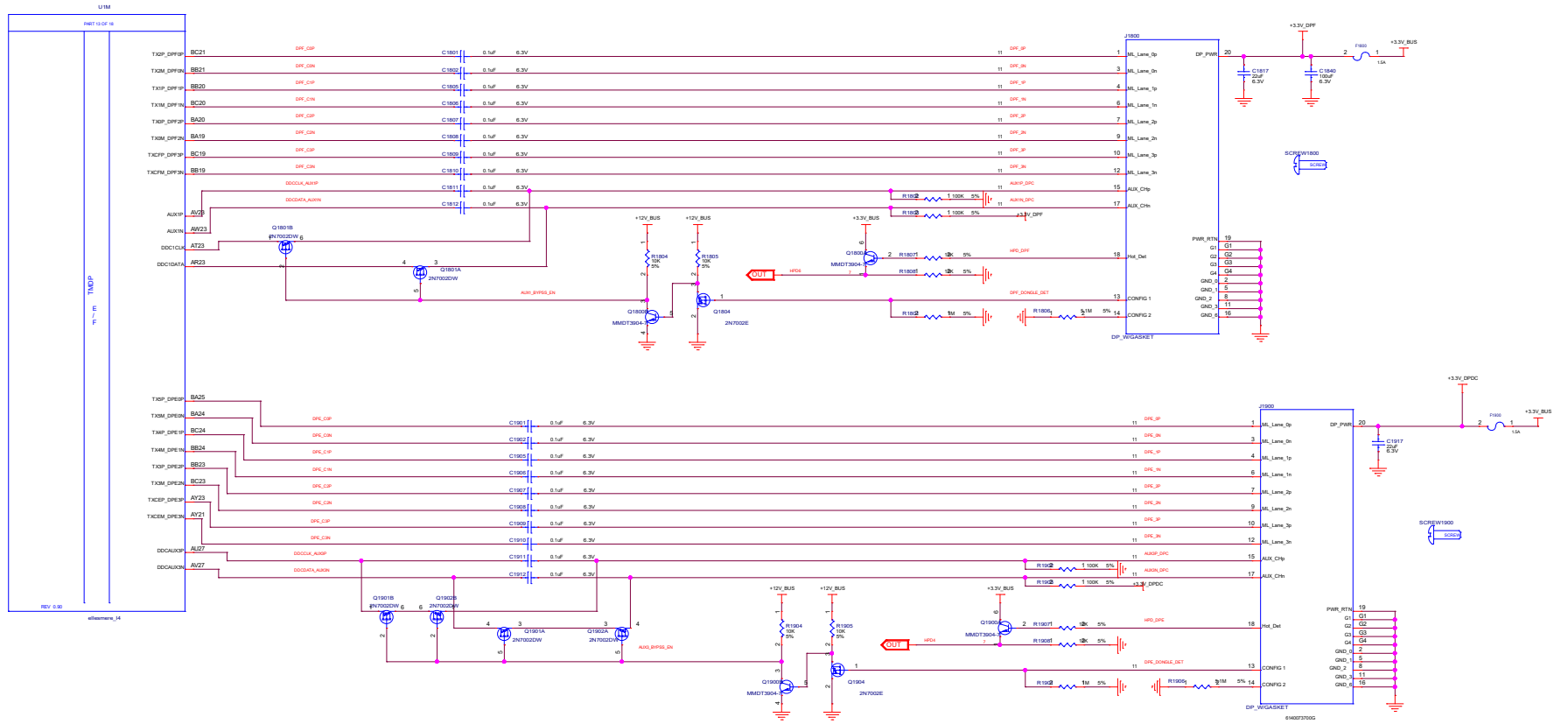
I2C ADDRESS			FUNCTION	DEVICE

I2C ADDRESS			FUNCTION	DEVICE
0x48			EXT TEMP SENSOR	LM93C83

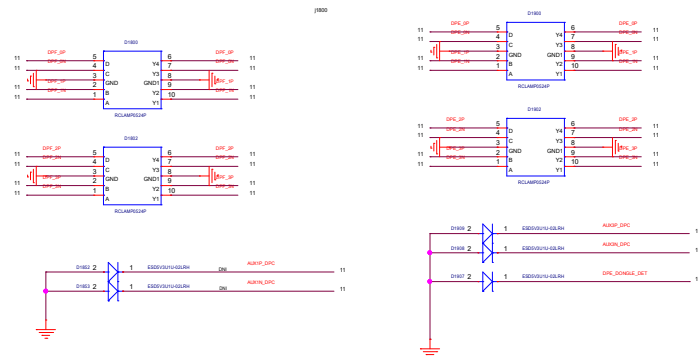


(8) ELLESMERE DAC1 LOCK





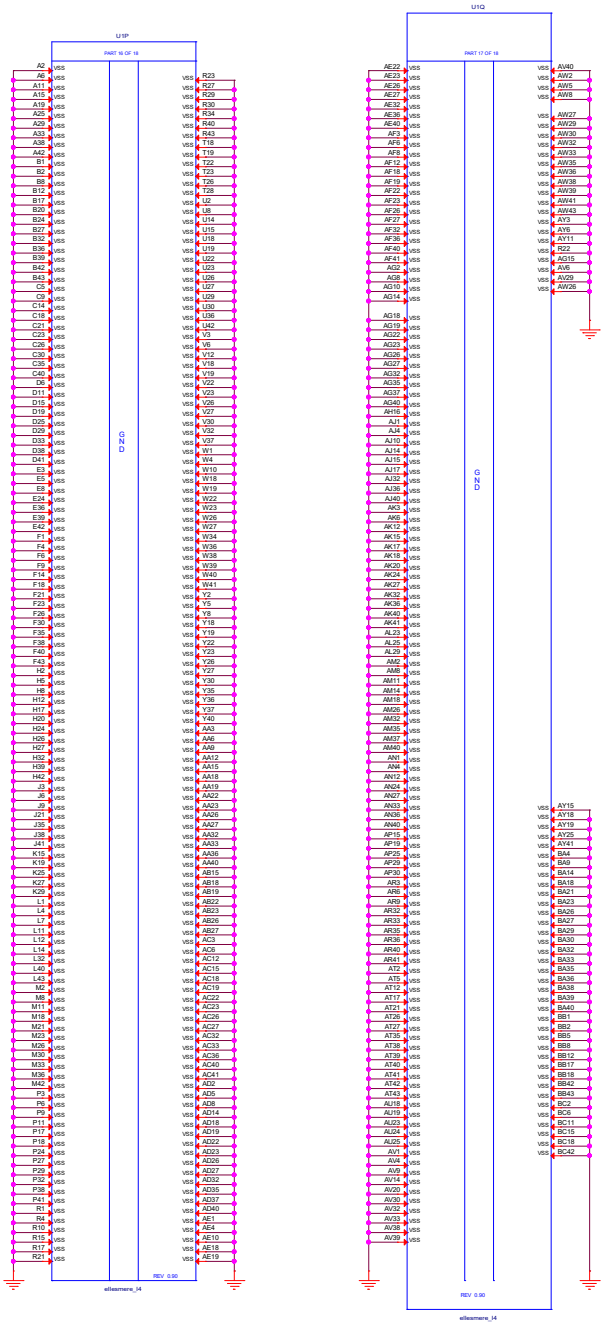
OPTIONAL ESD PROTECTION DIODES

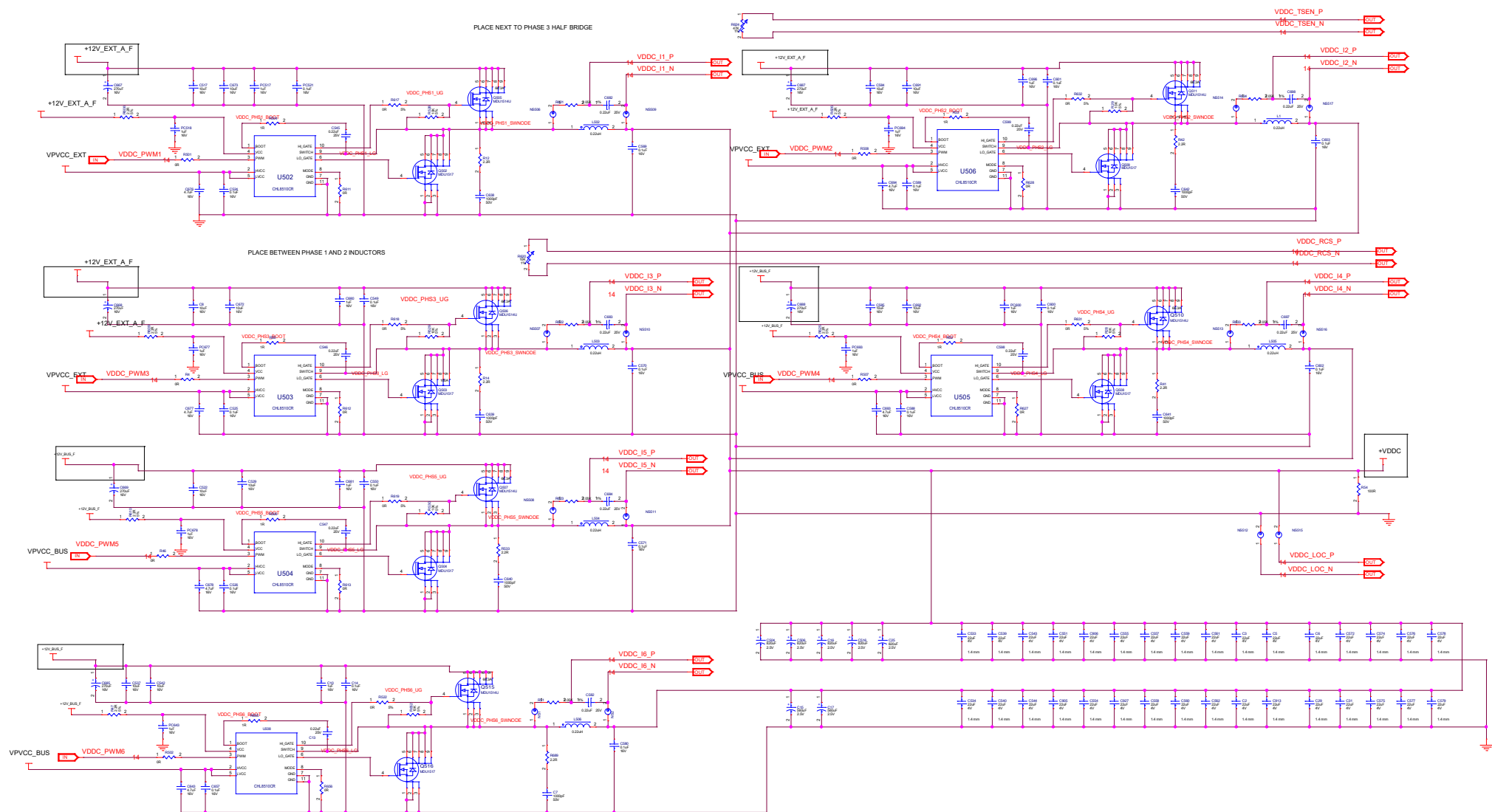


17ci203

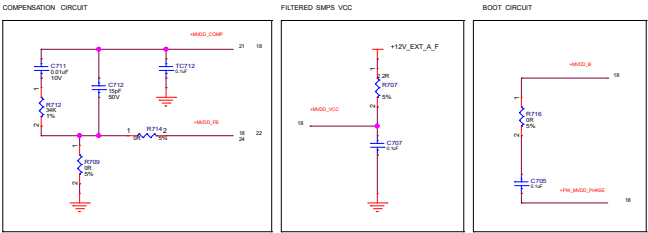
	MICRO-STAR INT'L CO.,LTD		
	MS-V341		
	Size Custom	Document Description 0011 ELLESHERE TMDPEF DP DP	Rev 2.0
	Date: Monday, June 27, 2016		Sheet 11 of 26

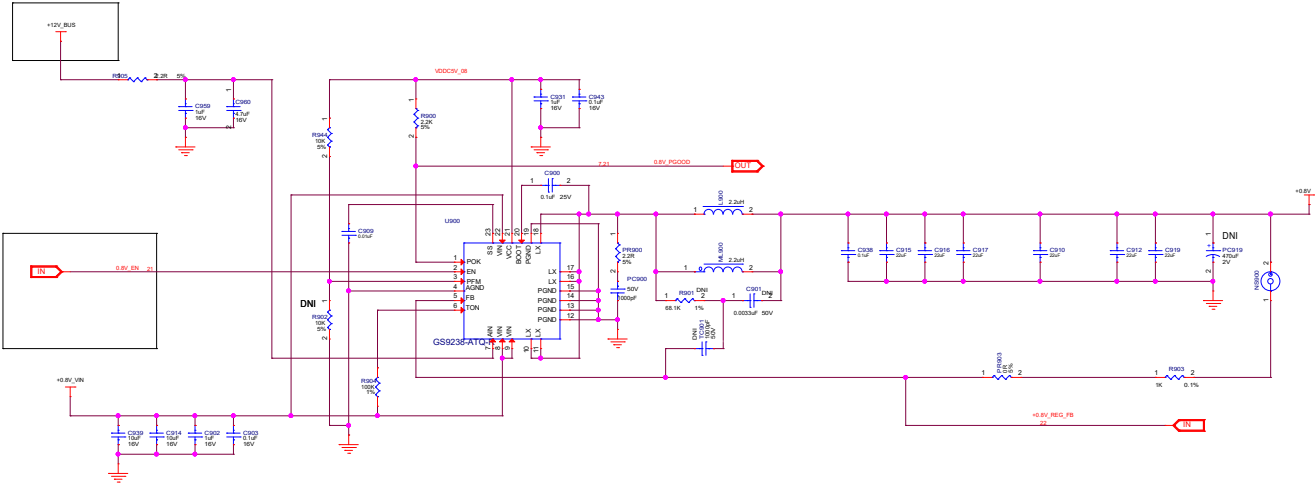
(13) ELLESMERE GROUND





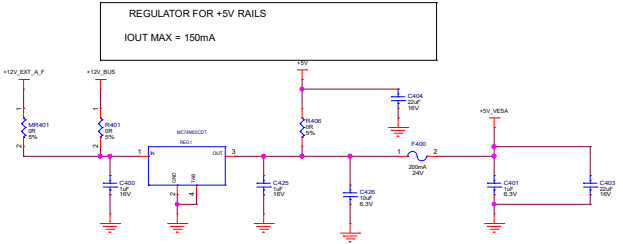
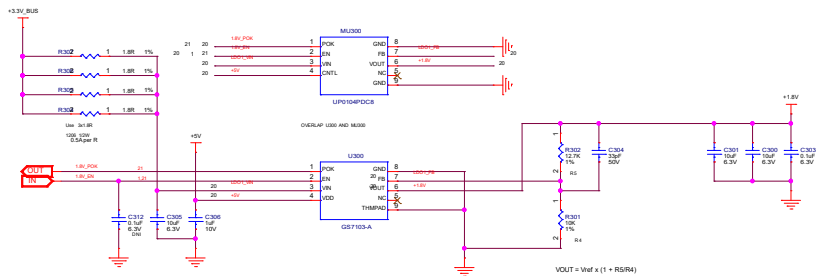
17ci203





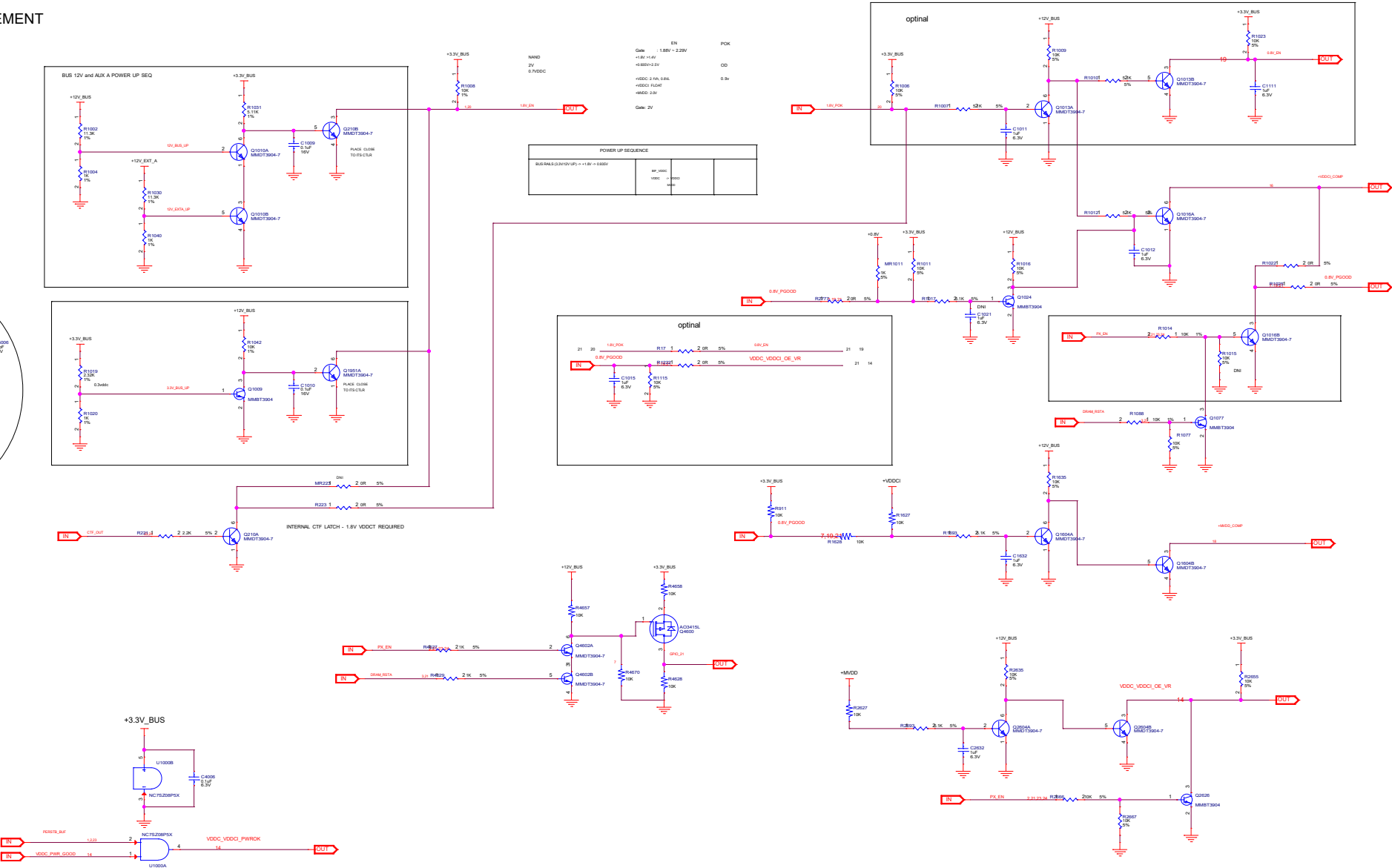
(18) SMALL RAIL REGULATORS

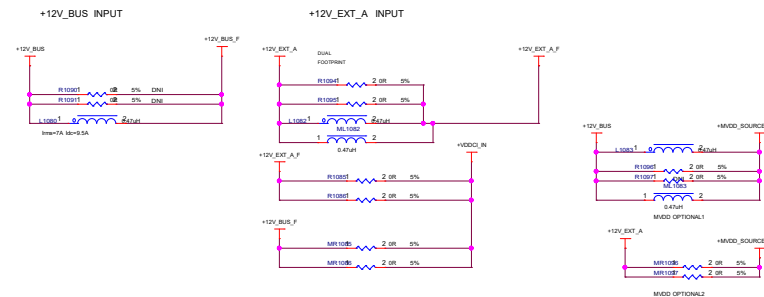
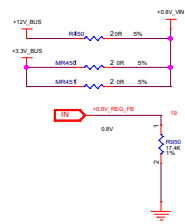
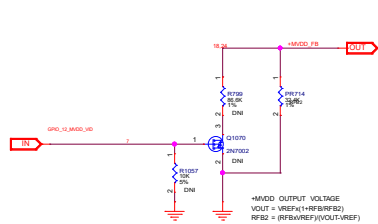
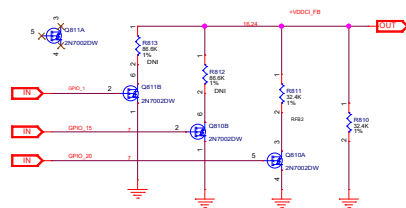
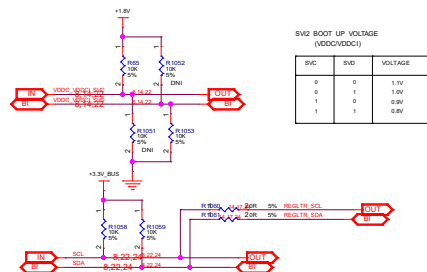
LDO #1:	VIN = 3.0V TO 3.6V MAX	VOUT = +1.8V +/- 2%	IOUT = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING			



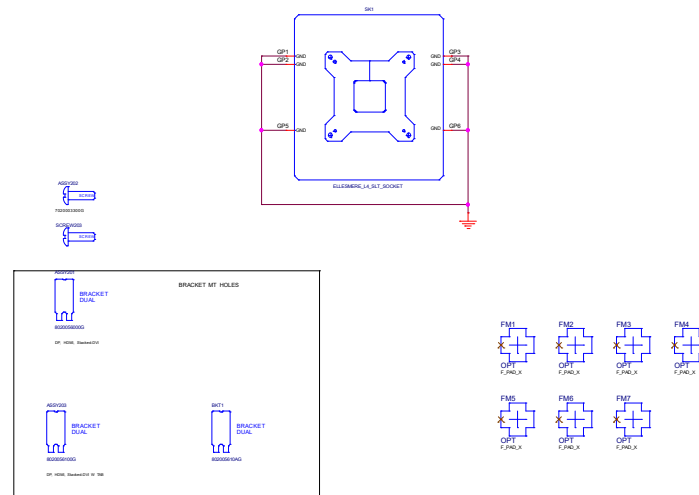
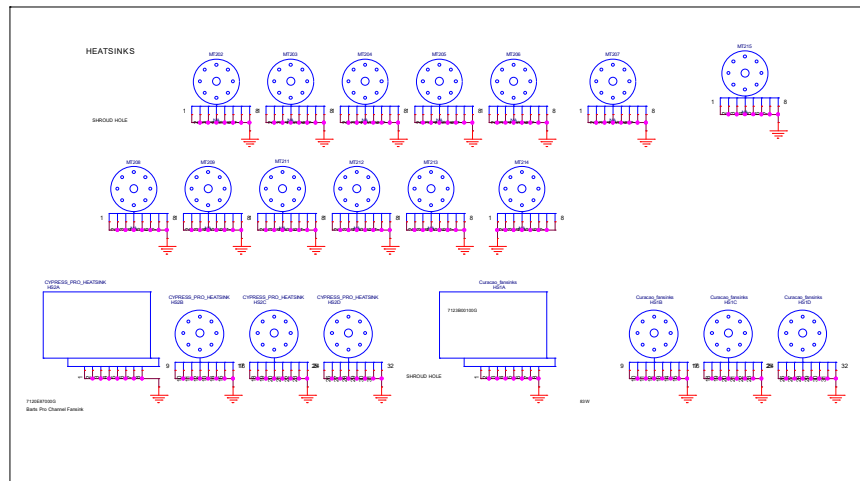
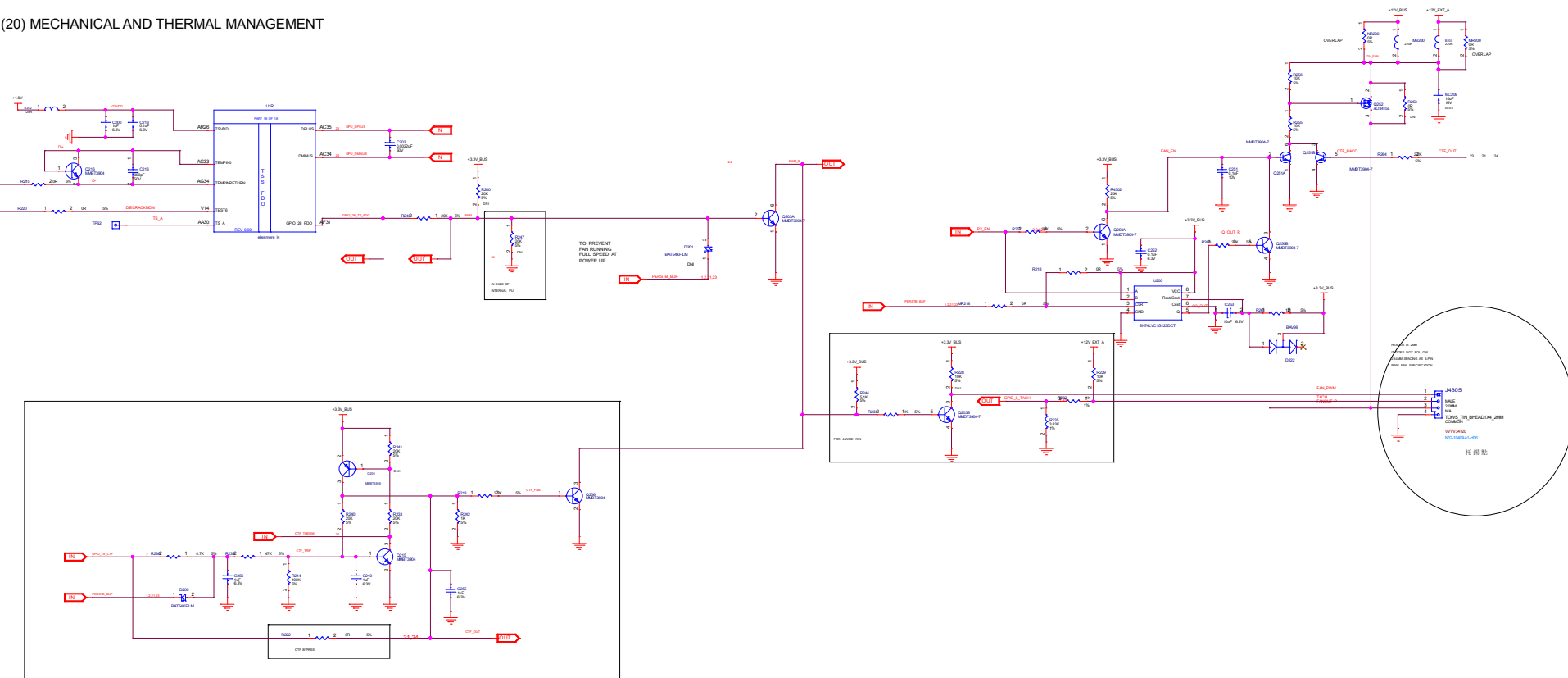
MICRO-STAR INT'L CO.,LTD			
MS-V341			
Size	Document Description	Rev	
Custom	0020 SMALL RAIL REGULATORS	2.0	
Date: Monday, June 27, 2016		Sheet 20 of 26	

(19) POWER MANAGEMENT

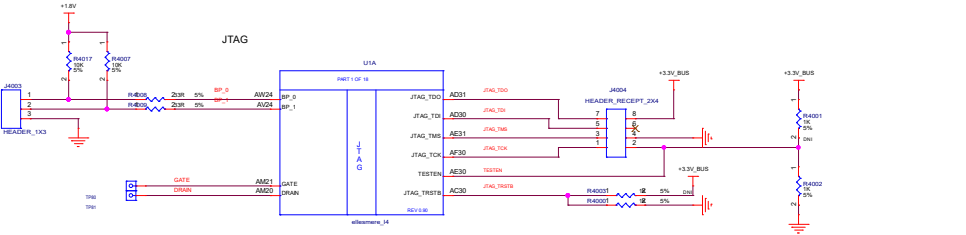




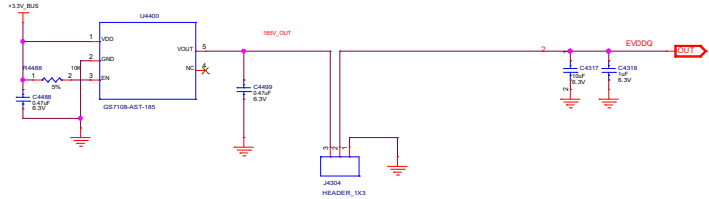
(20) MECHANICAL AND THERMAL MANAGEMENT



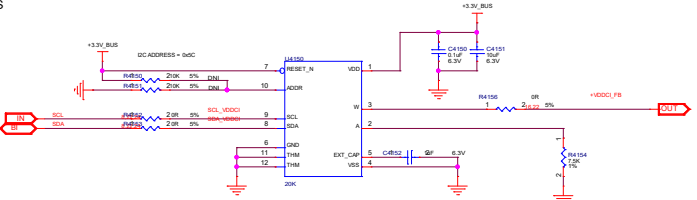
(21) DEBUG CIRCUITS



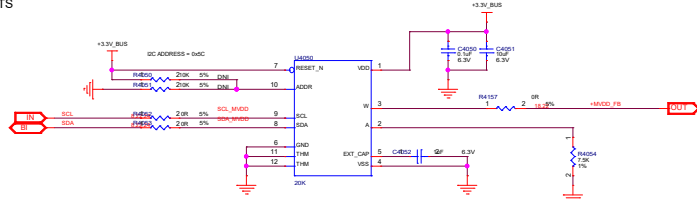
E-FUSE CAPABILITY
DEFAULT = GPIO-CONTROLLED
(MANUAL OPTION AS BACK-UP)



DIGITAL POTS



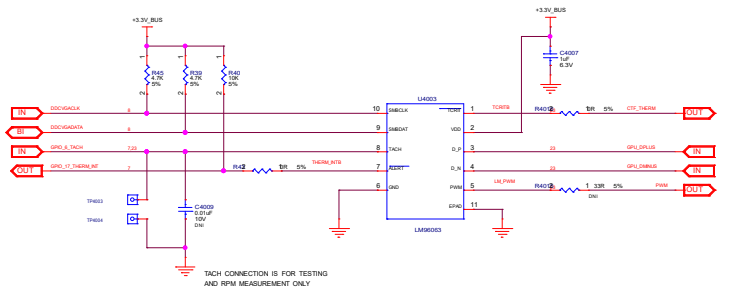
DIGITAL POTS



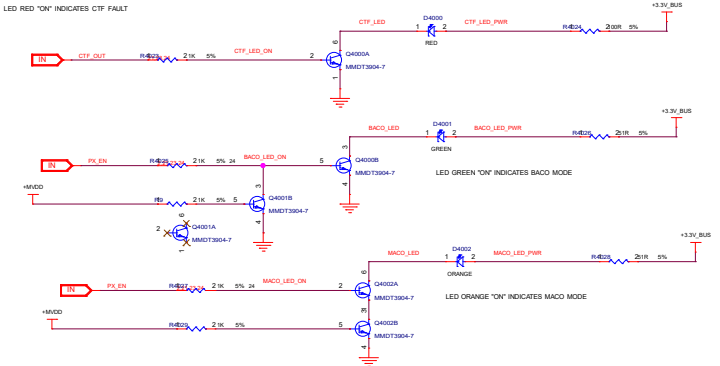
SWITCHES



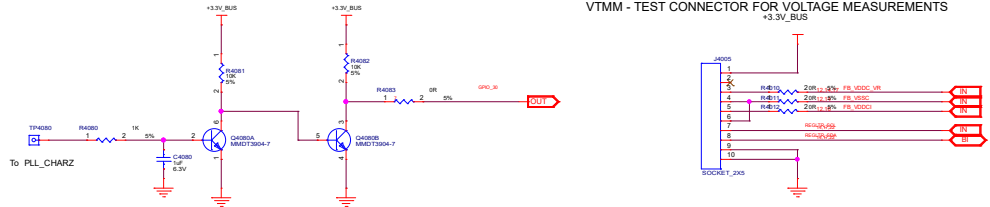
LM06163 FOR BACKUP THERMAL CONTROL



LED LIGHTS



VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS



23: 反向 6 PIN
26: 1*4 插孔