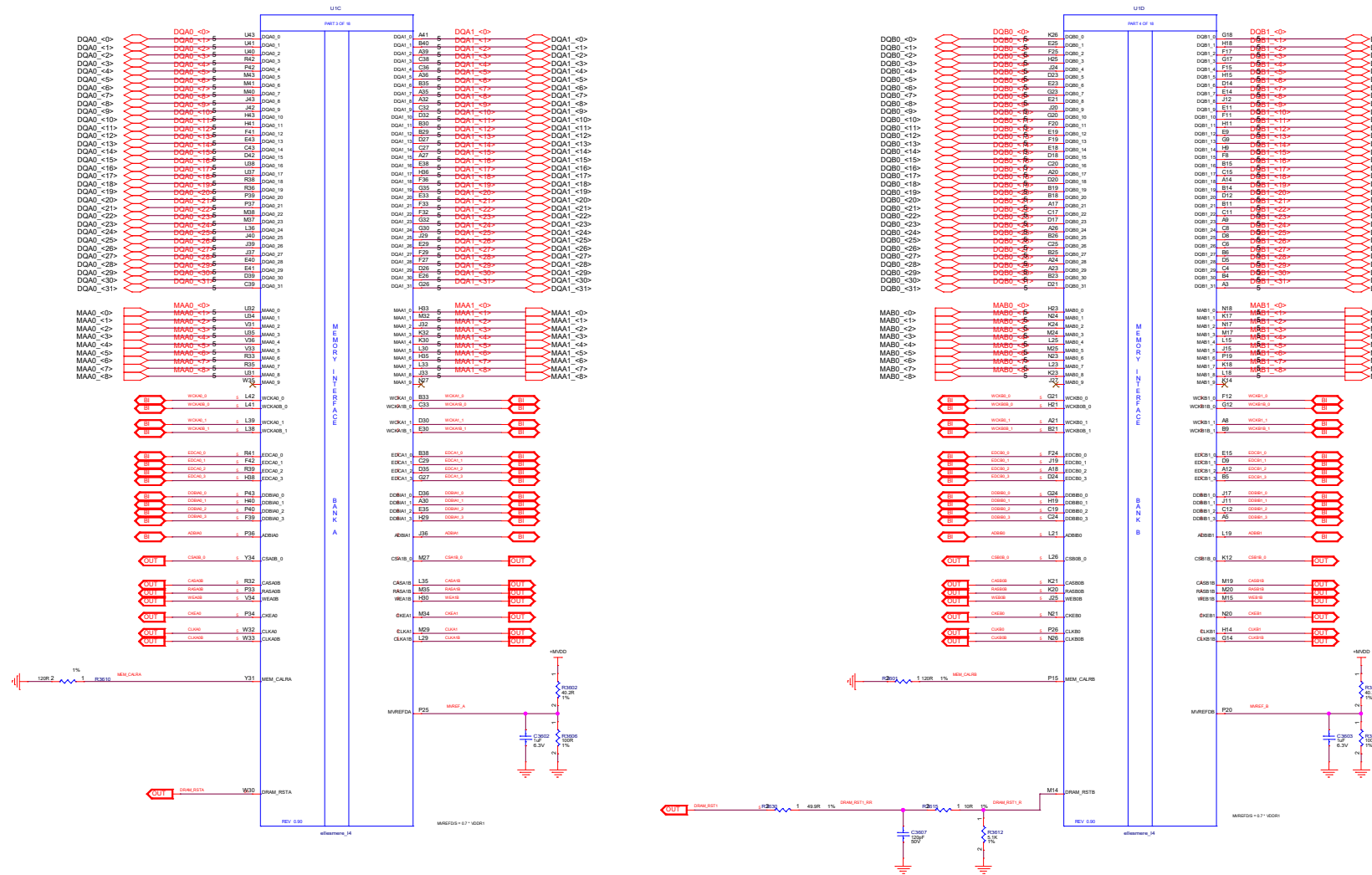
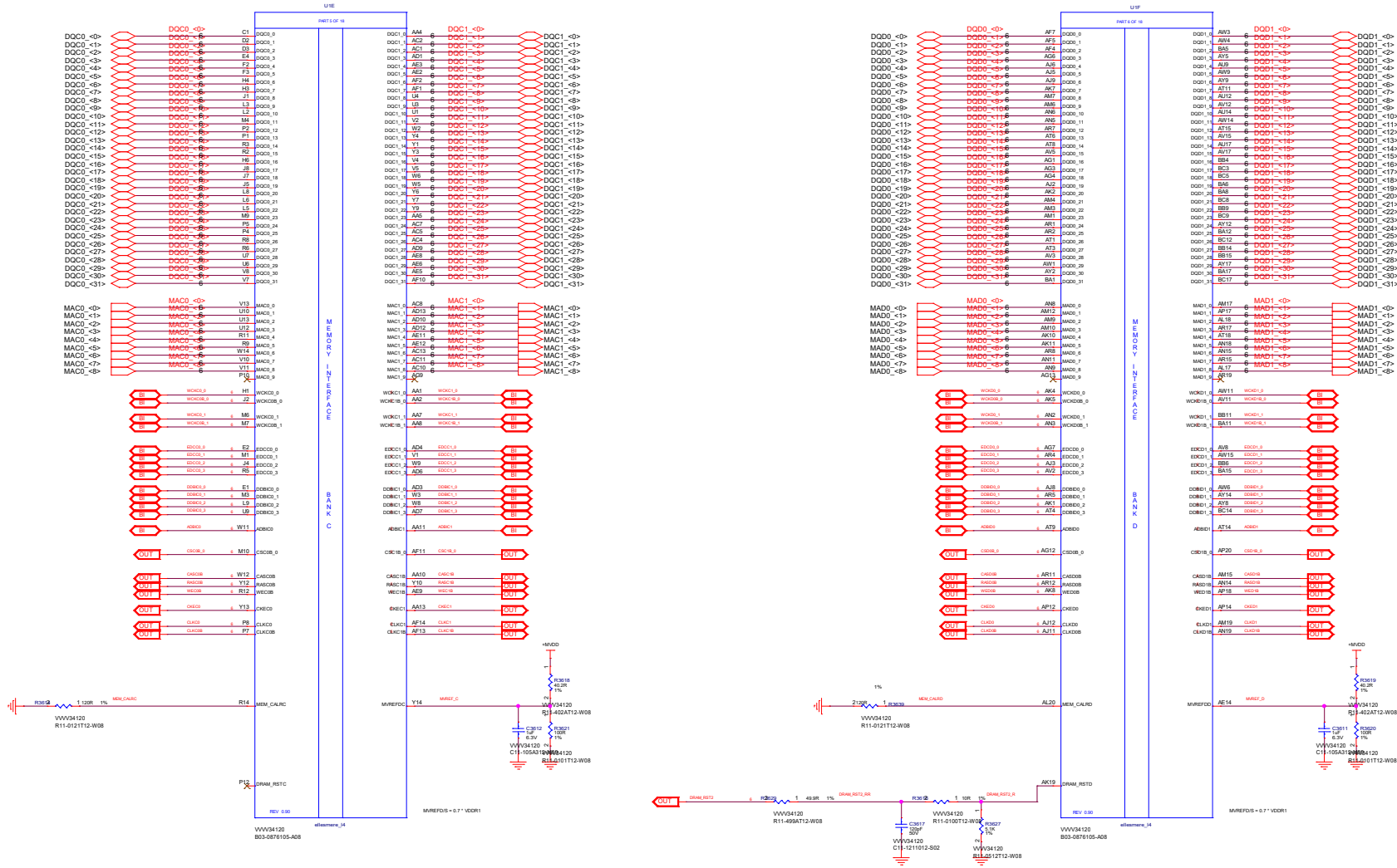


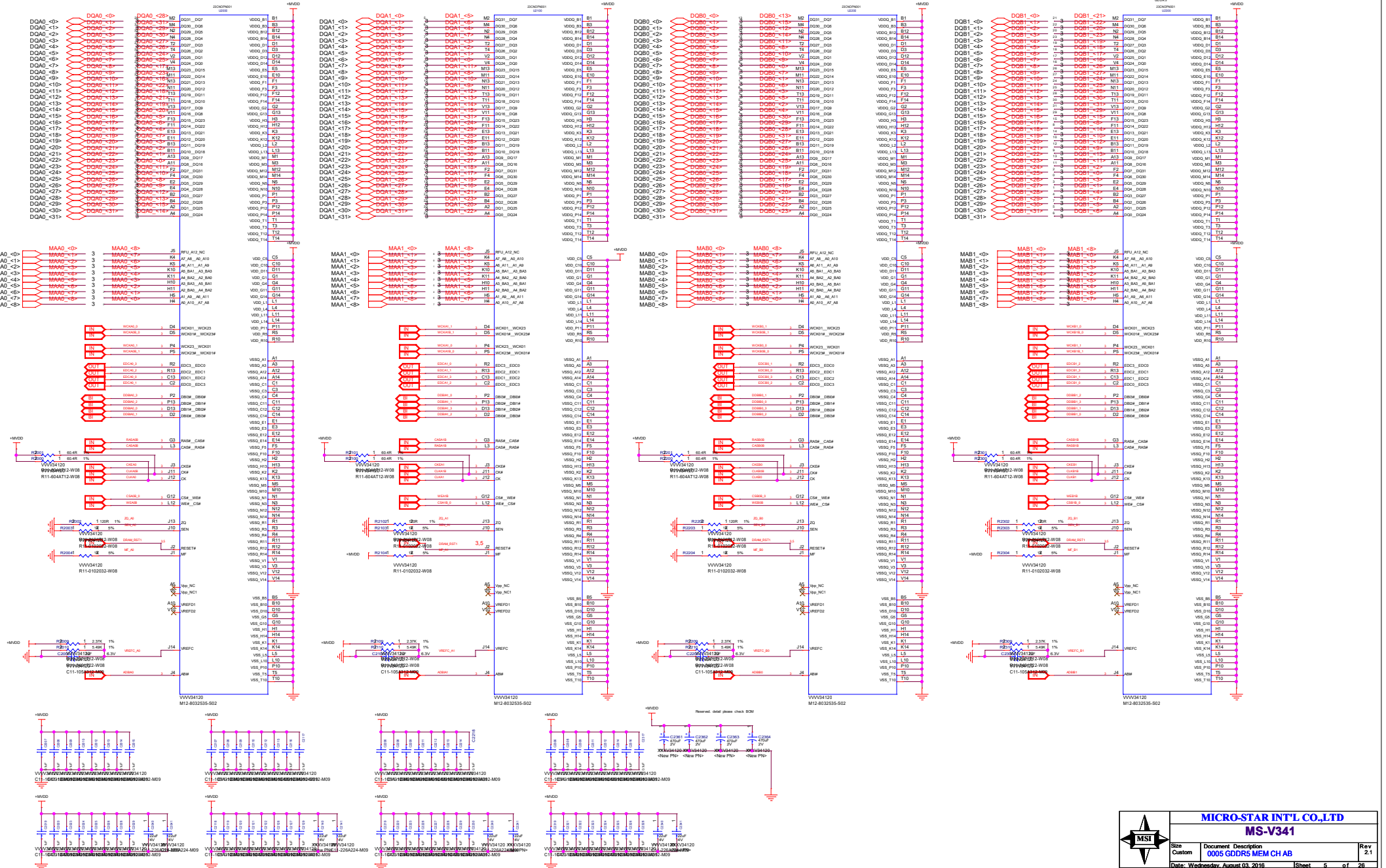
(3) ELLESMERE MEM INTERFACE CH A/B



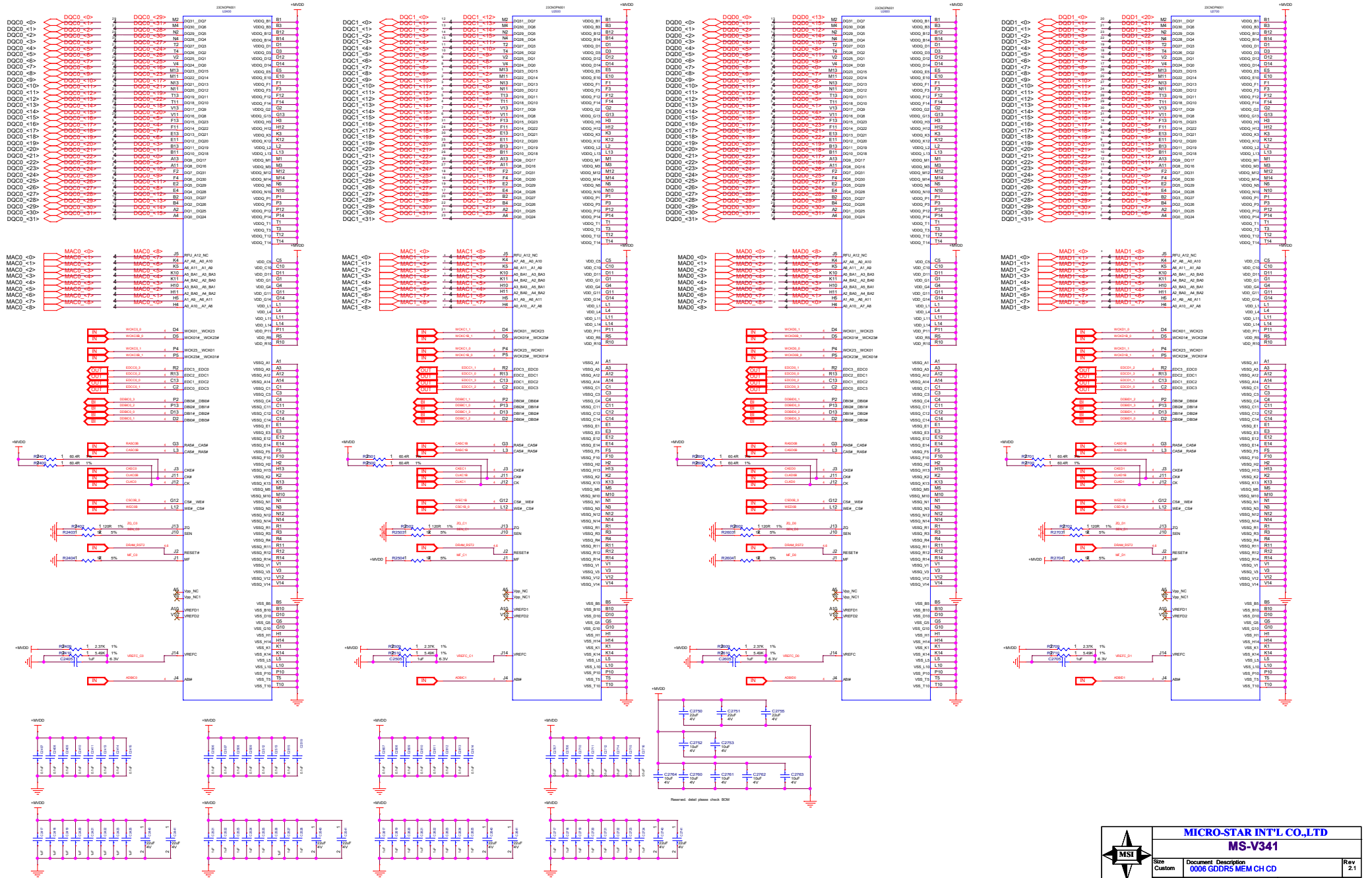
(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B



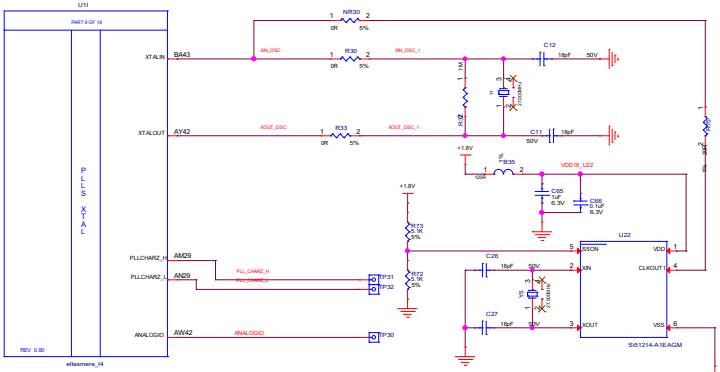
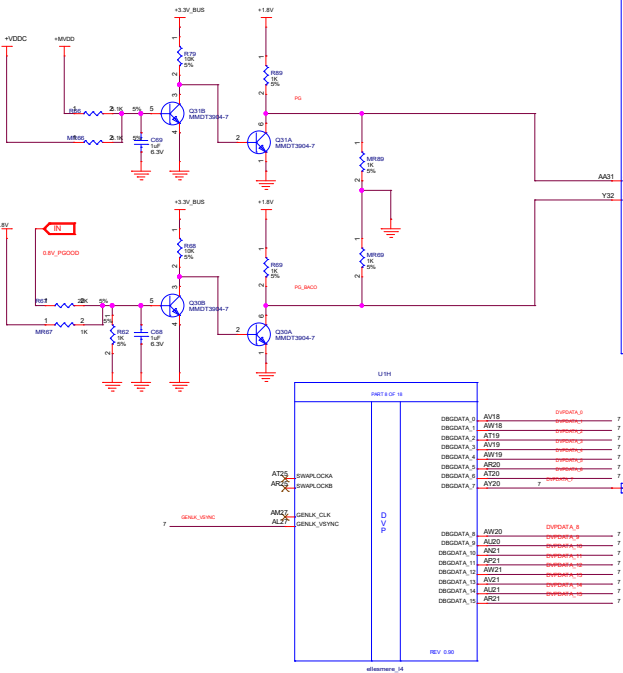
(6) GDDR5 MEMORY CH C/D



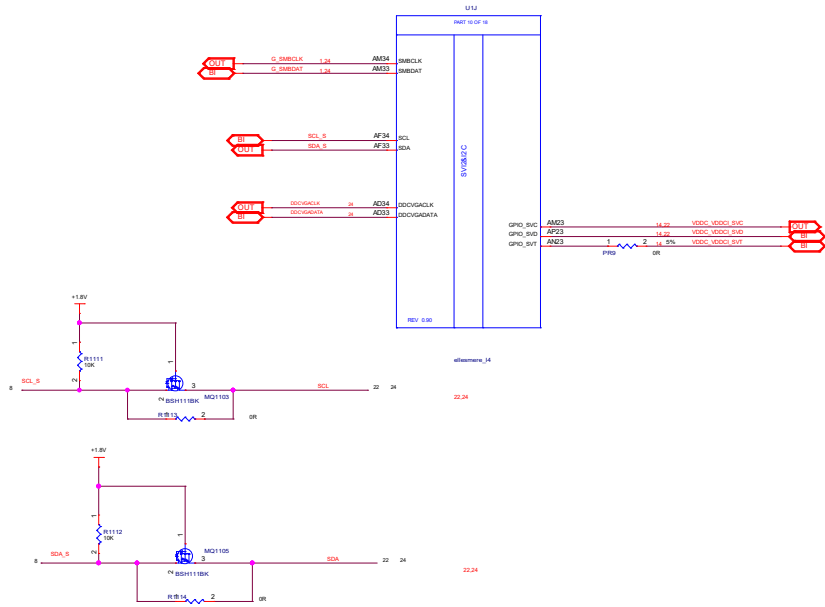
(7) ELLESMERE GPIO STRAP CF XTAL

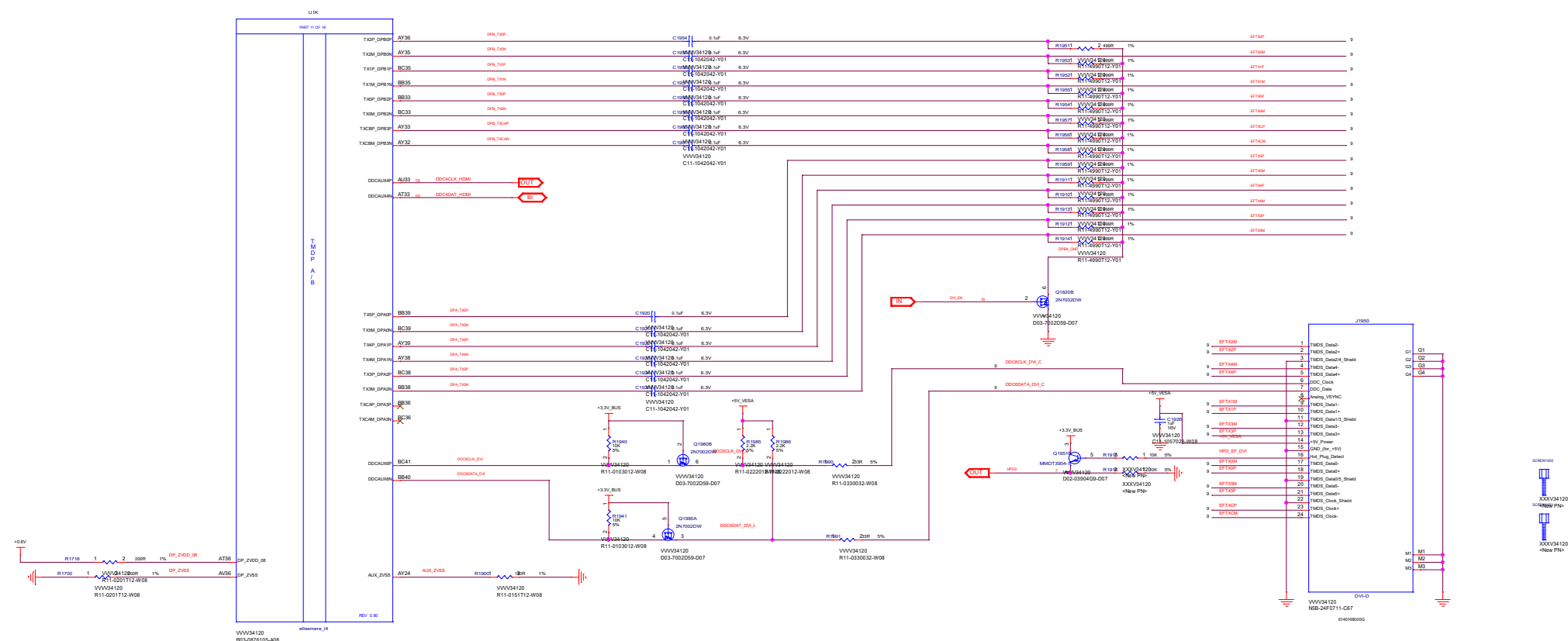
I2C ADDRESS			FUNCTION	DEVICE

I2C ADDRESS			FUNCTION	DEVICE
0x48			EXT TEMP SENSOR	LM93C83

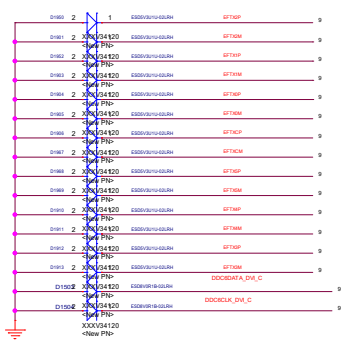


(8) ELLESMERE DAC1 LOCK

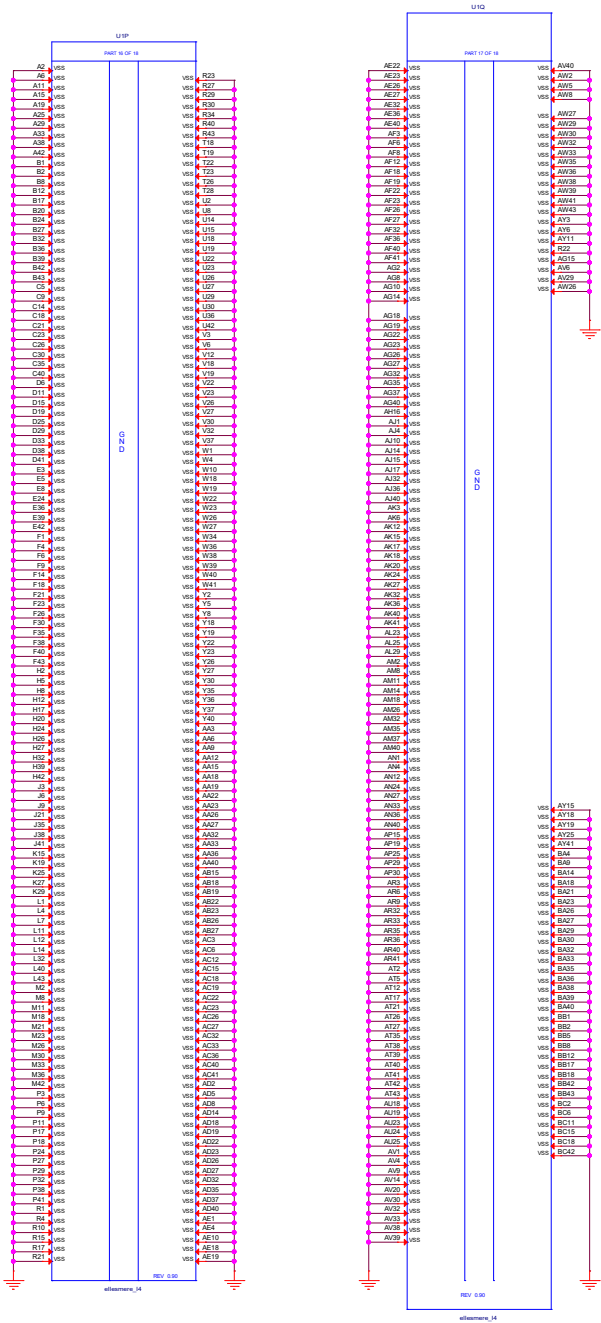


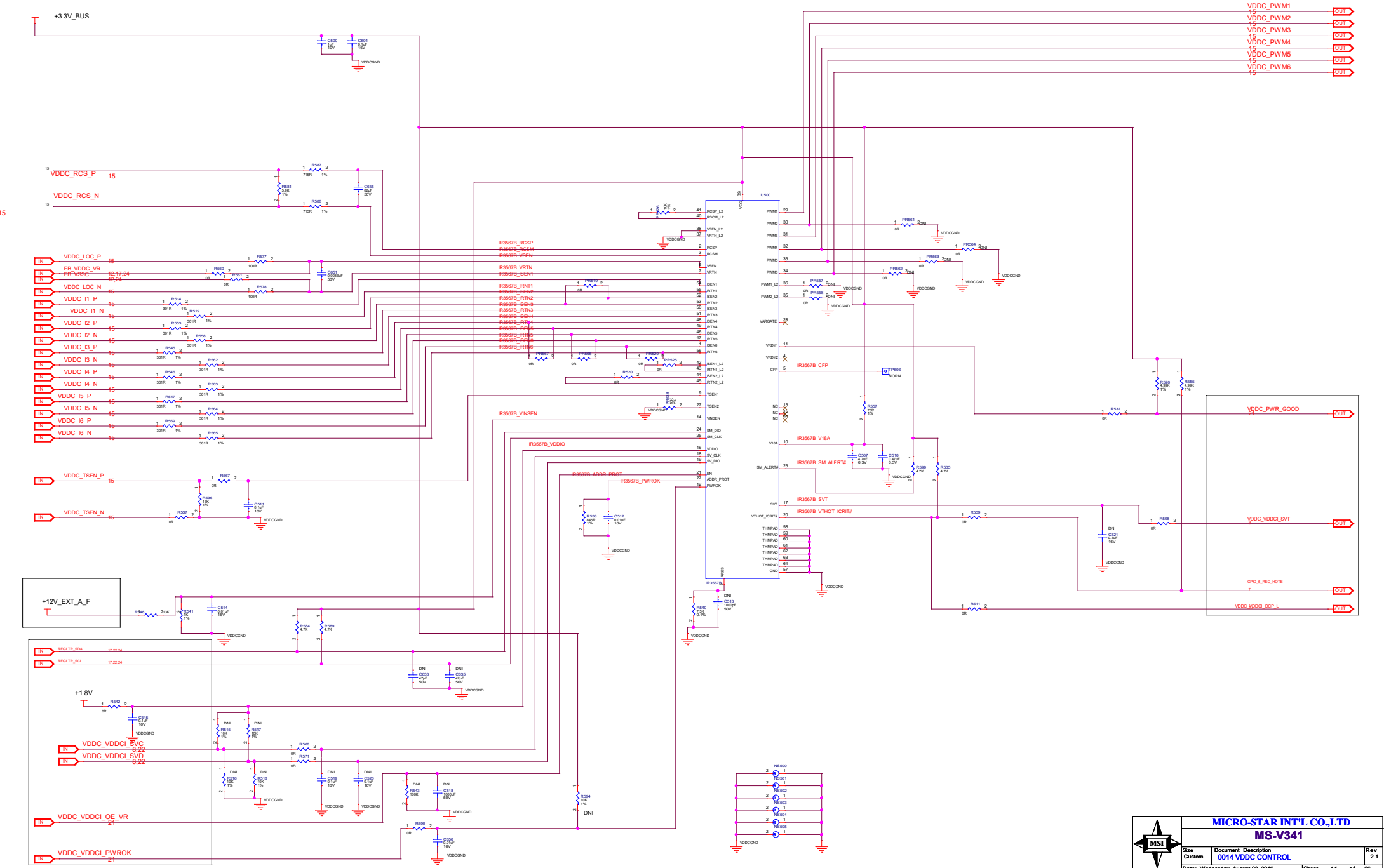


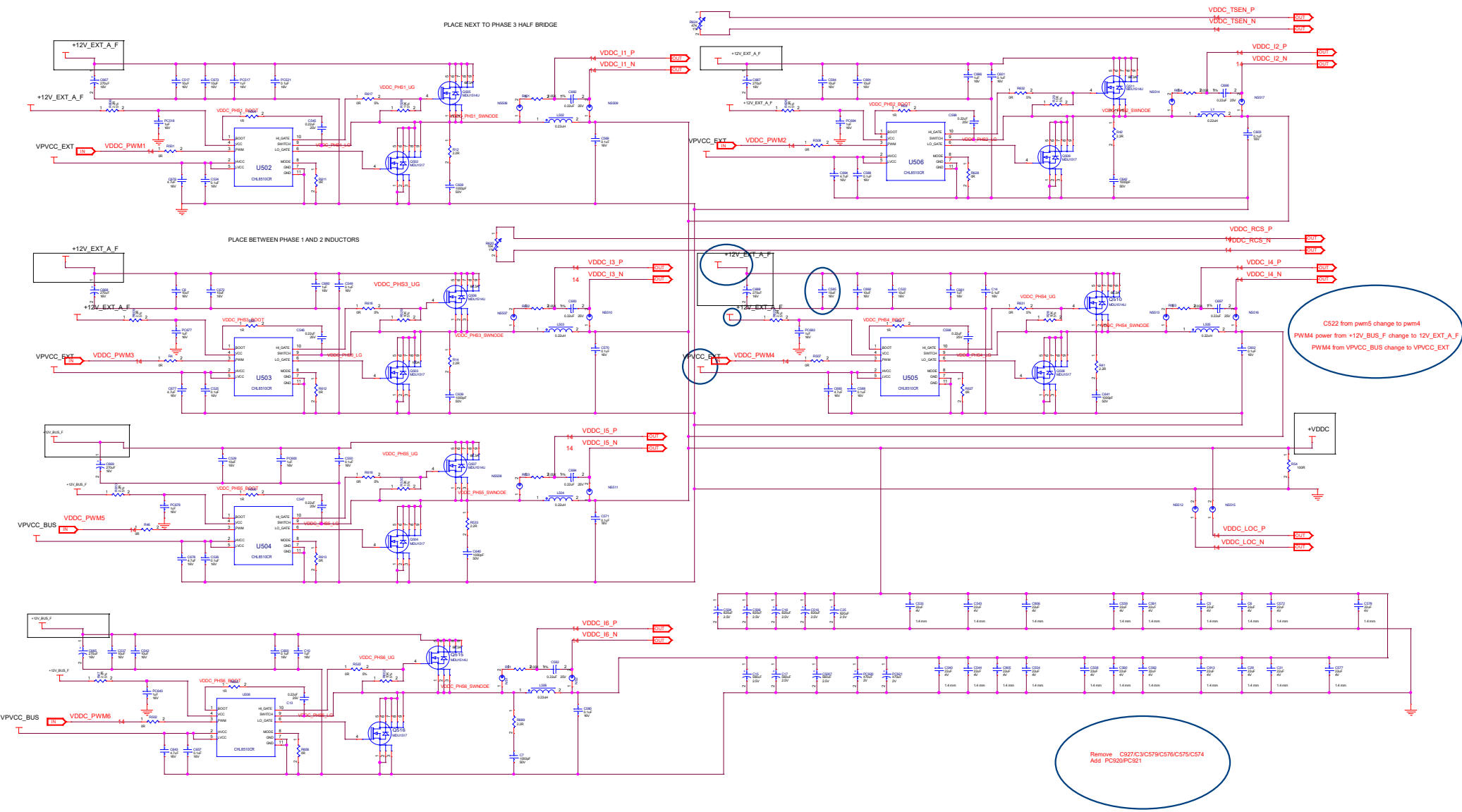
OPTIONAL ESD PROTECTION DIODES

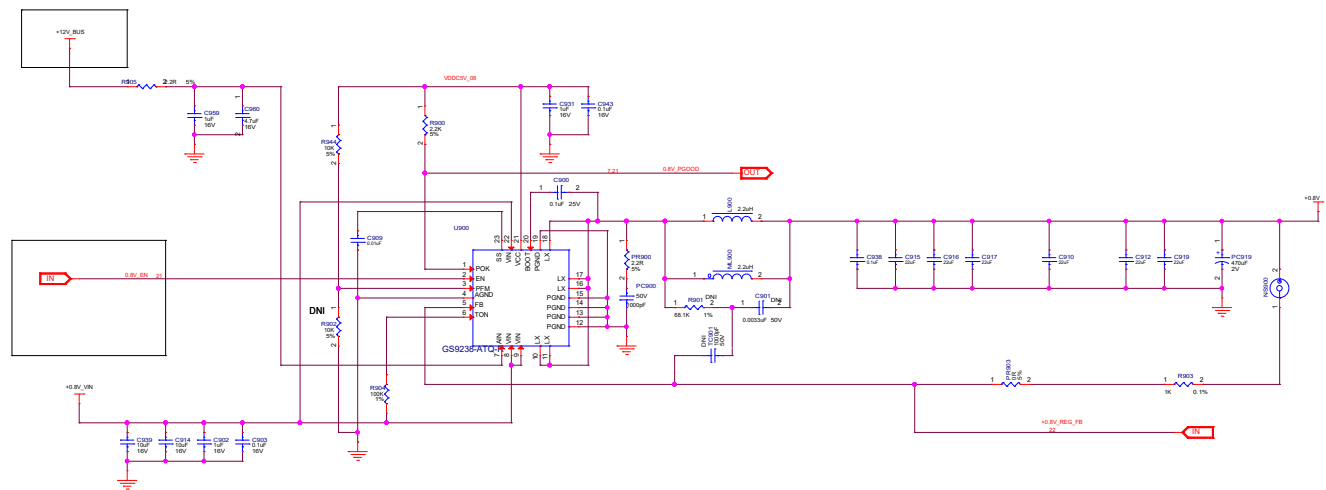


(13) ELLESMERE GROUND









(18) SMALL RAIL REGULATORS

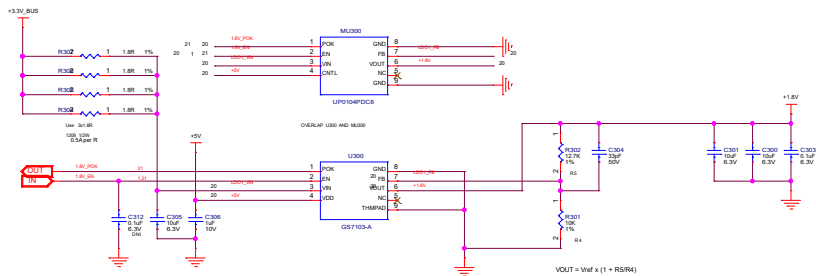
LDO #1:

VIN = 3.0V TO 3.6V MAX

VOUT = +1.8V +/- 2%

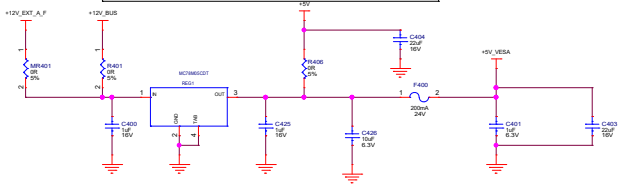
IOUT = 1.3A RMS MAX

PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

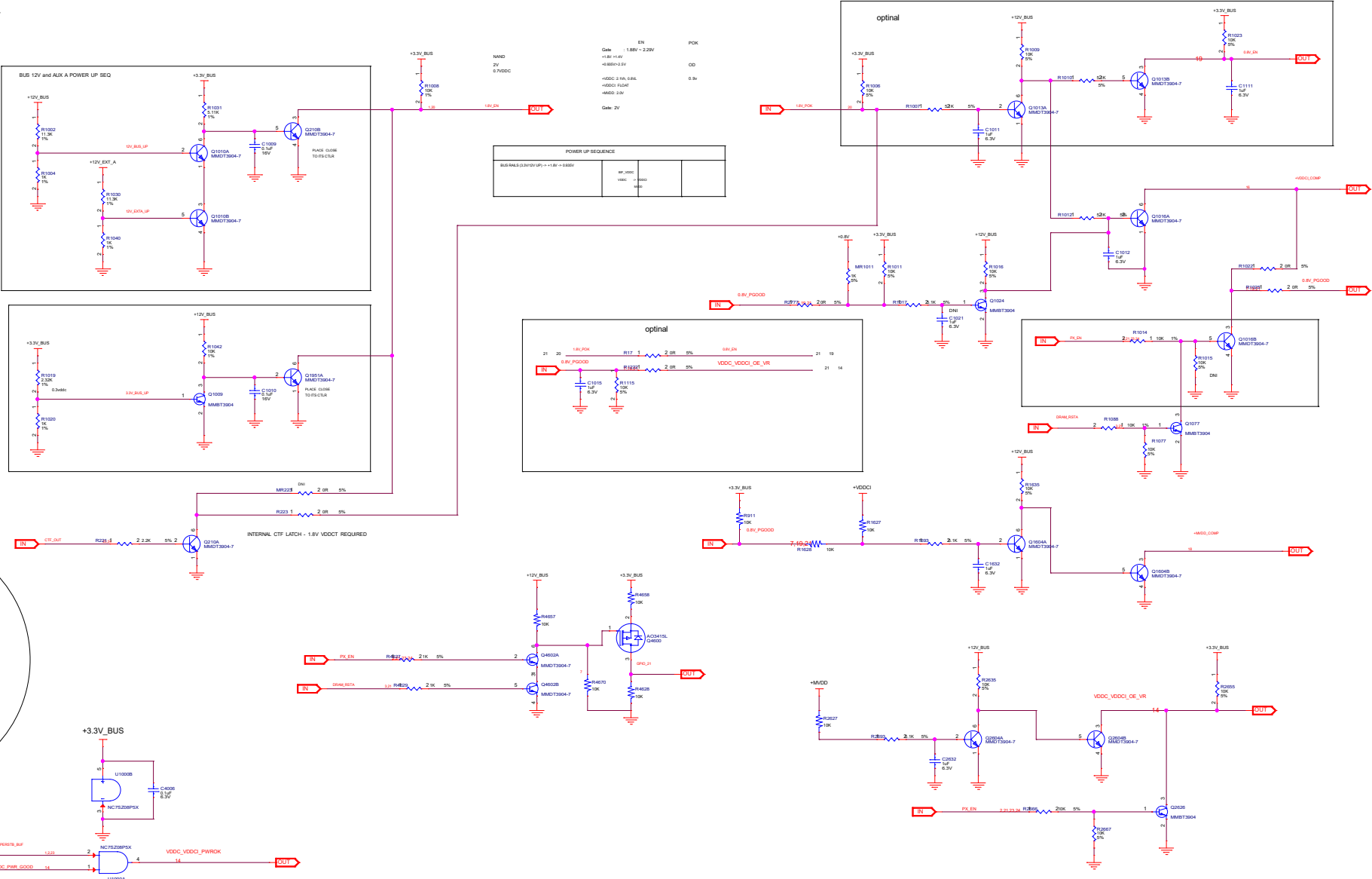


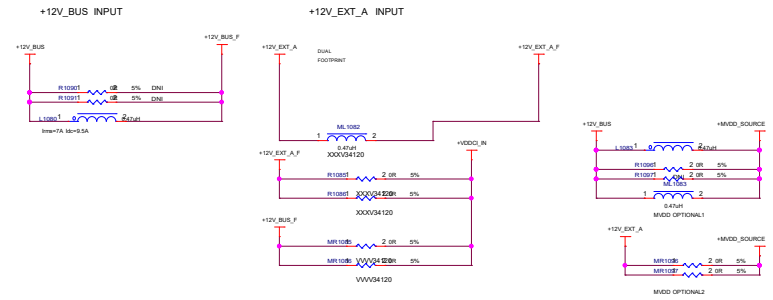
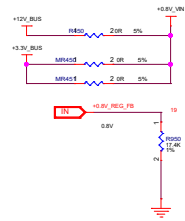
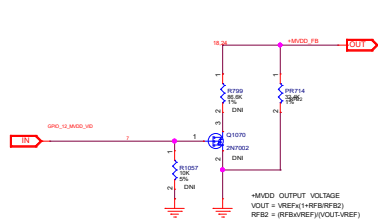
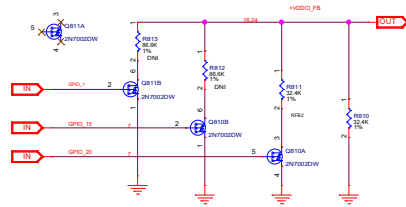
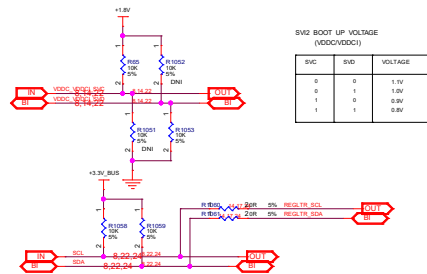
REGULATOR FOR +5V RAILS

IOUT MAX = 150mA

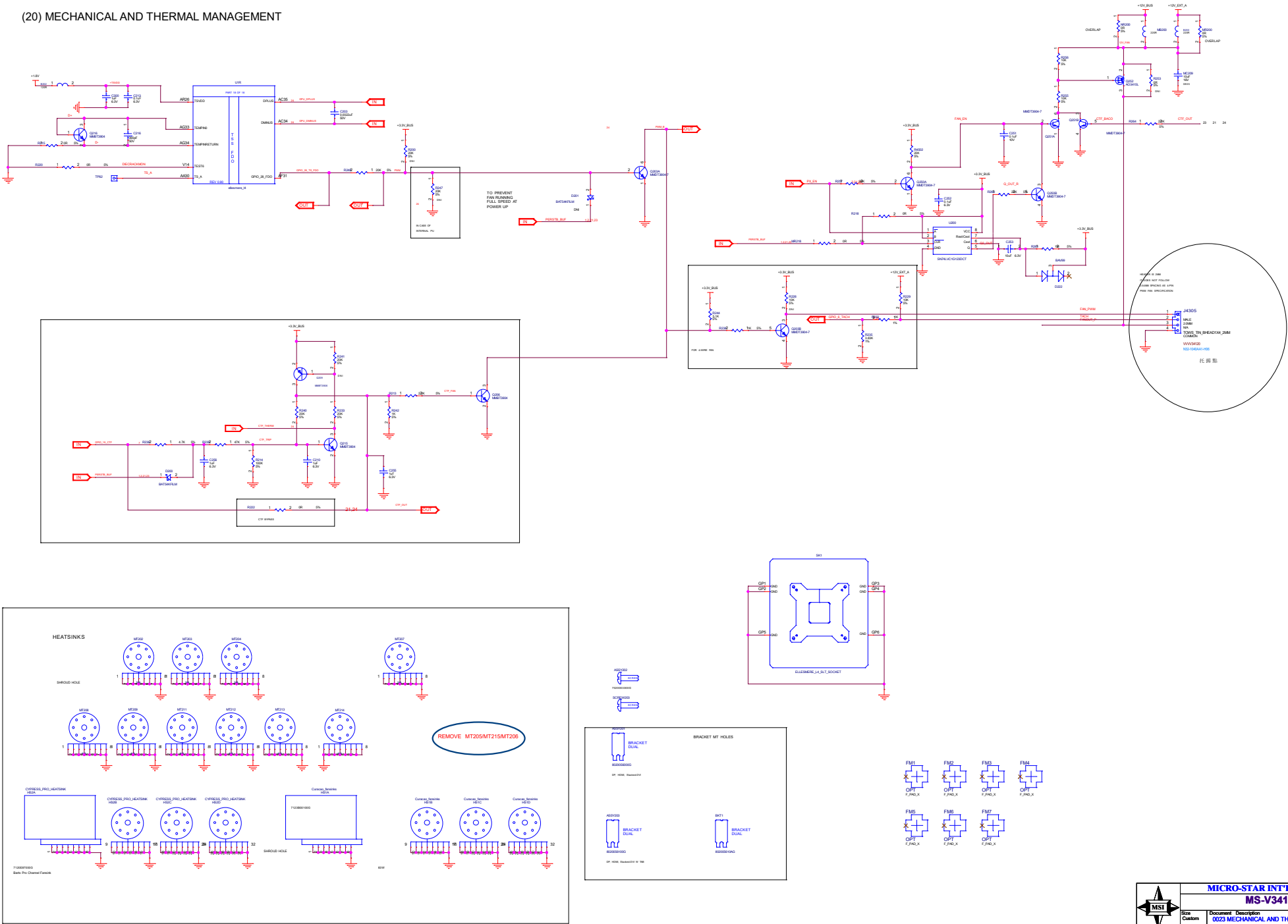


(19) POWER MANAGEMENT

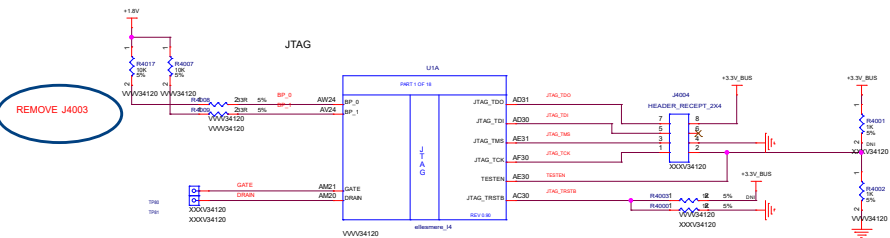




(20) MECHANICAL AND THERMAL MANAGEMENT



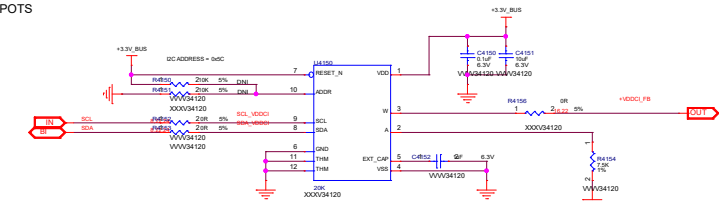
(21) DEBUG CIRCUITS



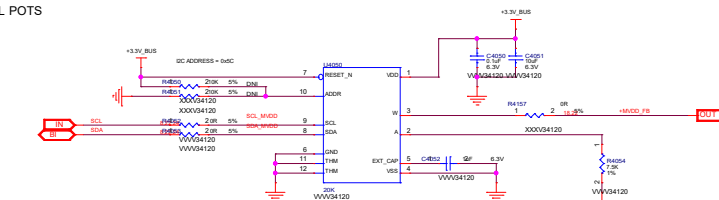
REMOVE

E-FUSE CAPABILITY
DEFAULT = GPIO-CONTROLLED
(MANUAL OPTION AS BACK-UP)

DIGITAL POTS



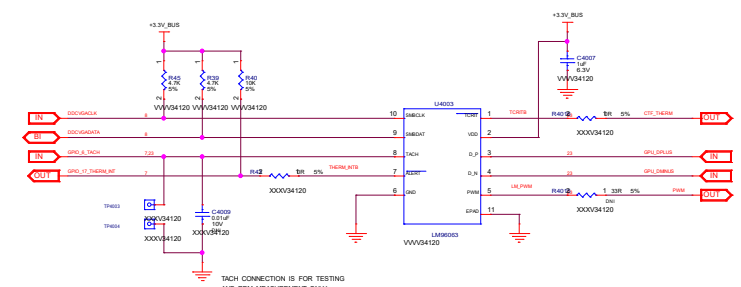
DIGITAL POTS



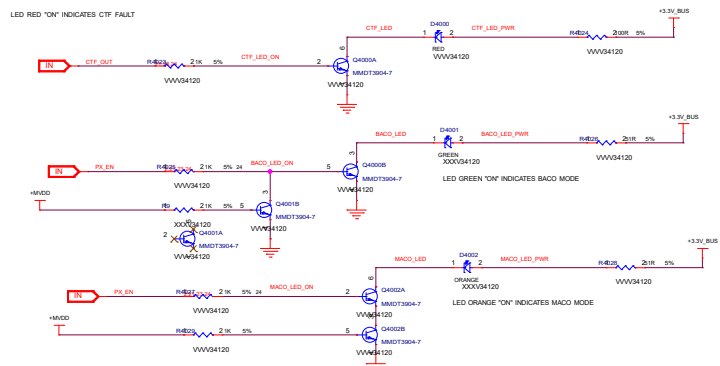
SWITCHES



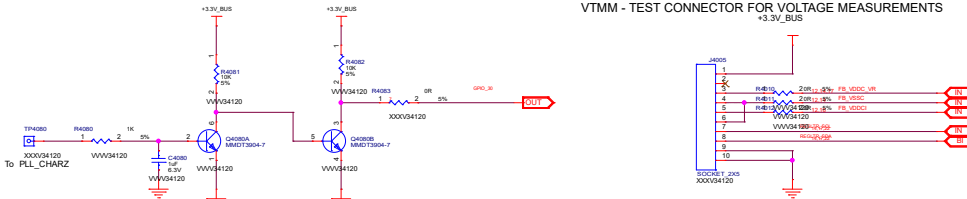
LM06163 FOR BACKUP THERMAL CONTROL

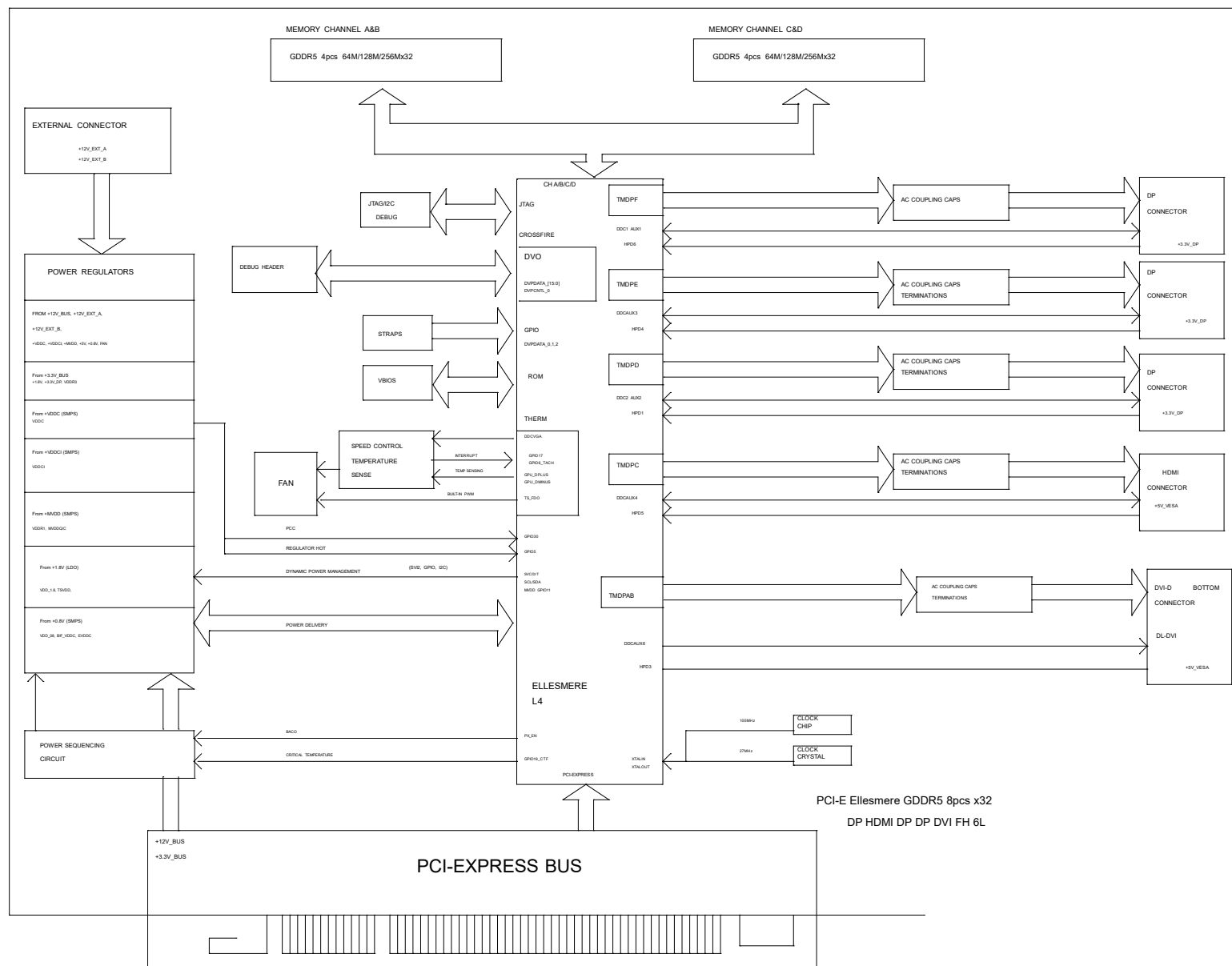


LED LIGHTS



VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS





23: EXT 6 PIN change to 反向 8 pin
REMOVE MT205/MT215/MT206

26: 1*4 打錫點

15: C522 from pwm5 change to pwm4
PWM4 power from +12V_BUS_F change to 12V_EXT_A_F
PWM4 from VPVCC_BUS change to VPVCC_EXT
Remove C927/C3/C579/C576/C575/C574
Add PC920/PC921

24: REMOVE J4003
REMOVE E-FUSE CAPABILITY