

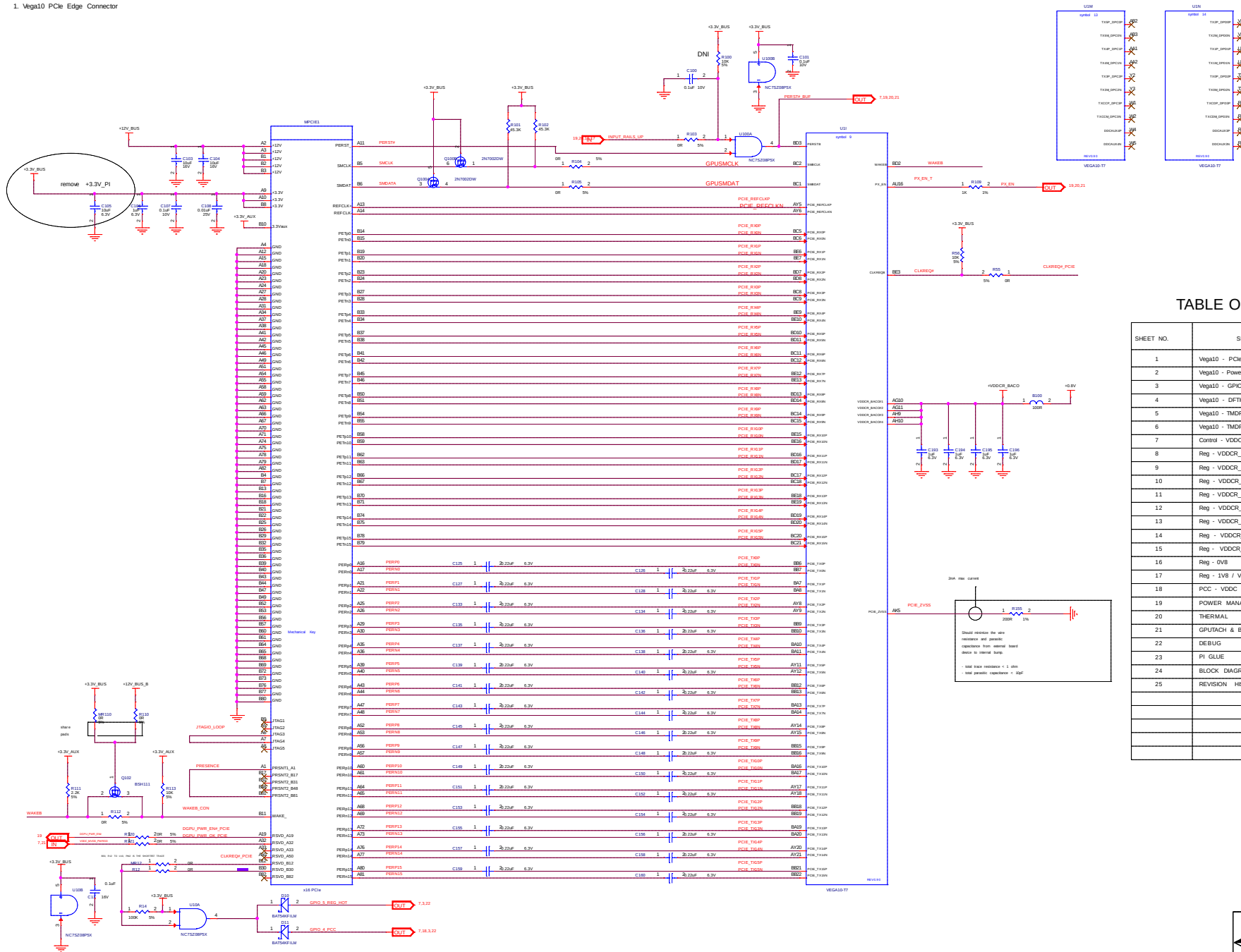
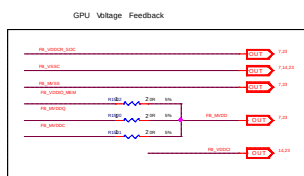
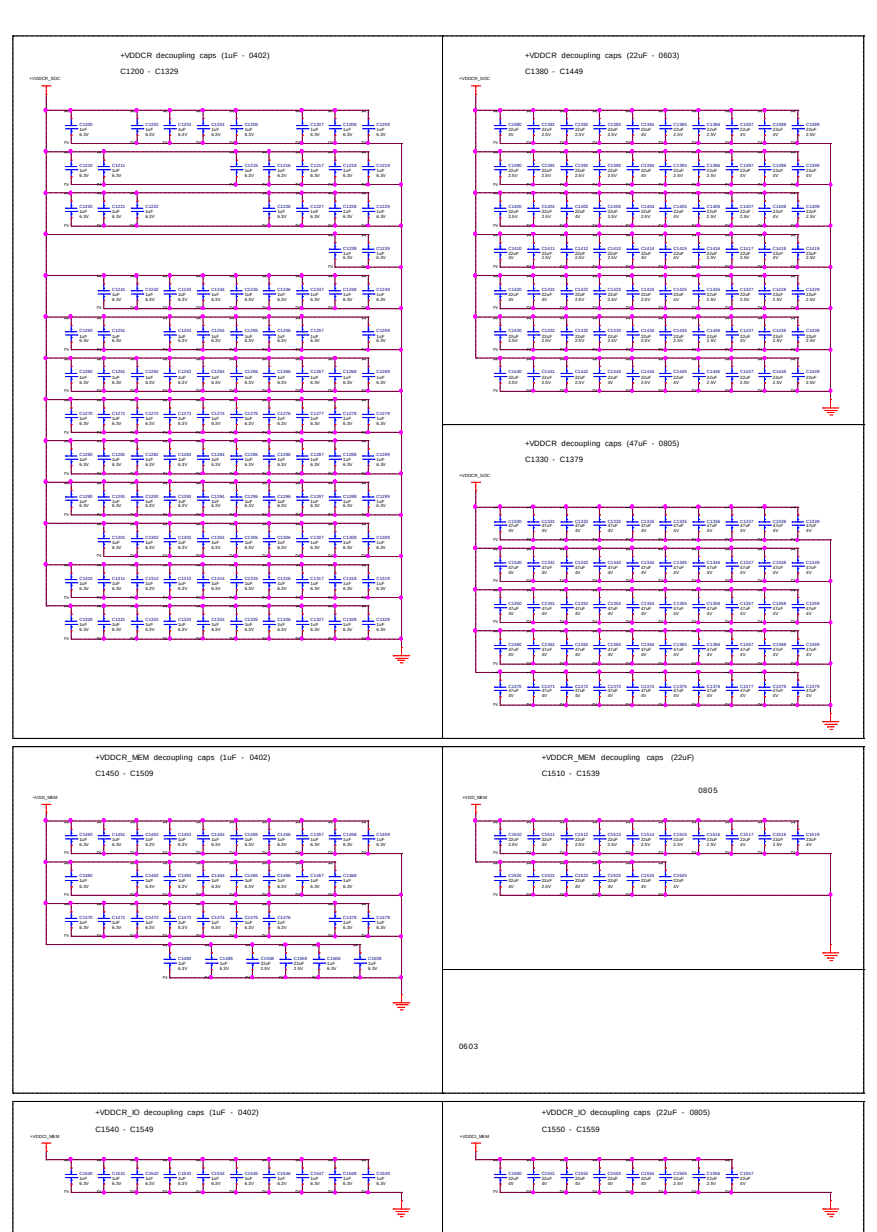
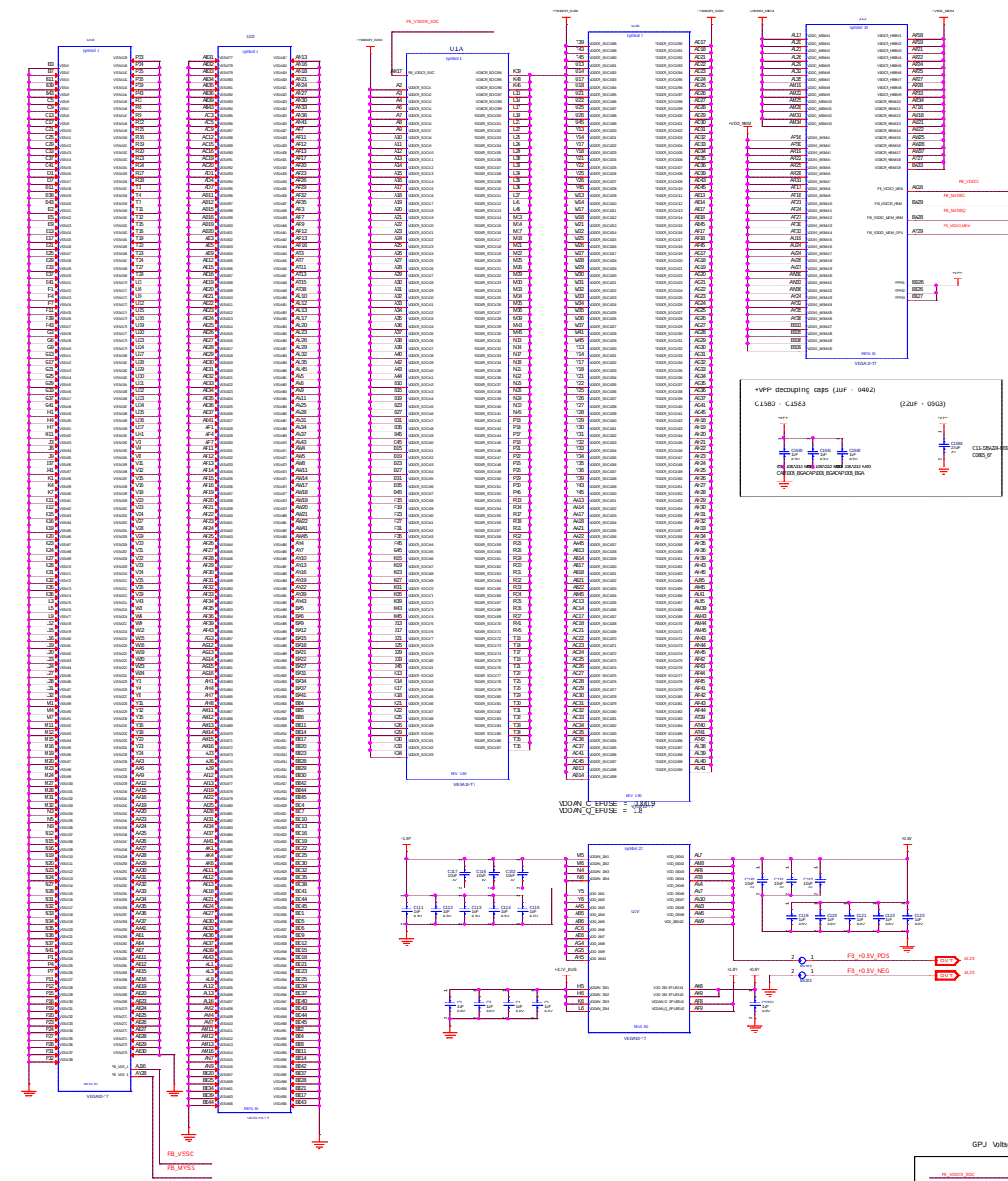
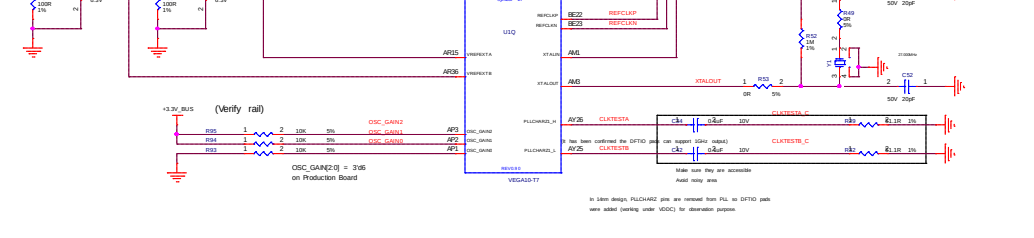
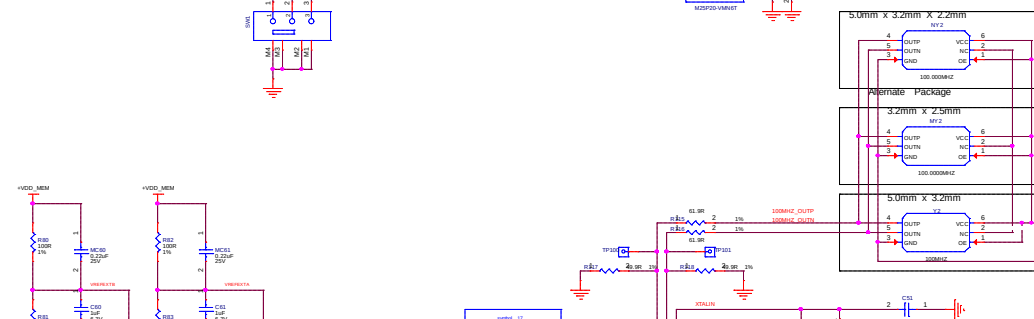
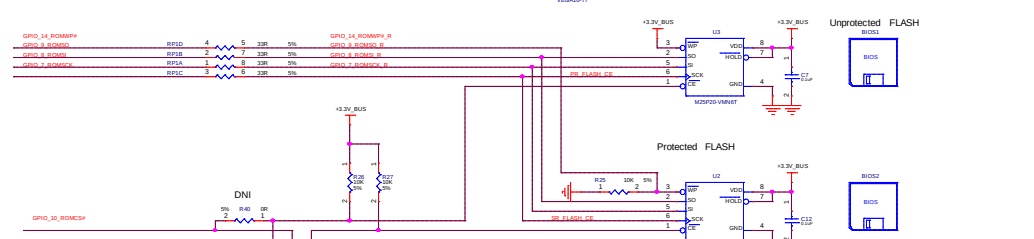
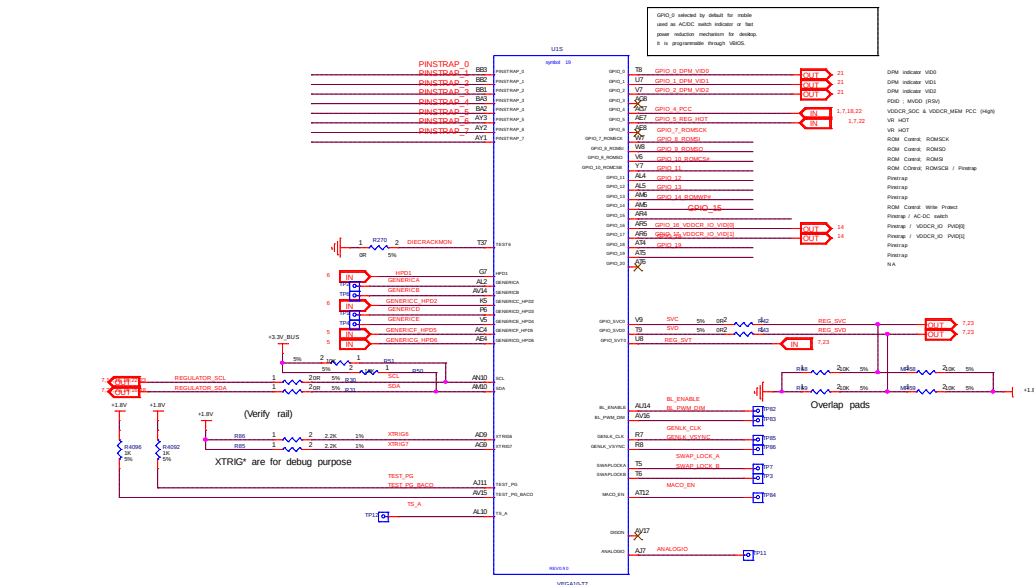


TABLE OF CONTENTS

[illegible]

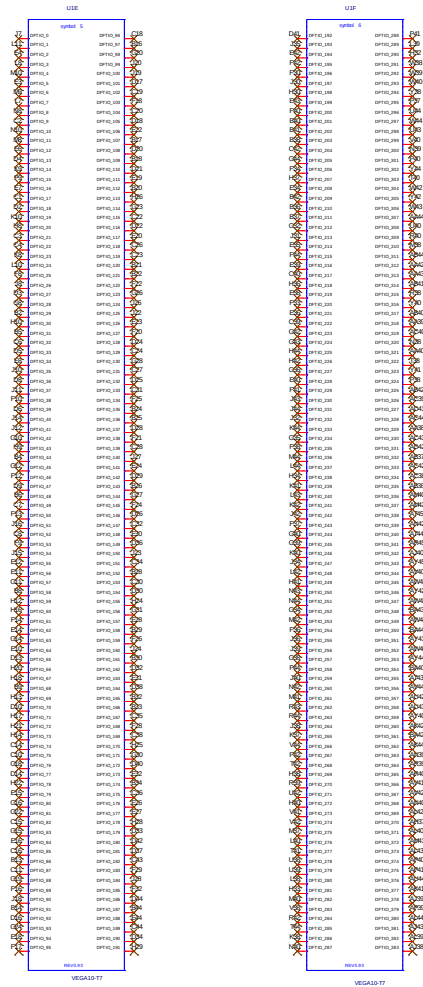




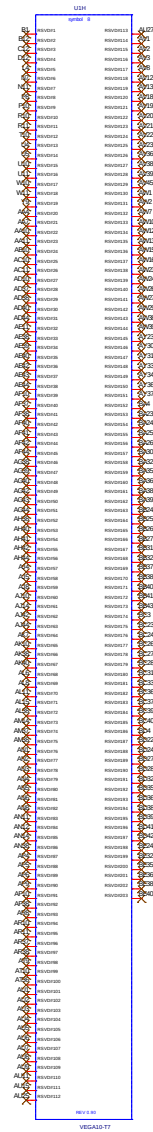
User	Internal Default Value	Definition
BIF	0	STRAP_BIF_GEN3_DIS_A 0: PCH Gen3 is supported 1: PCH Gen3 is not supported
	0	PINSTRAP_BIF_CLK_PM_EN 0: CLKREQ power management capability is disabled 1: CLKREQ power management capability is enabled
	0	PINSTRAP_BIF_LC_TX_SWING 0: VGA controller capacity is enabled 1: VGA controller capacity is disabled
	0	PINSTRAP_BIF_VGA_DIS 0: VGA controller capacity is enabled 1: VGA controller capacity is disabled
DCE	0	PINSTRAP_AUD_PORT_CONN[2:0] Number of audio-capable display outputs 0: 0 outputs connected 1: 1 output connected 2: 2 outputs connected 3: 3 outputs connected 4: 4 outputs connected 5: 5 outputs connected 6: 6 outputs connected 7: 7 outputs connected
	0	PINSTRAP_AUD[1:0] 1: Audio for DisplayPort only 2: Audio for DisplayPort and HDMI if display is detected 3: Audio for DisplayPort and HDMI if display is detected
	0	PINSTRAP_BOARD_CONFIG[2:0] TBD
	0	TBD
SMU	1	PINSTRAP_ROM_CONFIG[2:0] 0: - SUN8DT (ST) M89P8A 1: - SUN8DT (ST) M89P8A 2: - SUN8DT (ST) M89P8A 3: - SUN8DT (ST) M89P8A 4: - SUN8DT (ST) M89P8A 5: - SUN8DT (ST) M89P8A 6: - SUN8DT (ST) M89P8A 7: - SUN8DT (ST) M89P8A
	0	PINSTRAP_SMBUS_ADDR 0: 0x00 1: 0x01
	0	PINSTRAP_BIOS_ROM_EN 0: Disable the internal BIOS ROM device 1: Enable the internal BIOS ROM device
	1	PINSTRAP_BIOS_ROM_EN 0: Disable the internal BIOS ROM device 1: Enable the internal BIOS ROM device

//gpu/edoc/design/chip/padring/AI_dGPU/PinStrap_Plan.pptx

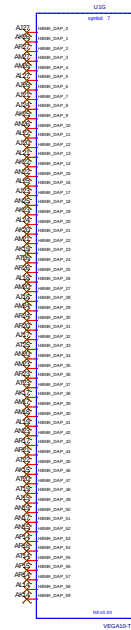
DFTIO



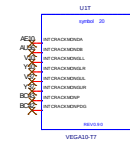
FREE



HBM_DAP

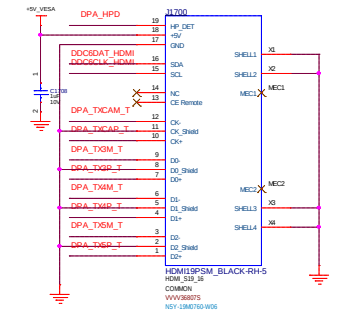
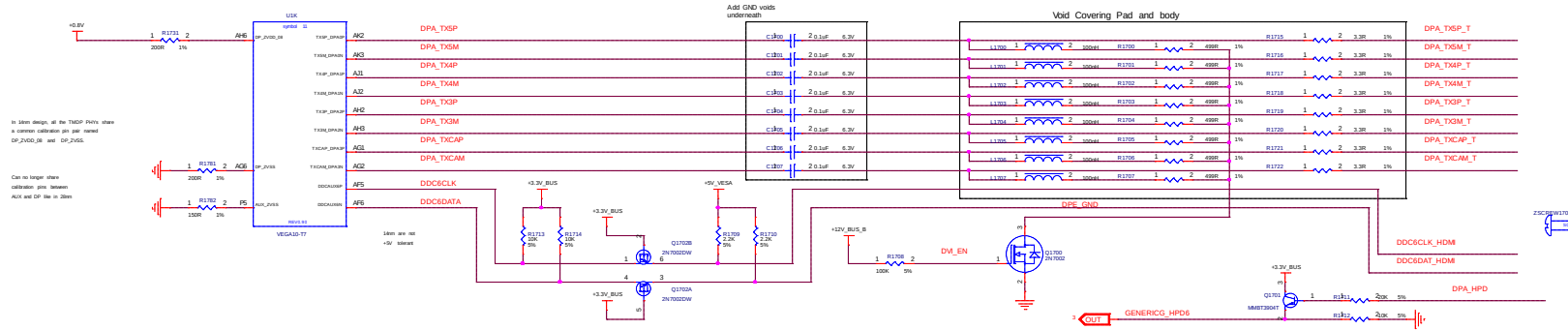


CRACKMON

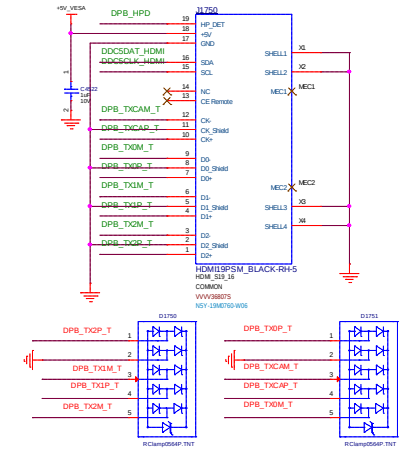
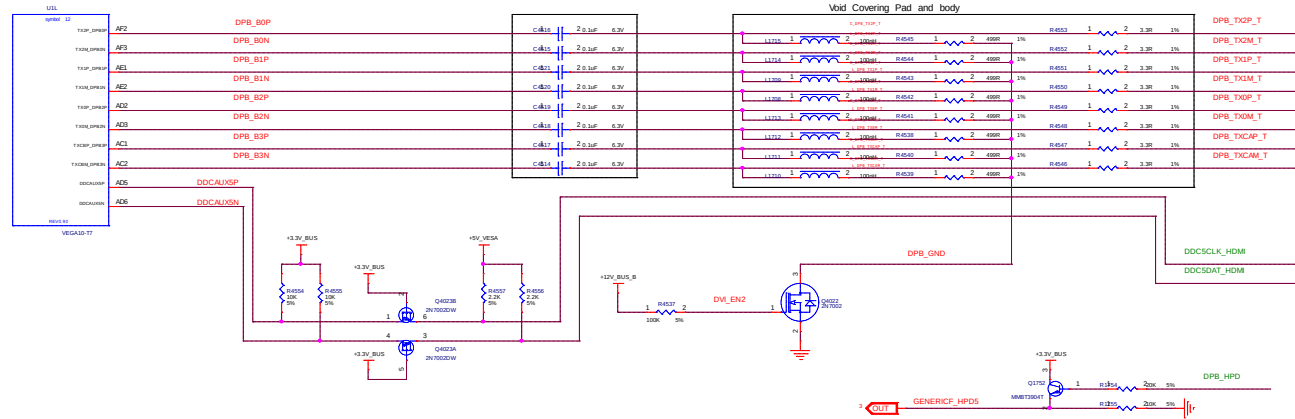


Ensure traces < 5 inches MAX (if routed on same layer)

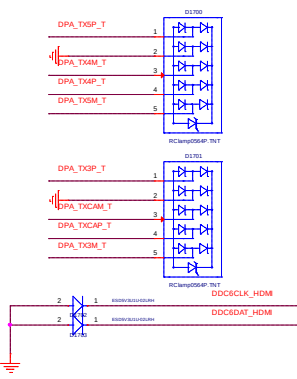
For differential routing on HDMI 2.0 connection, use dogbone shape antipads for the transitional vias of the same differential pair, and the distance from the via edge to the shape boundary should be



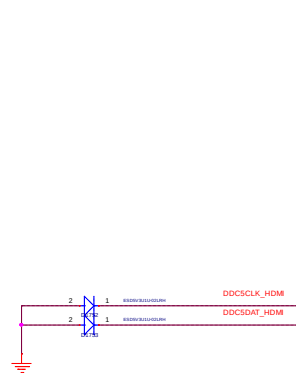
Ensure traces < 5 inches



OPTIONAL ESD PROTECTION DIODES

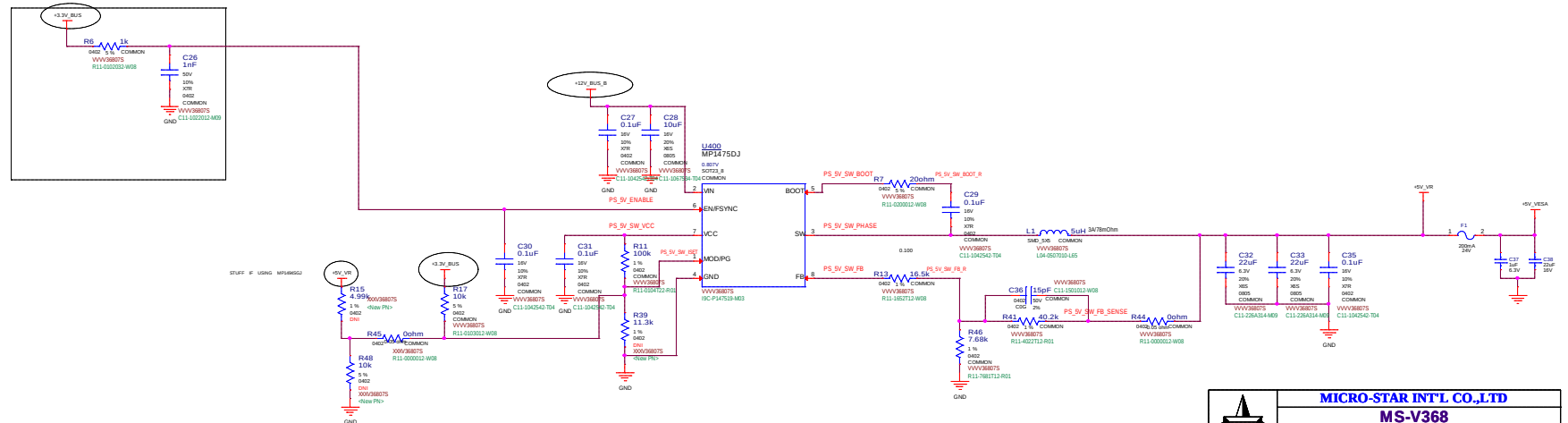


OPTIONAL ESD PROTECTION DIODES



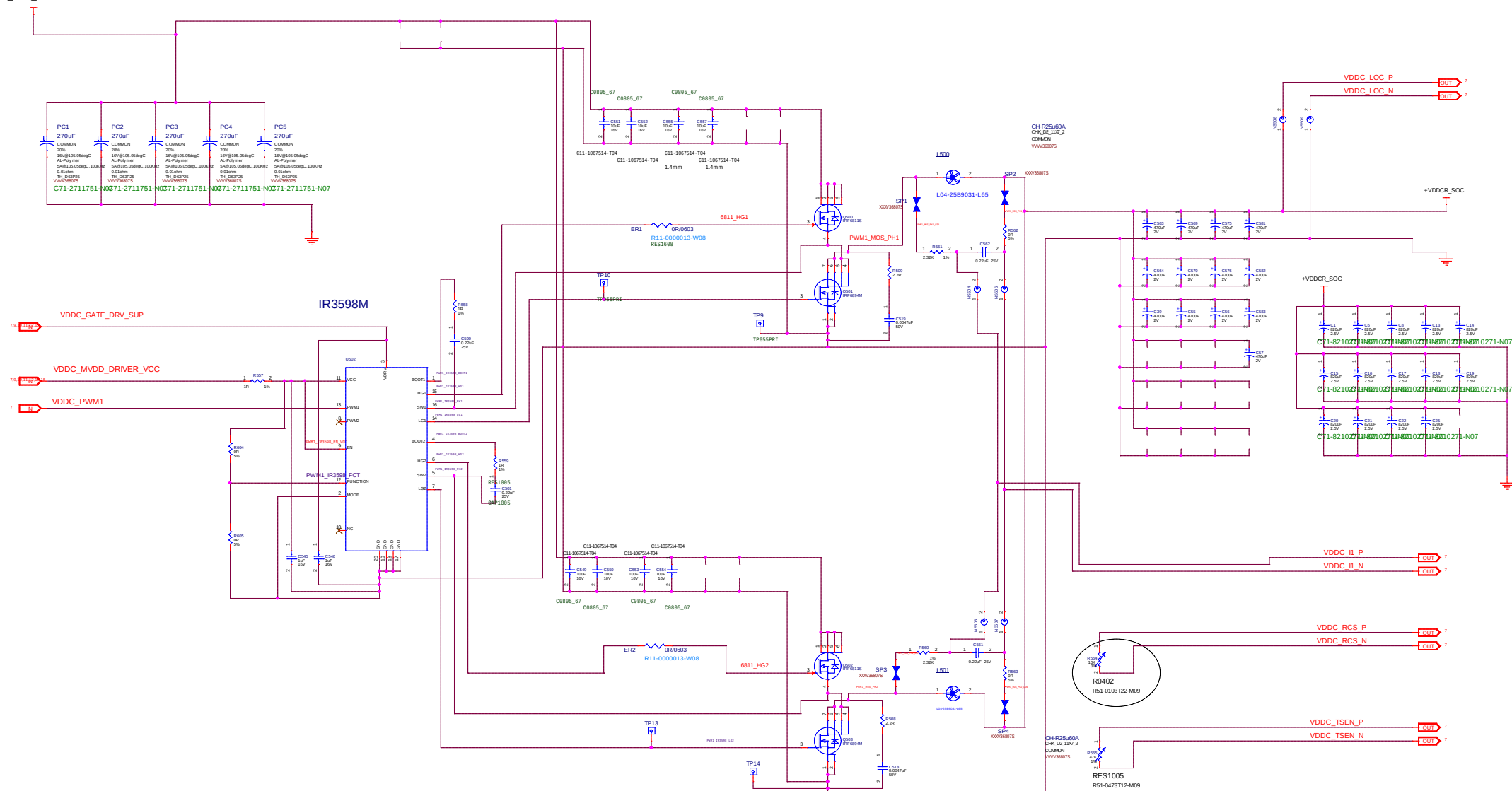
Ensure traces < 5 inches

The schematic diagram illustrates the DPE module's internal structure and signal connections. It features two comparators, C1980 and C1981, which are 5-bit comparators. The inputs to these comparators are DPE_0P, DPE_1N, DPE_2P, DPE_3N, DPE_4P, and DPE_5N. The outputs of the comparators are labeled as IC1980OUT and IC1981OUT. The DPE module is also connected to the DPE_AUXN and DPE_DONGLE_DET signals. The diagram shows the internal logic of the comparators, including the input buffers and the comparison logic.



Size Custom	Document Description TMDPEF	Rev 2.0
Date: Thursday, July 06, 2017		Sheet 6 of 27

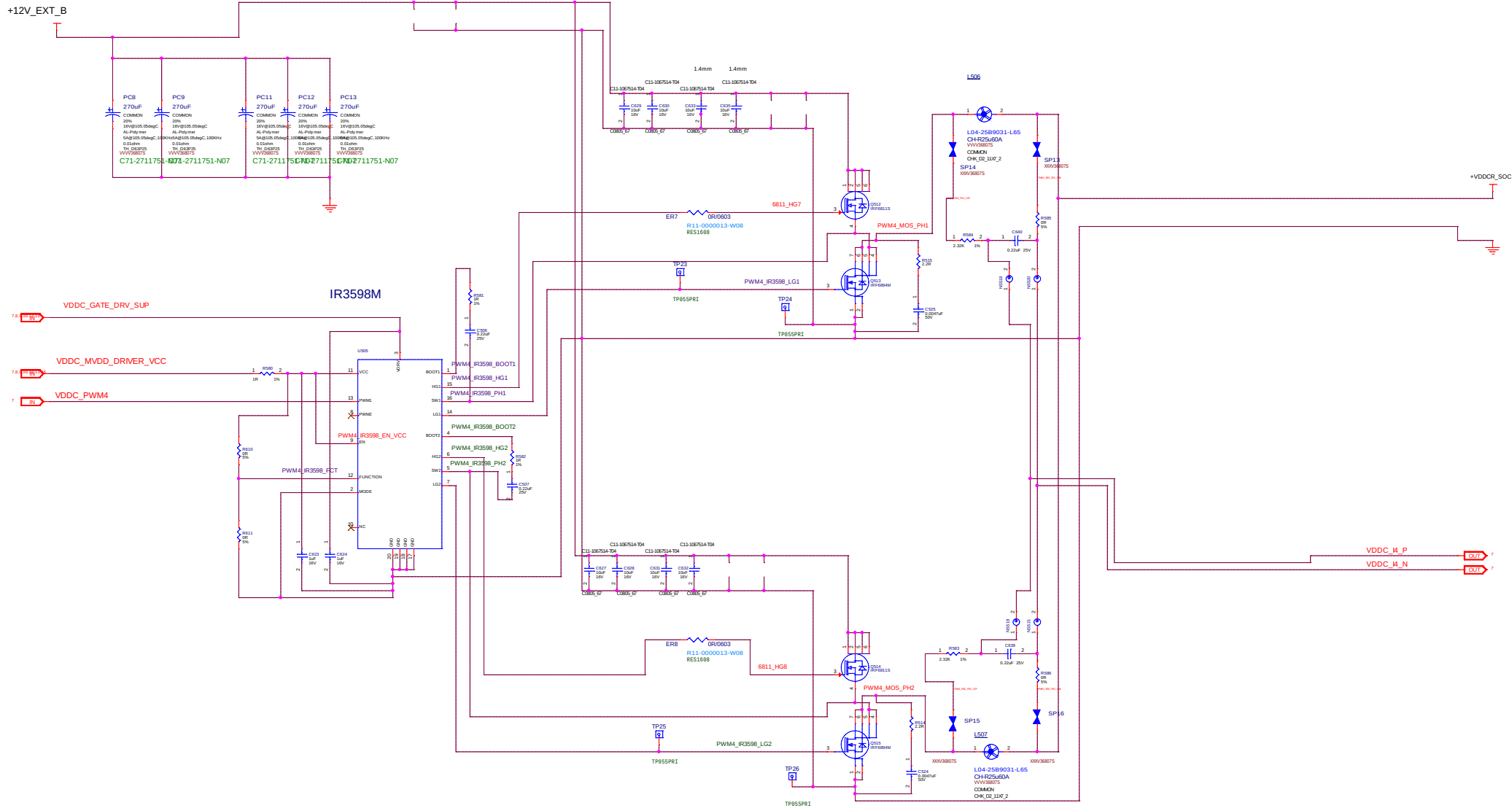
+12V_EXT_A

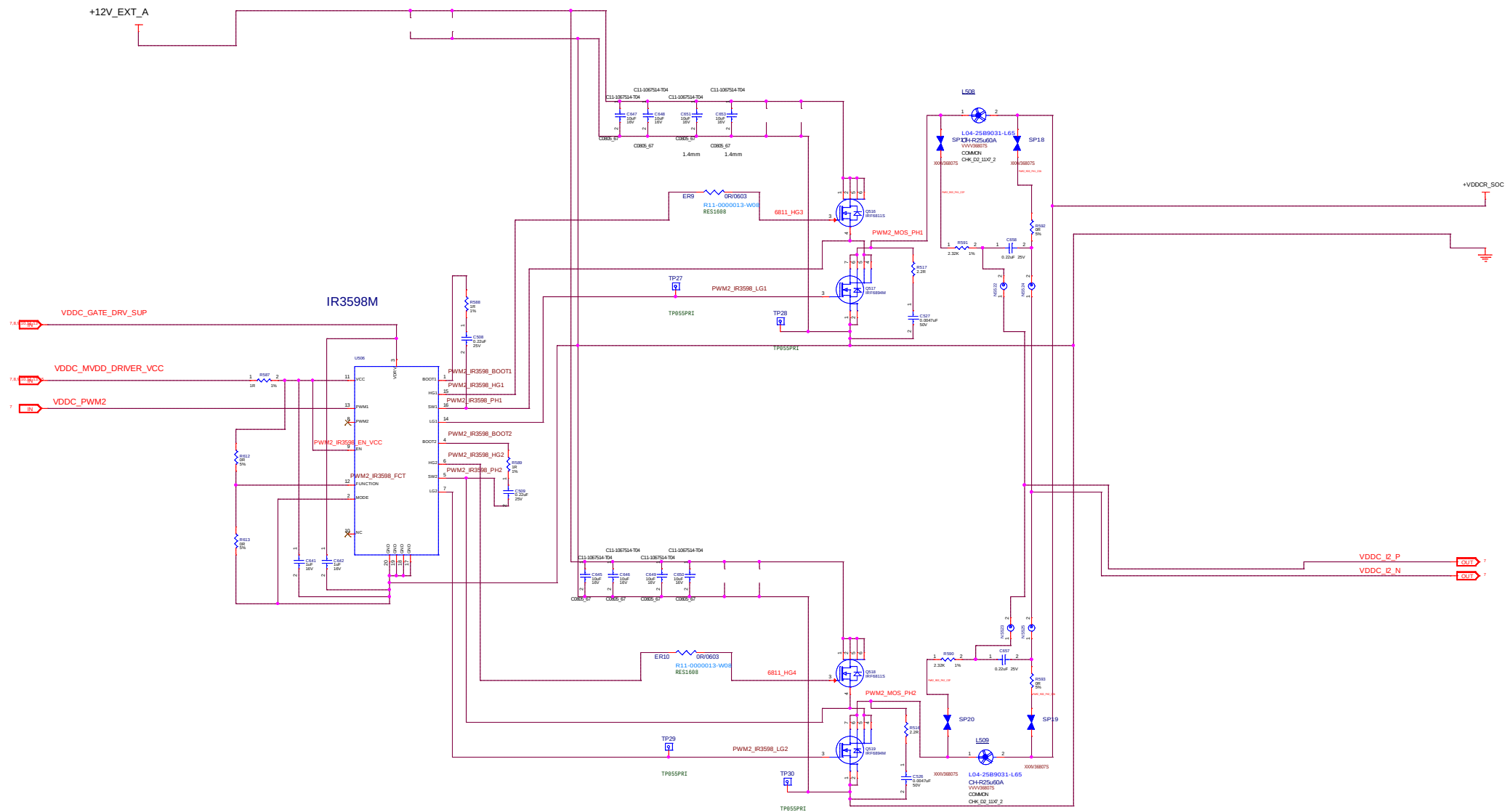


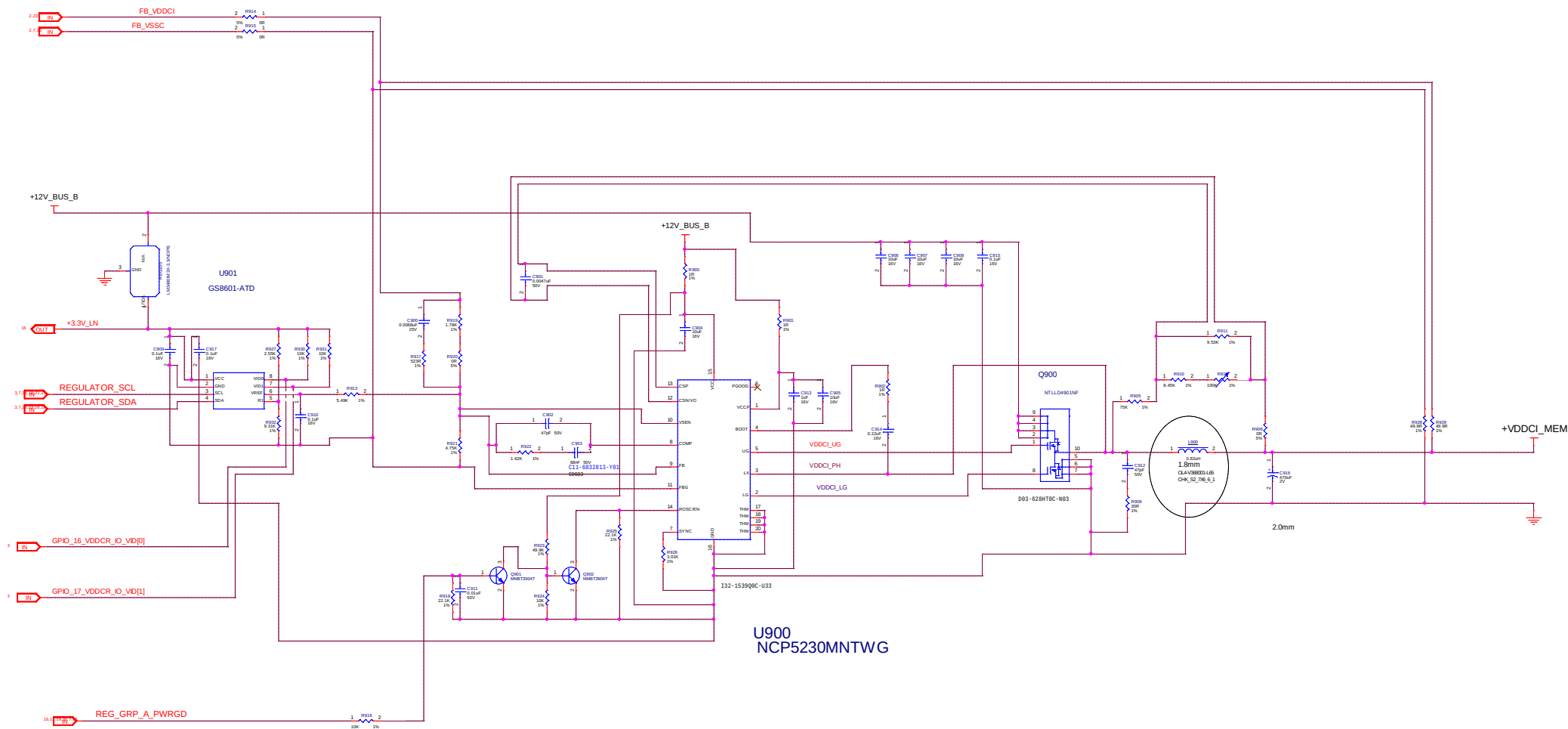
MICRO-STAR INT'L CO.,LTD
MS-V368

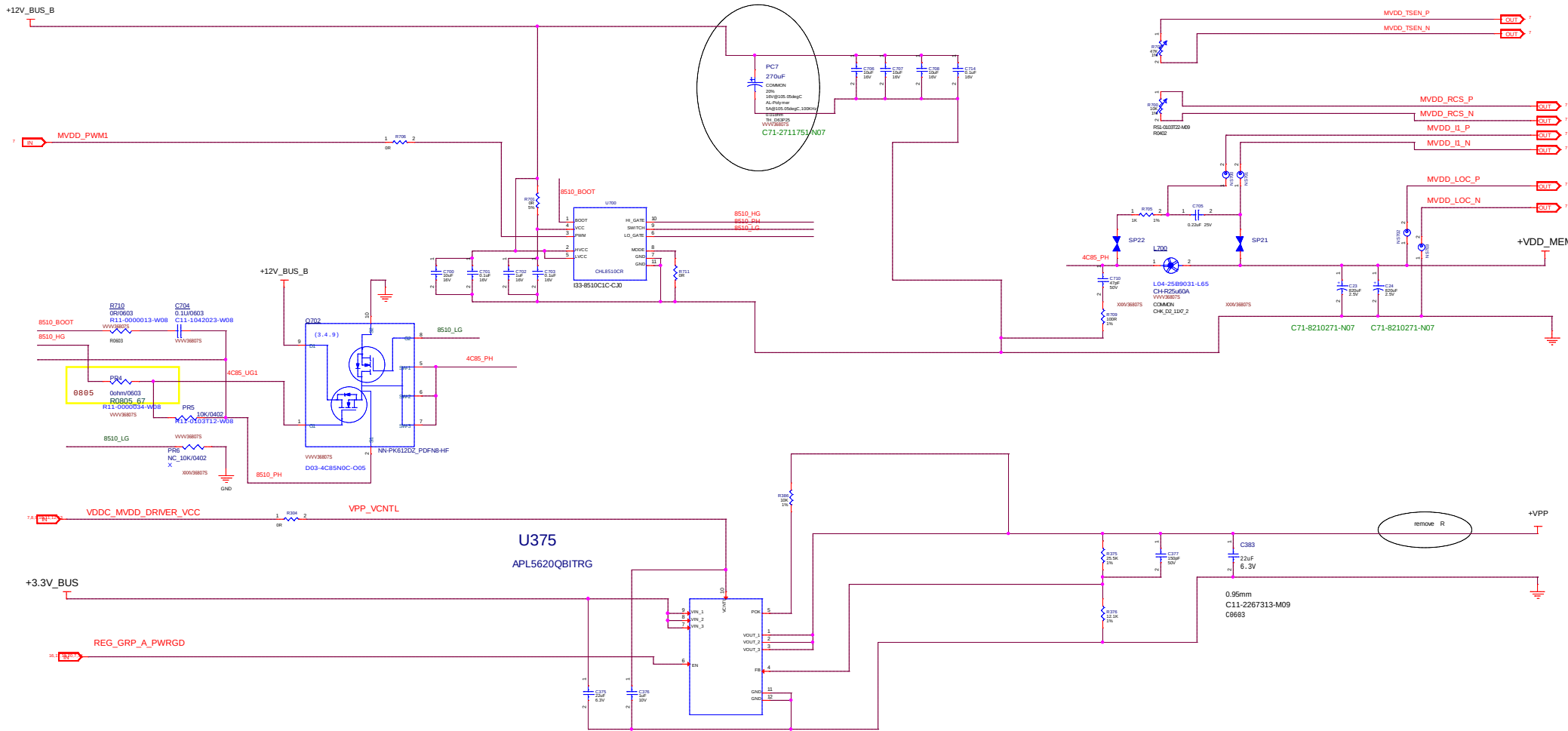
Size Custom	Document Description VDDC PH1	Rev 2.0
Date: Thursday, July 06, 2017		Sheet 8 of 27

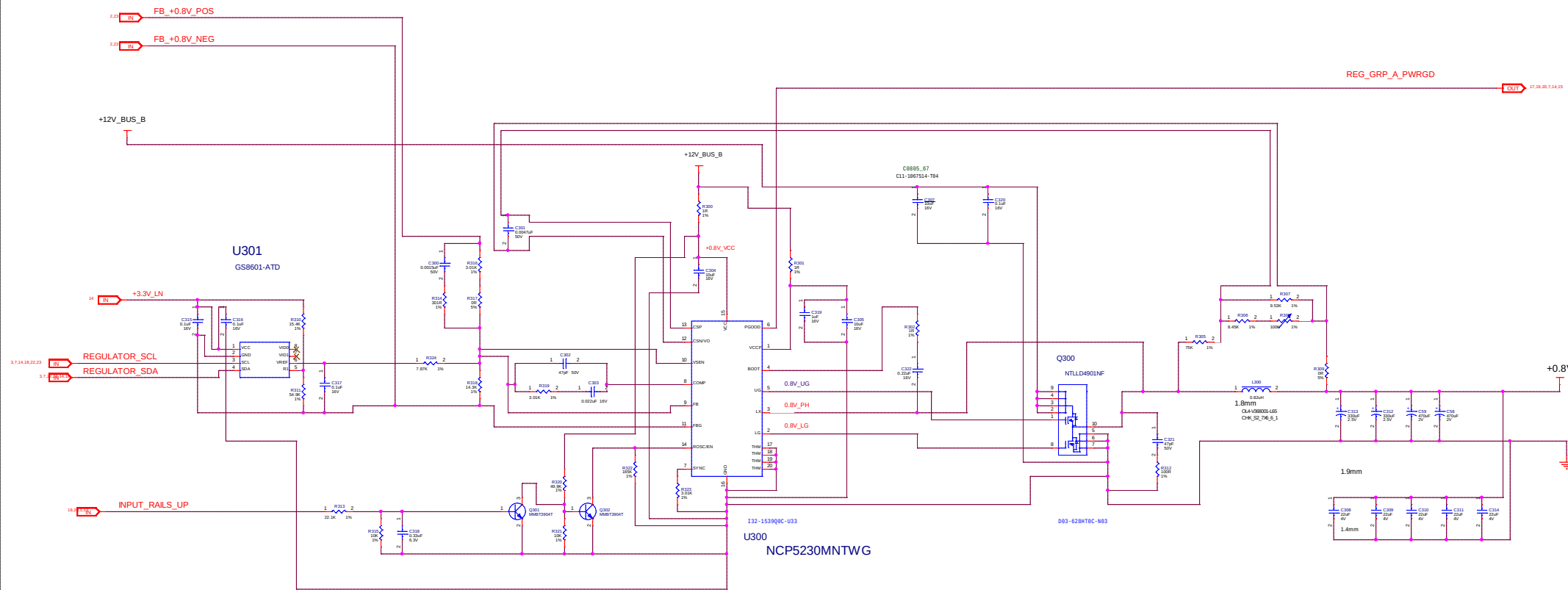
Rev





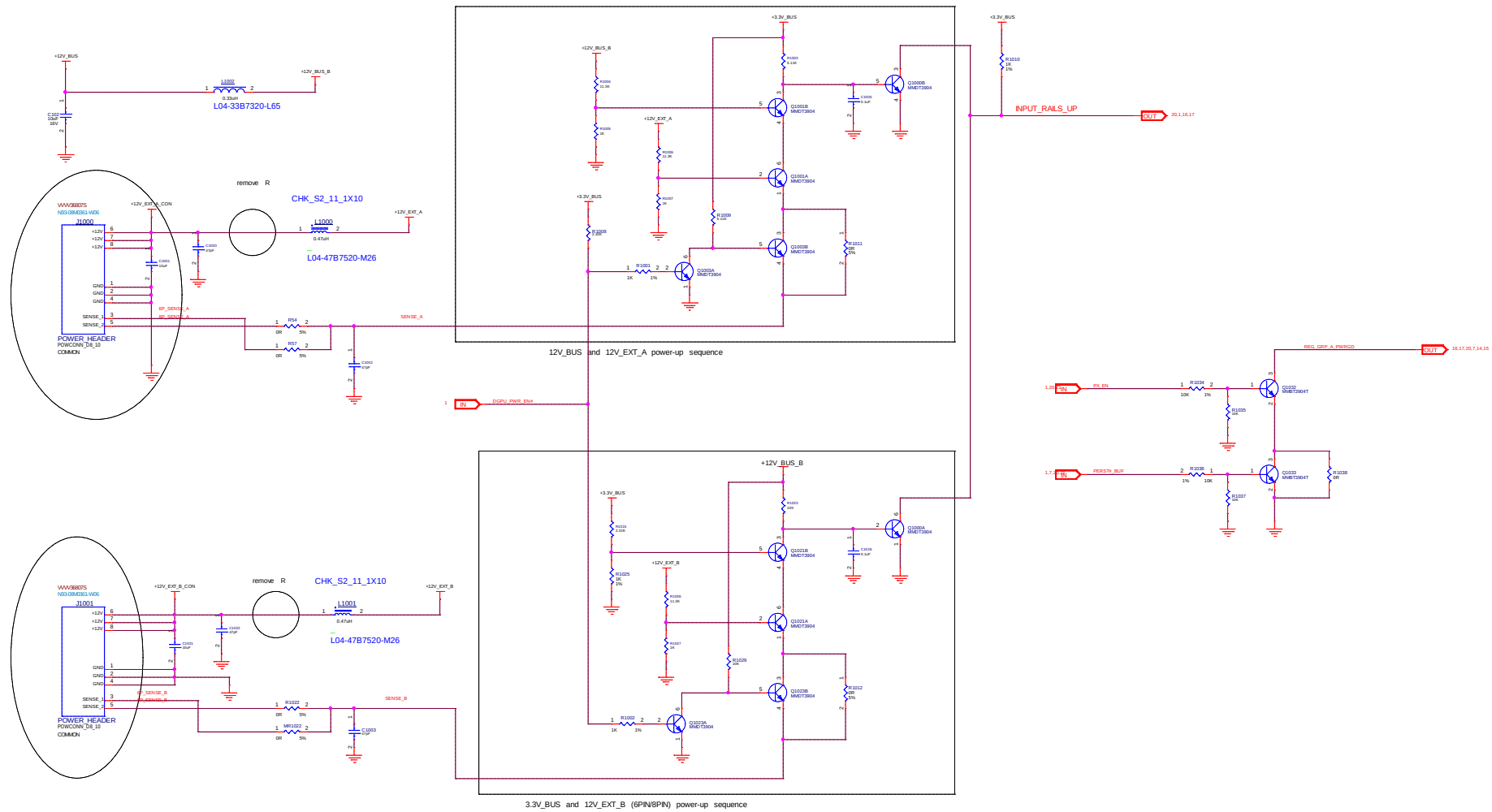


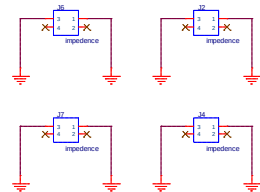
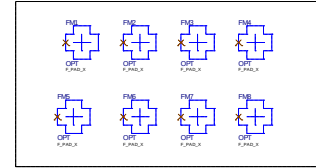
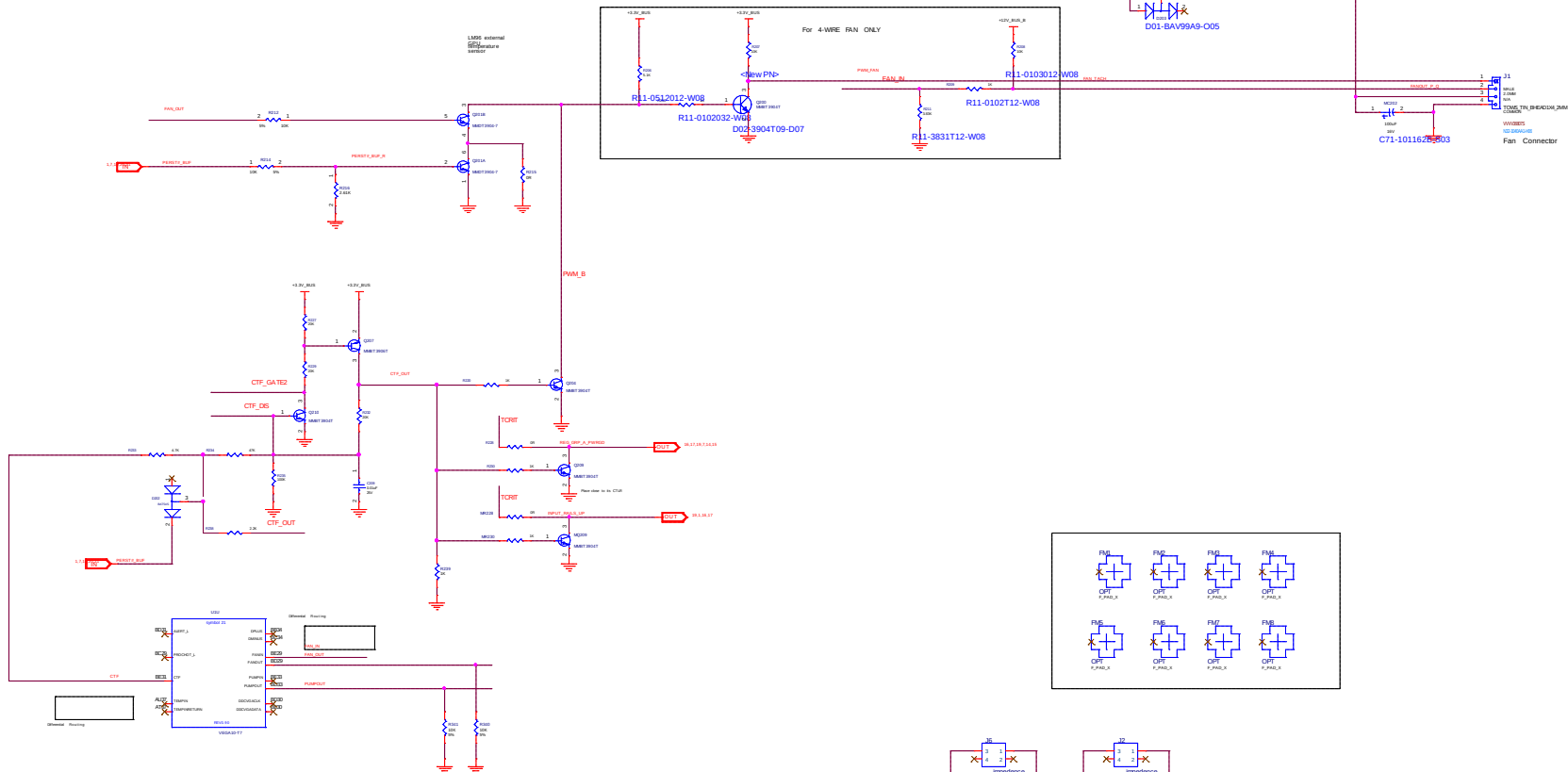
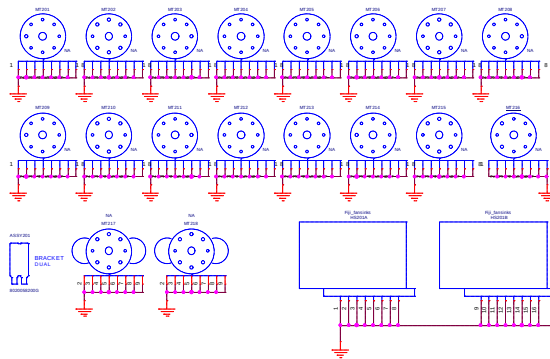




remove PCC

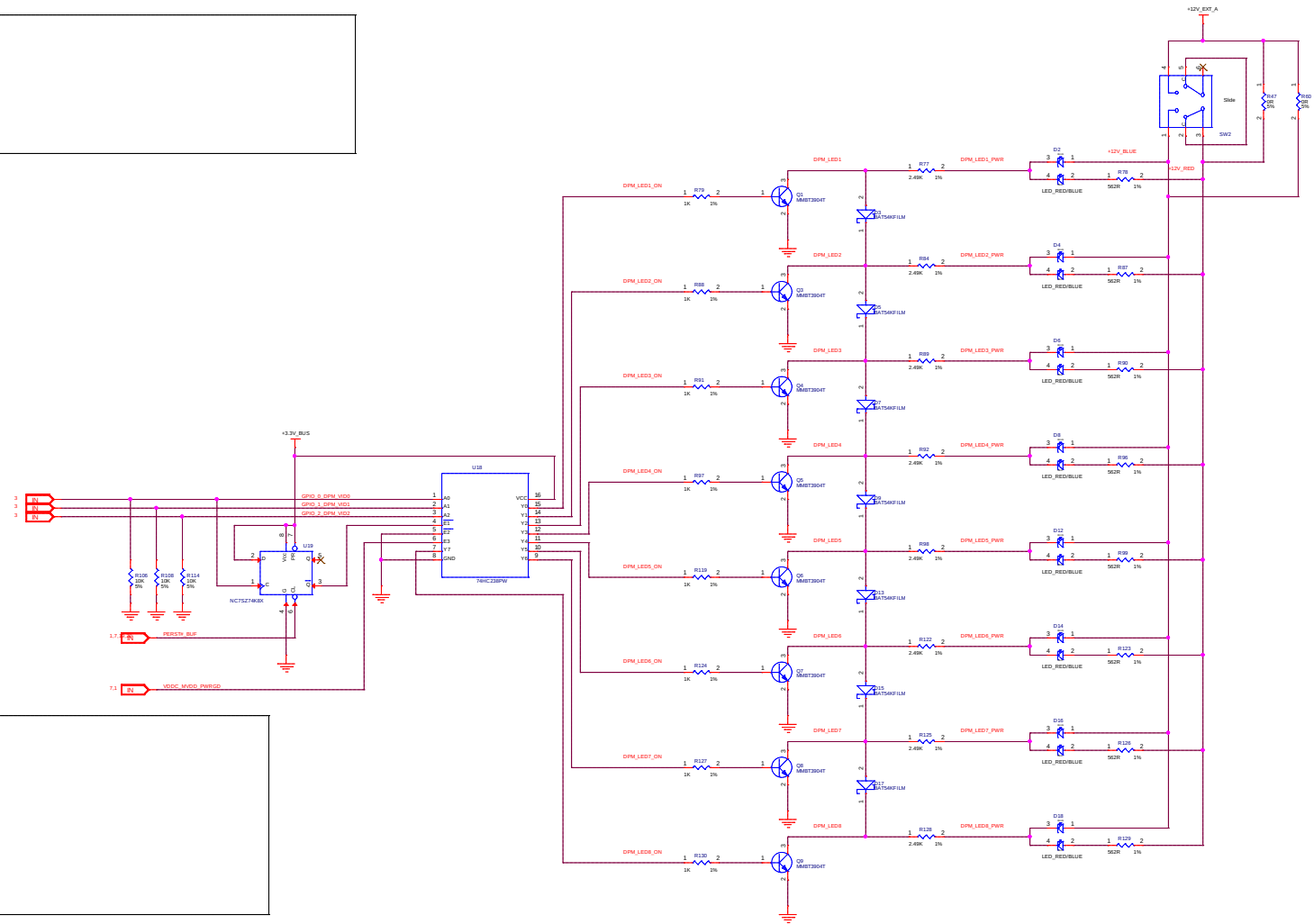
				MICRO-STAR INT'L CO.,LTD	
				MS-V368	
Size		Document	Description	Rev	
Custom		PCC		2.0	
Date: Thursday, July 06, 2017			Sheet	18	of 27





TOP
27.0.1226.178 80um
443.116.13 90um
L3
26.0.1140.186 80um
27.0.1020.229 90um

LED GREEN "ON" indicates BACO mode



RADEON Lightbar header



REMOVE

				MICRO-STAR INT'L CO.,LTD	
				MS-V368	
Size	Document	Description		Rev	
Custom	PIGLUE			2.0	
Date: Thursday, May 06, 2017		Sheet	23	of	27

				MICRO-STAR INT'L CO.,LTD	
				MS-V368	
Size	Document	Description			Rev
Custom	NA				2.0
Date: Thursday, July 06, 2017				Sheet	24 of 27

0	A	10/9/15	Initial schematic creation
B	6/30/16	Many Changes on PI circuit , Minor Chnages on SMPS	
C	9/30/16	Many Changes on PI circuit , ASIC SYMBOL UPDATED FOR FLASH ROM INTERFACE , flash ROM Si/So swapped , RP1 added	
D	11/29/16	Changes on PI circuit , 100MHz HCSL clock source replced (contact AMD for detailes)	
E	01/05/17	PI circuit removed , 200R added to DP AUX (6 resistors)	
-00	02/02/17	1.8V/VPP Kelvin resistors dual footprinted , DP AUX (6 resistors changed to 100R) , Changed R4033 to 360R 1206 and moved to 12V_AUX , SMBUS Address note swap , J4001 removed , R115/R116 changed to 61.9R , R205 added	

	MICRO-STAR INT'L CO.,LTD		
	MS-V368		
	Size Custom	Document NA	Description Rev 2.0
	Date: Thursday, July 06, 2017		Sheet 26 of 27

