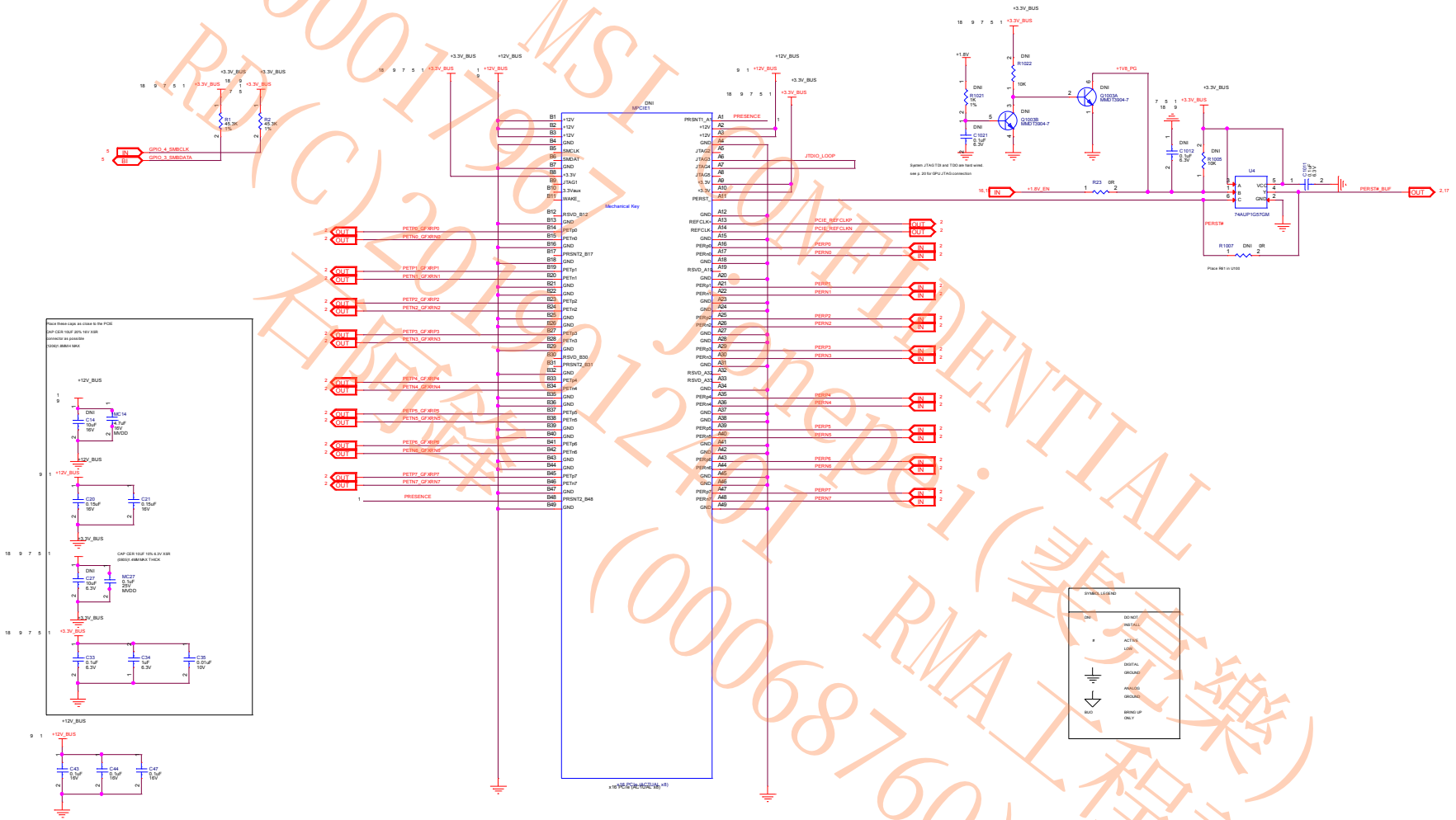


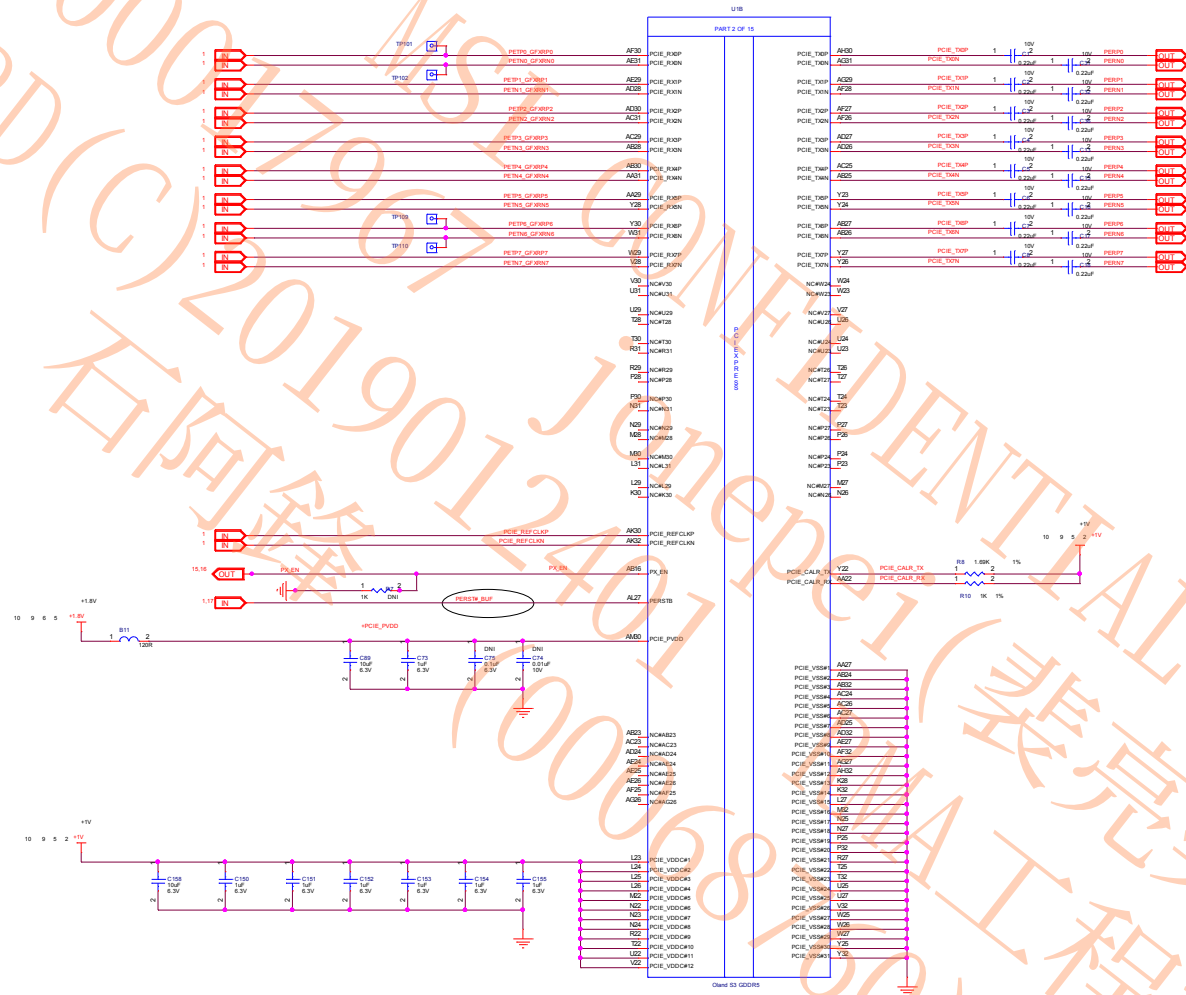
PCI-EXPRESS EDGE CONNECTOR



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SHEET: PCE Edge Connector	DATE: Wed Aug 29 03:27:33 2018		
SHEET NUMBER: 1 OF 20		TITLE: XXX	
DOCUMENT NUMBER: 105_d12900_00A			

OLAND PCIe Interface

NOTE: Some of the PCIe waypoints will
be available through vias on traces.



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SHEET: Oland PCIe48 Interface

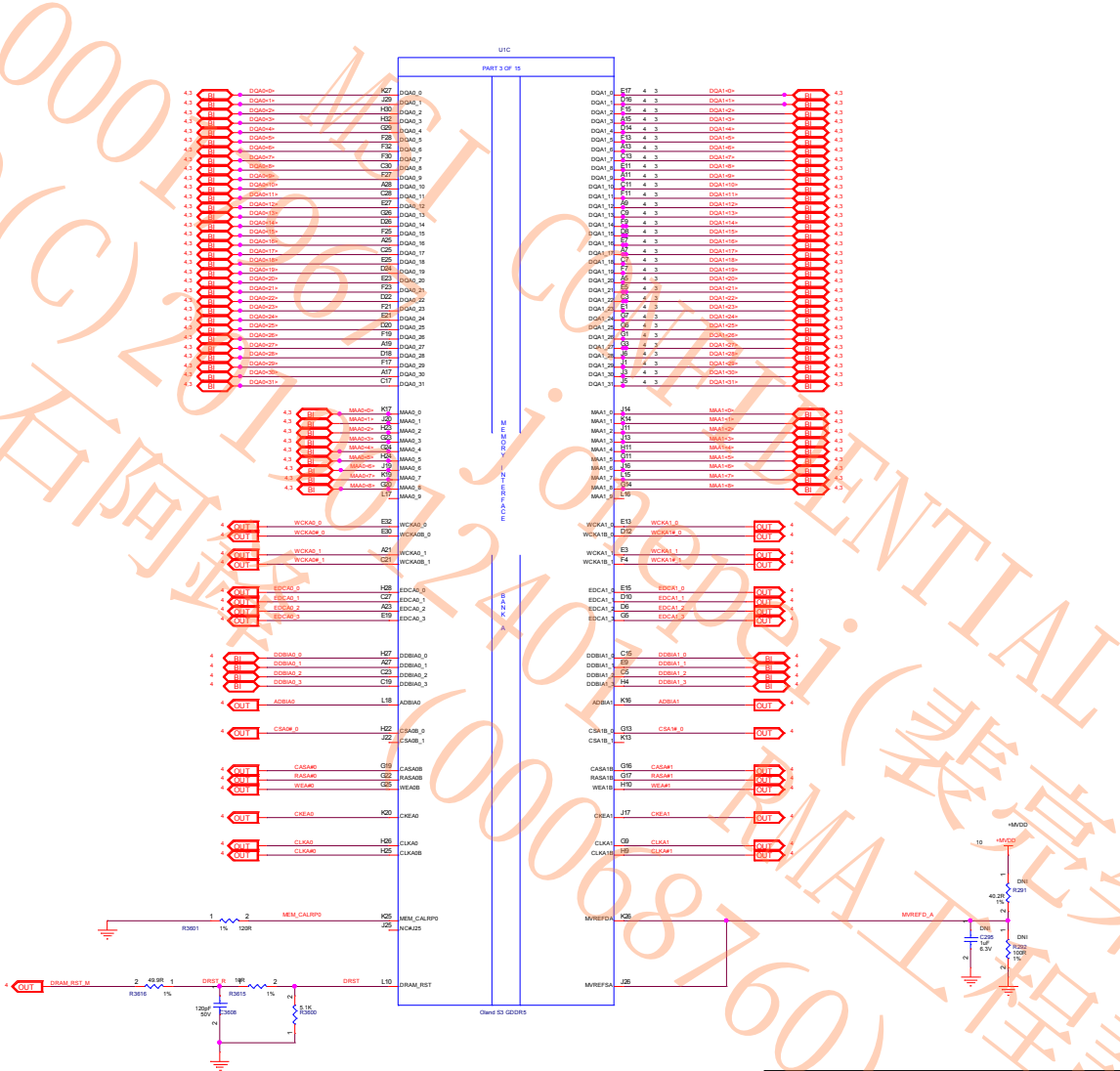
DATE: Thu Jan 05 05:41:57 2017

REV: P00

SHEET NUMBER: 2 OF 20

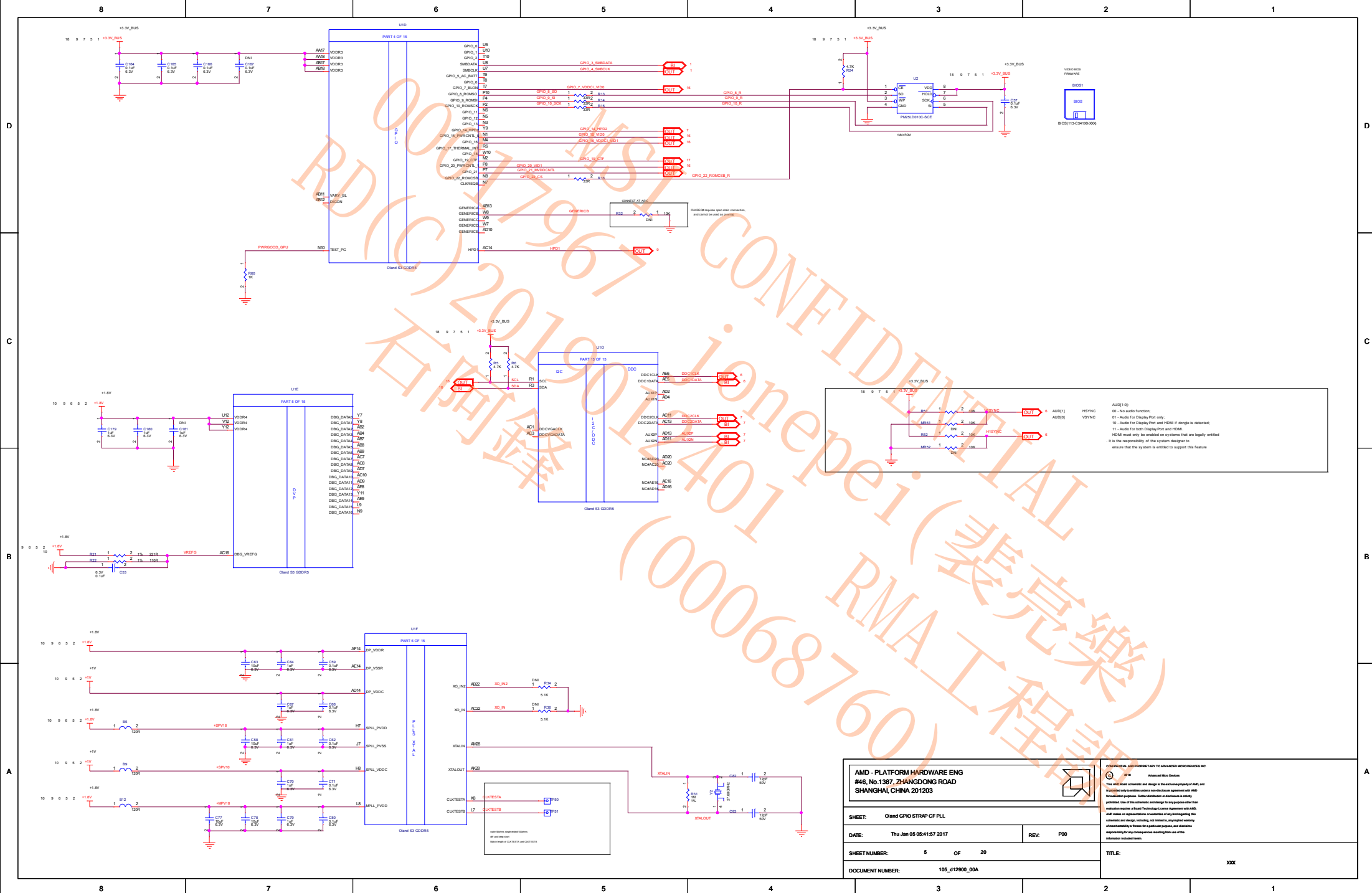
TITLE: XXX

DOCUMENT NUMBER: 105_412800_00A

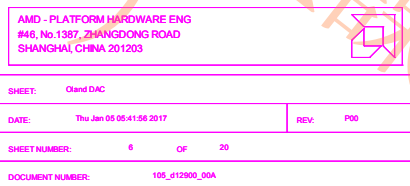


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SHEET: VDDCI		REV: P00		TITLE: XXX	
DATE: Wed Aug 29 03:24:49 2018					
SHEET NUMBER: 3 OF 20					
DOCUMENT NUMBER: 105_012900_00A					
NOTES: NOTE					





8



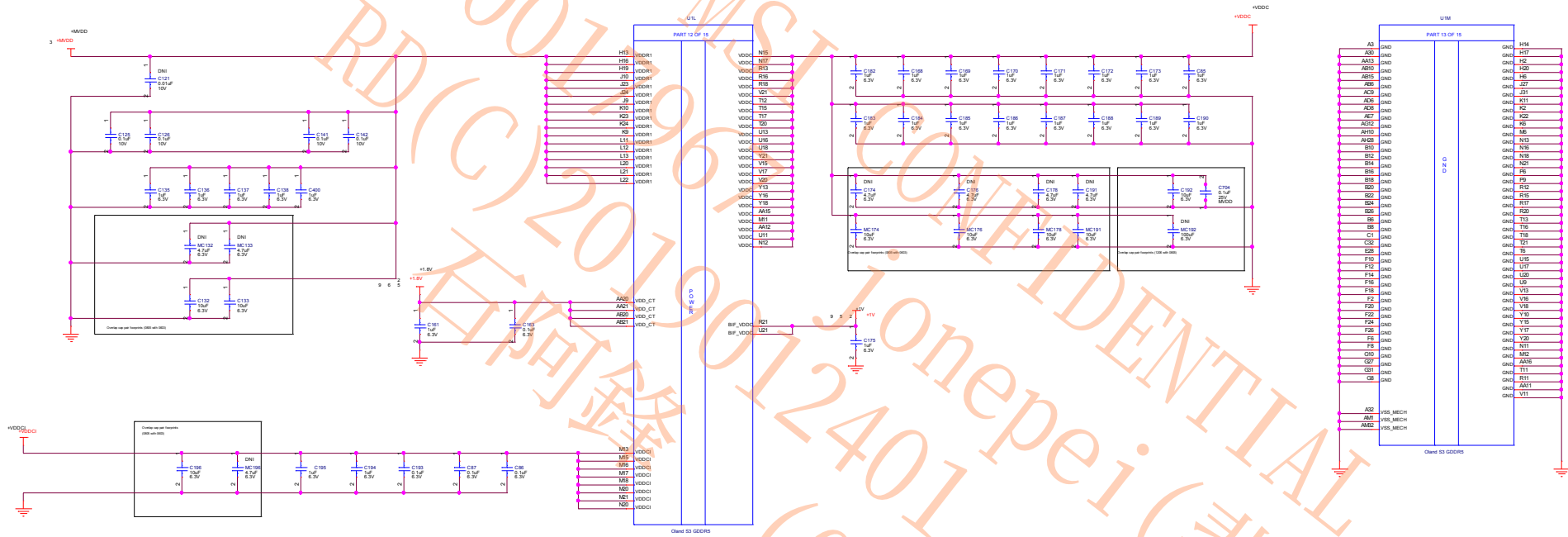
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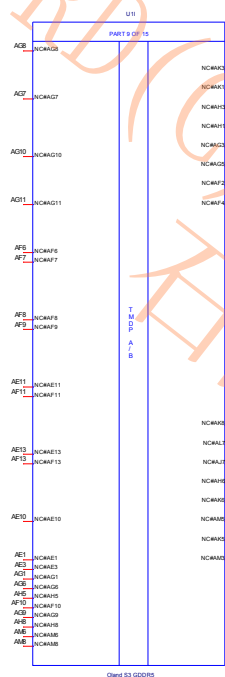
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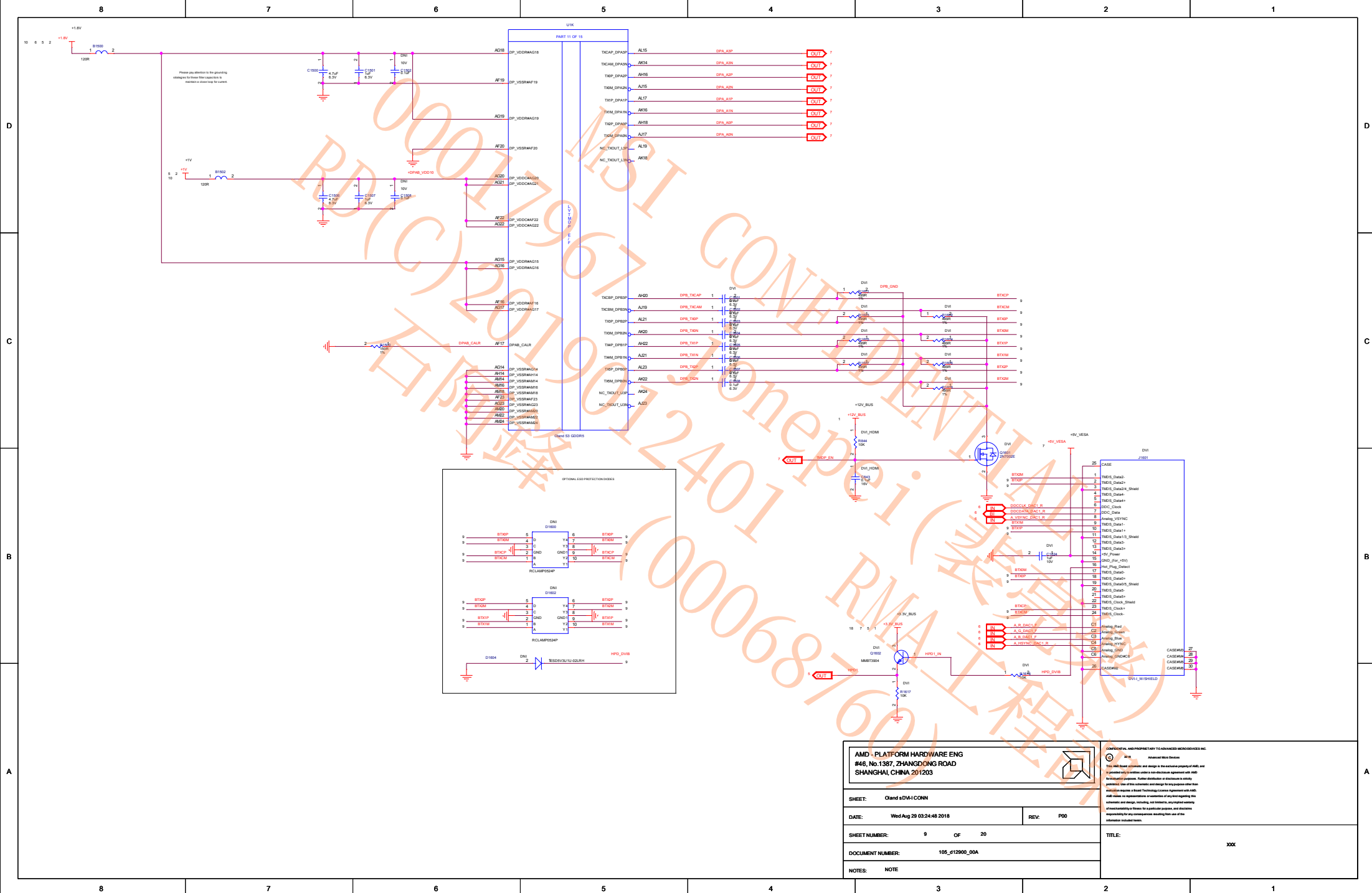
TITLE: XXX

XXX

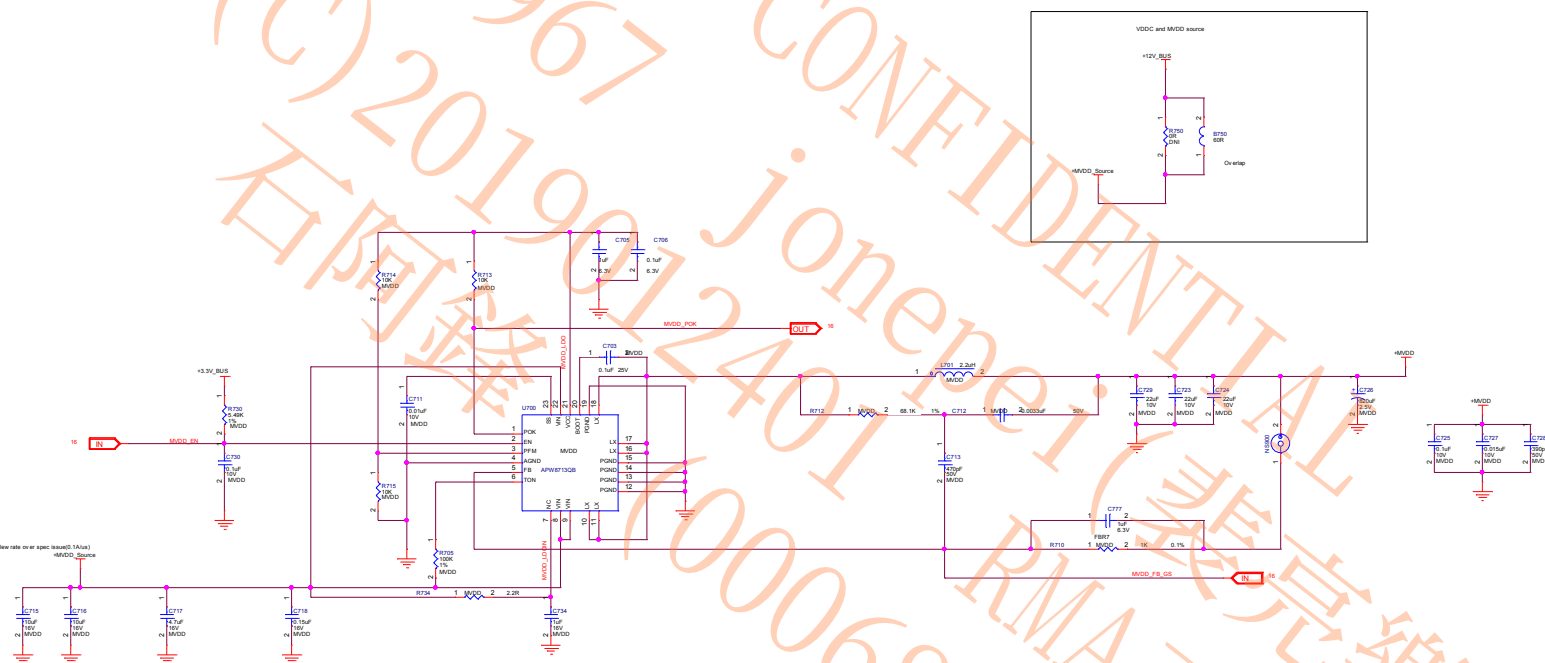
[illegible]

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DATE: Wed Aug 29 03:24:49 2018				TITLE: XXX	
SHEET NUMBER: 10 OF 20					
DOCUMENT NUMBER: 105_012900_00A					
NOTES: NOTE					





Regulator for MvDO



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SHEET: MVDD

DATE: Wed Aug 29 03:24:48 2018

REV: P00

SHEET NUMBER: 12 OF 20

DOCUMENT NUMBER: 105_d12900_00A

TITLE:

xxx

Regulator for VDD0

Use vVDDC_Sense to share L801 to eliminate PCIe 12V current slew rate over spec issue(0.1A/us)
vVDDC_Sense

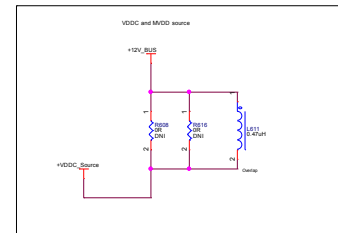
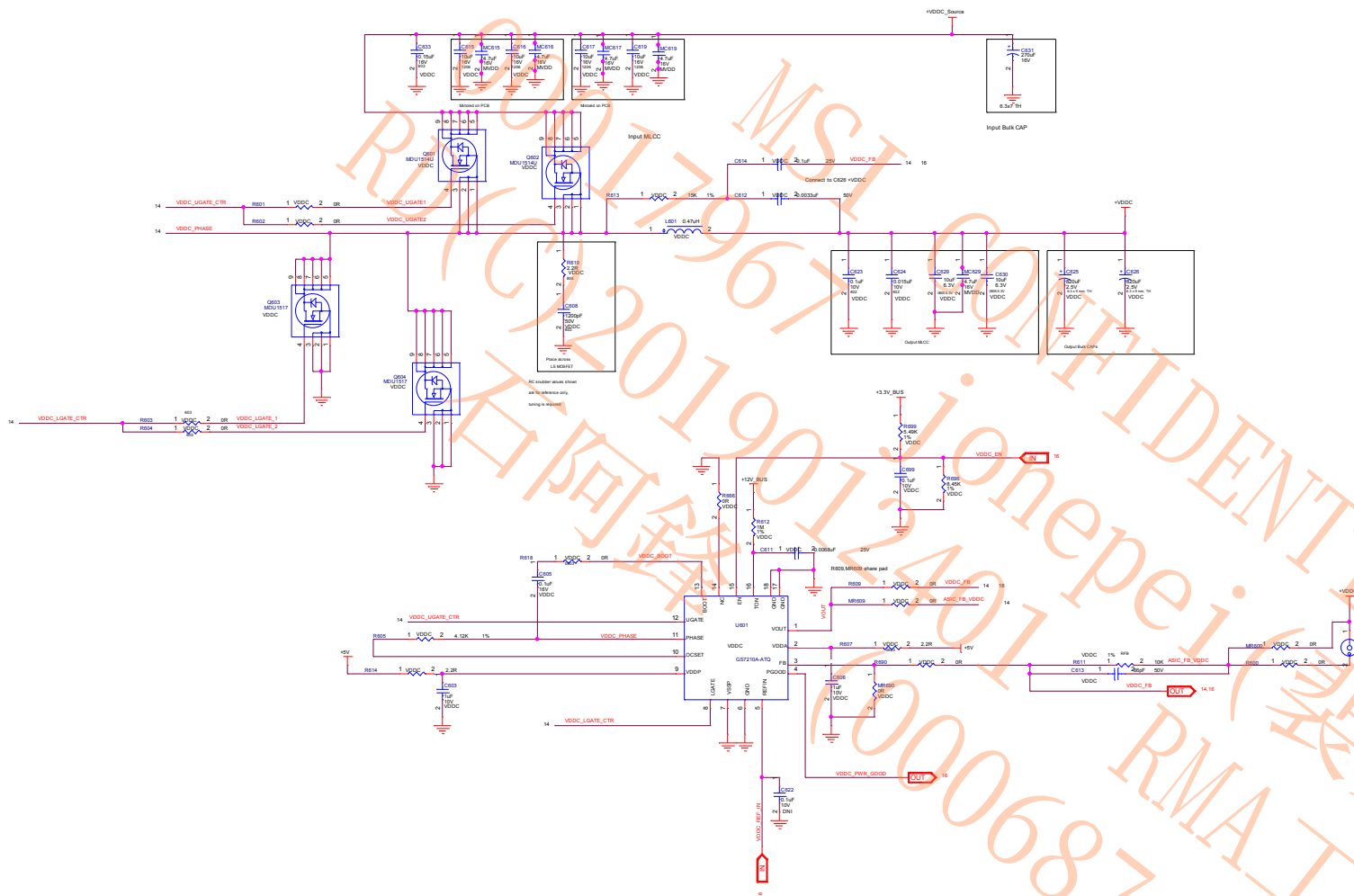
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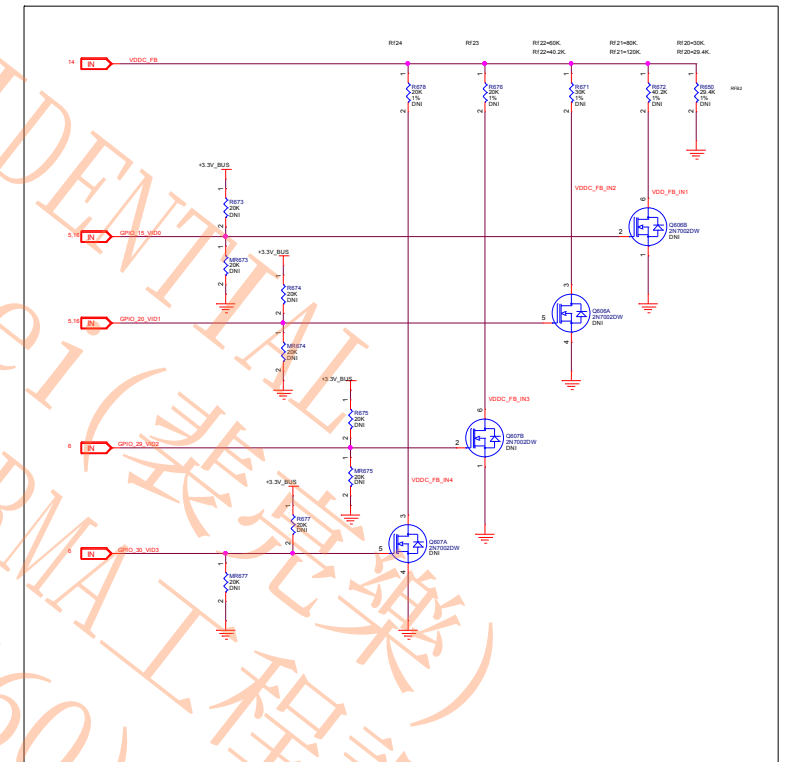
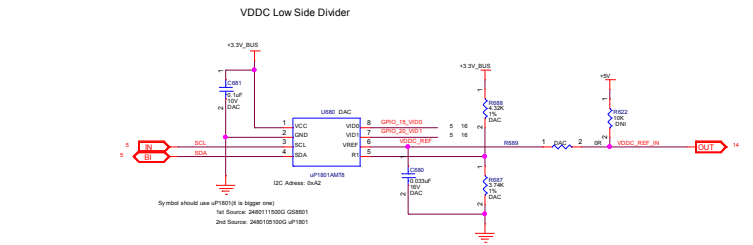
SHEET: VDDCI	
DATE: Wed Aug 29 03:24:49 2018	REV: P00
SHEET NUMBER: 13	OF 20
DOCUMENT NUMBER: 105_012900_00A	
NOTES: NOTE	

TITLE: XXX



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SHEET: VDDC		REV: P00		TITLE: XXX	
DATE: Thu Jan 05 05:41:56 2017					
SHEET NUMBER: 14		OF 20			
DOCUMENT NUMBER: 105_d12900_00A					

Don't support BACO
Support CTF (Internal)



SHEET: VDDCI	
DATE: Wed Aug 29 03:24:49 2018	REV: P00
SHEET NUMBER: 16 OF 20	
DOCUMENT NUMBER: 105_d12900_00A	
NOTES: NOTE NOTE	

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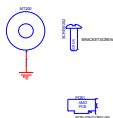
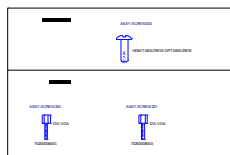
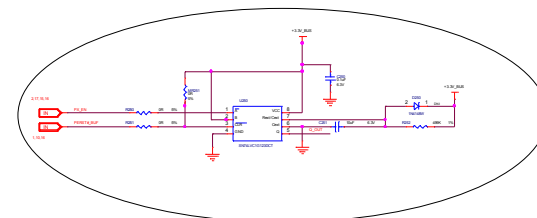
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TITLE: _____

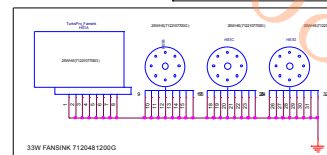
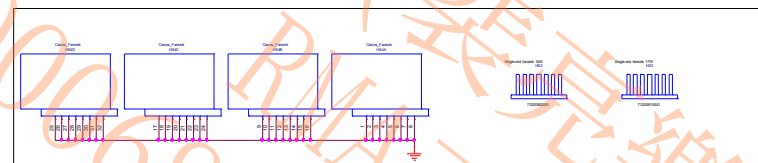
XXX

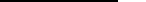
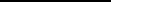
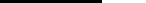
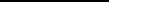


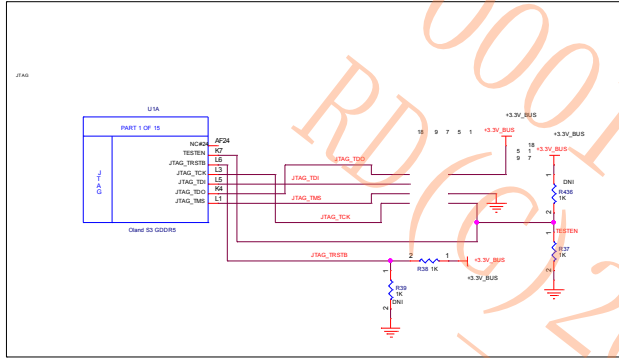
This circuit provides a minimum voltage for the subsequent circuit (TTL) even when the input is 0V.



Bracket, VGA, HDMI, DVI	8Q2005200G
Bracket, VGA, DP(16), DVI	8Q20052100G
Bracket, LP, DP(16), DVI	8Q20053300G
Bracket, LP, HDMI, DVI	8Q2005340G



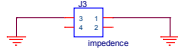
AMD - PLATFORM SOFTWARE ENG #46, No.1387, ZHANGSONG ROAD SHANGHAI CHINA 201303			1.  2.  3.  4.  5.  6.  7.  8.  9.  10. 
SHEET: VIDEO		REV: F00	
DATE: Wed Aug 08 00:00:00 2018		FILE:	
SHEET NUMBER: 07 OF 30		TITLE:	
DOCUMENT NUMBER: HW_01000_00A		XXX XXX	
NOTES: NONE		1.  2.  3.  4.  5.  6.  7.  8.  9.  10. 	



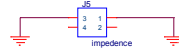
TOP
Single end
Memory data
45 ohm +/- 5 ohm
5.12 mils



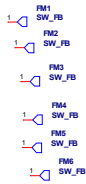
Bottom
Single end
Address trunk
45 ohm +/- 5 ohm
5.12 mils



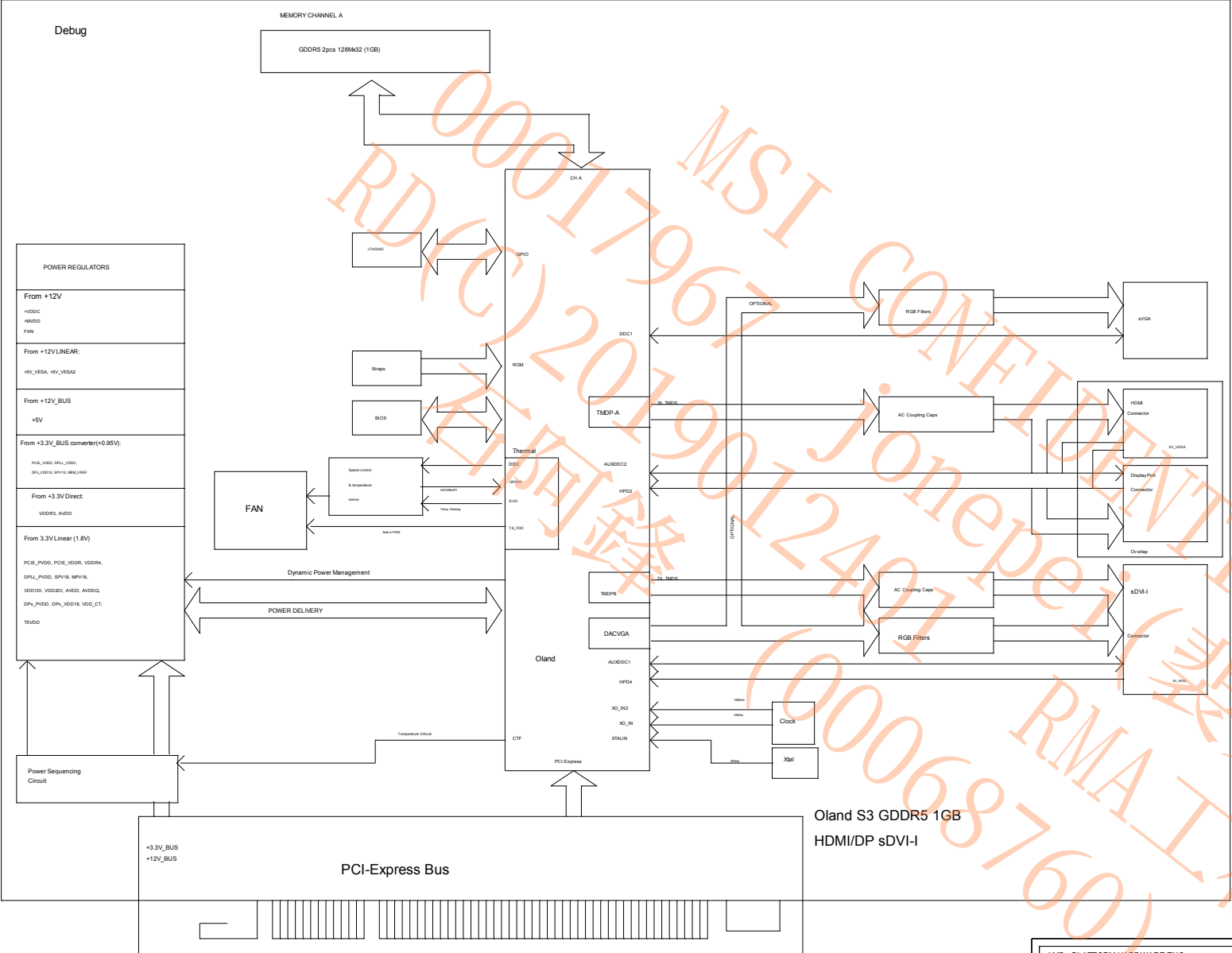
TOP
Different
TMS
85 ohm +/- 10 %
4.33 mils / 5.51 mils



Bottom
Different
PEX_PCIE
85 ohm +/- 10 %
4.33 mils / 5.51 mils



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SHEET: Debug		REV: P00		TITLE:	
DATE: Thu Jan 05 05:41:58 2017				XXX	
SHEET NUMBER: 18 OF 20					
DOCUMENT NUMBER: 105_d12900_00A					



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SHEET: Diagram	REV: P00
DATE: Thu Jan 05 05:41:15 2017	
SHEET NUMBER: 19 OF 20	
DOCUMENT NUMBER: 105_d12900_00A	

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TITLE: XXX	

