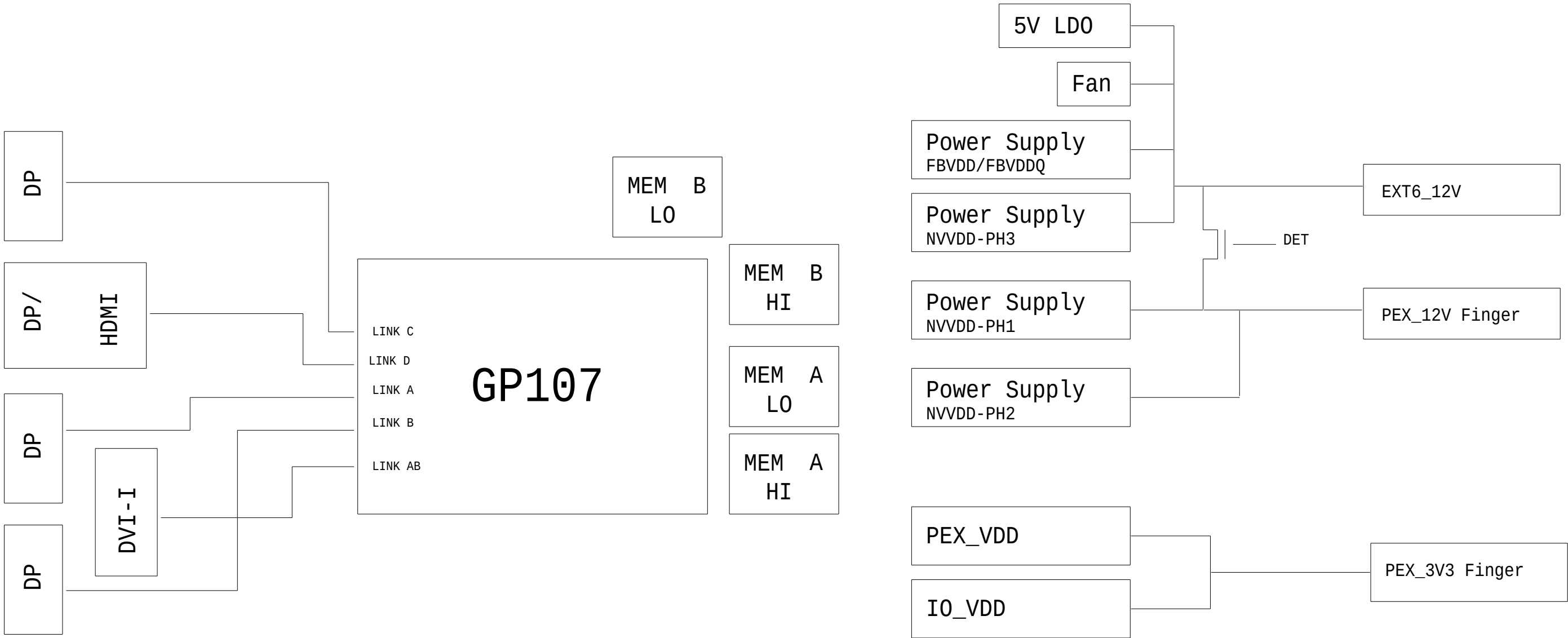


Block Diagram



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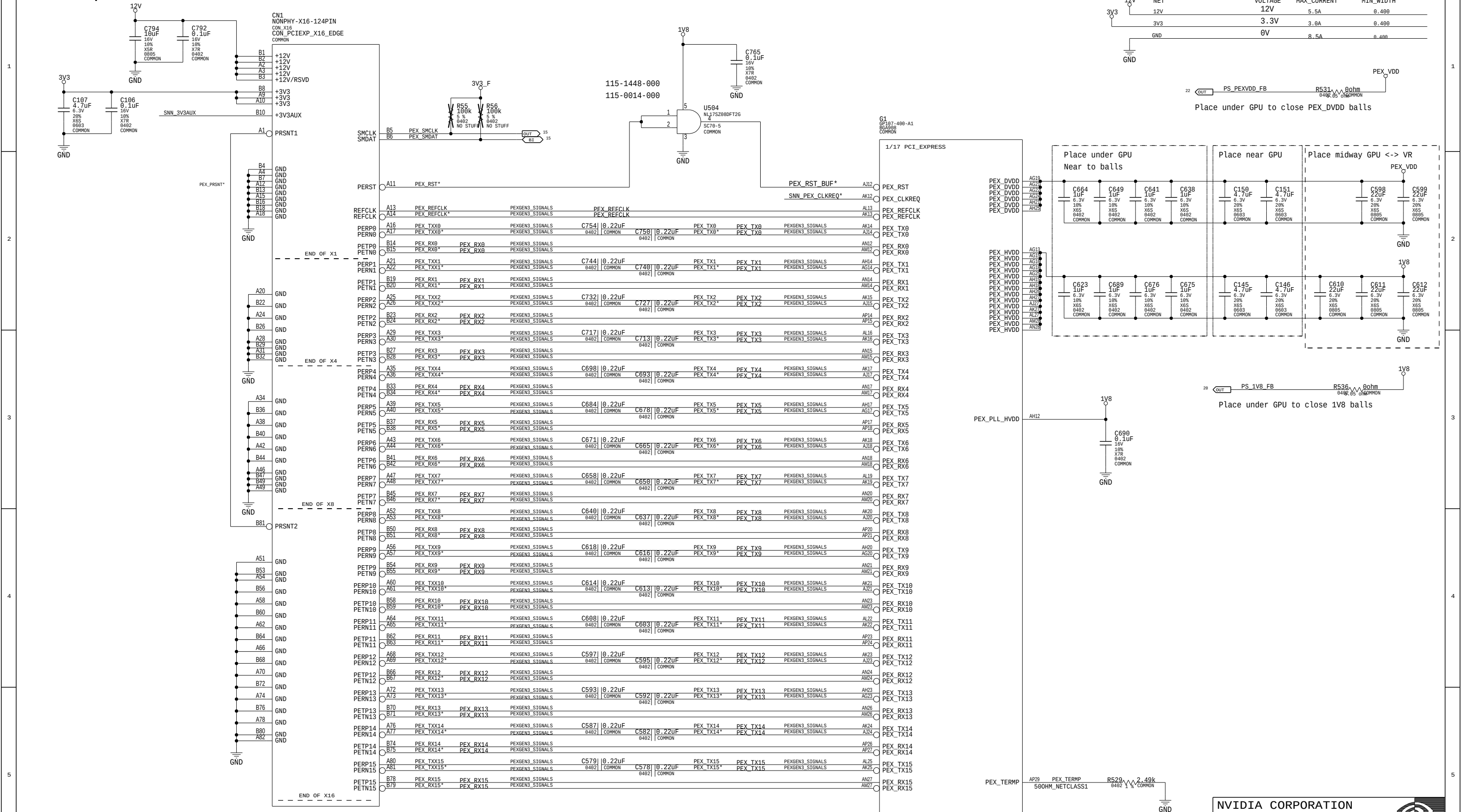
PCB REV PG210-B00

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PCI Express

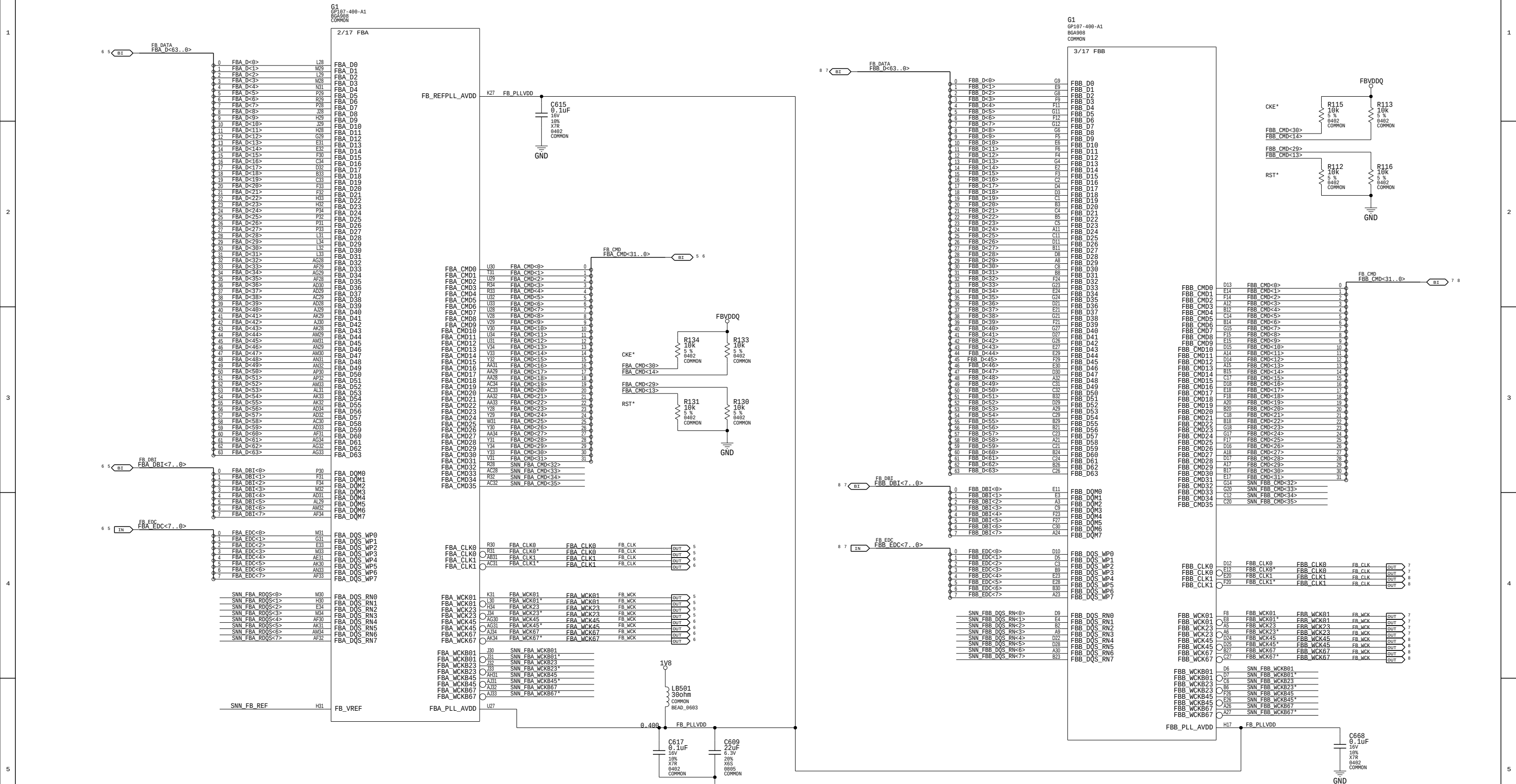


ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PCI Express

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PCB REV	PG210-B00	PAGE	3 OF 27
BOM REV	D	DATE	08-AUG-2016

MEMORY: GPU Partition A/B

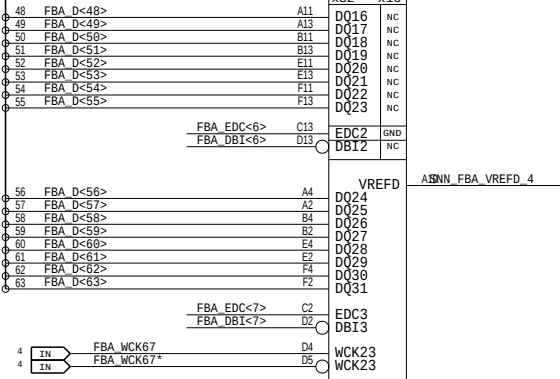
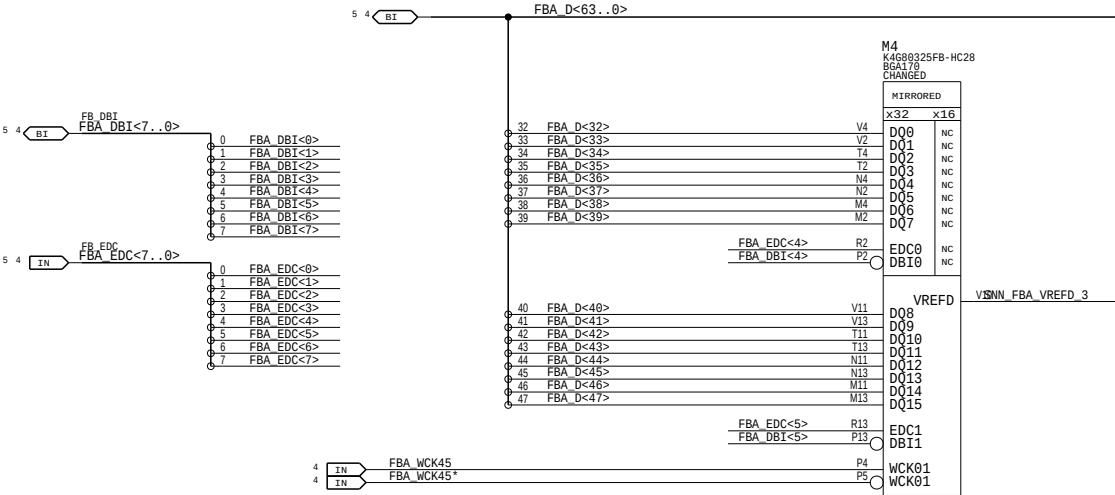
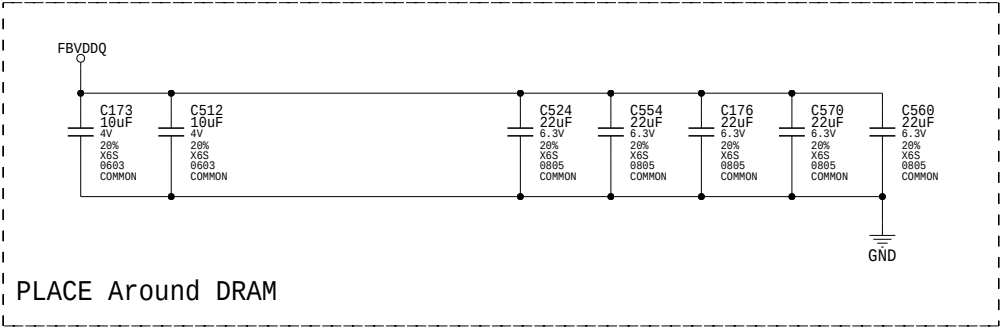
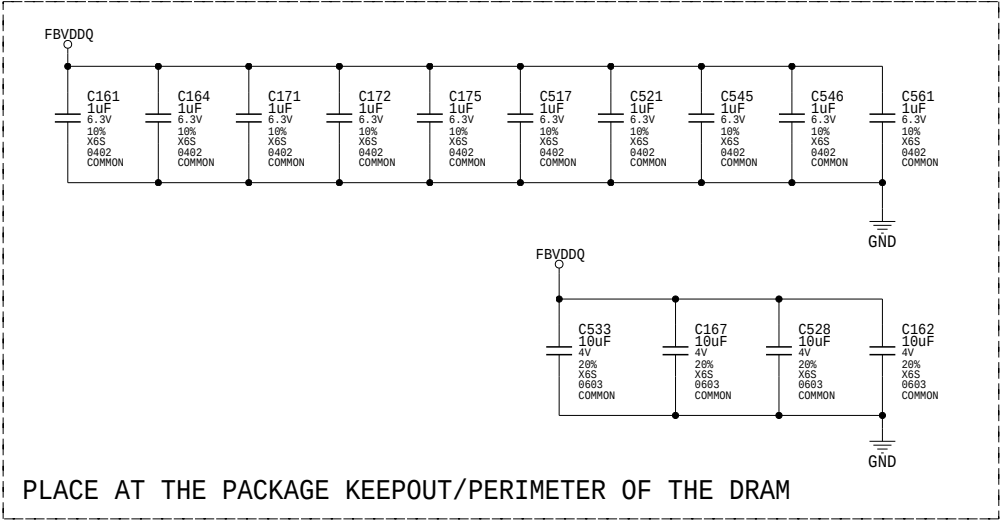
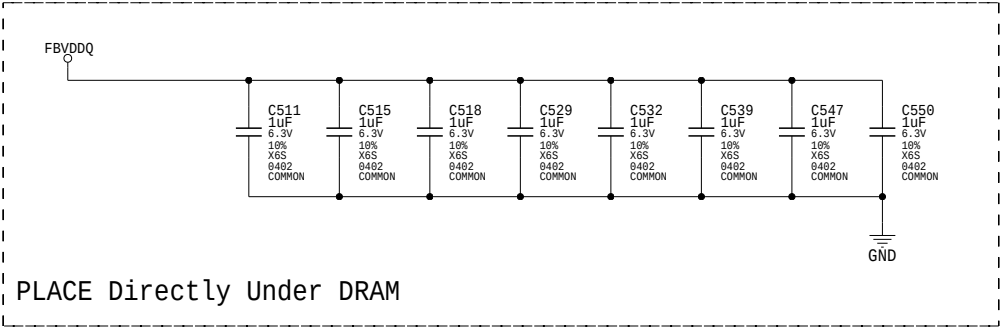
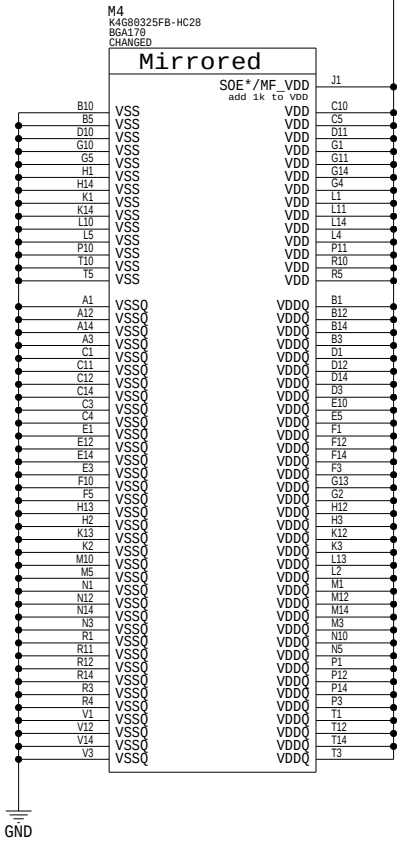
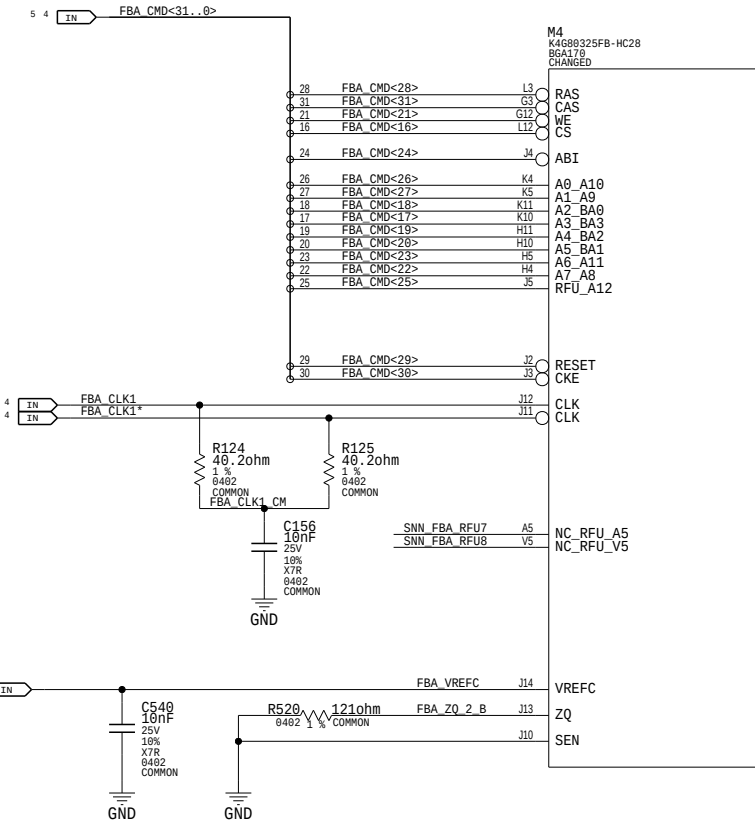


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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	MEMORY: GPU Partition A/B

MEMORY: FBA Partition 63..32

CMD	0..31	32..63
CMD0	CS*	
CMD1	A3_BA3	
CMD2	A2_BA0	
CMD3	A4_BA2	
CMD4	A5_BA1	
CMD5	WE*	
CMD6	A7_A8	
CMD7	A6_A11	
CMD8	AB1*	
CMD9	A12_RFU	
CMD10	A0_A10	
CMD11	A1_A9	
CMD12	RAS*	
CMD13	RST*	
CMD14	CKE*	
CMD15	CAS*	
CMD16		CS*
CMD17		A3_BA3
CMD18		A2_BA0
CMD19		A4_BA2
CMD20		A5_BA1
CMD21		WE*
CMD22		A7_A8
CMD23		A6_A11
CMD24		AB1*
CMD25		A12_RFU
CMD26		A0_A10
CMD27		A1_A9
CMD28		RAS*
CMD29		RST*
CMD30		CKE*
CMD31		CAS*



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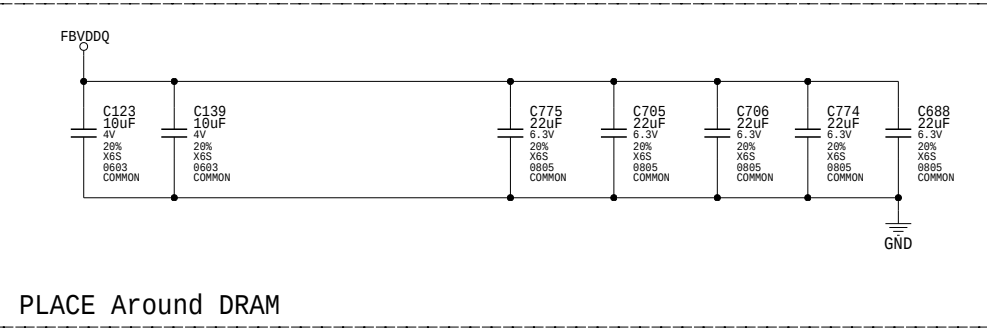
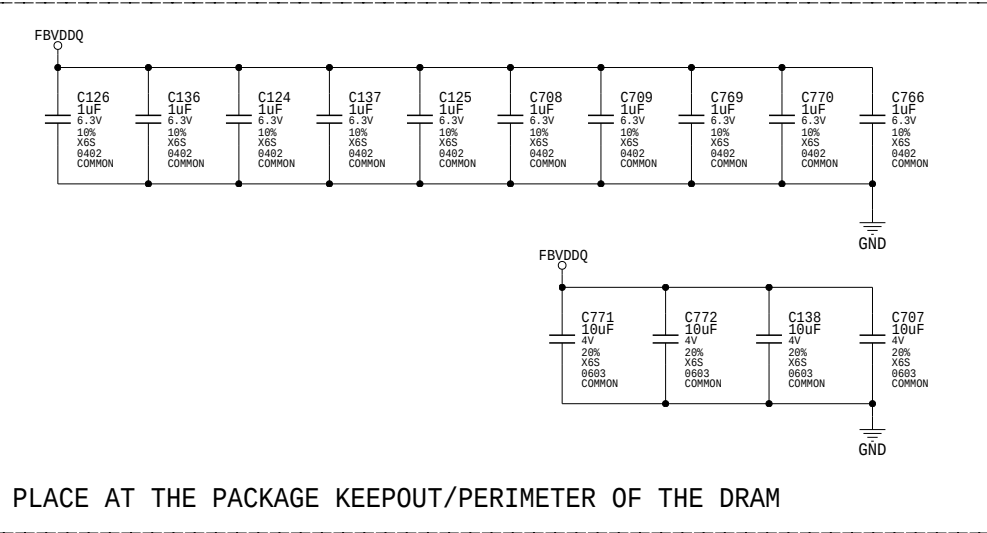
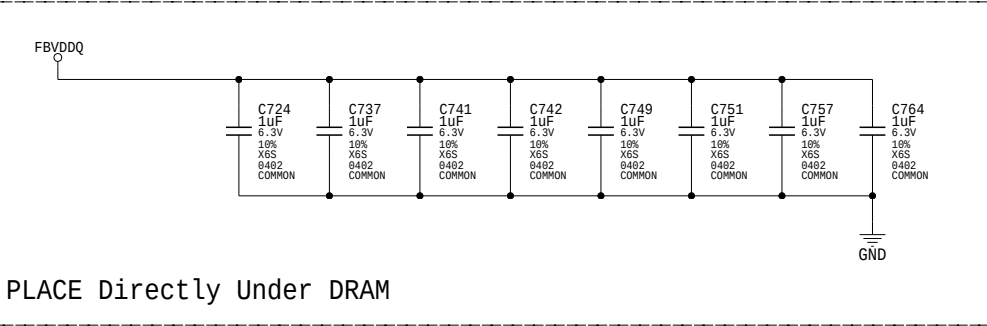
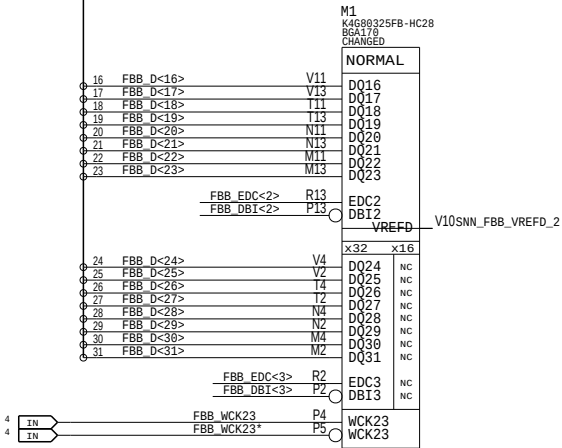
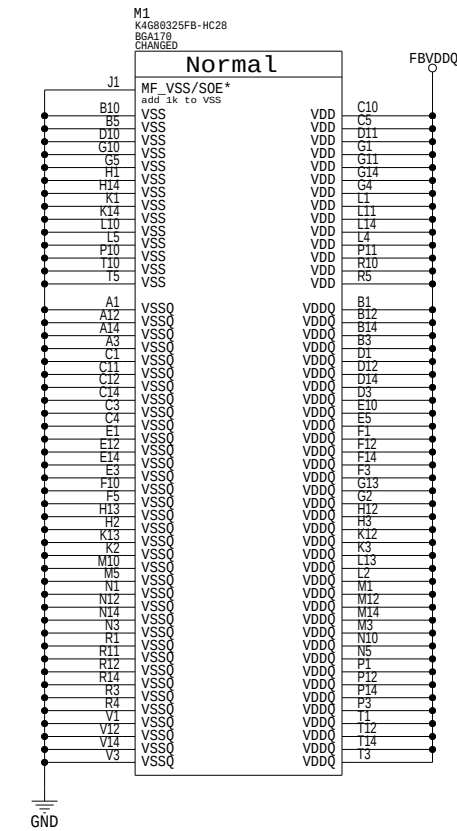
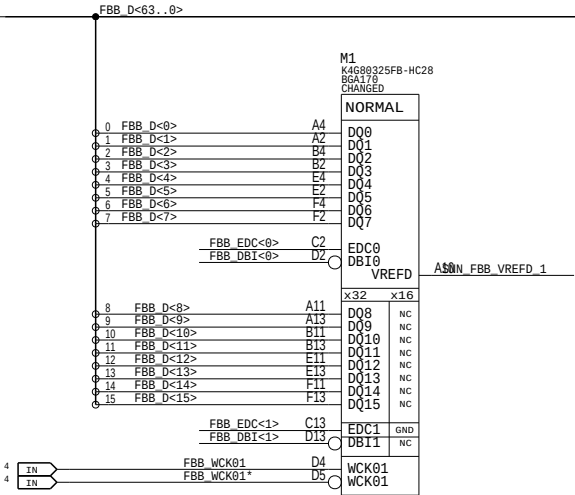
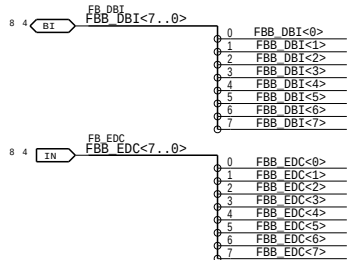
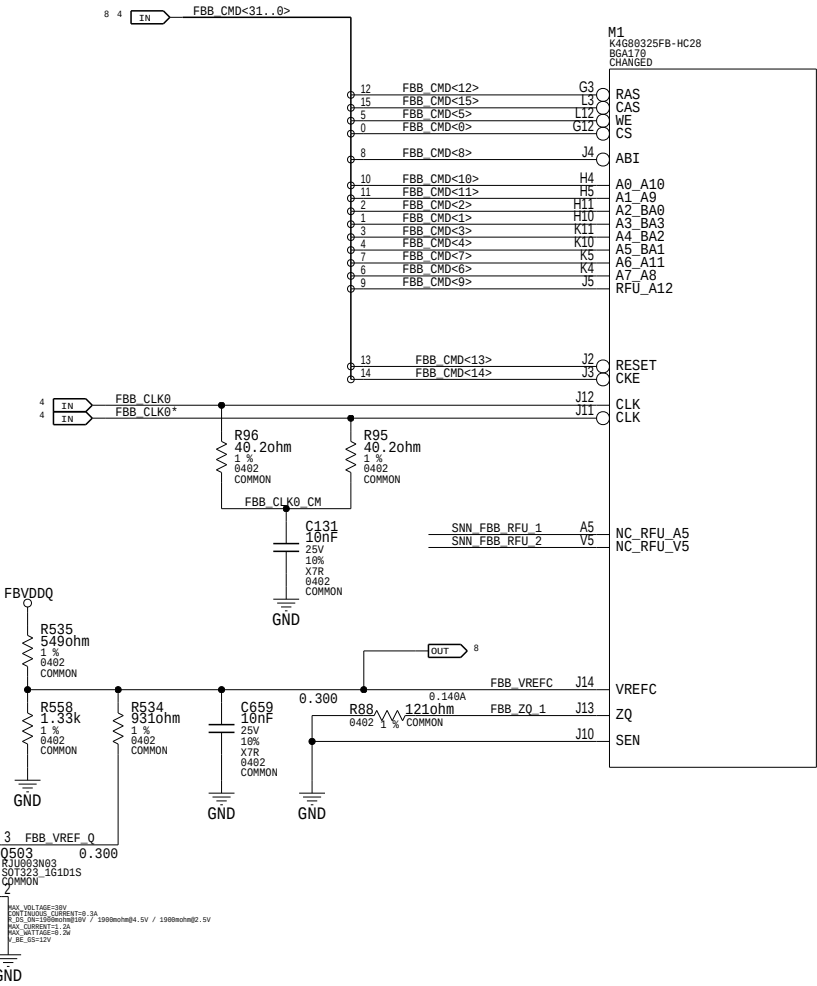
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MEMORY: FBB Partition 31..0

CMD	0..31	32..63
CMD0	CS*	
CMD1	A3_BA3	
CMD2	A2_BA0	
CMD3	A4_BA2	
CMD4	A5_BA1	
CMD5	WE*	
CMD6	A7_A8	
CMD7	A6_A11	
CMD8	ABT*	
CMD9	A12_RFU	
CMD10	A0_A10	
CMD11	A1_A9	
CMD12	RAS*	
CMD13	RST*	
CMD14	CKE*	
CMD15	CAS*	
CMD16	CS*	
CMD17	A3_BA3	
CMD18	A2_BA0	
CMD19	A4_BA2	
CMD20	A5_BA1	
CMD21	WE*	
CMD22	A7_A8	
CMD23	A6_A11	
CMD24	ABT*	
CMD25	A12_RFU	
CMD26	A0_A10	
CMD27	A1_A9	
CMD28	RAS*	
CMD29	RST*	
CMD30	CKE*	
CMD31	CAS*	



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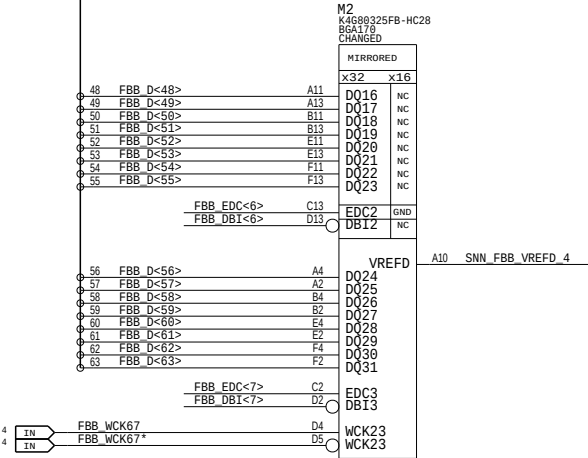
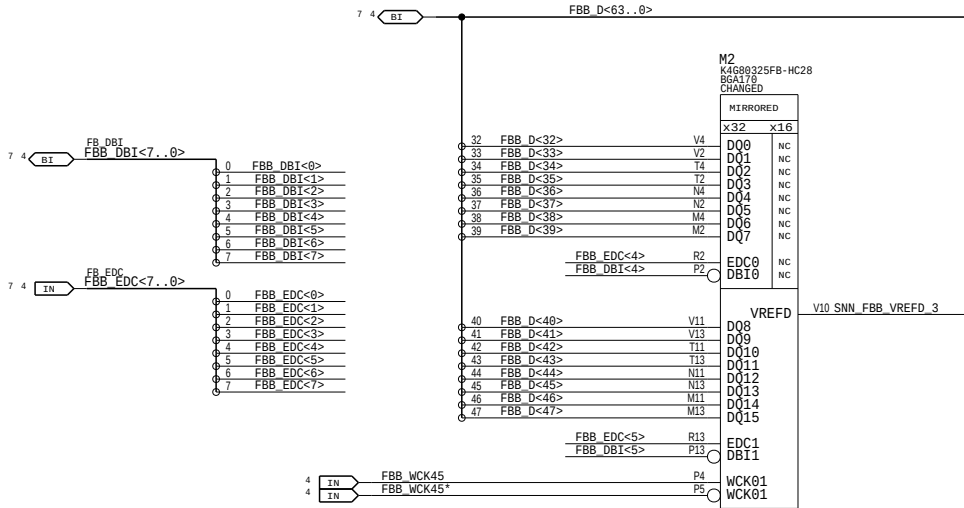
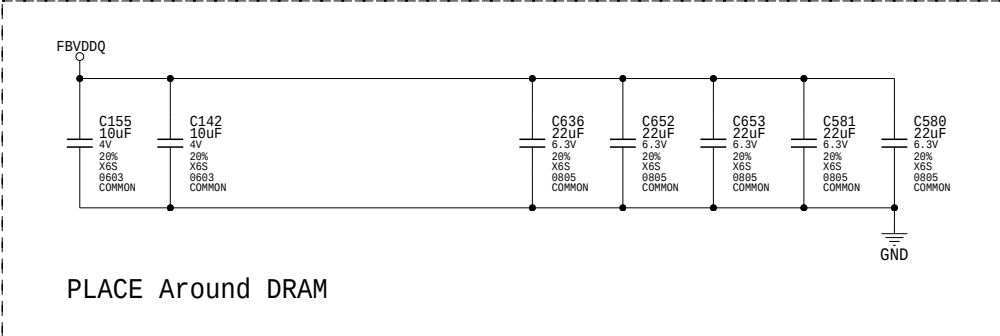
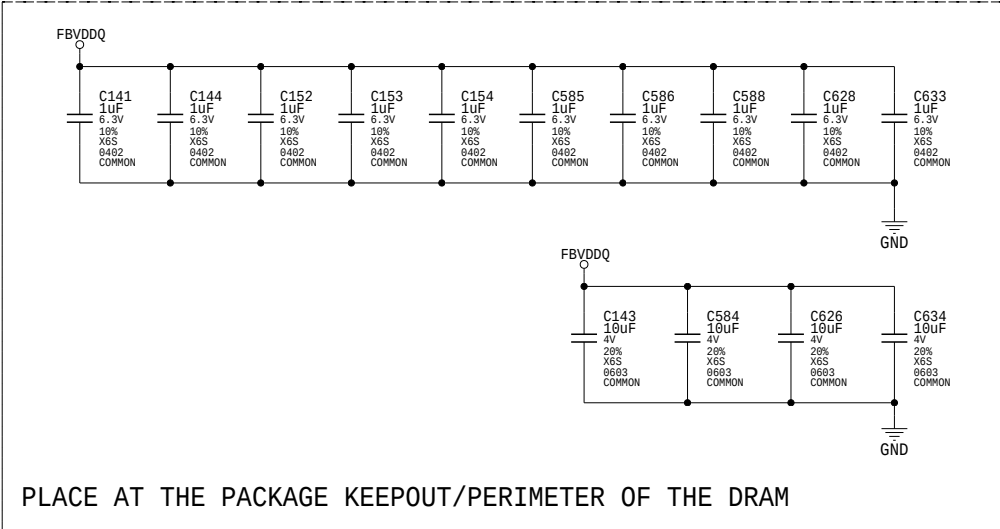
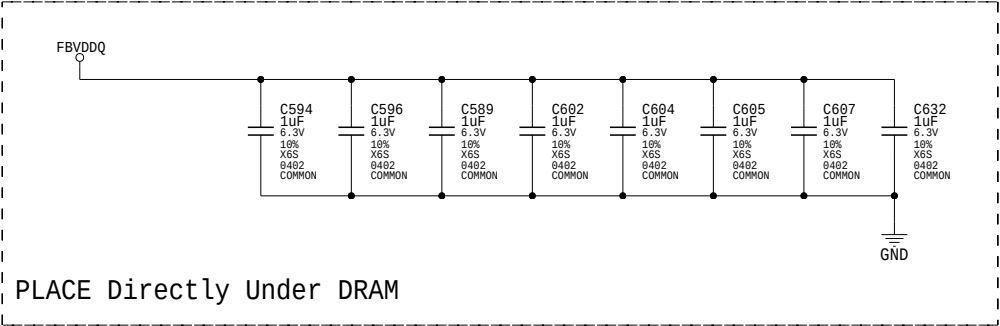
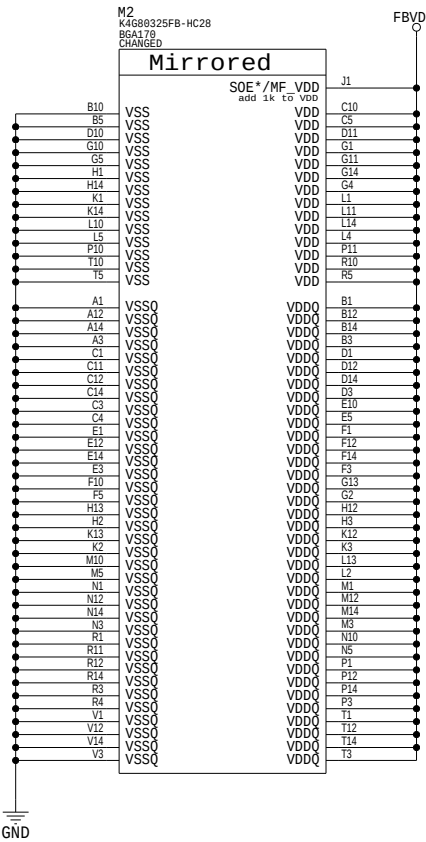
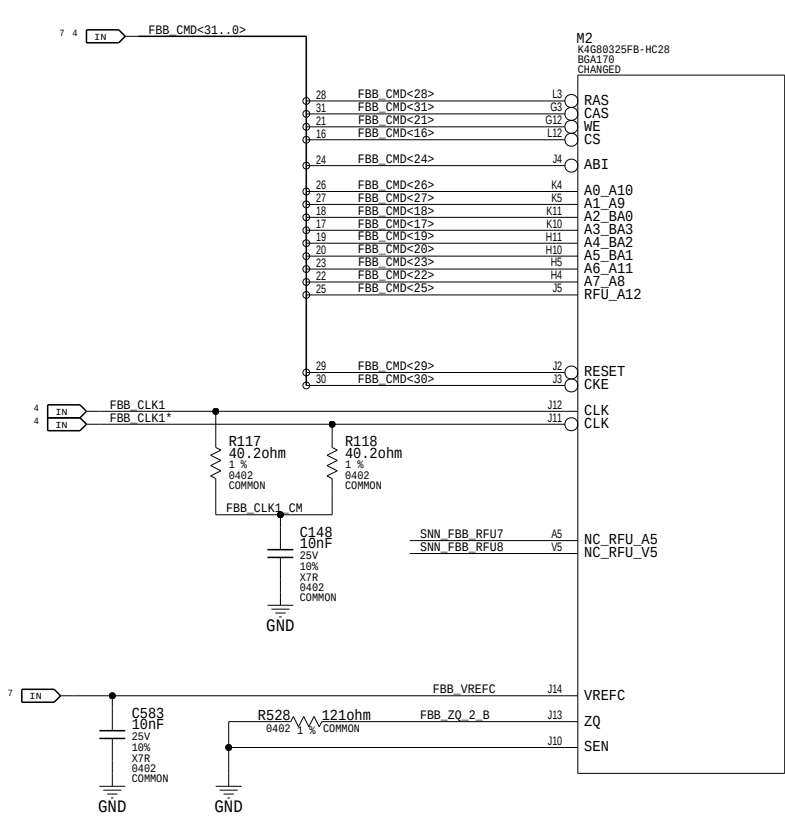
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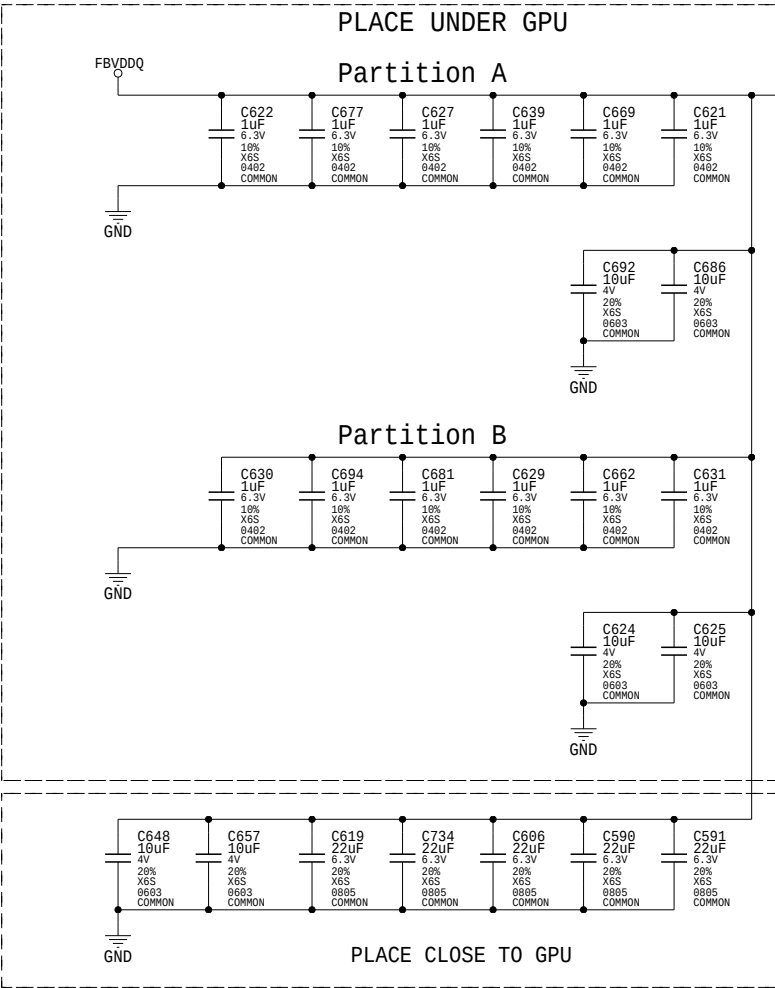
DATE 08-AUG-2016

MEMORY: FBB Partition 63..32

CMD	0..31	32..63
CMD0	CS*	
CMD1	A3_BA3	
CMD2	A2_BA0	
CMD3	A4_BA2	
CMD4	A5_BA1	
CMD5	WE*	
CMD6	A7_A8	
CMD7	A6_A11	
CMD8	AB1*	
CMD9	A12_RFU	
CMD10	A0_A10	
CMD11	A1_A9	
CMD12	RAS*	
CMD13	RST*	
CMD14	CKE*	
CMD15	CAS*	
CMD16		CS*
CMD17		A3_BA3
CMD18		A2_BA0
CMD19		A4_BA2
CMD20		A5_BA1
CMD21		WE*
CMD22		A7_A8
CMD23		A6_A11
CMD24		AB1*
CMD25		A12_RFU
CMD26		A0_A10
CMD27		A1_A9
CMD28		RAS*
CMD29		RST*
CMD30		CKE*
CMD31		CAS*



GPU FBVDDQ & 1V8 Decoupling

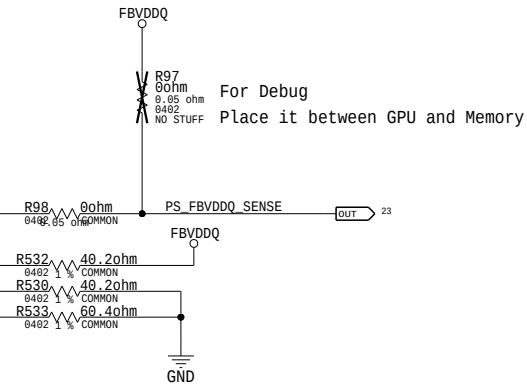


G1
GP107-400-A1
BGA908
COMMON

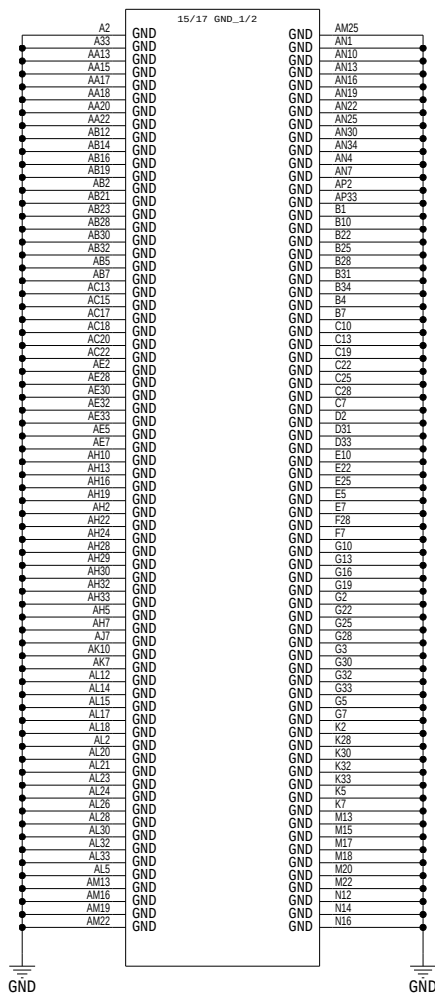
14/17 FBVDDQ

AA27 FBVDDQ
AA30 FBVDDQ
AB27 FBVDDQ
AB33 FBVDDQ
AC27 FBVDDQ
AD27 FBVDDQ
AE27 FBVDDQ
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AG27 FBVDDQ
B13 FBVDDQ
B16 FBVDDQ
B19 FBVDDQ
B27 FBVDDQ
E13 FBVDDQ
E16 FBVDDQ
E19 FBVDDQ
H10 FBVDDQ
H11 FBVDDQ
H12 FBVDDQ
H13 FBVDDQ
H14 FBVDDQ
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H8 FBVDDQ
H9 FBVDDQ
L27 FBVDDQ
M27 FBVDDQ
N27 FBVDDQ
P27 FBVDDQ
R27 FBVDDQ
T27 FBVDDQ
T33 FBVDDQ
V27 FBVDDQ
W27 FBVDDQ
W30 FBVDDQ
W33 FBVDDQ
Y27 FBVDDQ

FBVDDQ_SENSE
PROBE_FB_GND
FB_CAL_PD_VDDQ
FB_CAL_PU_GND
FB_CAL_TERM_GND



G1
GP107-400-A1
BGA908
COMMON



G1
GP107-400-A1
BGA908
COMMON

16/17 GND_2/2

N19 GND
N21 GND
N23 GND
N28 GND
N30 GND
N32 GND
N33 GND
N5 GND
P13 GND
P15 GND
P17 GND
P18 GND
P20 GND
P22 GND
R12 GND
R14 GND
R16 GND
R19 GND
R21 GND
R23 GND
T13 GND
T15 GND
T17 GND
T18 GND
T2 GND
T22 GND
T23 GND
AG11 GND

T28 GND
T32 GND
T5 GND
T7 GND
U12 GND
U14 GND
U16 GND
U19 GND
U21 GND
U23 GND
V12 GND
V14 GND
V16 GND
V19 GND
V21 GND
W13 GND
W15 GND
W17 GND
W18 GND
W20 GND
W22 GND
W28 GND
Y12 GND
Y14 GND
Y16 GND
Y19 GND
Y21 GND
Y23 GND
AH11 GND

GND_OPT
GND_OPT

Optional CMD GNDs (2)
NC For 4-Lyr cards

C16
W32

GND

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PAGE DETAIL	GPU FBVDDQ & 1V8 Decoupling

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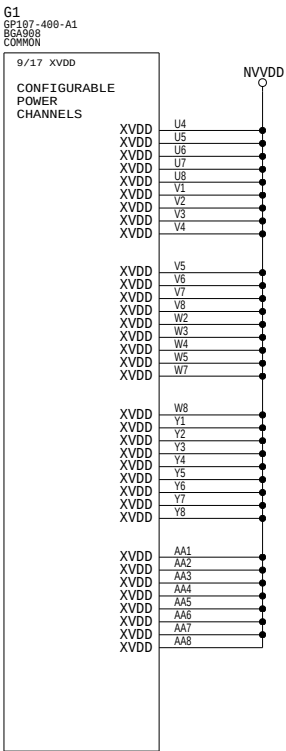
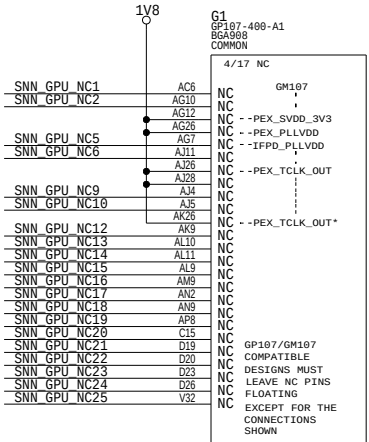
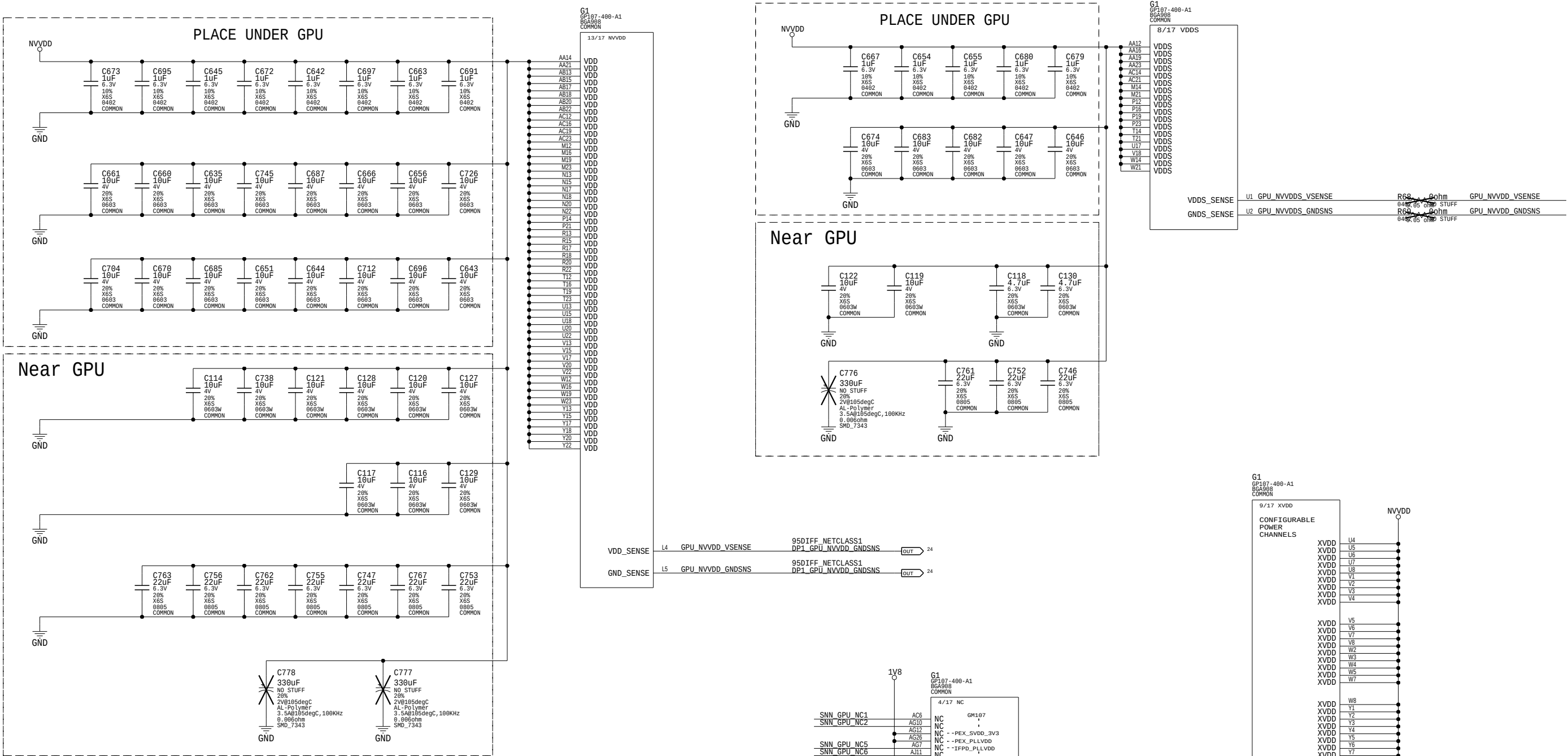
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GPU NVVDD & NVVDDS Decoupling



IFPD DP

1

2

3

4

5

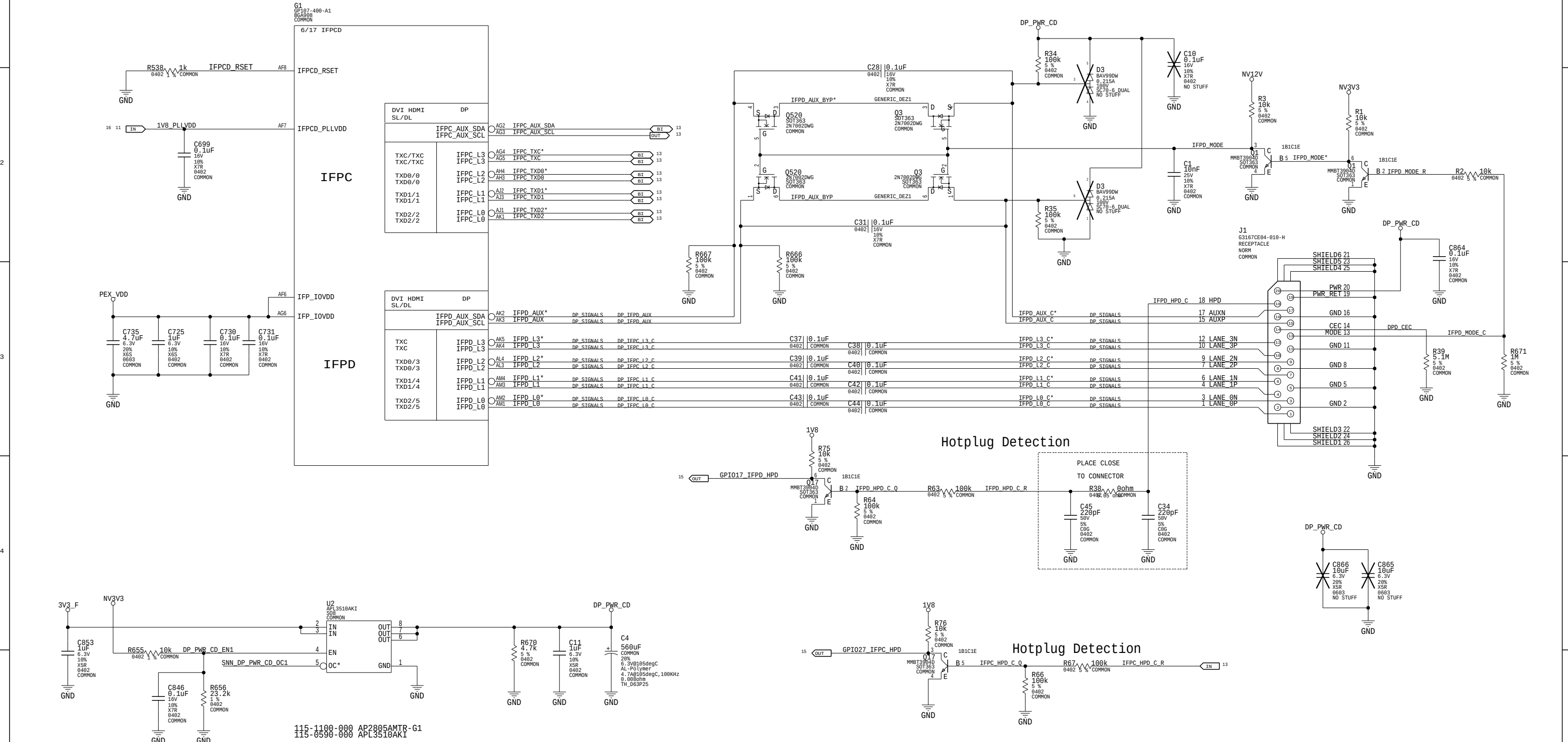
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2

3

4

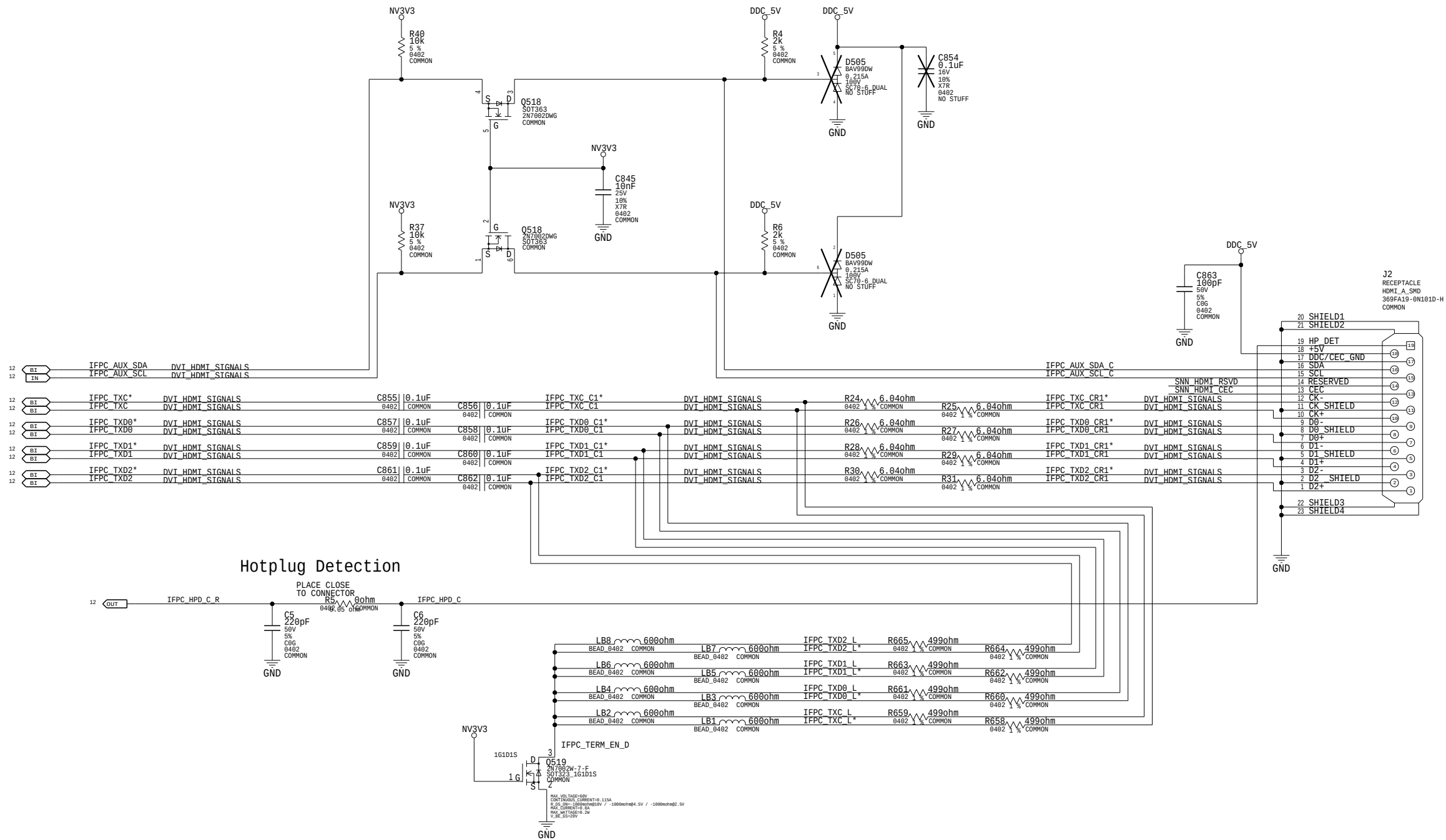
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IFPC HDMI



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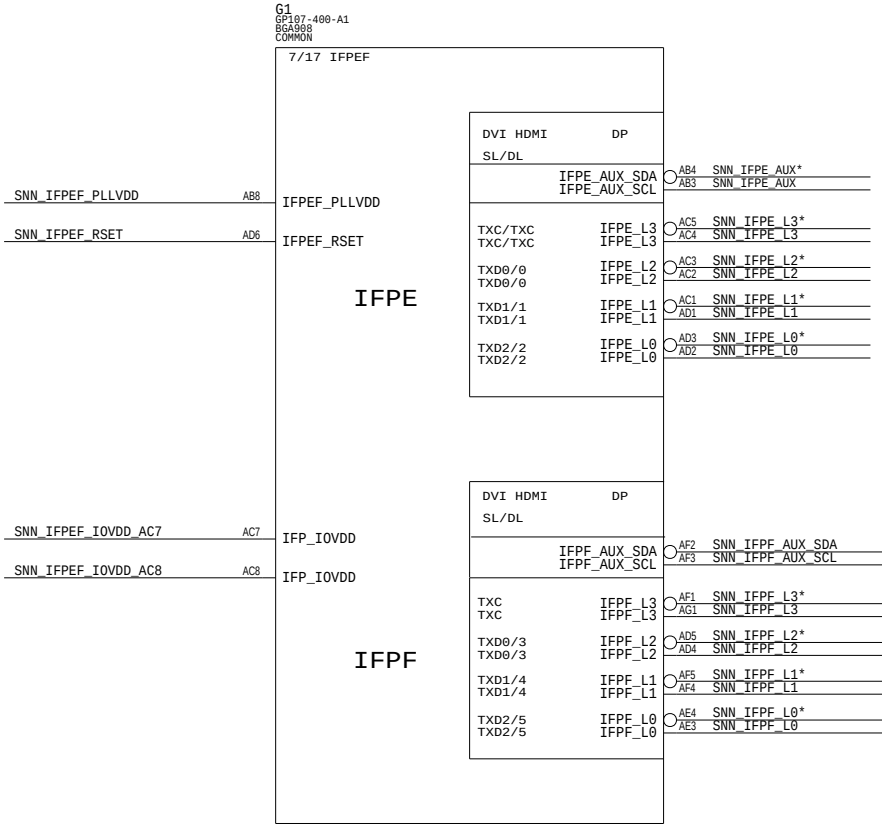
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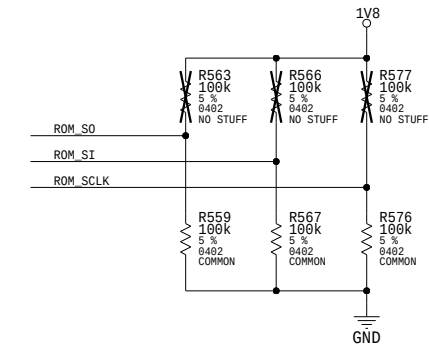
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	IFPEF UNUSED

MISC2: ROM, XTAL, Straps

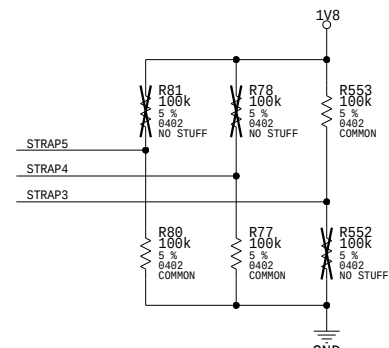
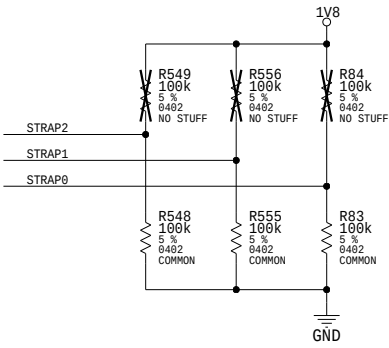
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L	L	L	1111	Default
L	L	H	1110	
L	H	L	1101	
L	H	H	1100	
H	L	L	1011	
H	L	H	1010	
H	H	L	1001	
H	H	H	1000	
L	L	M	0111	
L	M	L	0110	
L	M	H	0101	
L	H	M	0100	
H	L	M	0011	
H	M	L	0010	
H	M	H	0001	
H	H	M	0000	

STRAP2	STRAP1	STRAP0	RAMCFG[4:0]	
L	L	L	0000	Samsung B-Die 8Gb 256Mx32
L	L	H	0001	Micron F-Die 8Gb 256Mx32
L	H	L	0010	Hynix M-Die 8Gb 256Mx32
L	H	H	0011	
H	H	L	0110	Hynix A-Die 4Gb 128Mx32
H	H	H	0111	Samsung E-Die 4Gb 128Mx32
L	L	M	1000	Micron B-Die 4Gb 128Mx32
L	M	L	1001	

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0



H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

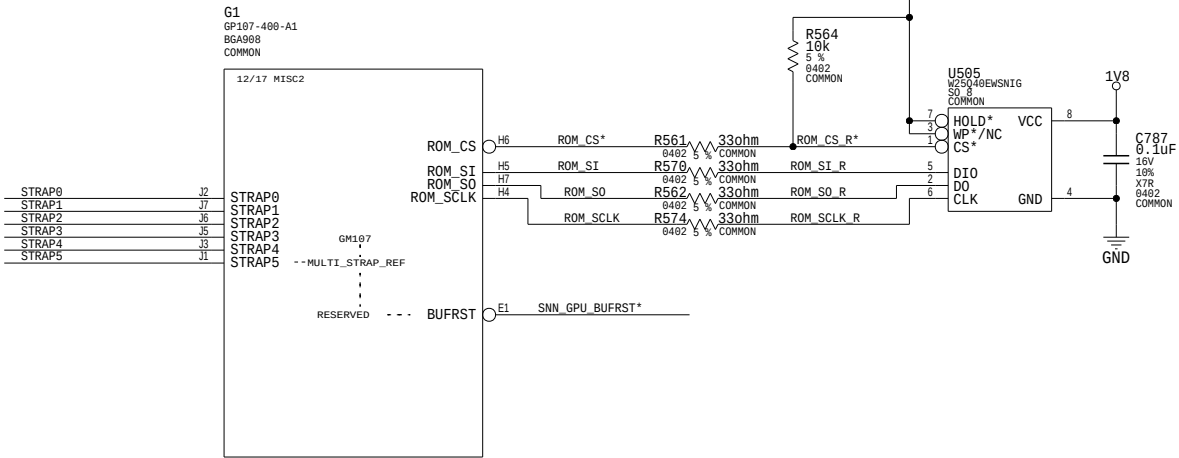


1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGNAL

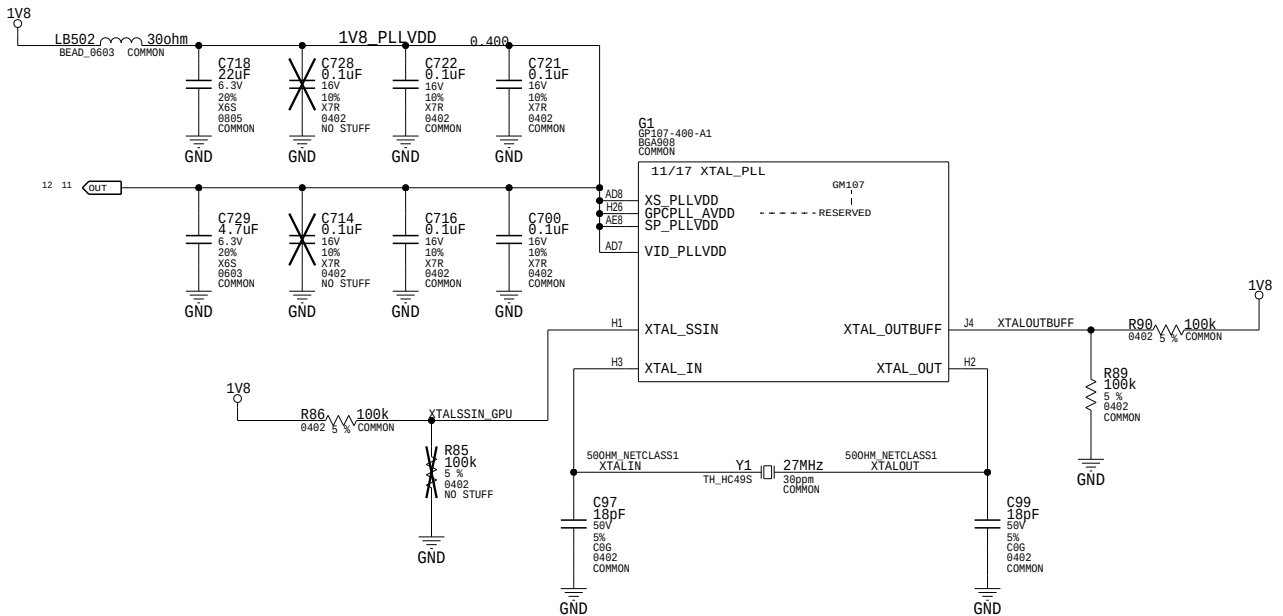
1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE

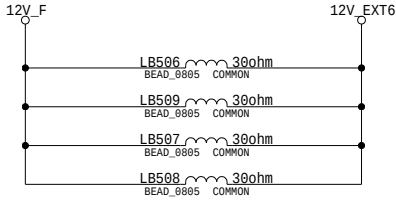
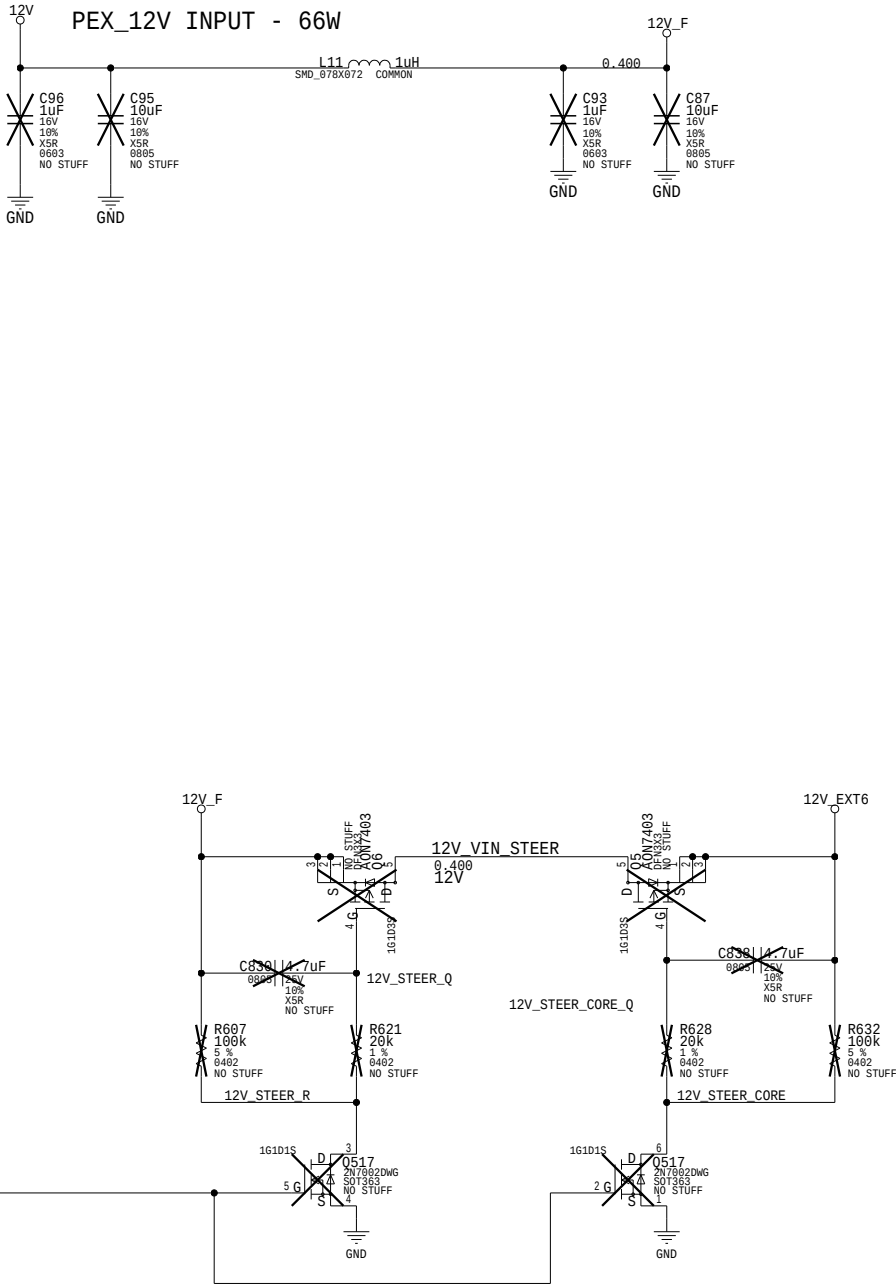
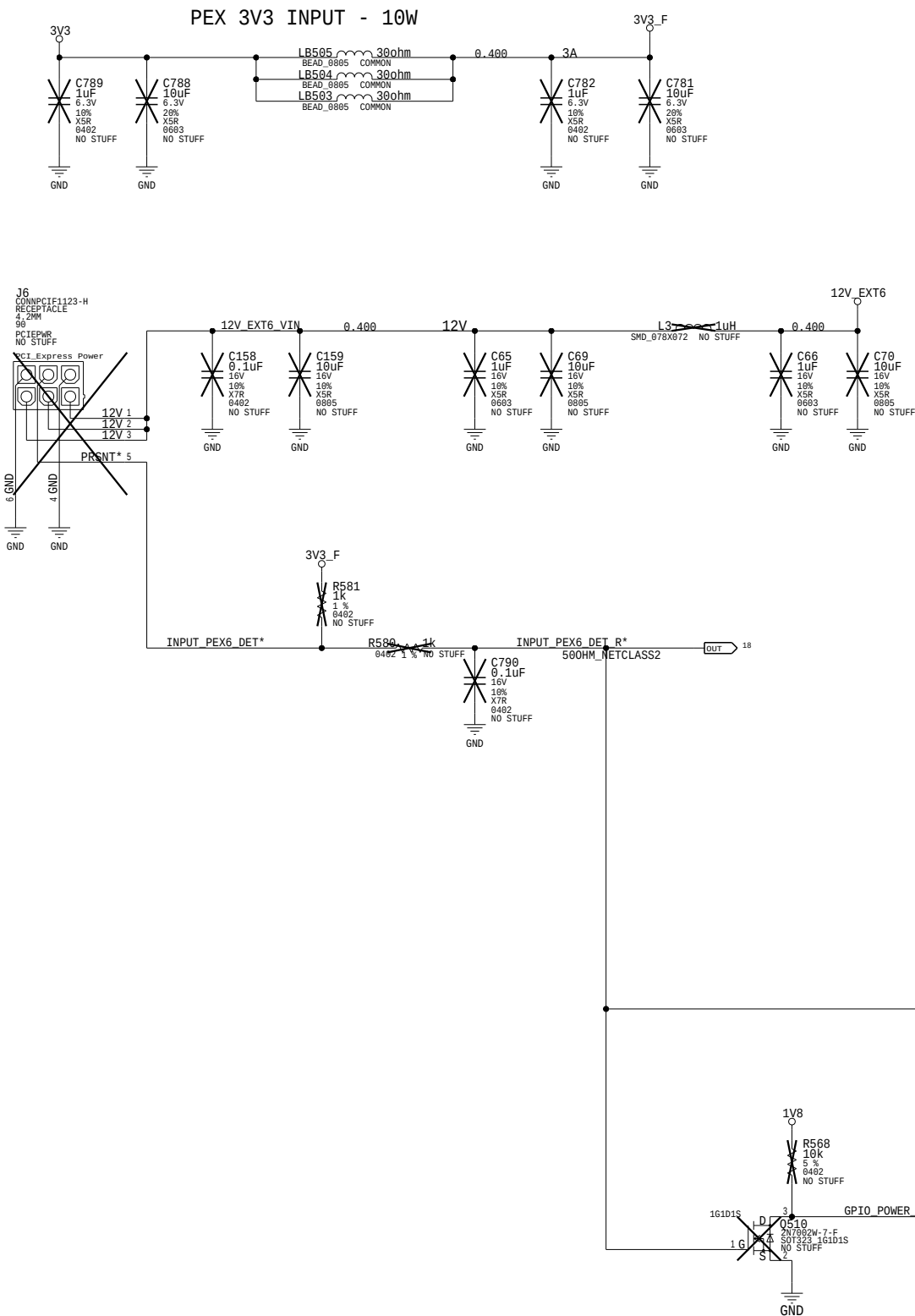


Smart Fan

XTALOUTBUFF	Volatge	SmartFan PWM	GPIO/PWM at the FAN
Low	0V	GPIO Disabled	100%
Middle	0.9V	66% PWM	33% PWM (Typical)
High	1.8V	33% PWM	66% PWM



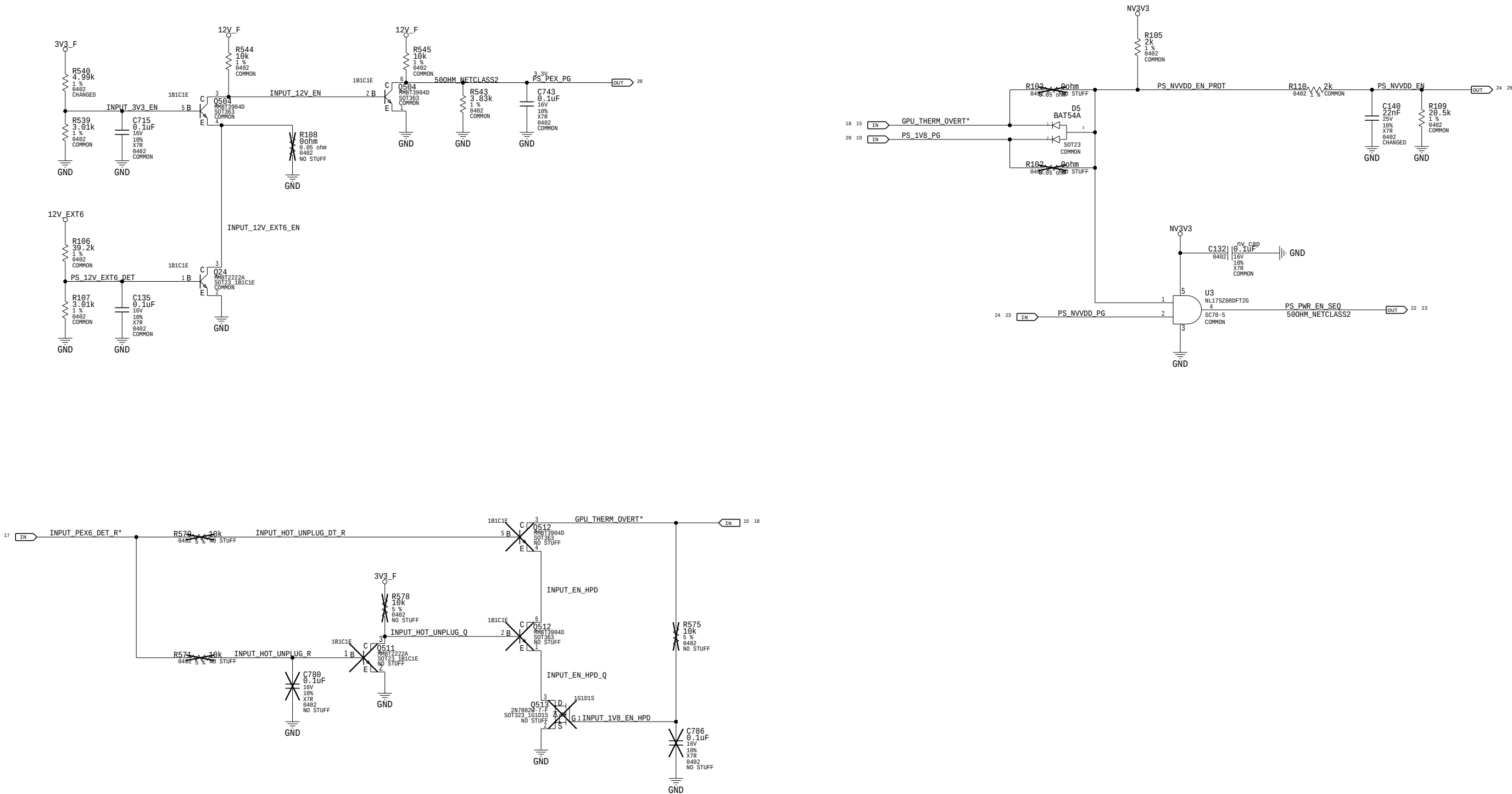
Power Inputs and Filtering



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Power sequence



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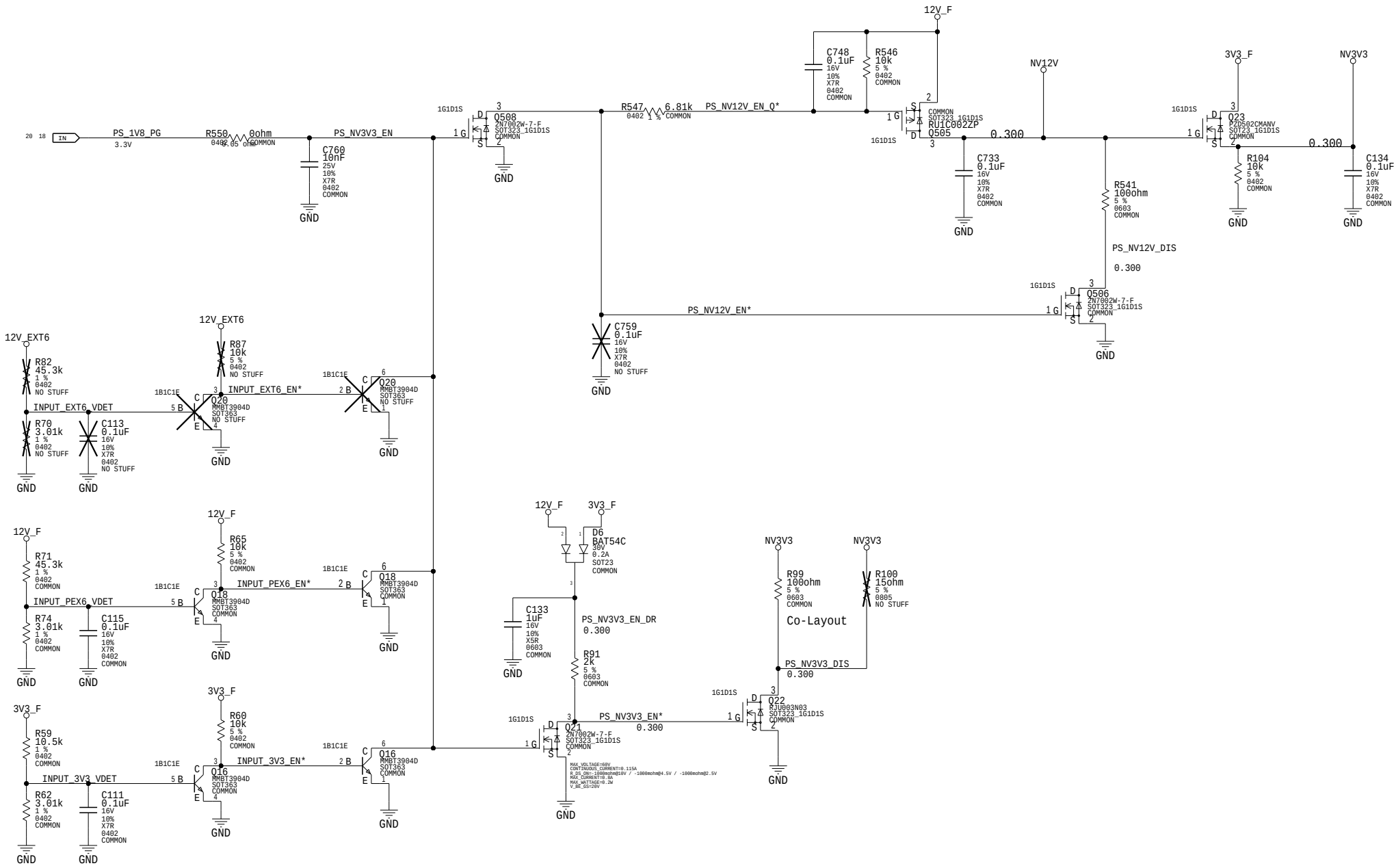
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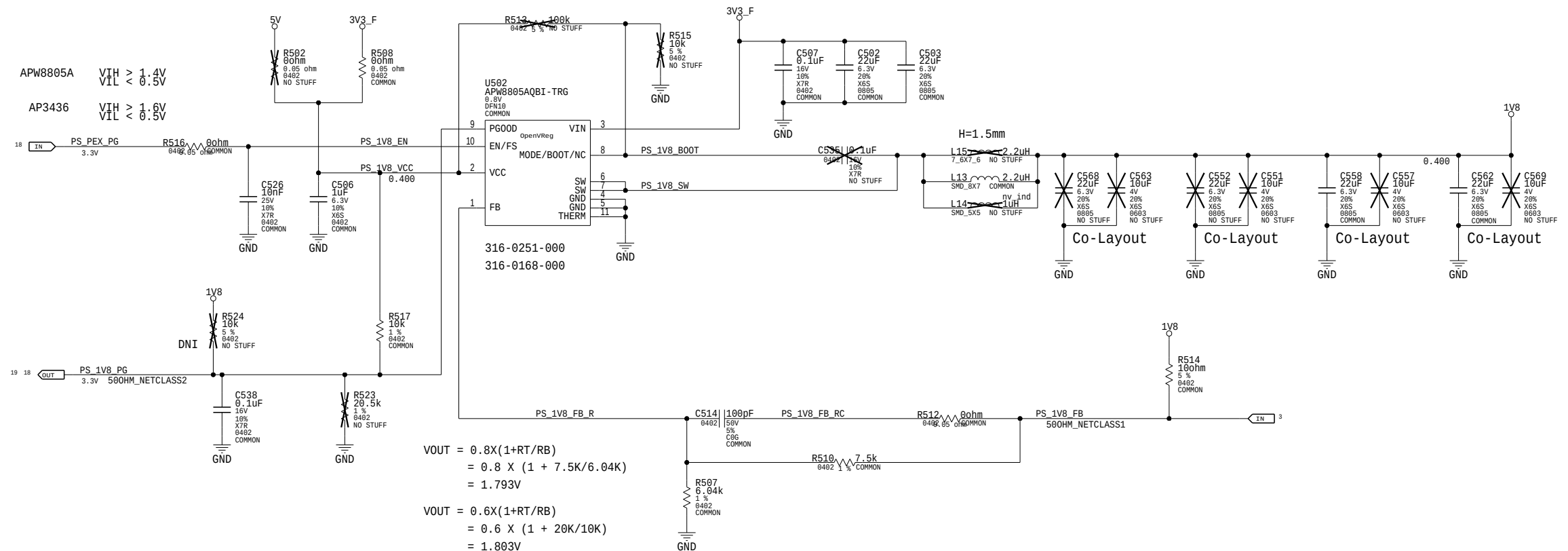
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PS: NV3V3 & NV12V



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PS I: 1V8

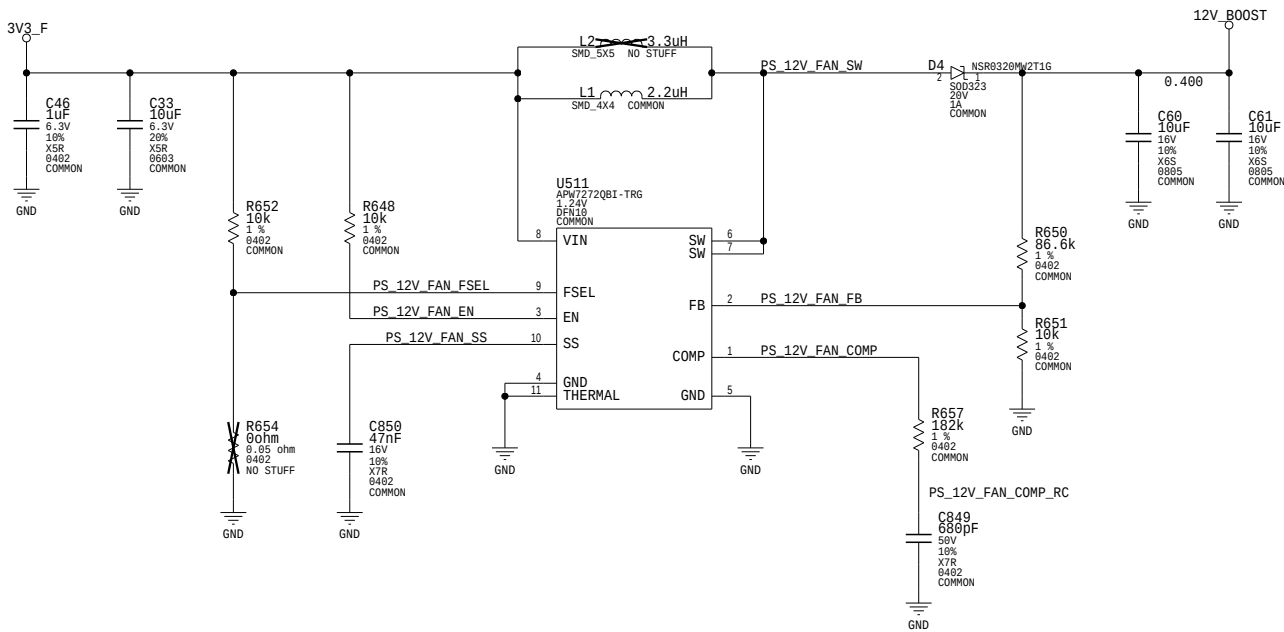
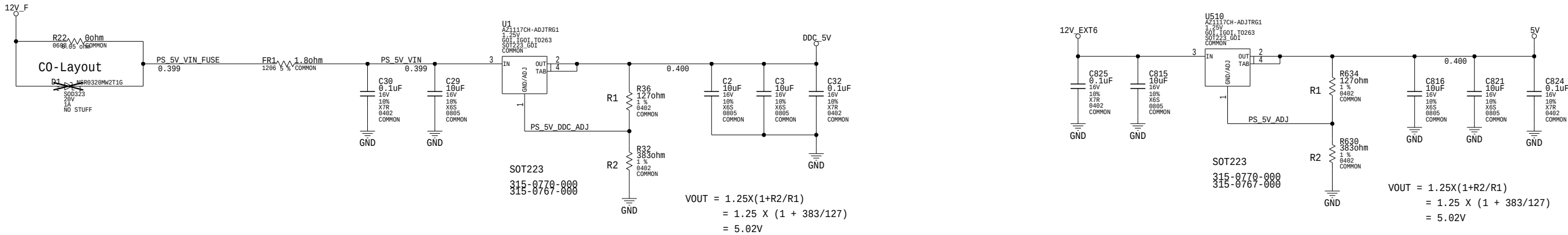


ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO_STUFF ASSEMBLY NOTES AND BOM NOT FINAL
PAGE DETAIL	PSI: 1V8

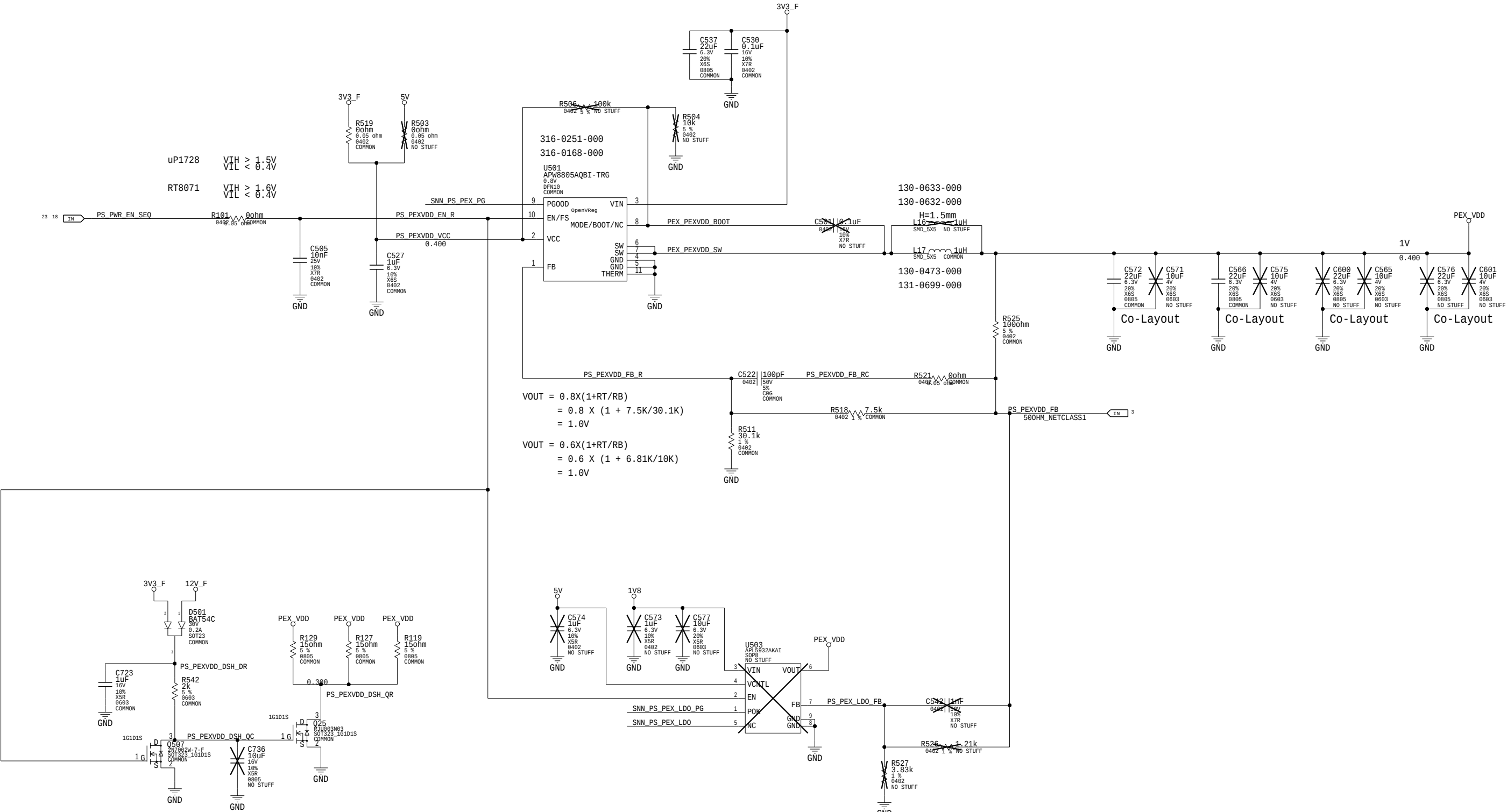
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PSII:5V & 12V_FAN



PS III: PEXVDD



$$V_{OUT} = 0.8X(1+RT/RB)$$
$$= 0.8 \times (1 + 7.5K/30.1K)$$
$$= 1.0V$$

$$V_{OUT} = 0.6X(1+RT/RB)$$
$$= 0.6 \times (1 + 6.81K/10K)$$
$$= 1.0V$$

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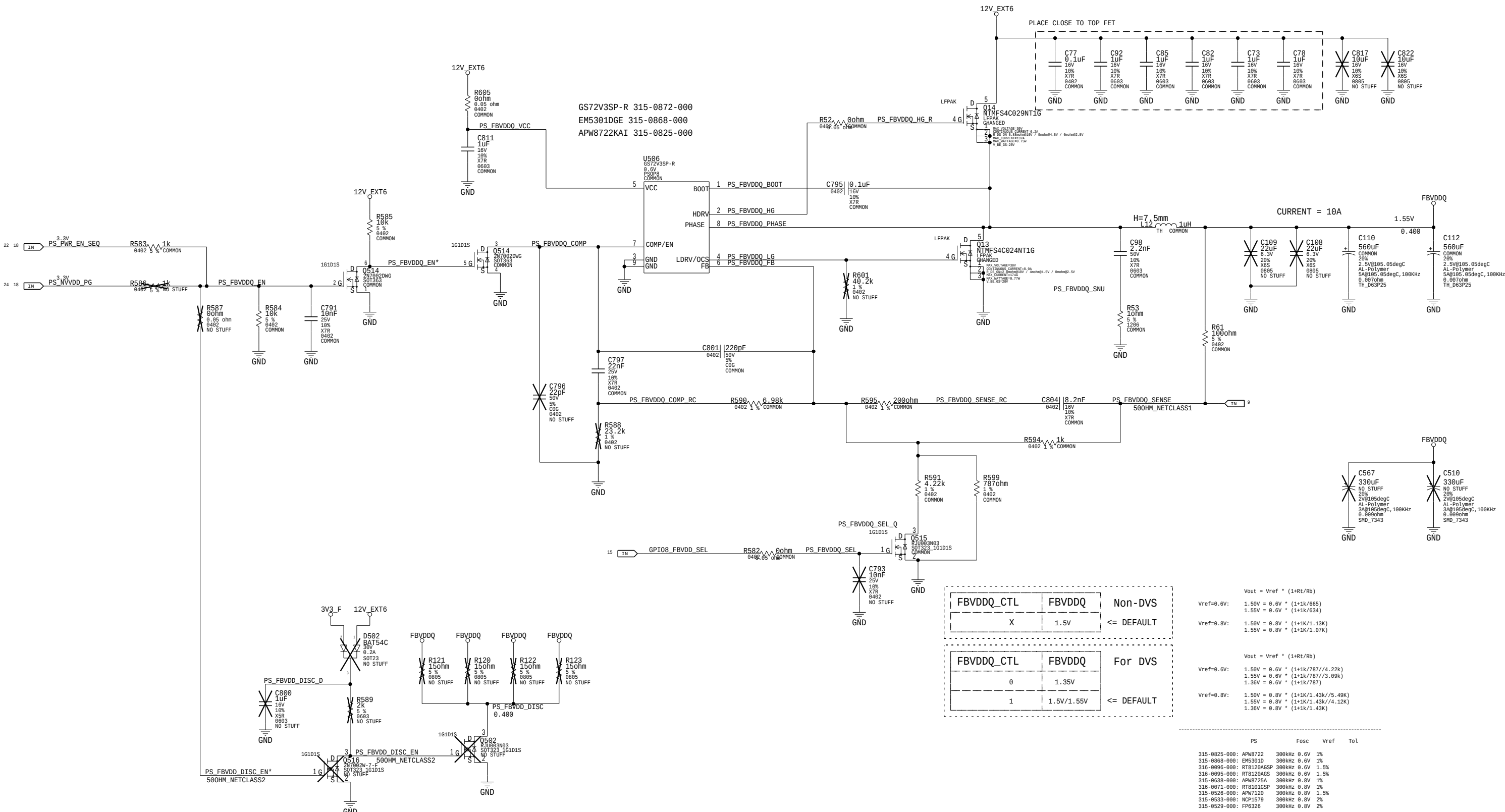


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PS IV: FBVDDQ



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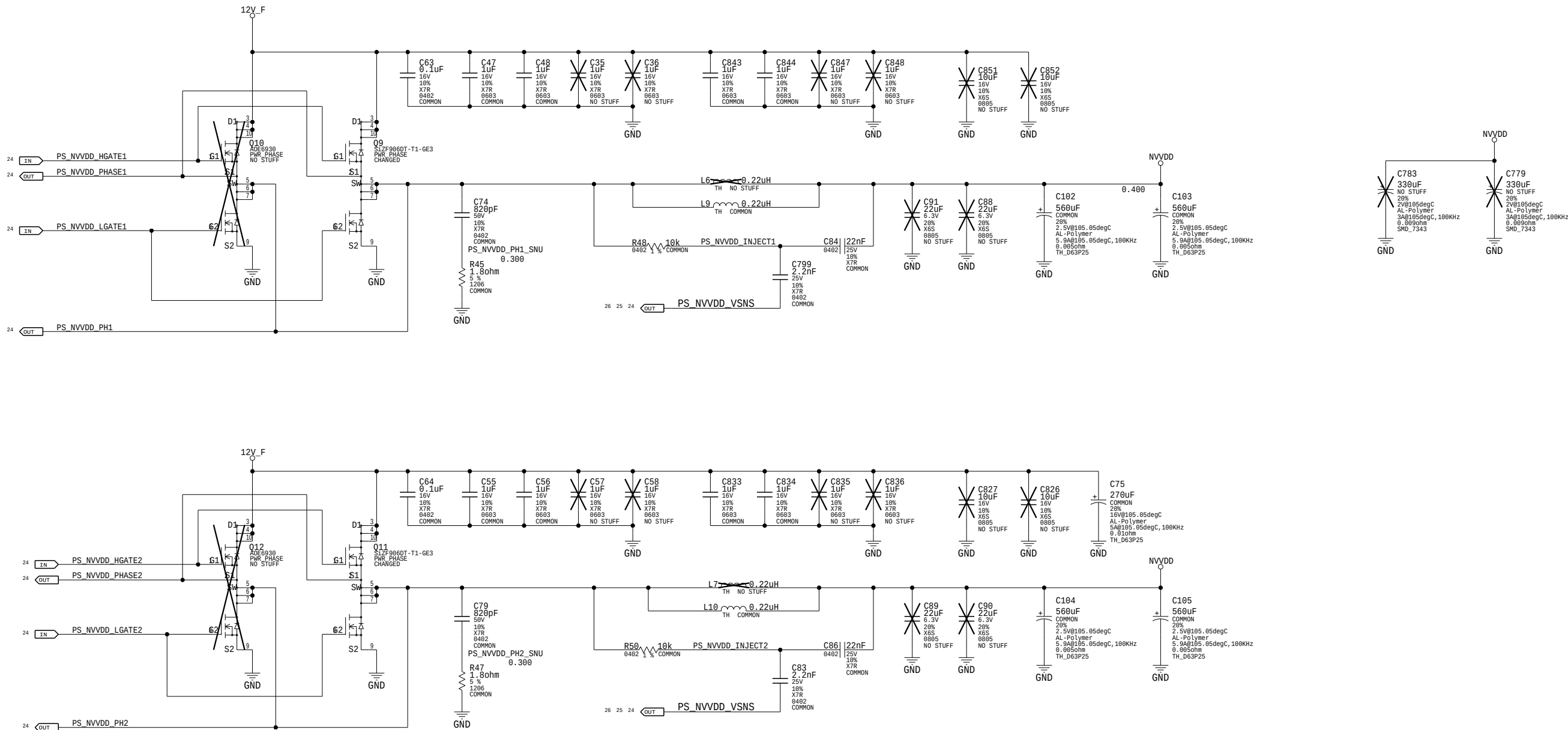
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PS V: NVVDD Phase 1,2



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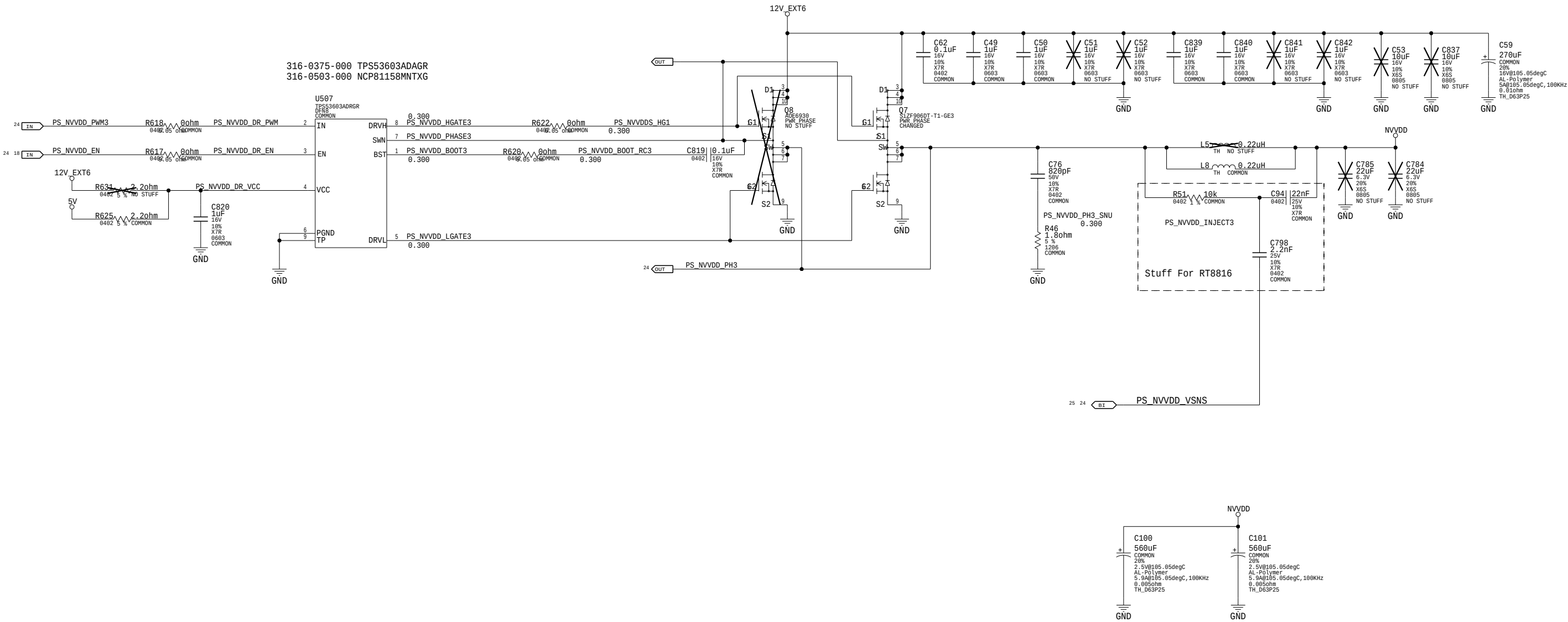
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PS V: NVVDD Phase 3



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Mechanical: Bracket/Thermal Solution

Brackets:

