PG411 A01

GP104 - 8GB GDDR5, 256b, 256Mx32
Tall DVI-D + DP + DP + HDMI + DP

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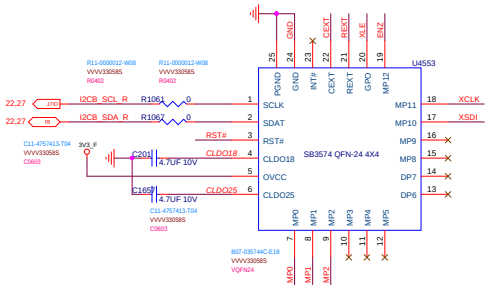
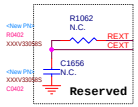
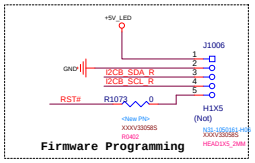
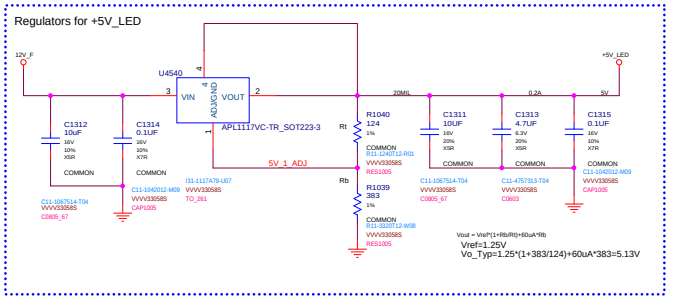
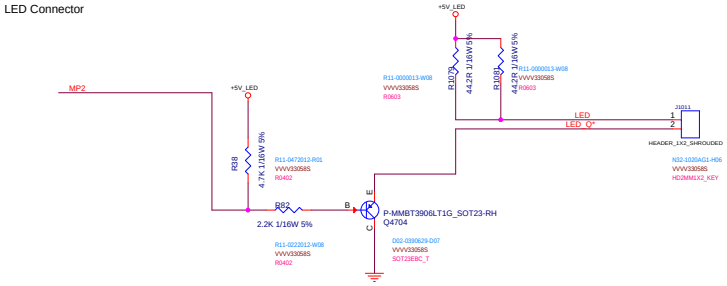
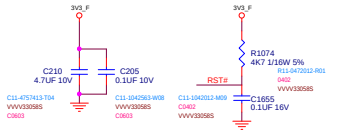

U4553
SB3574 QFN-24 4X4
SCLK
SDAT
RST#
CLDO18
OVCC
CLDO25
I2CB_SCL_R
I2CB_SDA_R
R1061
R1062
C201
C1657
4.7UF 10V
3V3_F
XCLK
XSDI

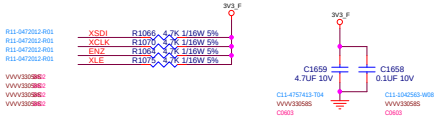

R1062
N.C.
C1656
N.C.
Reserved

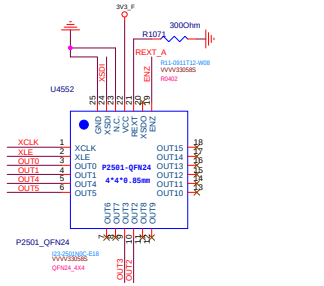

J1006
I2CB_SDA_R
I2CB_SCL_R
R1072
H1X5
(Not)
Firmware Programming

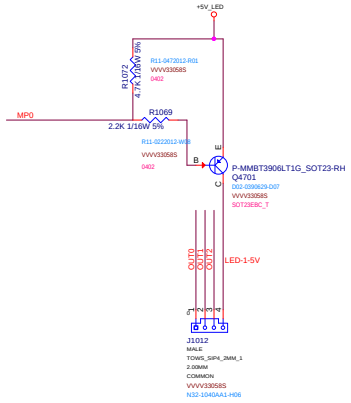

Regulators for +5V_LED
12V_F
U4540
APL1117VC-TR_SOT223-3
VIN
VOUT
ADJ/GND
5V_1_ADJ
C1312
10uF
C1314
0.1UF
R1040
124
R1039
383
C1311
10UF
C1313
4.7UF
C1315
0.1UF
+5V_LED
Vout = Vref*(1+Rb/Rt)+60uA*Rb
Vref=1.25V
Vo_Typ=1.25*(1+383/124)+60uA*383=5.13V

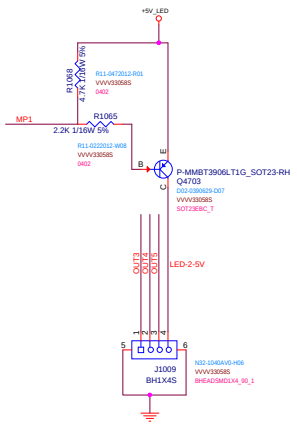

LED Connector
MP2
+5V_LED
R38
4.7K 1/16W 5%
R82
2.2K 1/16W 5%
P-MMBT3906LT1G_SOT23-RH
Q4704
R1070
R1068
44.2R 1/16W 5%
LED
LED_Q*
J1011
HEADER_1X2_SHROUDED

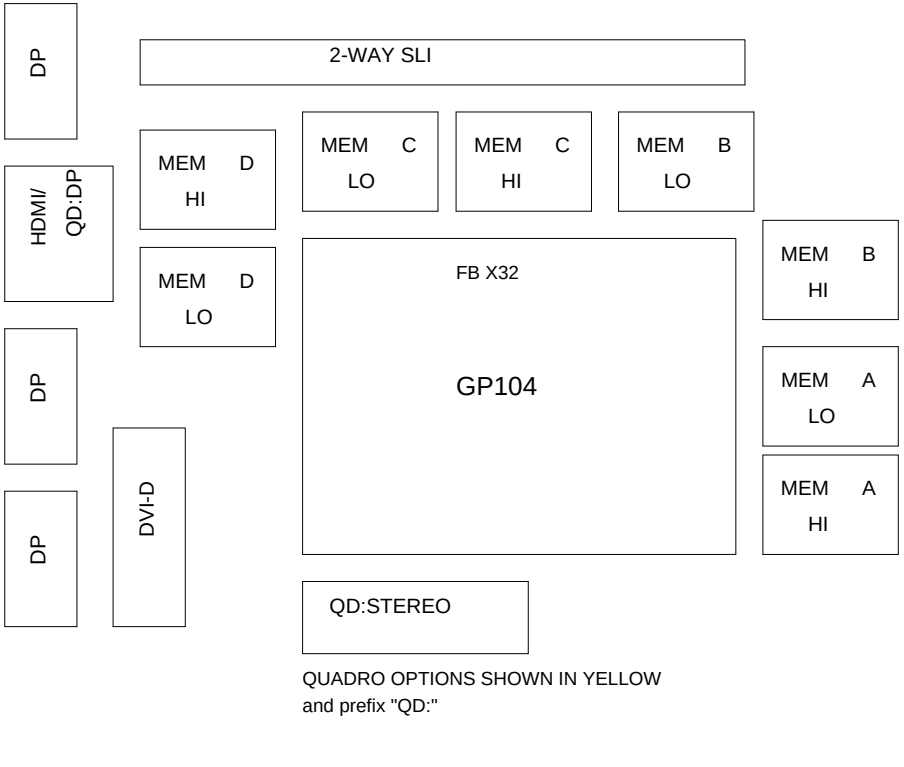

3V3_F
C210
4.7UF 10V
C205
0.1UF 10V
R1074
4K7 1/16W 5%
RST#
C1655
0.1UF 16V


3V3_F
XSDI
XCLK
ENZ
XLE
R1066
R1070
R1064
R1071
4.7K 1/16W 5%
C1659
4.7UF 10V
C1658
0.1UF 10V

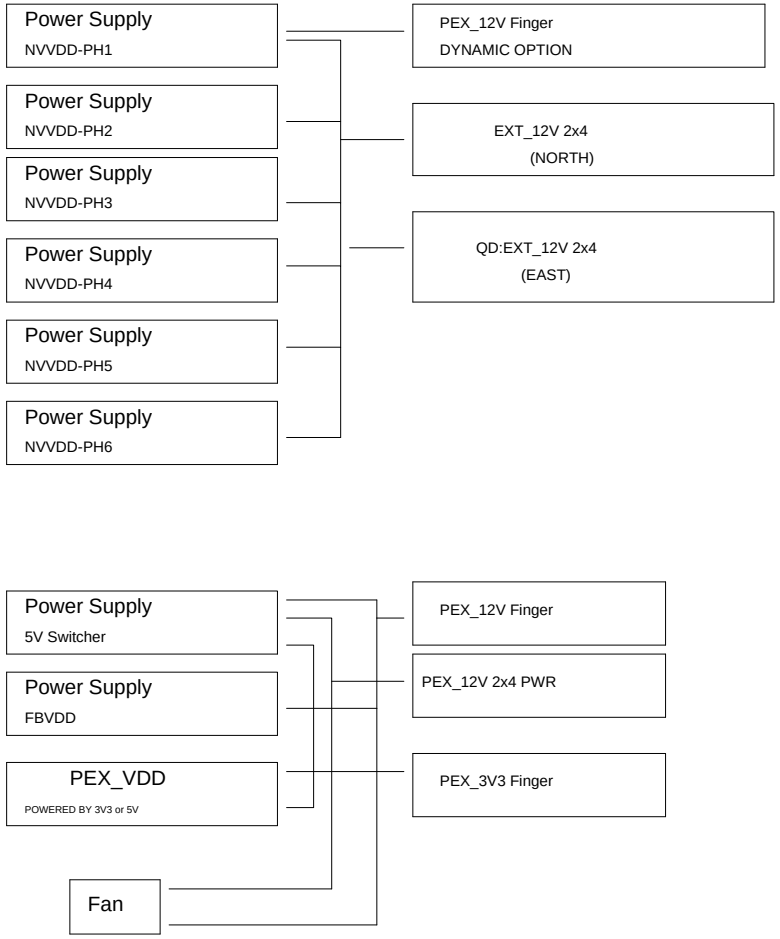

3V3_F
R1071
300Ohm
REXT_A
U4552
P2501-QFN24
4*4*0.85mm
XCLK
XLE
OUT0
OUT1
OUT4
OUT5
OUT15
OUT14
OUT13
OUT12
OUT11
OUT10
OUT6
OUT7
OUT3
OUT2
OUT8
OUT9
P2501_QFN24


+5V_LED
MP0
R1072
4.7K 1/16W 5%
R1069
2.2K 1/16W 5%
P-MMBT3906LT1G_SOT23-RH
Q4701
OUT0
OUT1
OUT2
LED-1-5V
J1012


+5V_LED
MP1
R1068
4.7K 1/16W 5%
R1065
2.2K 1/16W 5%
P-MMBT3906LT1G_SOT23-RH
Q4703
OUT3
OUT4
OUT5
LED-2-5V
J1009
BH1X4S



QUADRO OPTIONS SHOWN IN YELLOW
and prefix "QD:"



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Page3: PCI Express

Remove JTAG

Remove POWER BRAKE

NET	VOLTAGE	MAX_CURRENT	MIN_WIDTH
12V	12V	5.5A	0.400
3V3	3.3V	3.0A	0.400
GND	0V	8.5A	0.400

Place under GPU

Place between GPU and PS

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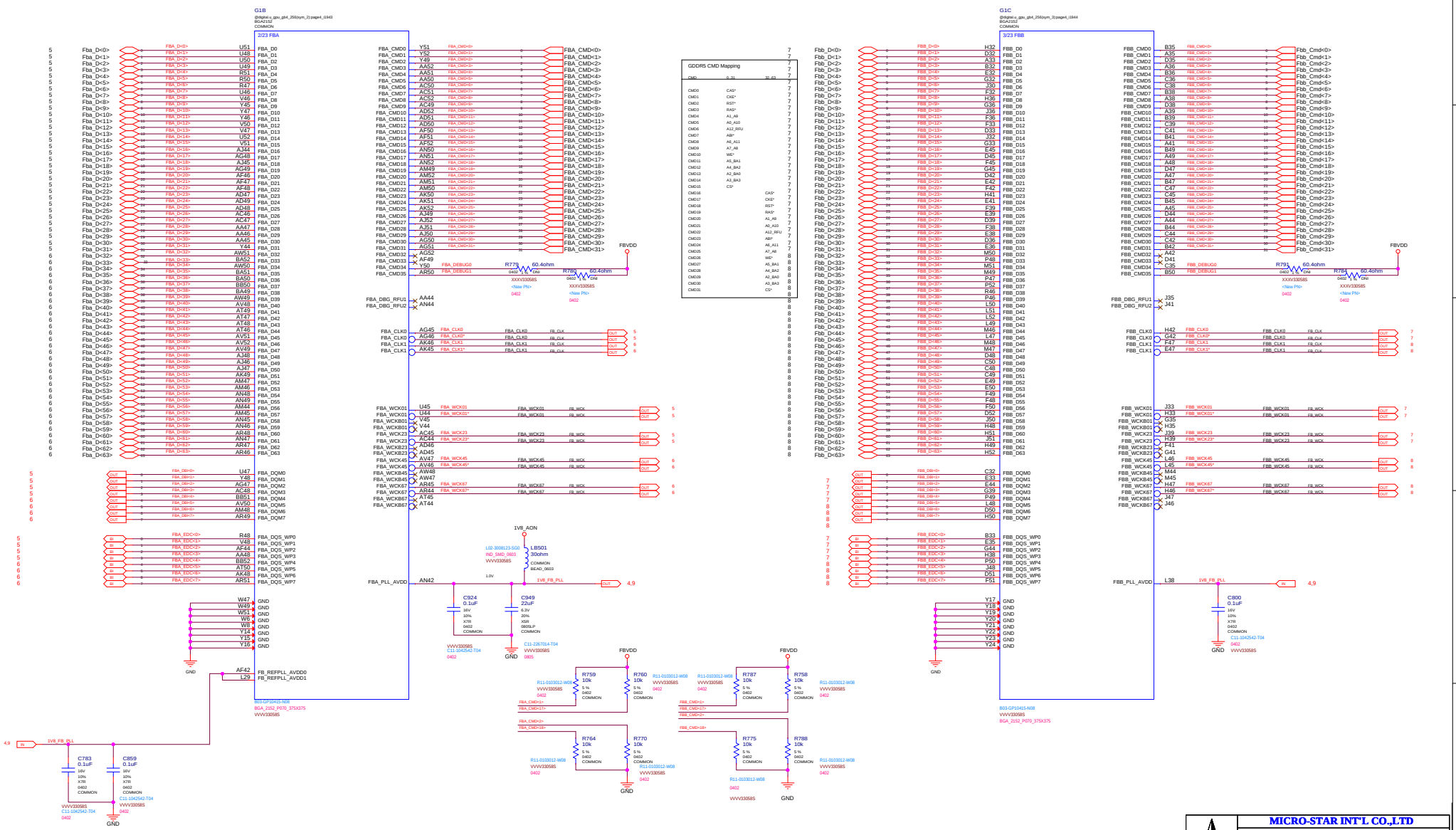
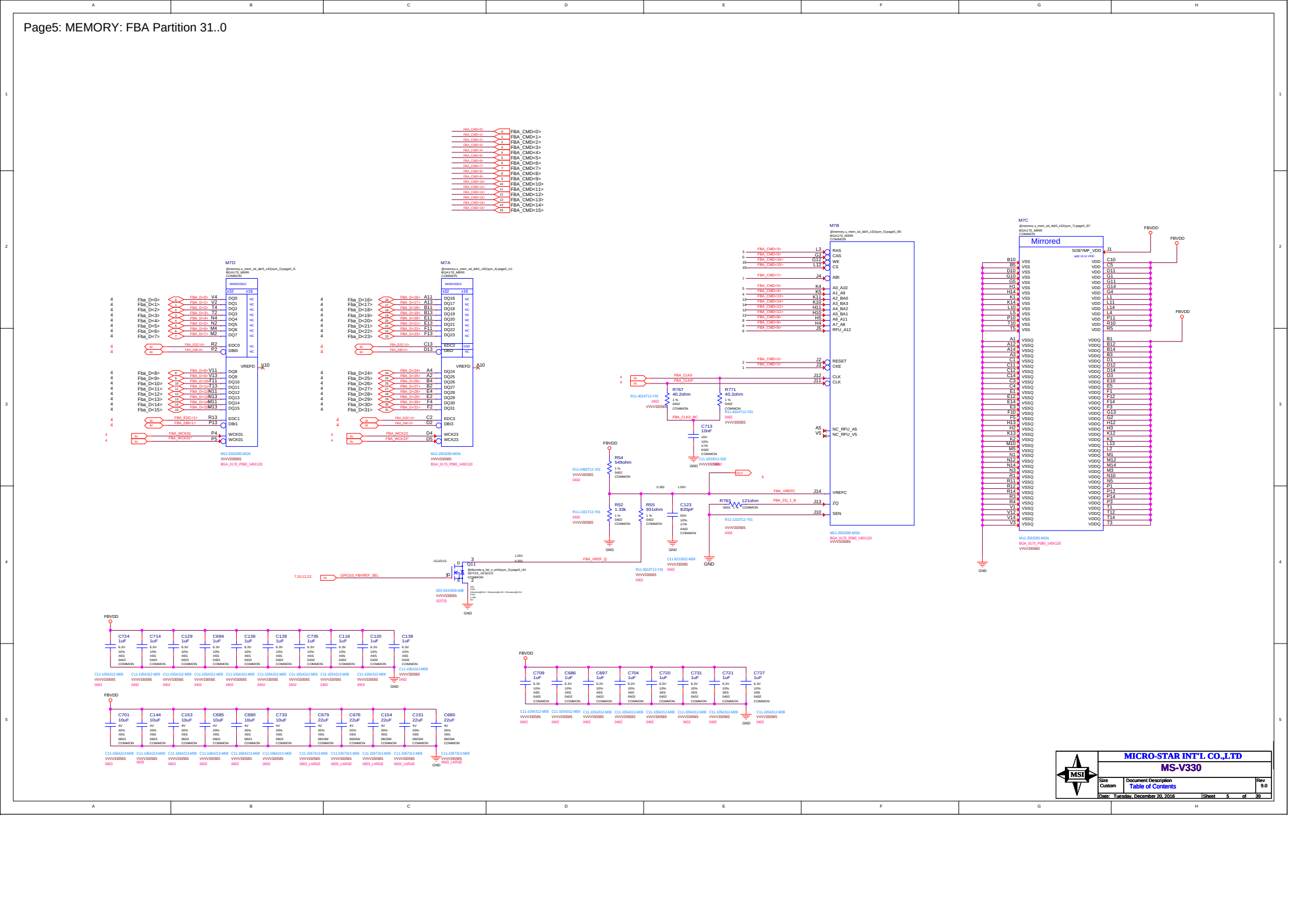
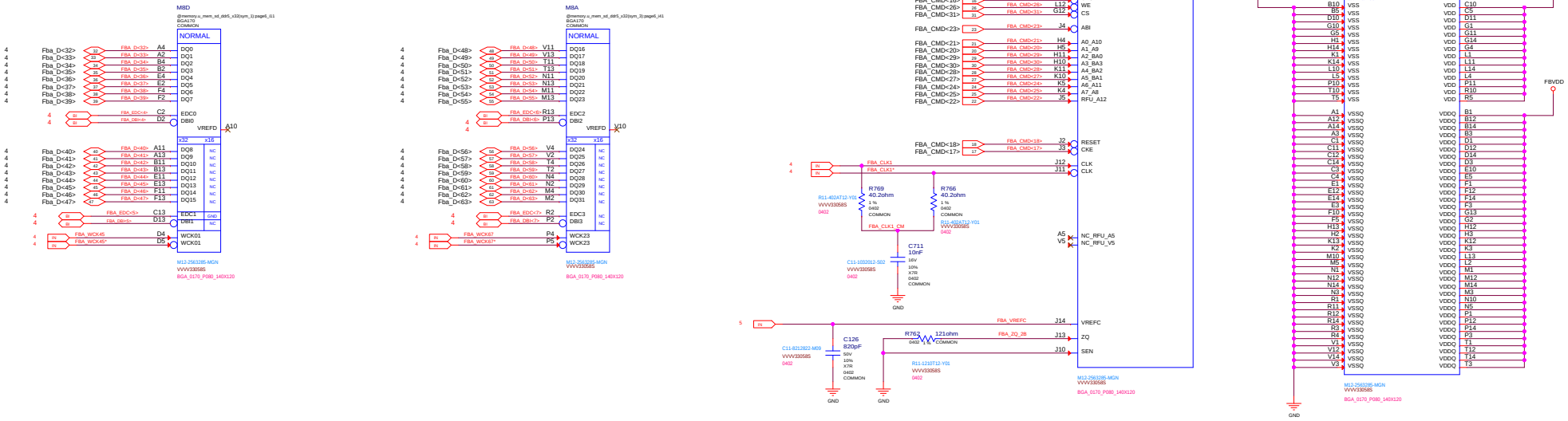
Page4: MEMORY: GPU Partition A/B

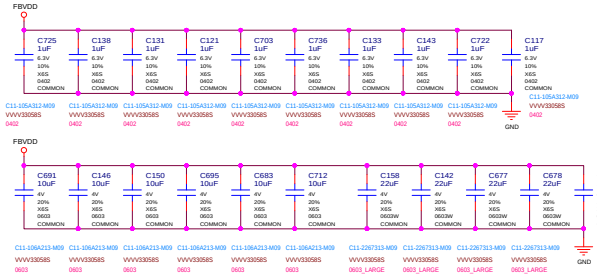


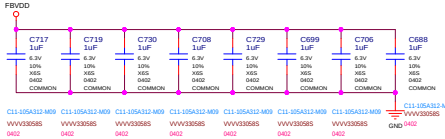
Page5: MEMORY: FBA Partition 31..0



Page6: MEMORY: FBA Partition 63..32







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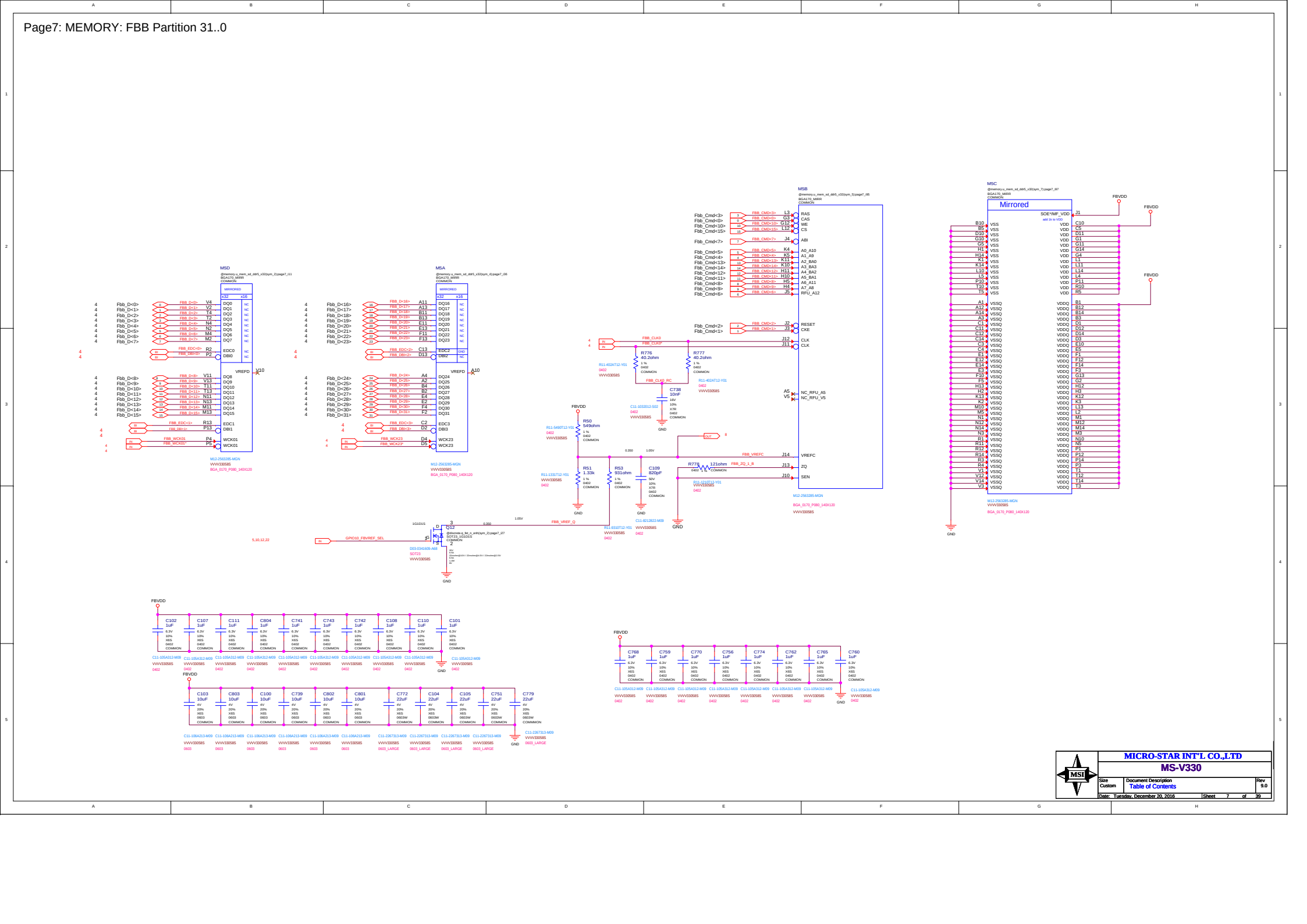
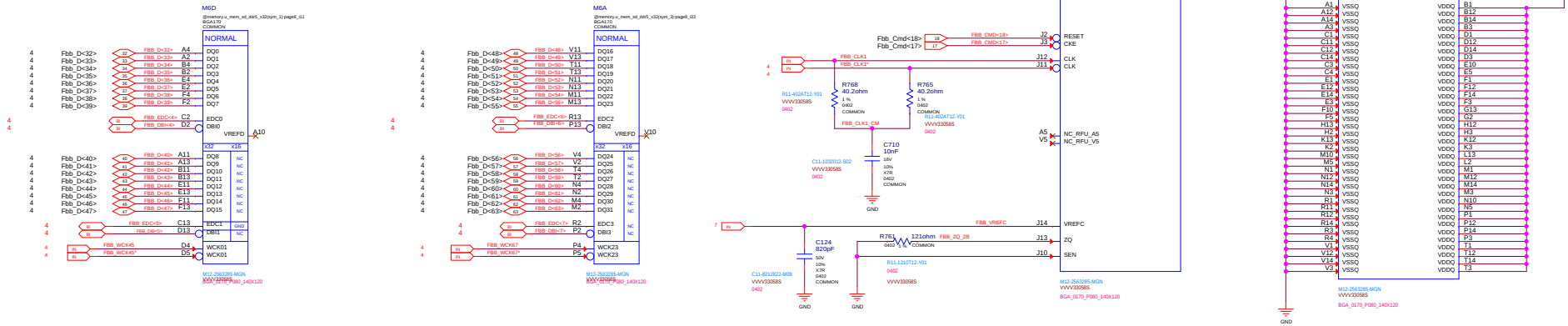
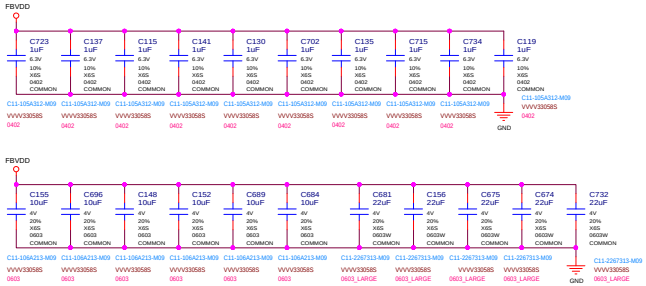
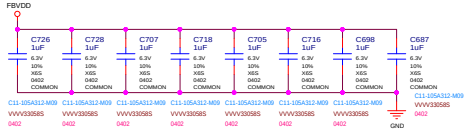
Page7: MEMORY: FBB Partition 31..0



Page8: MEMORY: FBB Partition 63..32







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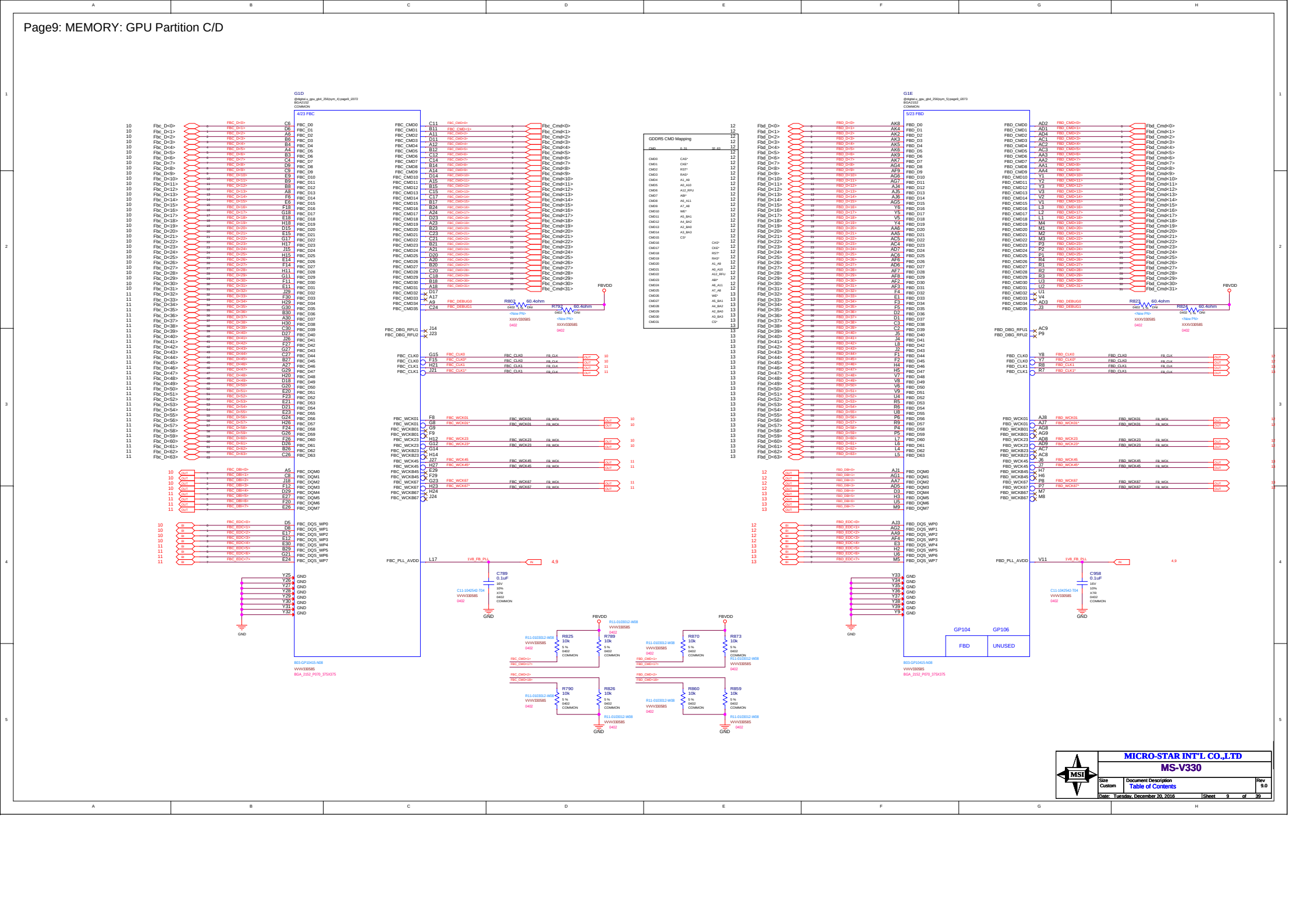
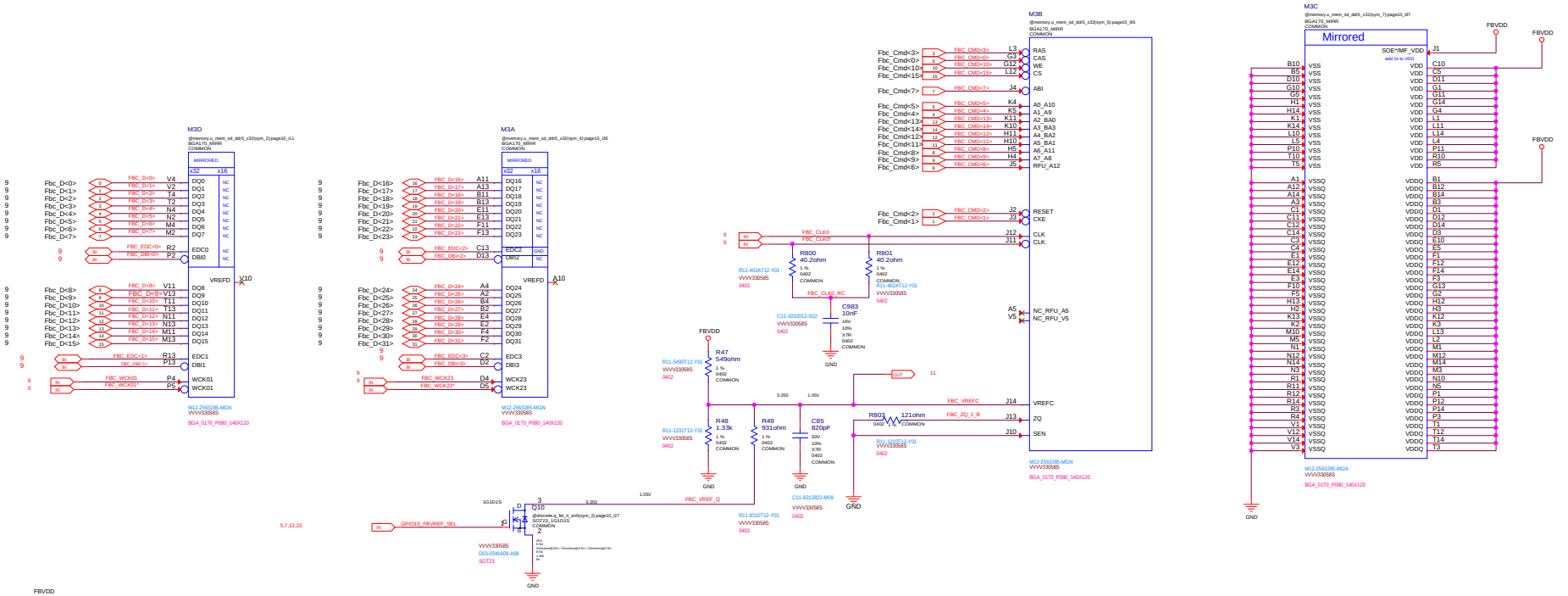
Page9: MEMORY: GPU Partition C/D




M3D
M3A
M3B
M3C
Mirrored
FBVDD
GND
R800
40.2ohm
R801
40.2ohm
R47
549ohm
R48
1.33k
R49
931ohm
C85
820pF
C983
10nF
R803
121ohm
Q10
FBC_VREFC
FBC_VREF_Q
FBC_CLK0
FBC_CLK0_RC
GPIO10_FBVREF_SEL
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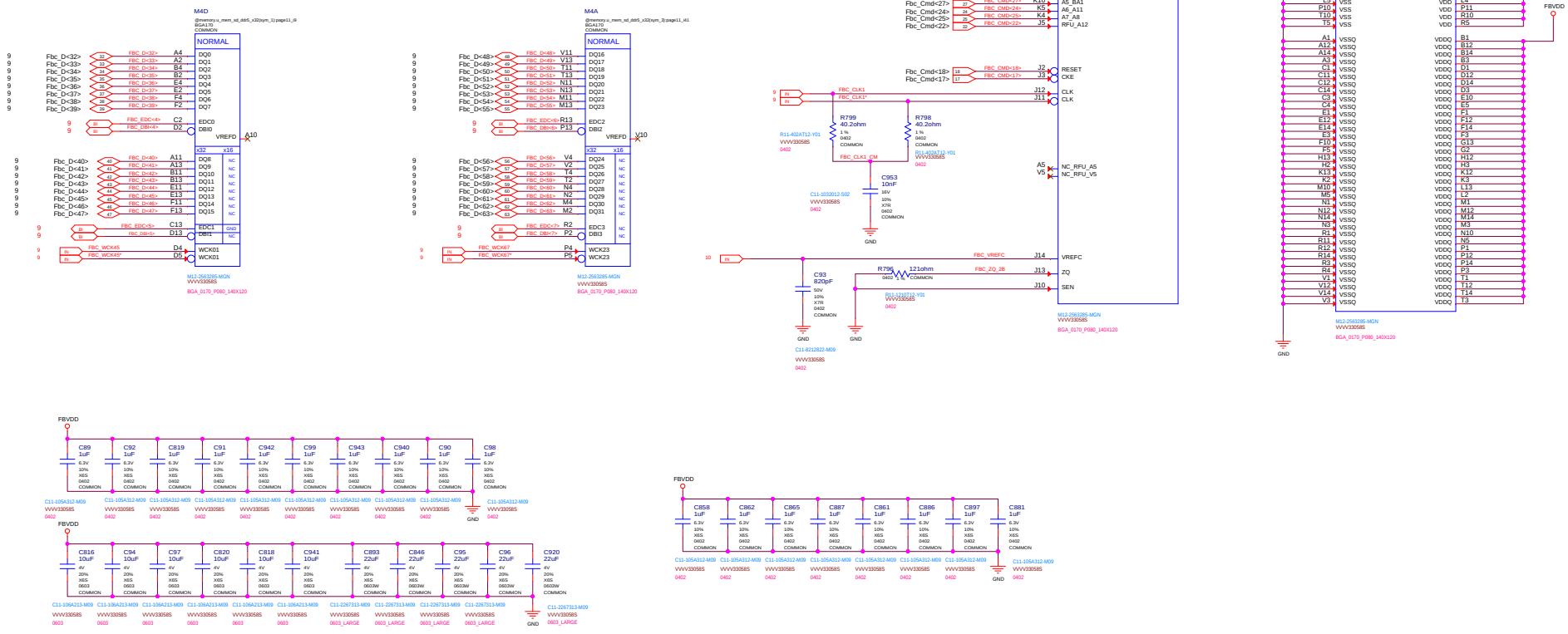
Page11: MEMORY: FBC Partition 63..32



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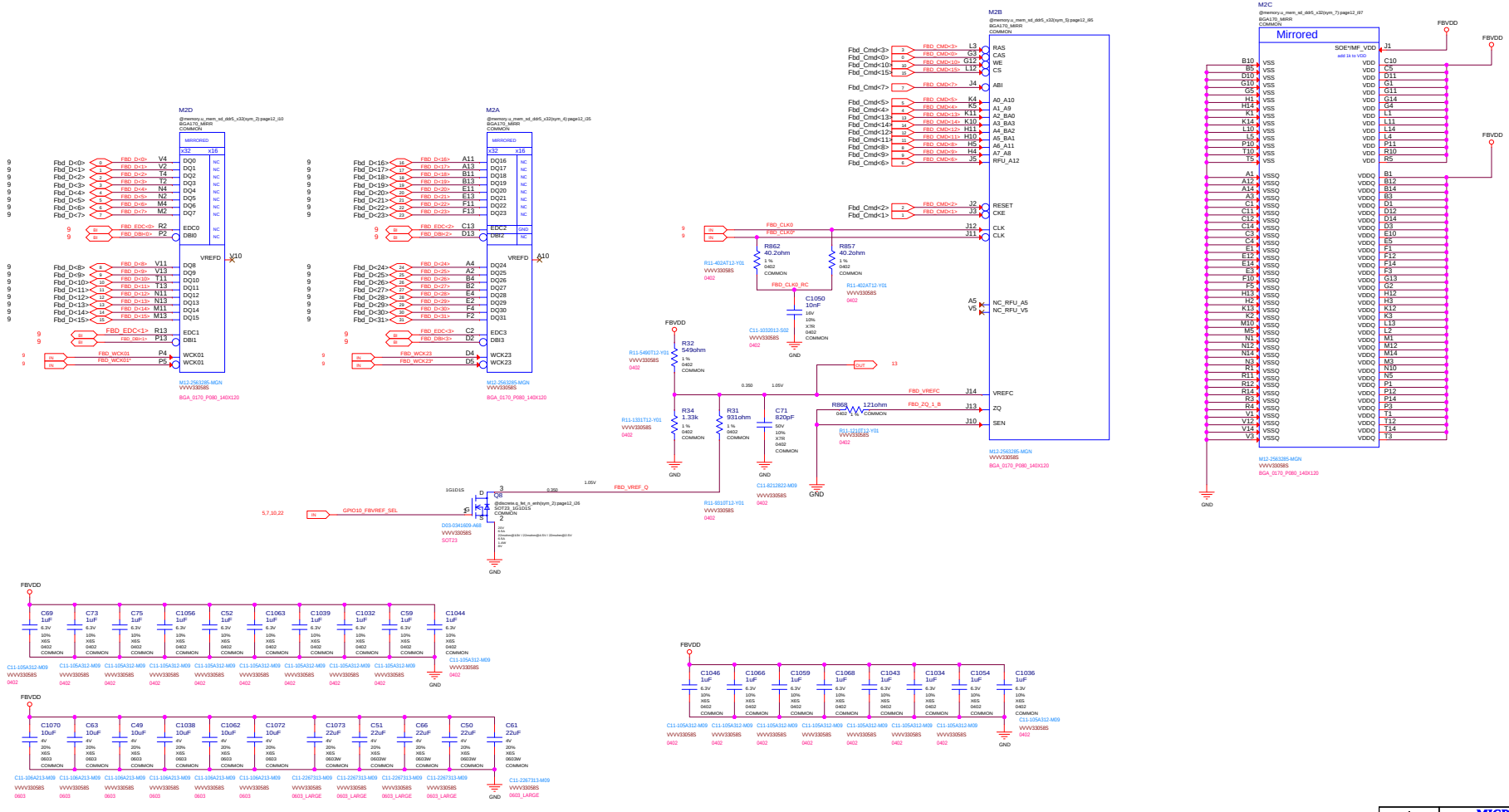
Page12: MEMORY: FBD Partition 31..0



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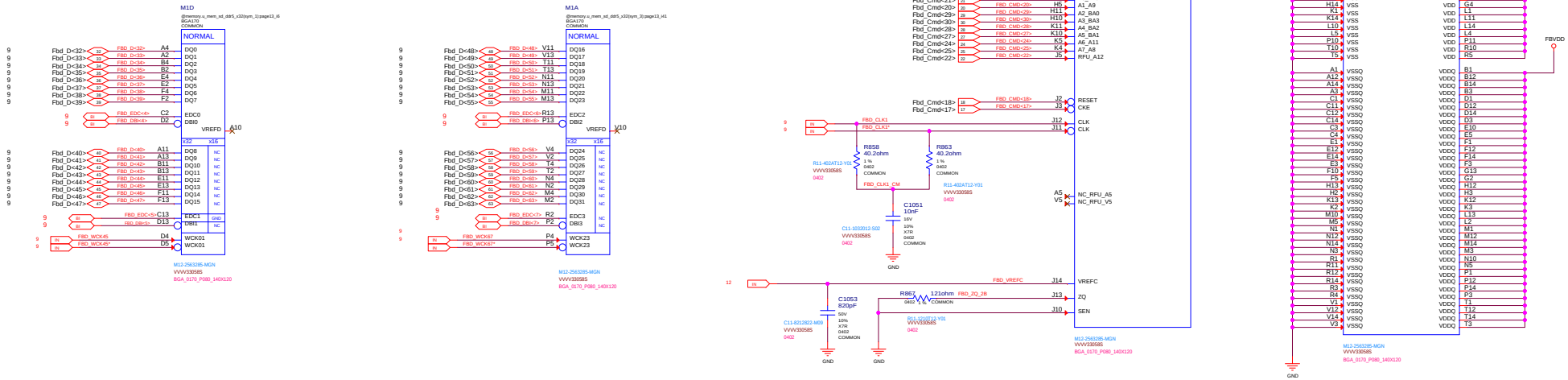
Page13: MEMORY: FBD Partition 63..32



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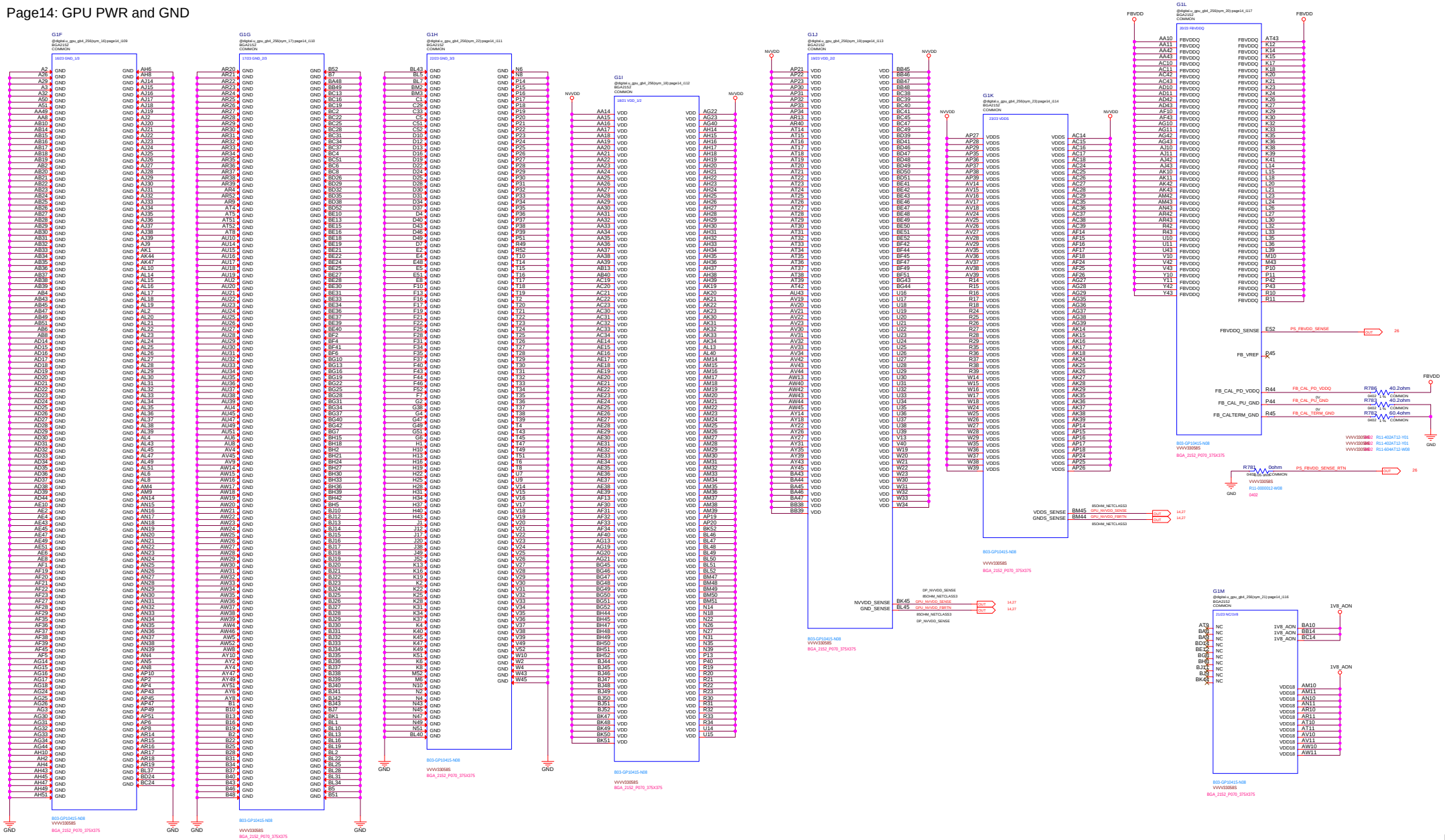
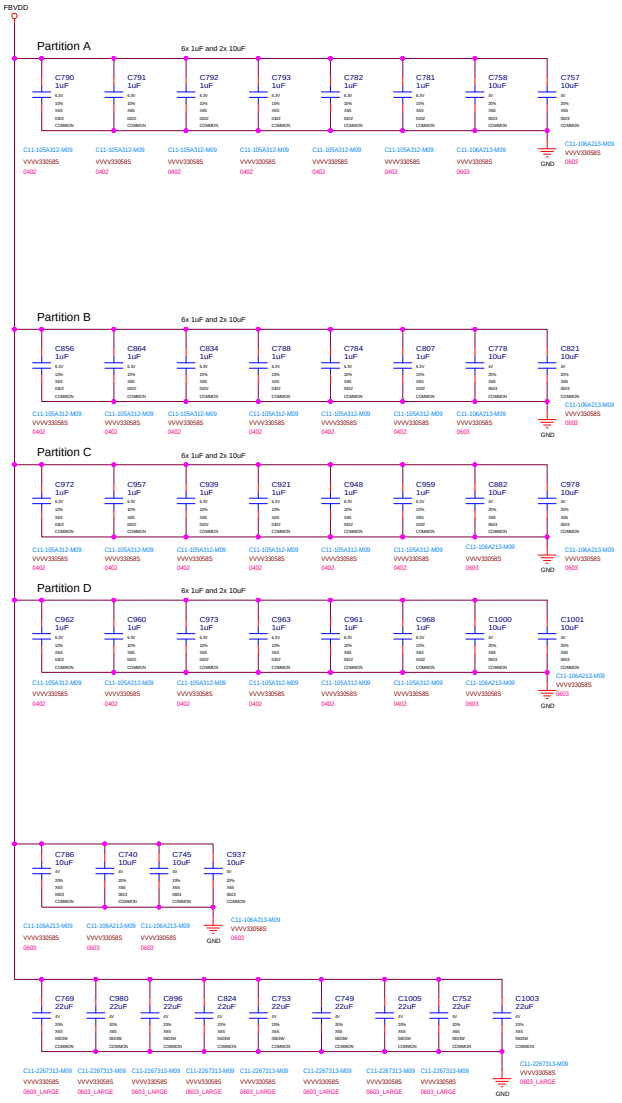
Page14: GPU PWR and GND

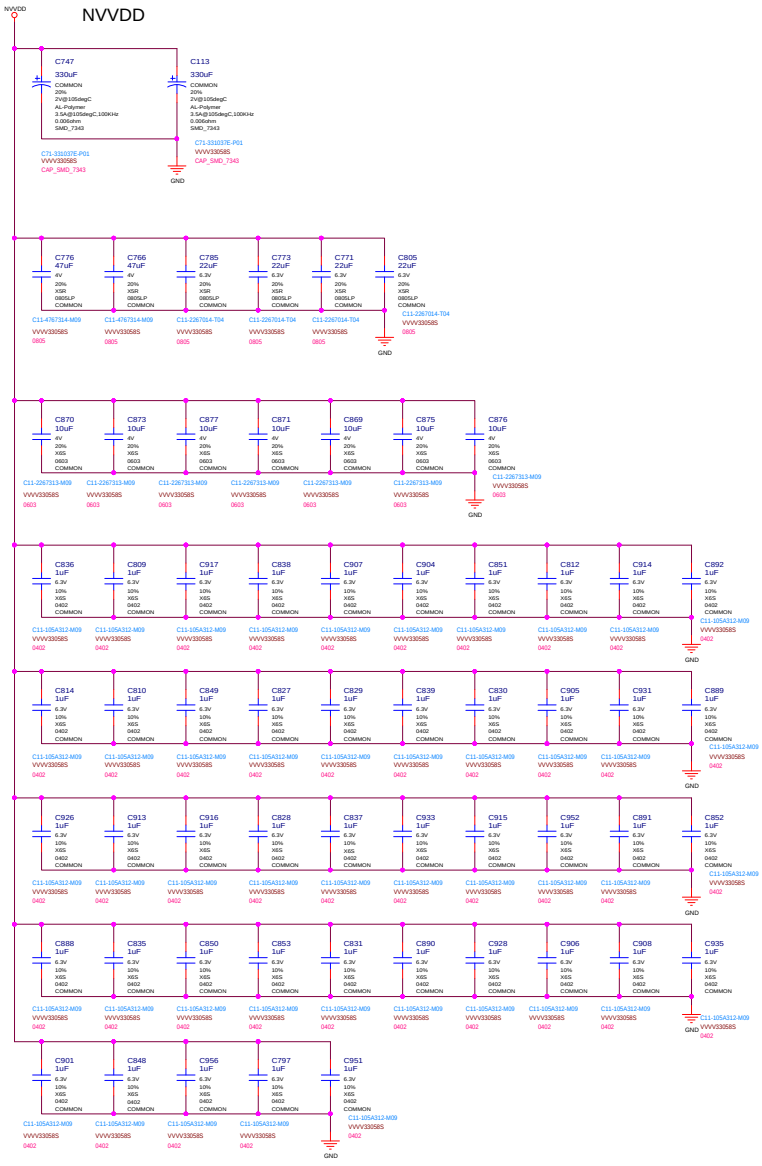


Page15: GPU Decoupling

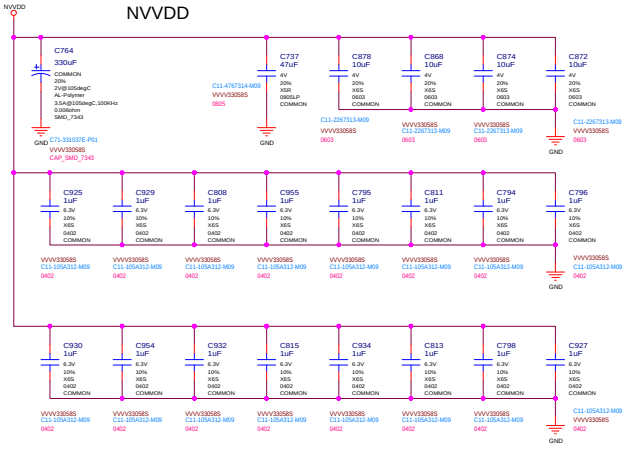
FBVDD

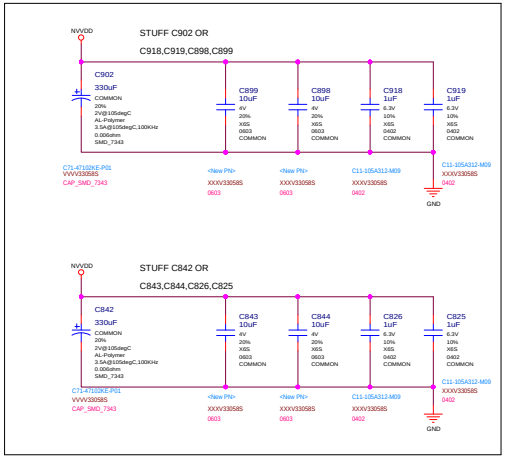


NVVDD

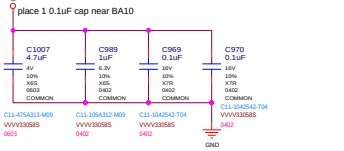


NVVDD



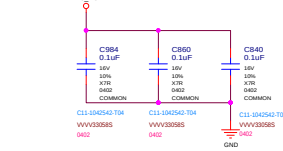


1V8_AON



place 1 0.1uf cap for BB14 and BC14 to share

VDD18



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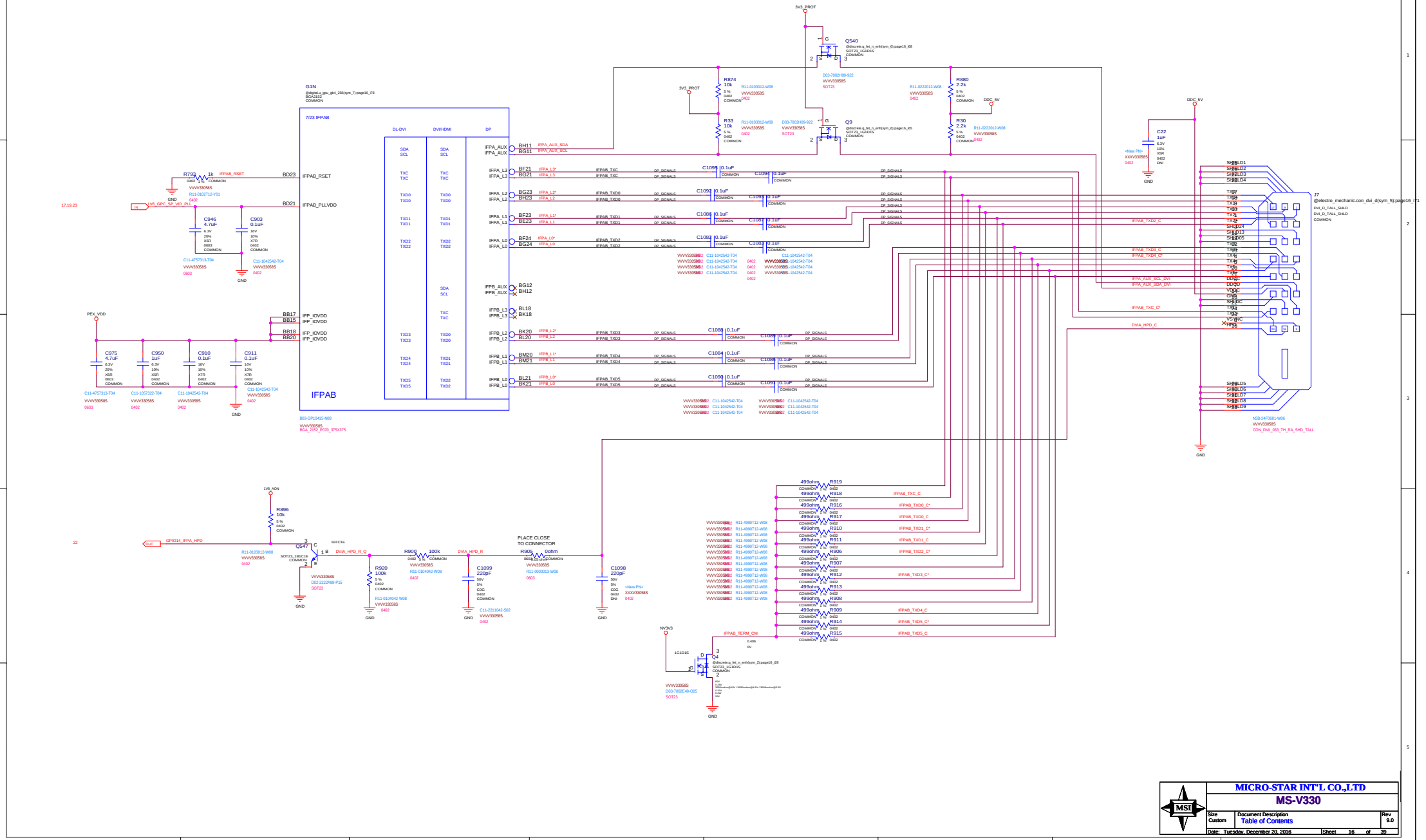
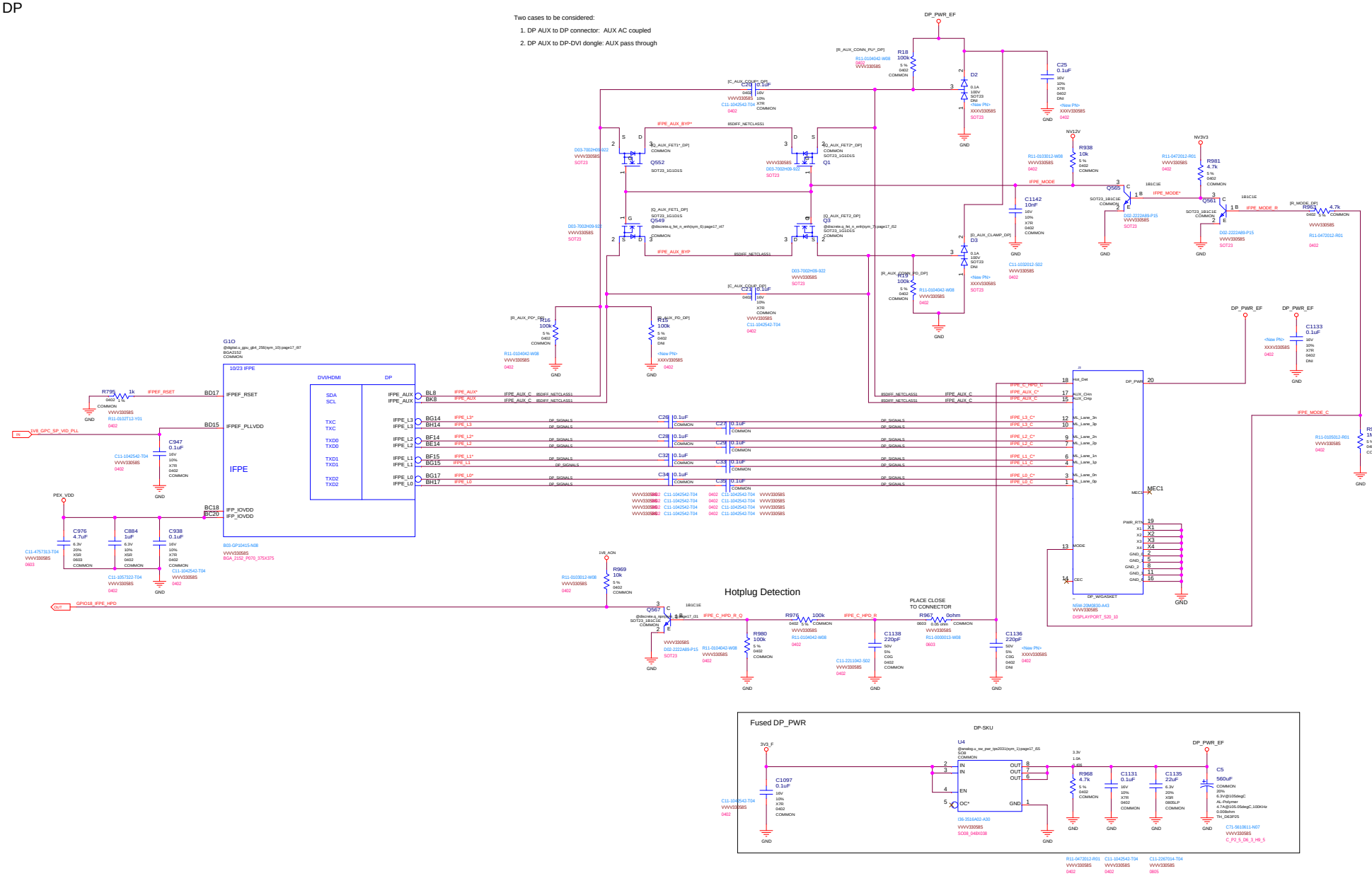
Page16: IFPAB DVI-D-DL



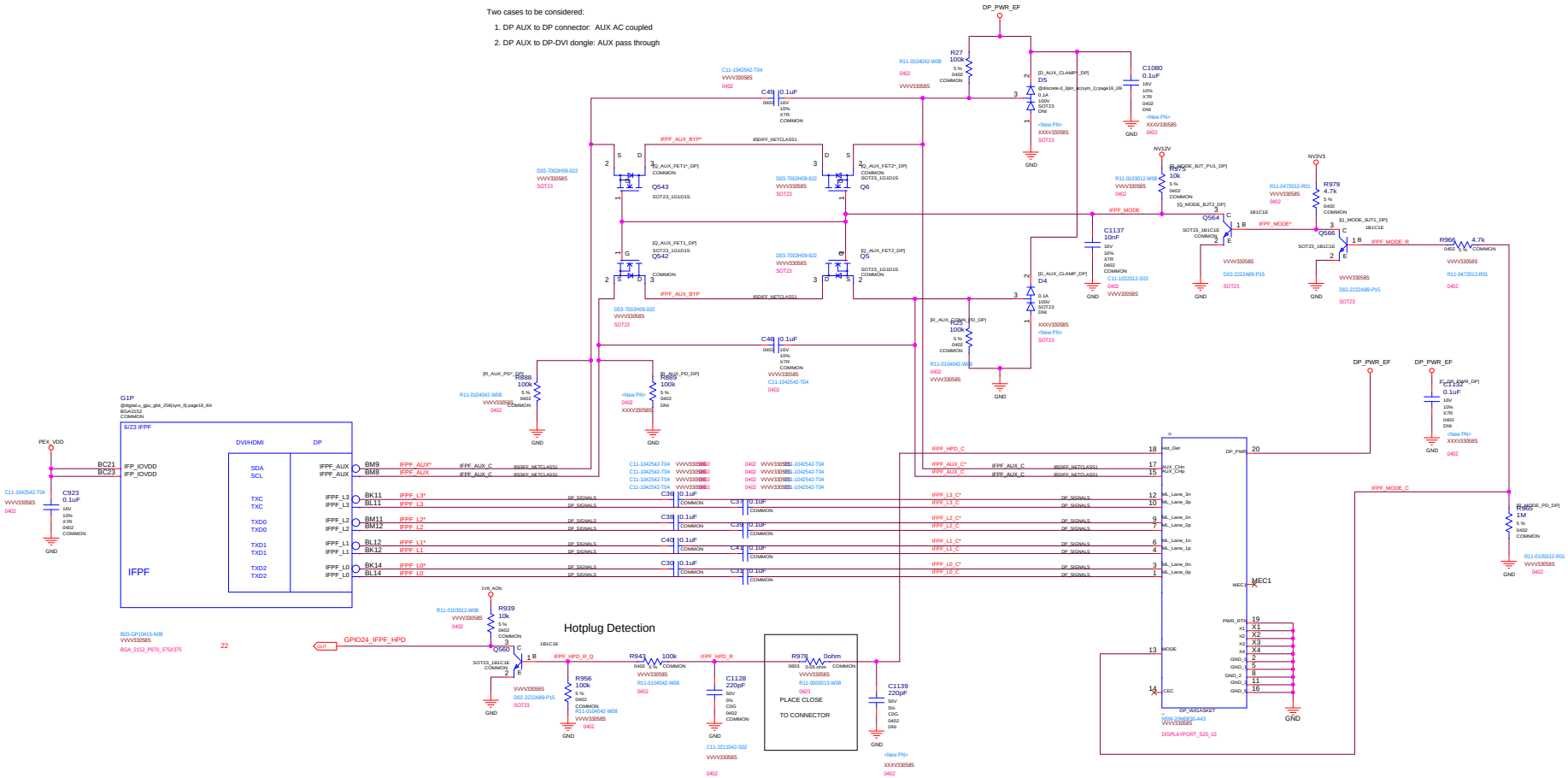
Page17: IFPE DP

Two cases to be considered:

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



Page18: IFPF DP



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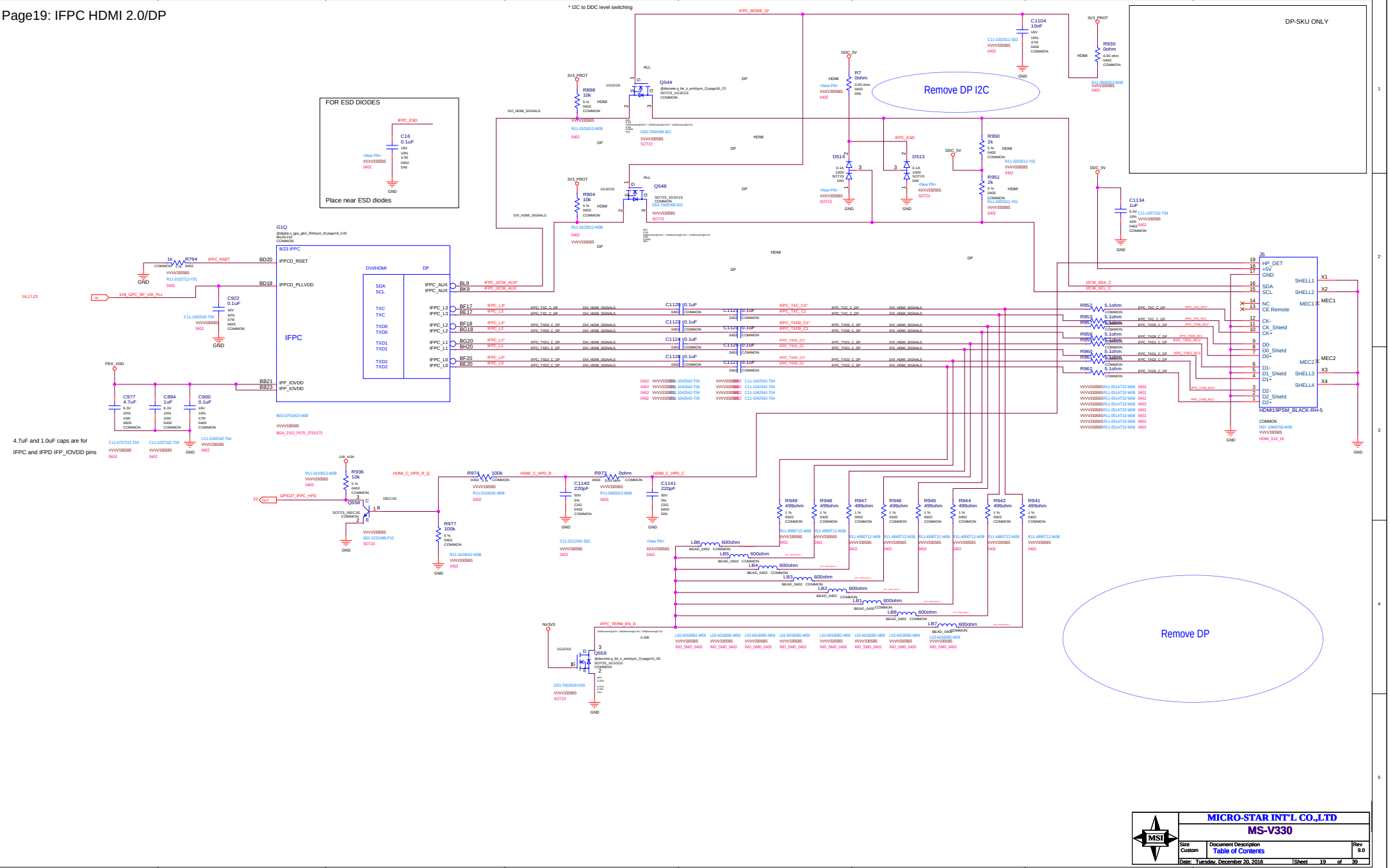
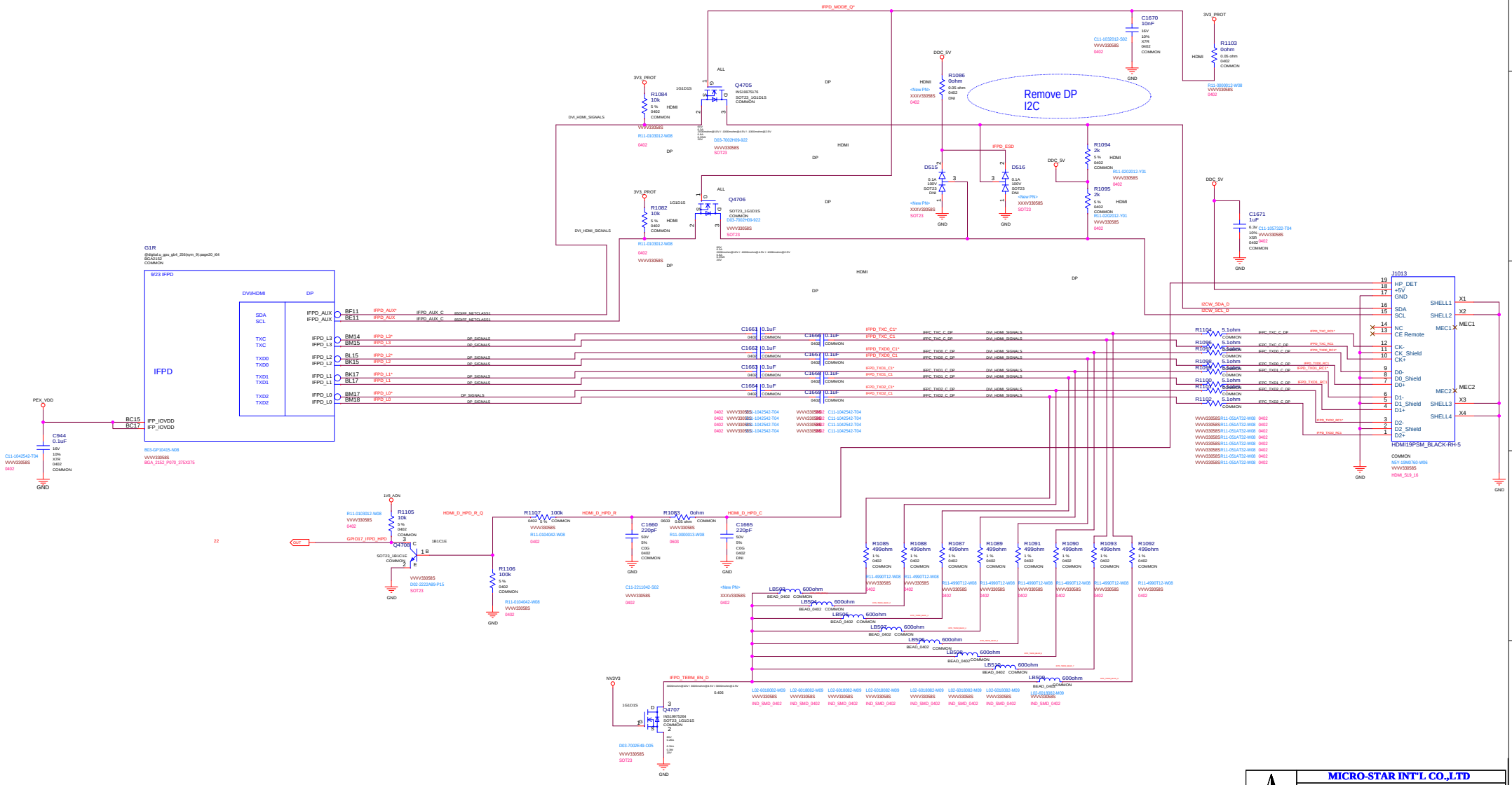
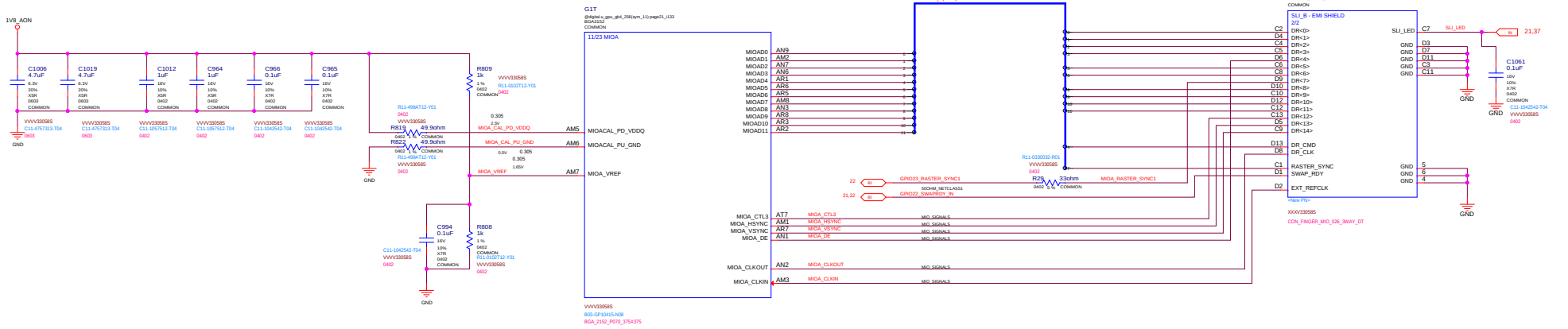
Page19: IFPC HDMI 2.0/DP

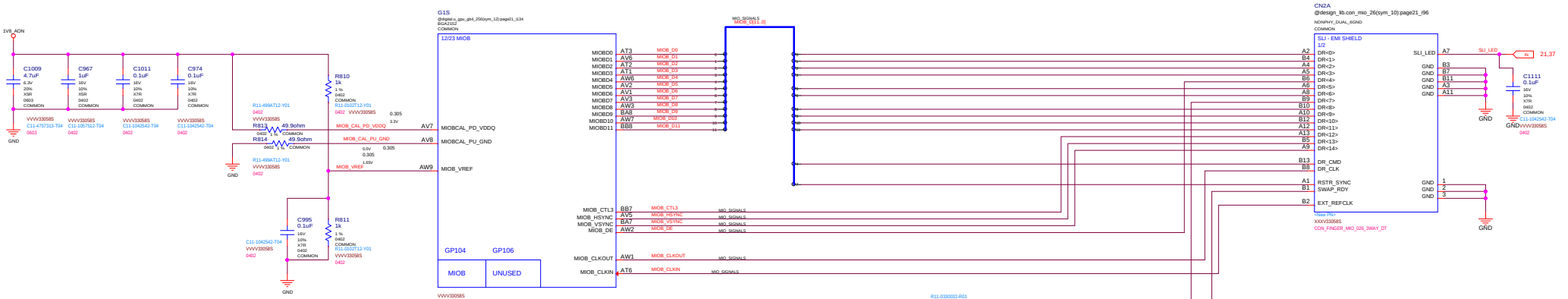


Page20: IFPD DP



Page21: MIOA/B Interface and FRAME LOCK





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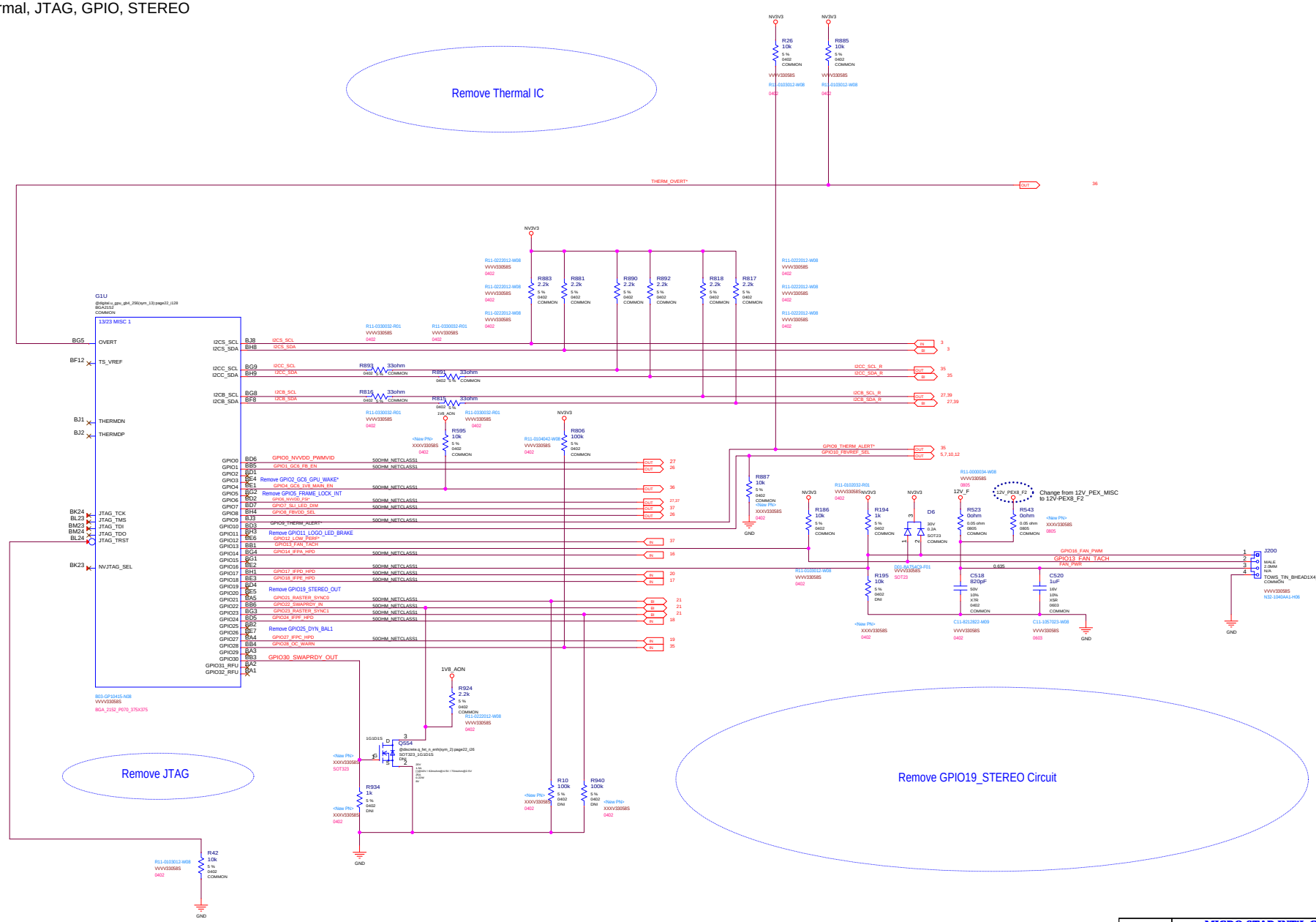
Page22: MISC1: Fan, Thermal, JTAG, GPIO, STEREO



Page23: MISC2: ROM, XTAL, Straps

STRAP2	STRAP1	STRAP0	RAMCFG[4:0]
L	L	L	00000
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111

ROM_SO	ROM_SI	ROM_SCLK	SOR_EXPOSED[3:0]	1:ENABLE 0:DISABLE
L	L	L	1111 DEFAULT	SOR0/1/2/3 ENABLE
L	L	H	1110	
L	H	L	1101	
L	H	H	1100	
H	L	L	1011	
H	L	H	1010	
H	H	L	1001	
H	H	H	1000	
L	L	M	0111	
L	M	L	0110	
L	M	H	0101	
L	H	M	0100	
H	L	M	0011	
H	M	L	0010	
H	M	H	0001	
H	H	M	0000	

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0

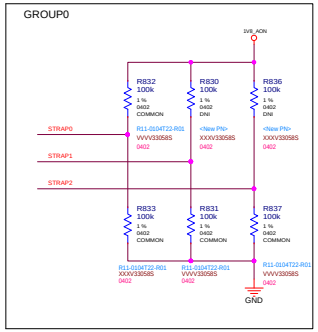
H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

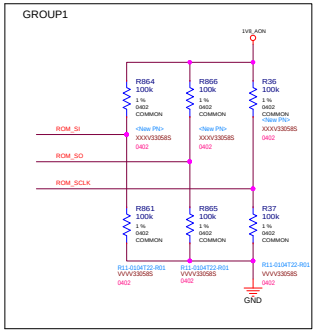
1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

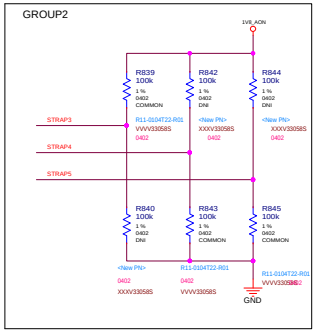
1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGNAL

1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

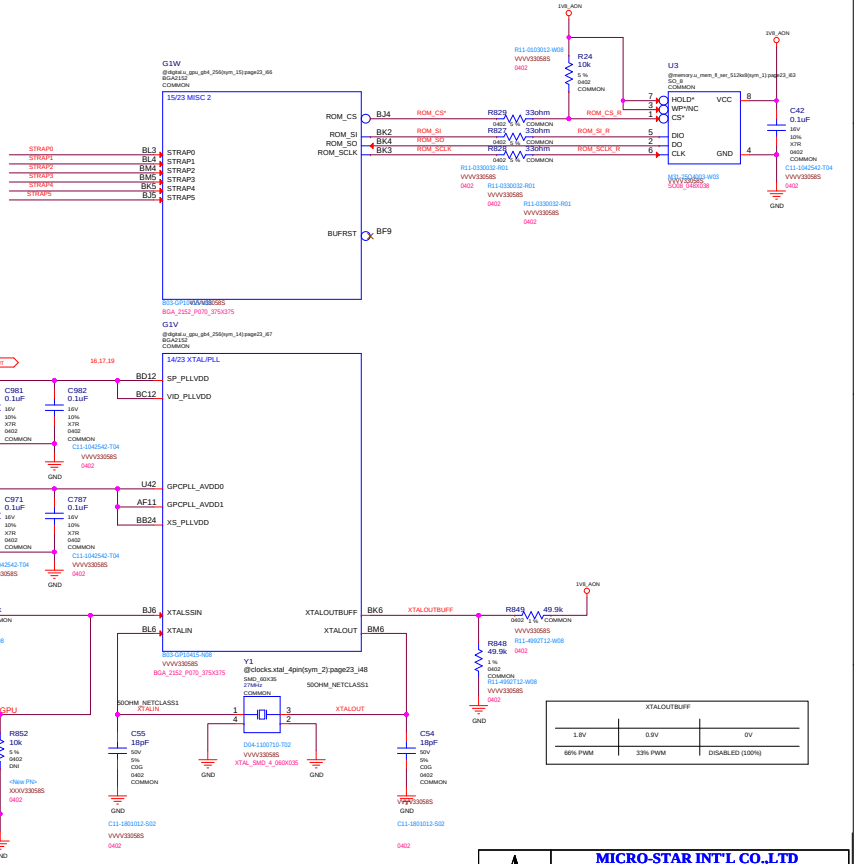
1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE







CFG[3:0]	Config Width Vendor
0000	256Mx32 256-bit Samsung
0001	256Mx32 256-bit Micron
0010	256Mx32 256-bit Hynix
0011	Reserved
0100	Reserved
0101	Reserved
0110	Reserved
0111	Reserved



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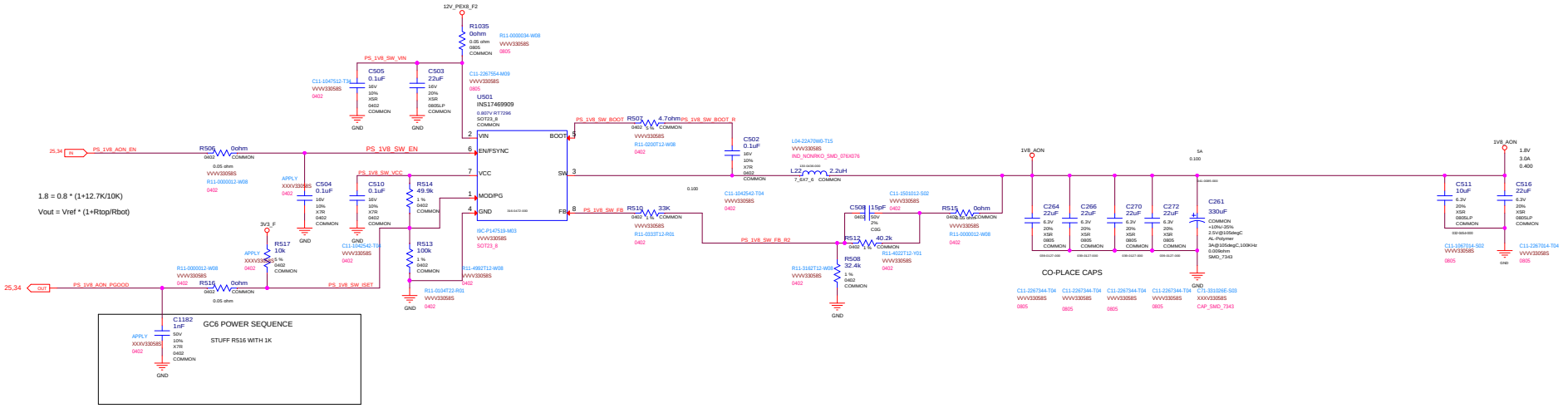
Page24: PS: 1V8, 1V8_AON



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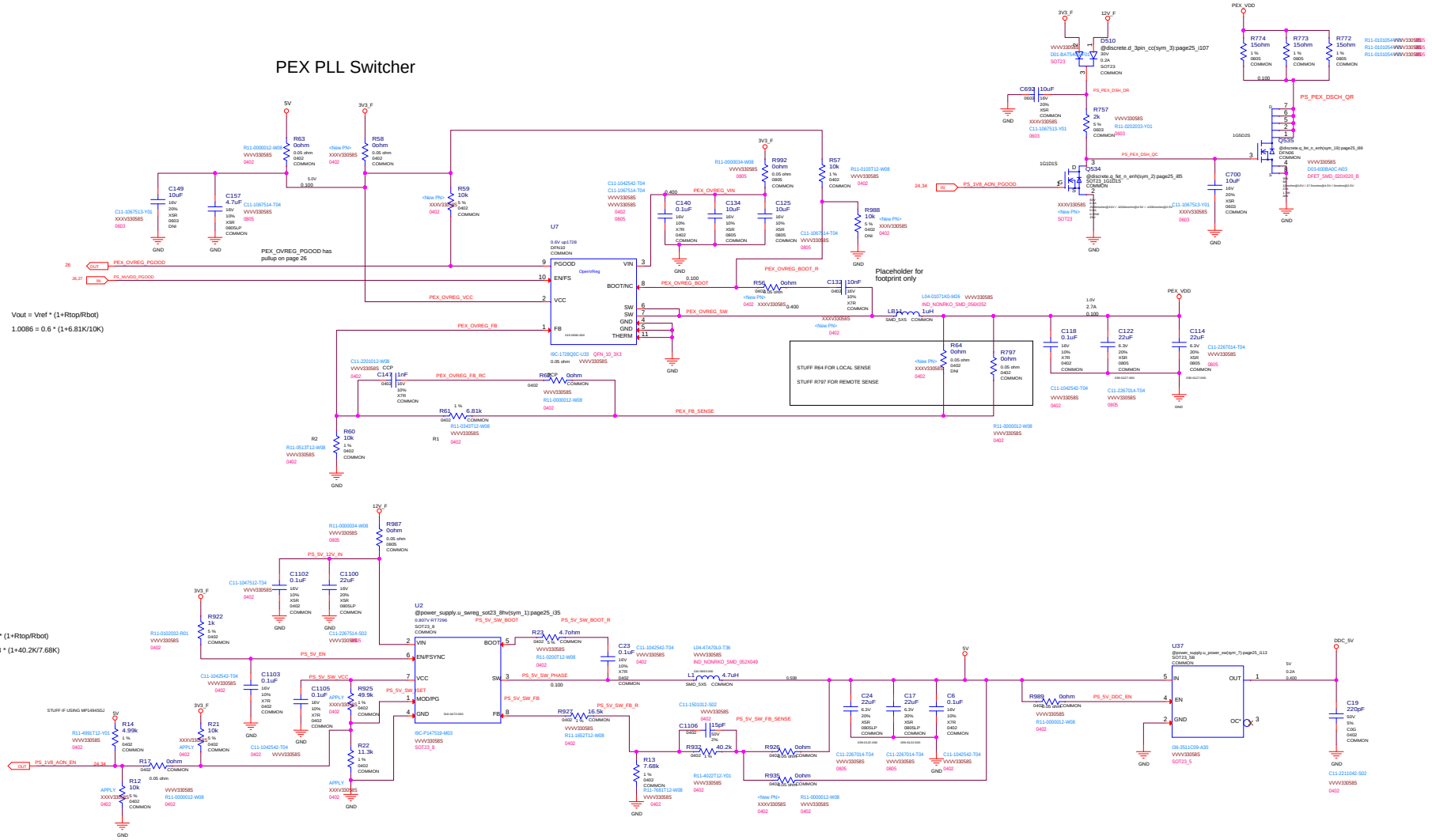
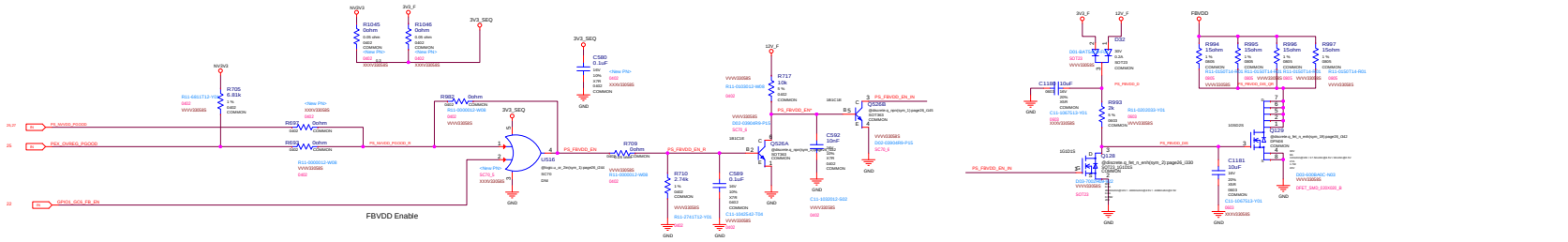
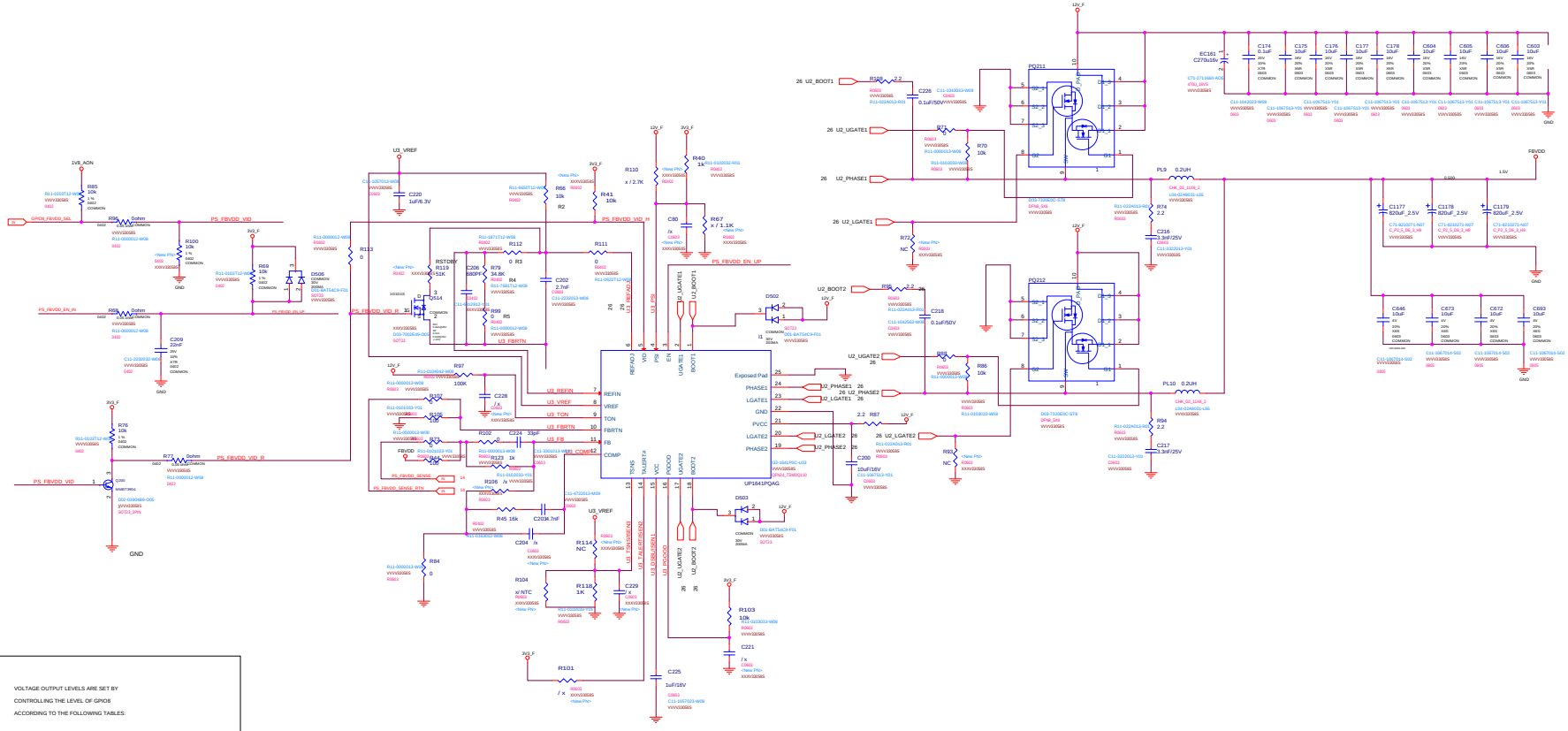
PEX PLL Switcher







VOLTAGE OUTPUT LEVELS ARE SET BY

CONTROLLING THE LEVEL OF GPIO8

ACCORDING TO THE FOLLOWING TABLES:

SAMSUNG K4G1325FC-HC28

GPIO8 LEVEL	FBVDD OUTPUT VOLTAGE
0	1.35V
1	1.55V

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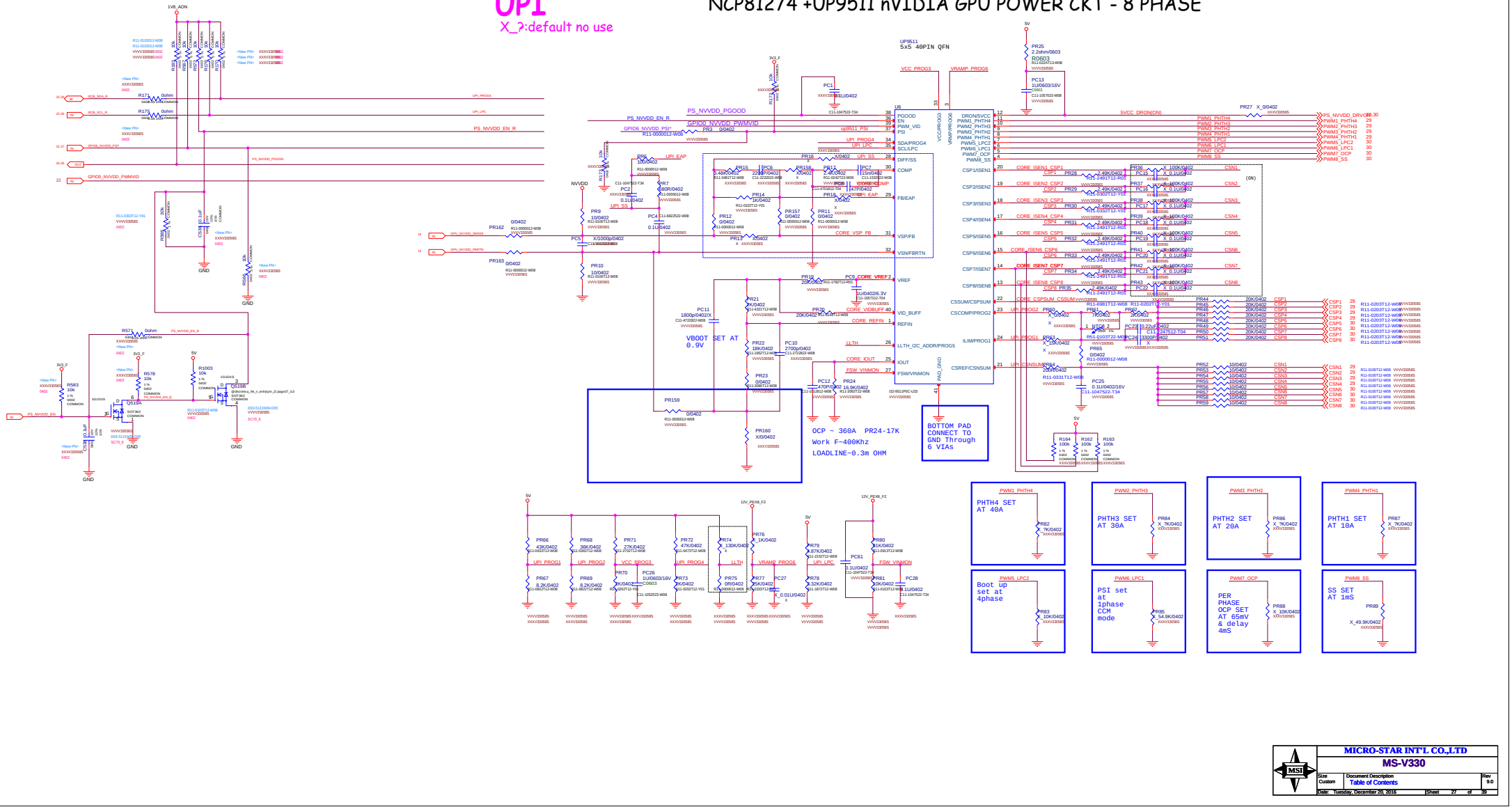
UPI

X_?:default no use

NCP81274 +UP9511 nVIDIA GPU POWER CKT - 8 PHASE



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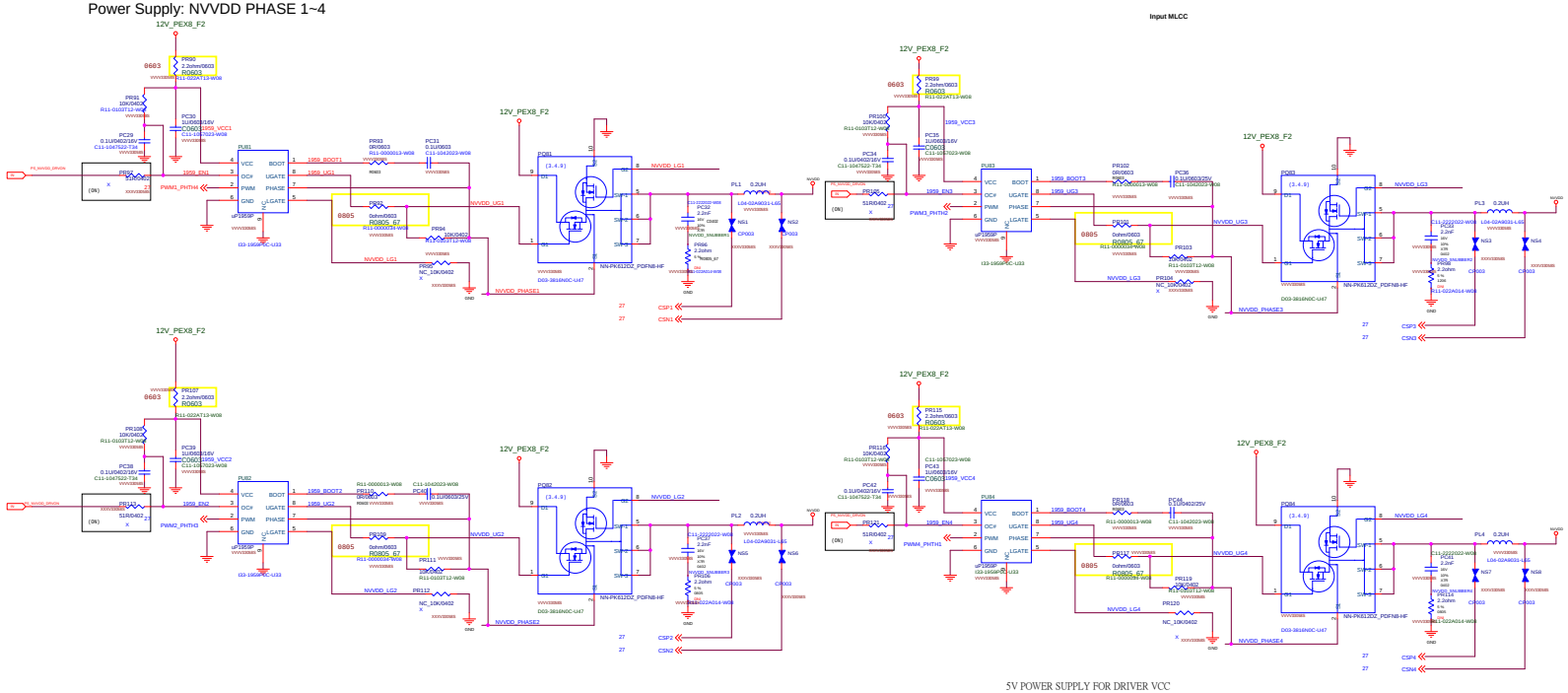
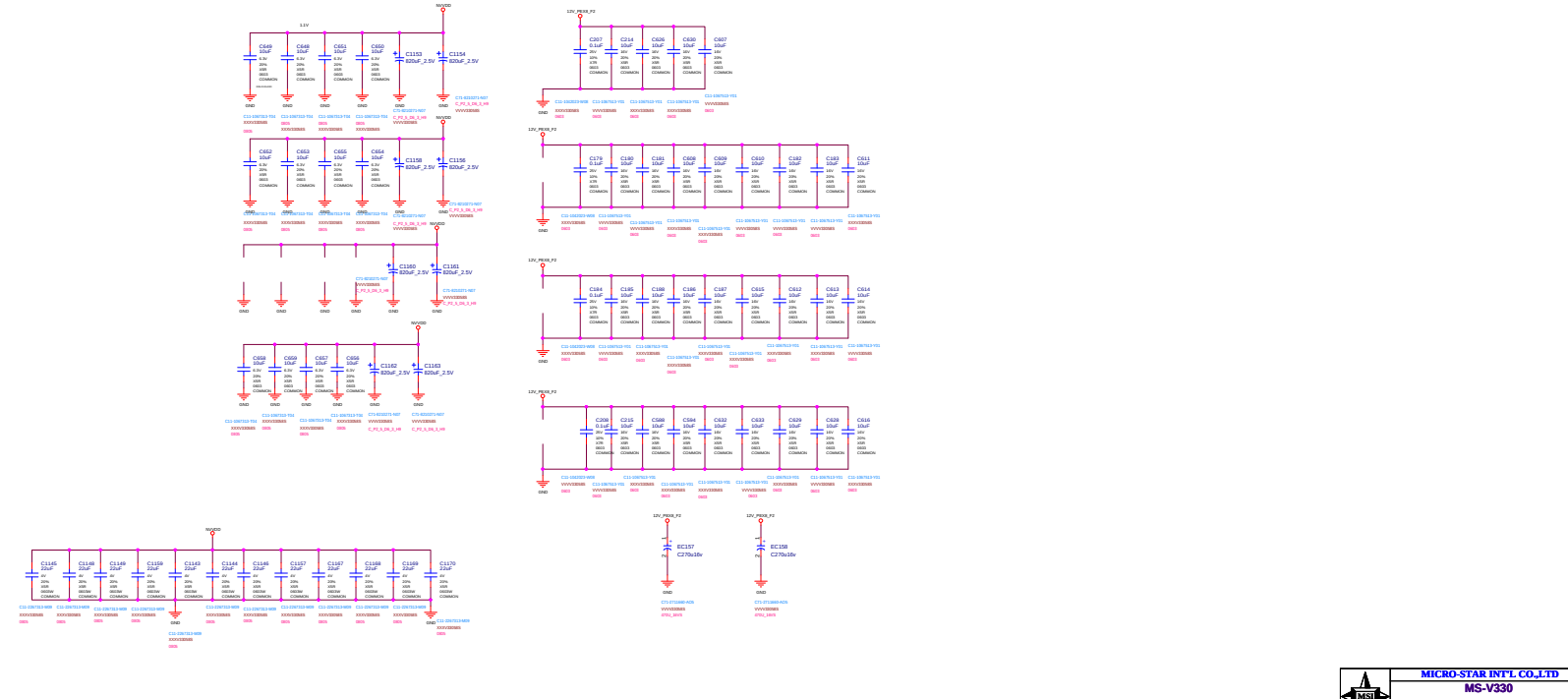
Power Supply: NVVDD PHASE 1~4



5V POWER SUPPLY FOR DRIVER VCC



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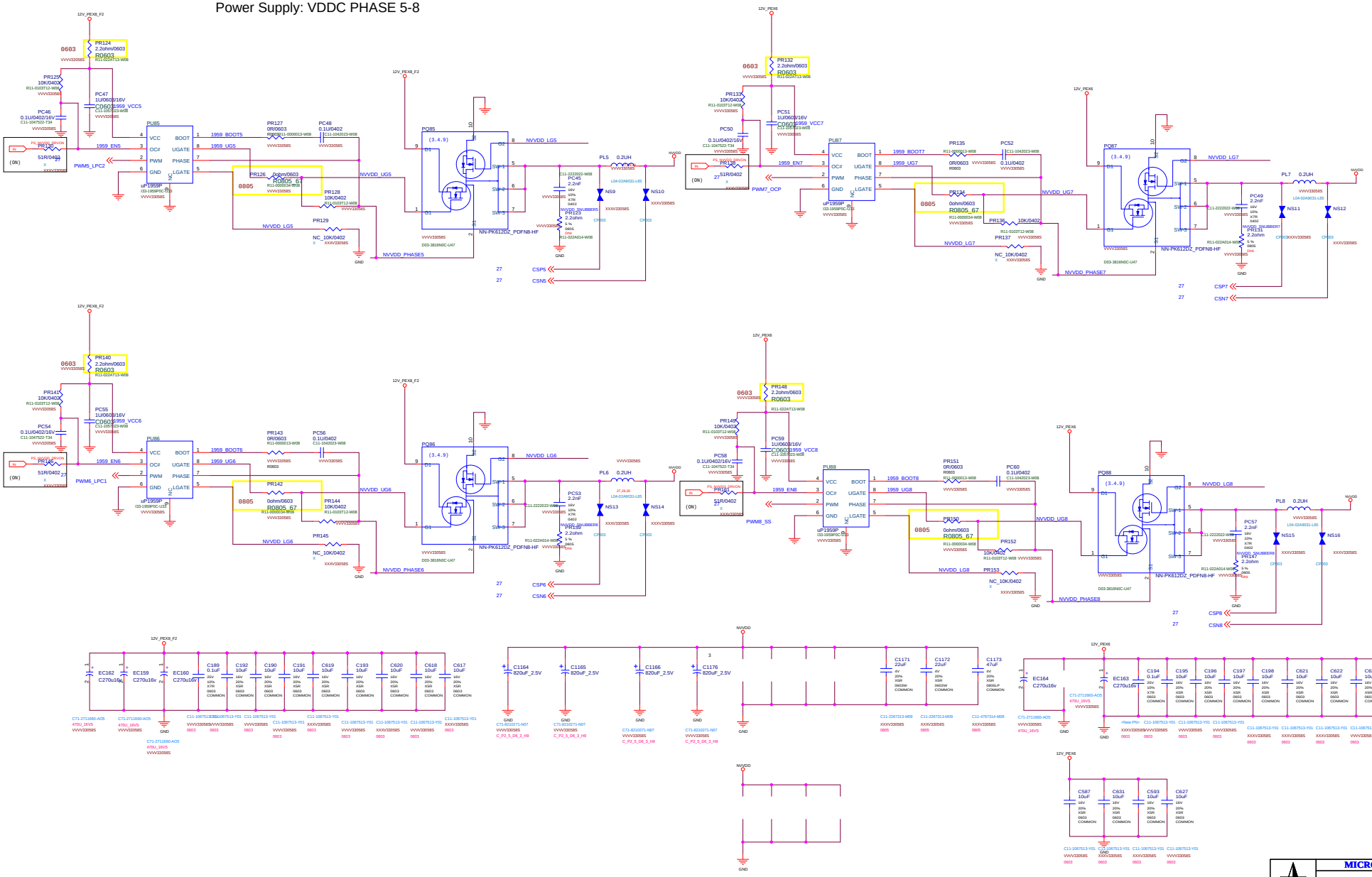
Page30: PS: NVVDD Phase 5 ~8

Power Supply: VDDC PHASE 5-8



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Page32: PS: Dynamic Power Balance Phases

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Page33: PS: Dynamic Power Balance Logic

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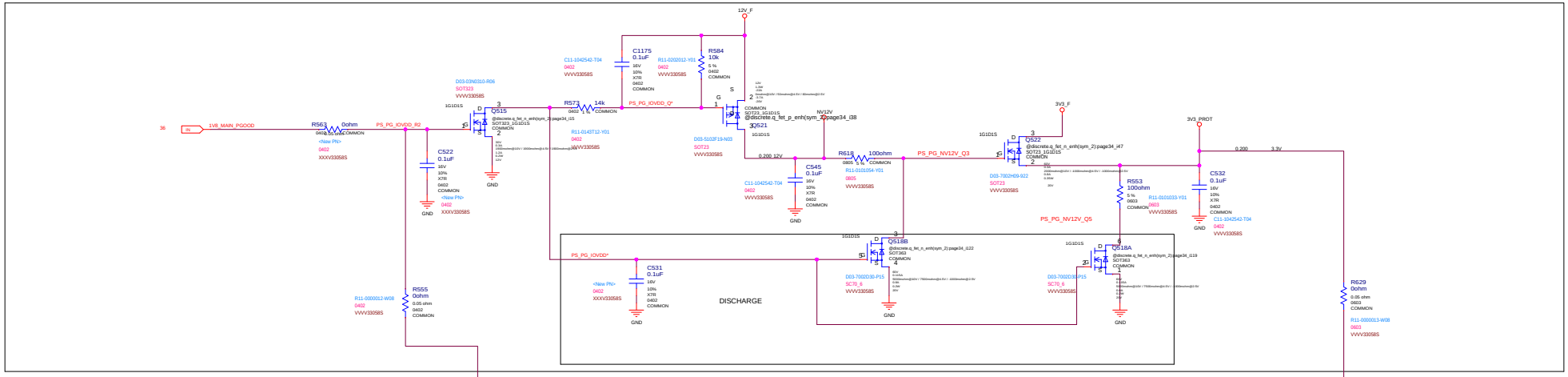
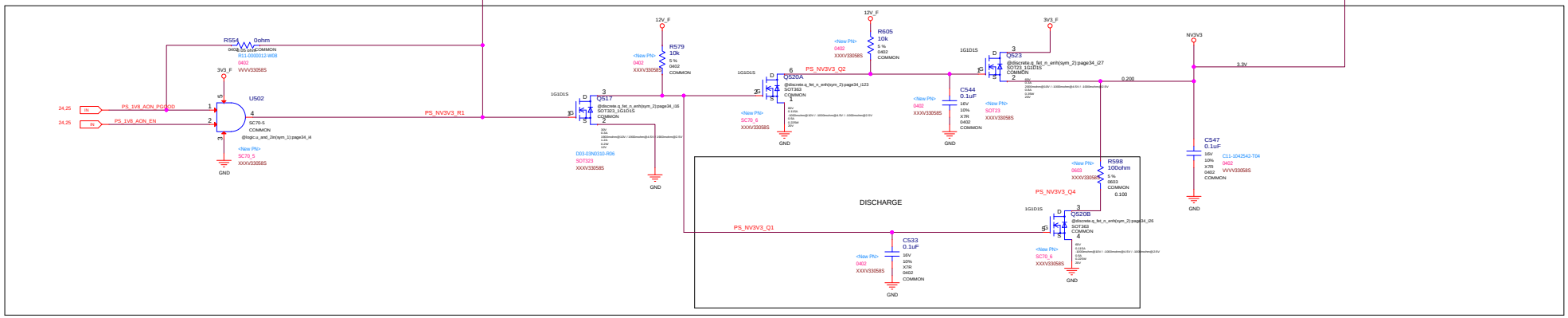
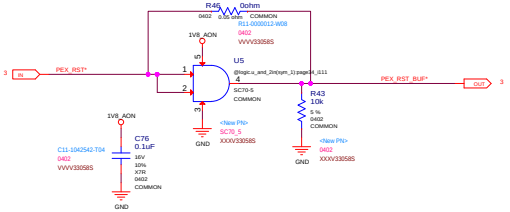
Page34:PS: NV3V3, NV12V







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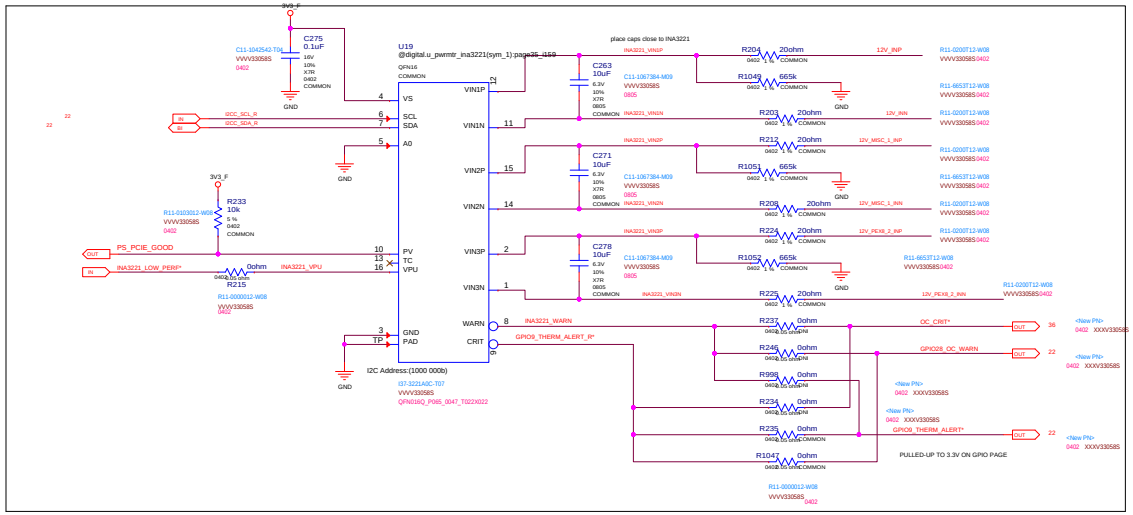
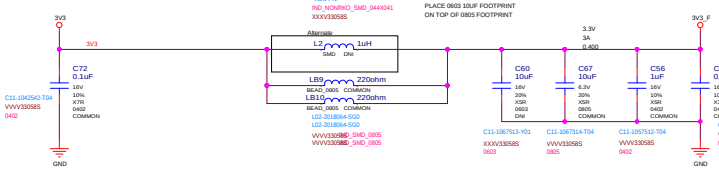
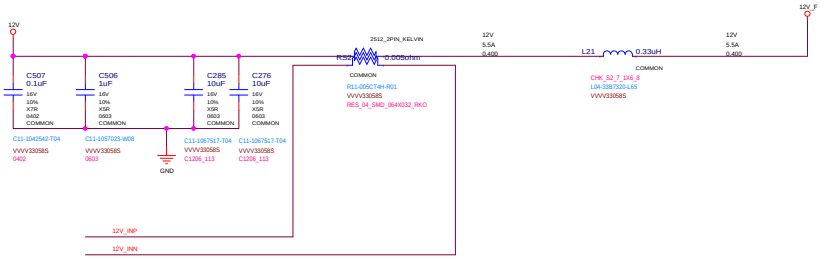
Page35: PS: Inputs, Filtering, and Monitoring



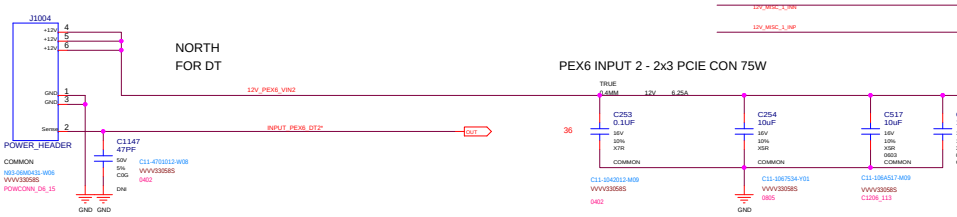
PEX 3V3 INPUT - 10W



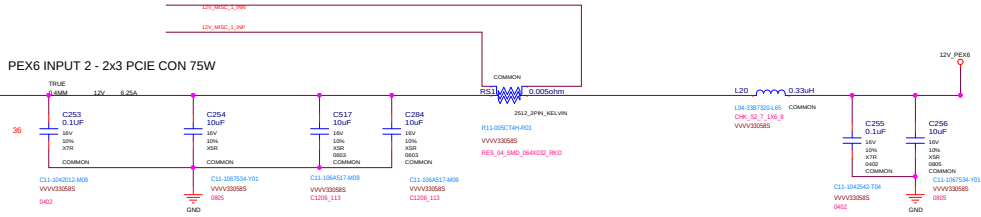
PEX_12V INPUT - 66W



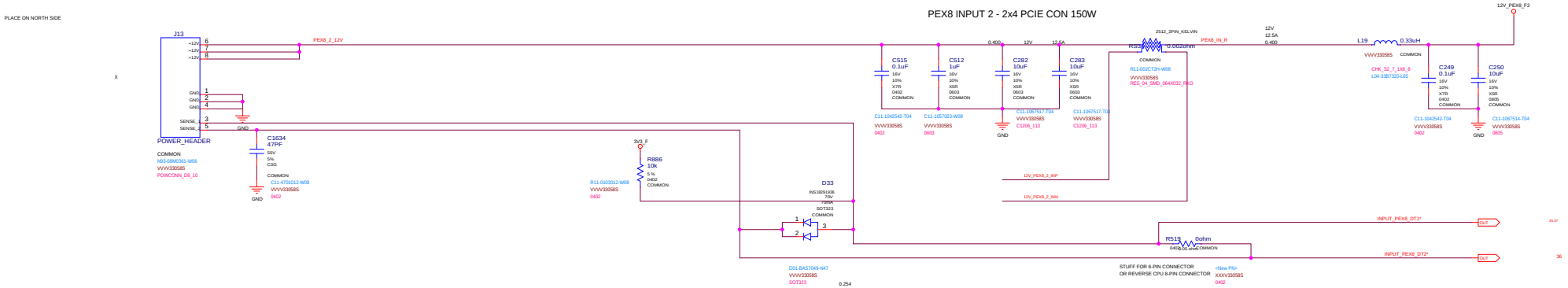
NORTH FOR DT



PEX6 INPUT 2 - 2x3 PCIE CON 75W



PEX8 INPUT 2 - 2x4 PCIE CON 150W



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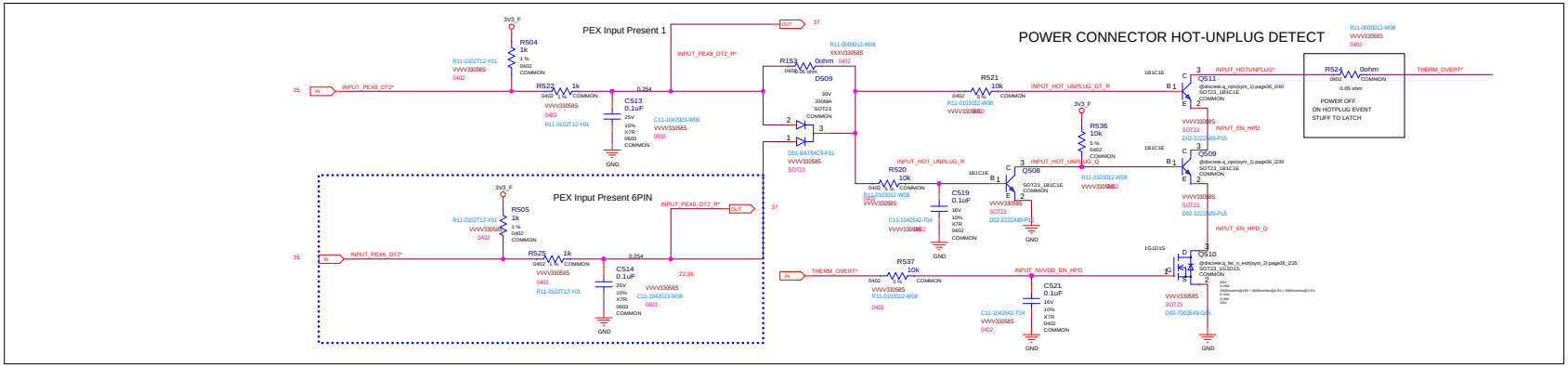
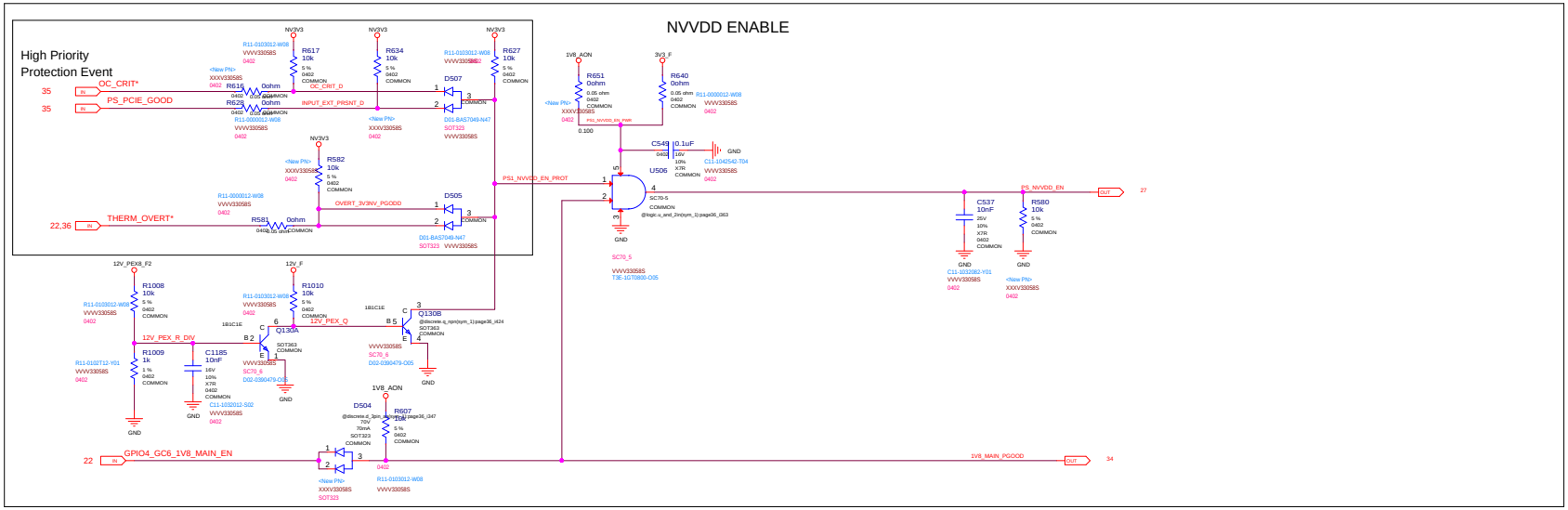
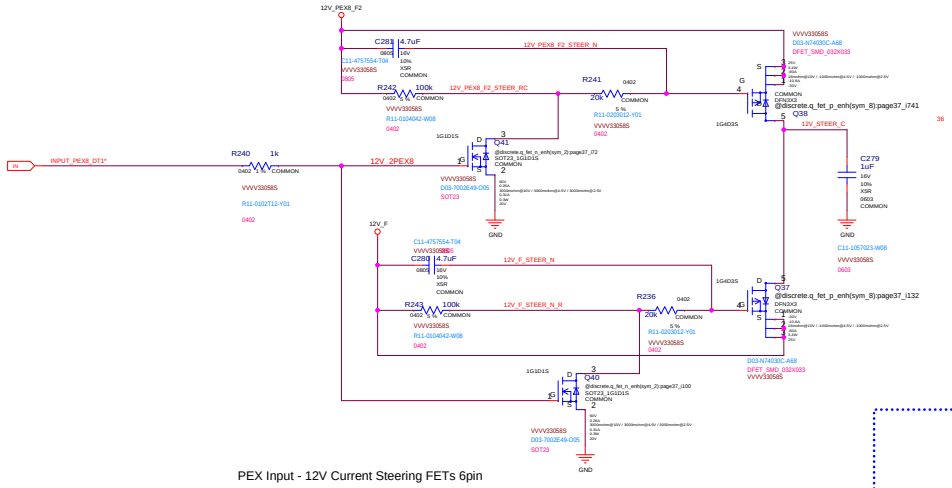
Page36: PS: Shutdown and Sequencing





Page37: PS: 12V Current Steering, PSI Control, and LED

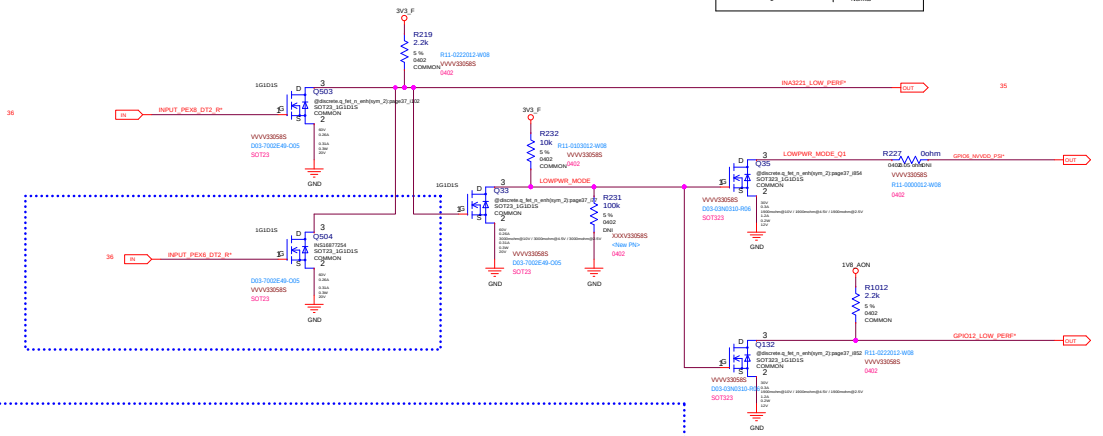
PEX Input - 12V Current Steering FETs 8pin



PEX Input - 12V Current Steering FETs 6pin

PEX Input - Power Level/PSI* Control

GPIO12_LOW_PERF*	GPU SPEED
0	Slow
1	Normal

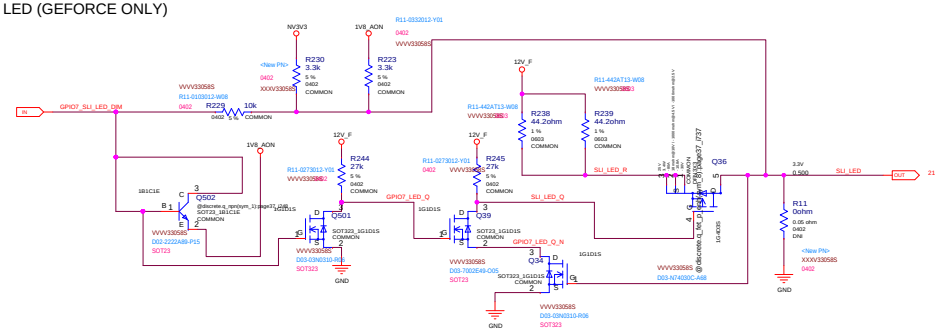


Remove POWER BRAKE

POWER BRAKE

GeForce Logo LED

SLI LED (GEFORCE ONLY)



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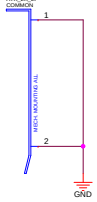
Page38: Mechanical: Bracket/Thermal Solution

Brackets:

BKT

@mechanic:bracket(sym_4)page38_i27
ATX_2X_2P
COMMON



FAN

SPECIAL MECHANIC
No connected mounting pins
MECH_PEX_BREAKOFF_RETENTION_B
DNI
VVVV330585
E32-0405261-A87
–

PCI_E_SLOT

SPECIAL MECHANIC
No connected mounting pins
MECH_PEX_BREAKOFF_RETENTION_B
DNI
VVVV330585
E24-V320010-P09
–

MEMHS

SPECIAL MECHANIC
No connected mounting pins
MECH_PEX_BREAKOFF_RETENTION_B
DNI
VVVV330585
E31-0503990-A87
–

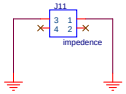
MOSHS

SPECIAL MECHANIC
No connected mounting pins
MECH_PEX_BREAKOFF_RETENTION_B
DNI
VVVV330585
E31-0503980-A87
–

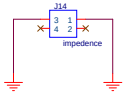
BOTHS

SPECIAL MECHANIC
No connected mounting pins
MECH_PEX_BREAKOFF_RETENTION_B
DNI
VVVV330585
E31-0408160-A87
–

J11



J14



J17

J16

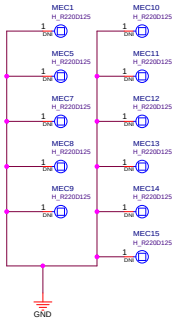
PCB

PCB
109-GN582-00A
COMMON
VVVV330585
PD0-0V33060-E48

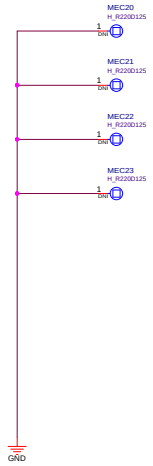
HDMI_FEE

HDMI
$$$$$$$
HDMI_FEE
COMMON
VVVV330585
Y01-RHDM003-000

Mechanical Holes Symbol



GPU Stiffner



BKTSCREW

MEC_SCREW_HEX_JACK
COMMON
VVVV330585
E41-1304099-H75

MEC6

MEC_SCREW_HEX_JACK
COMMON
VVVV330585
E42-5047006-C67

HDMISCREW

MEC_SCREW_HEX_JACK
COMMON
VVVV330585
E41-1303012-H75

MEC19

MEC_SCREW_HEX_JACK
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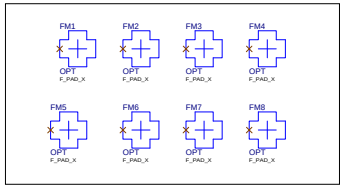

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