

PG611 A00

10GB GDDR5X, 320b, 256Mx32
DP + DP + HDMI/DP + DP

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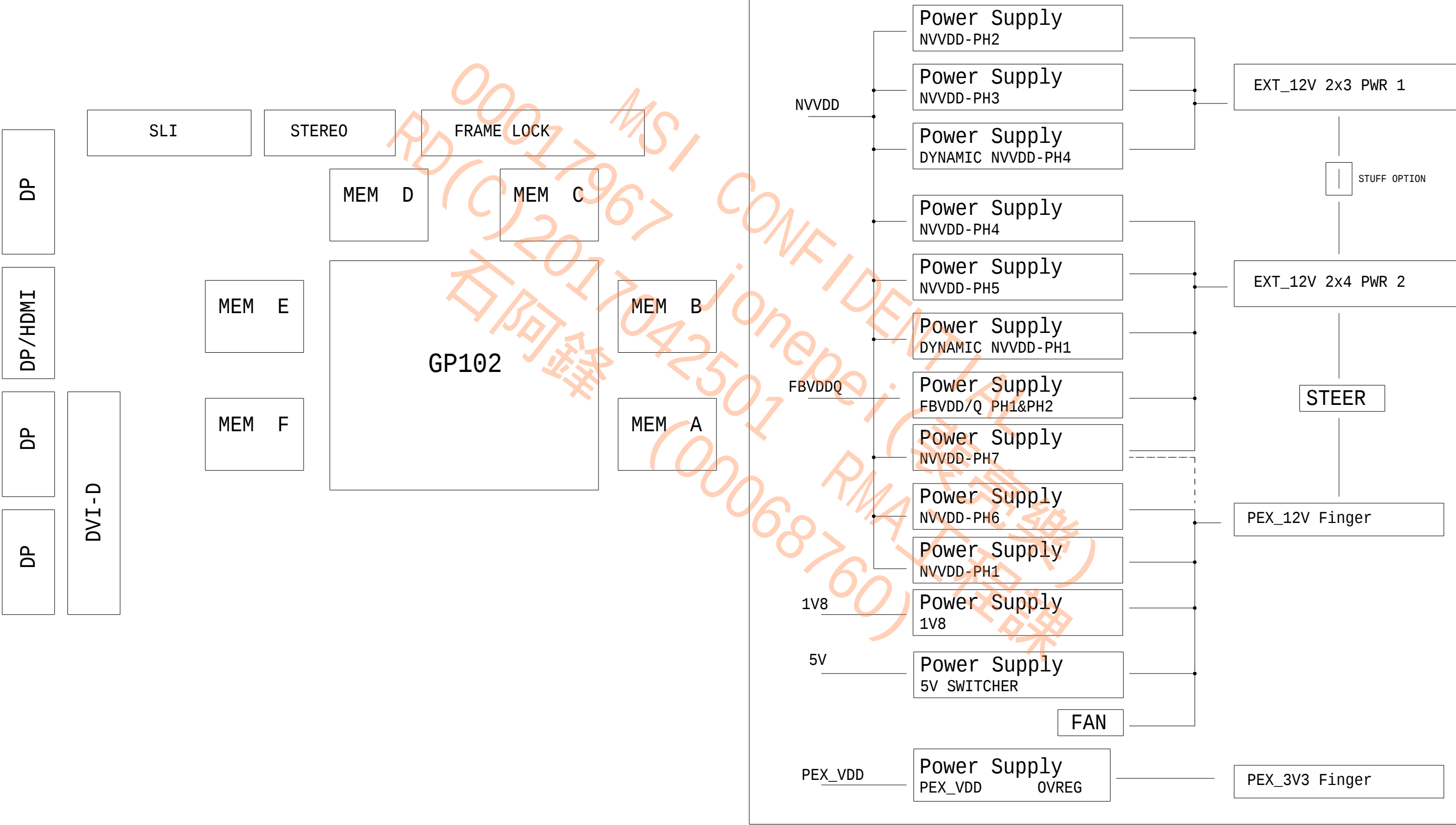
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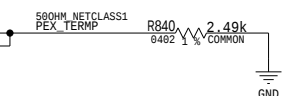
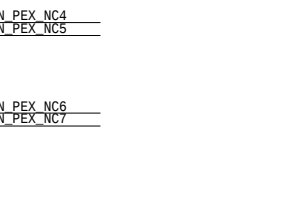
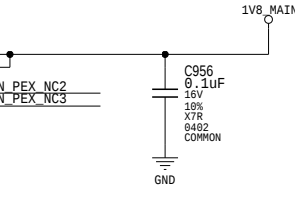
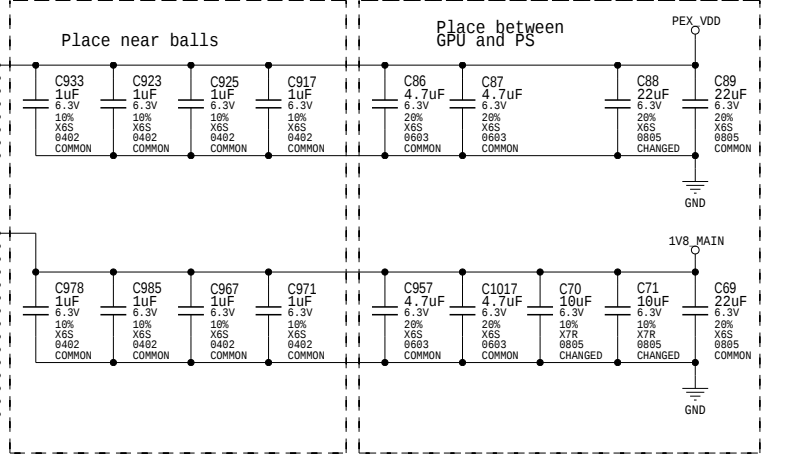
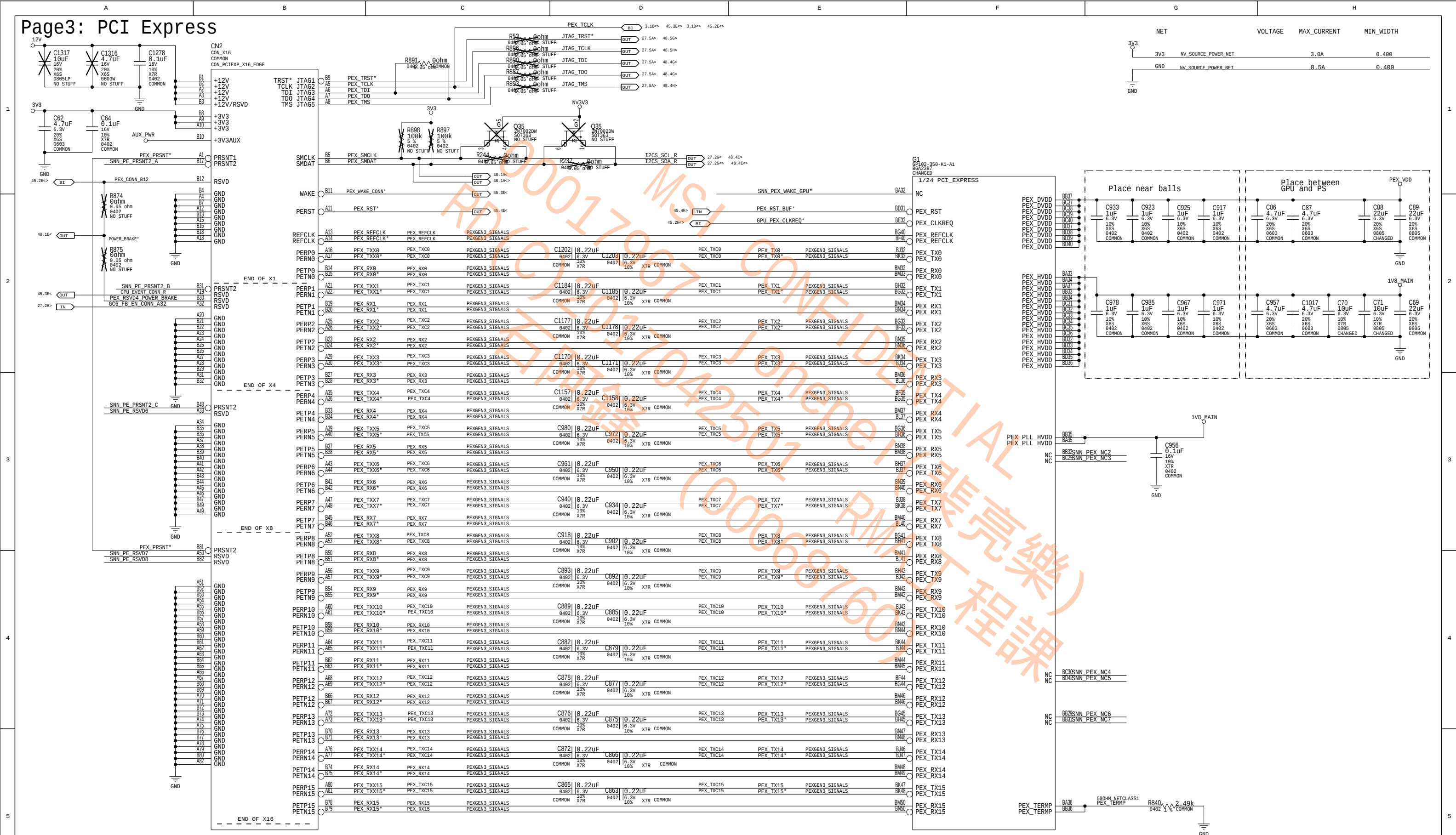
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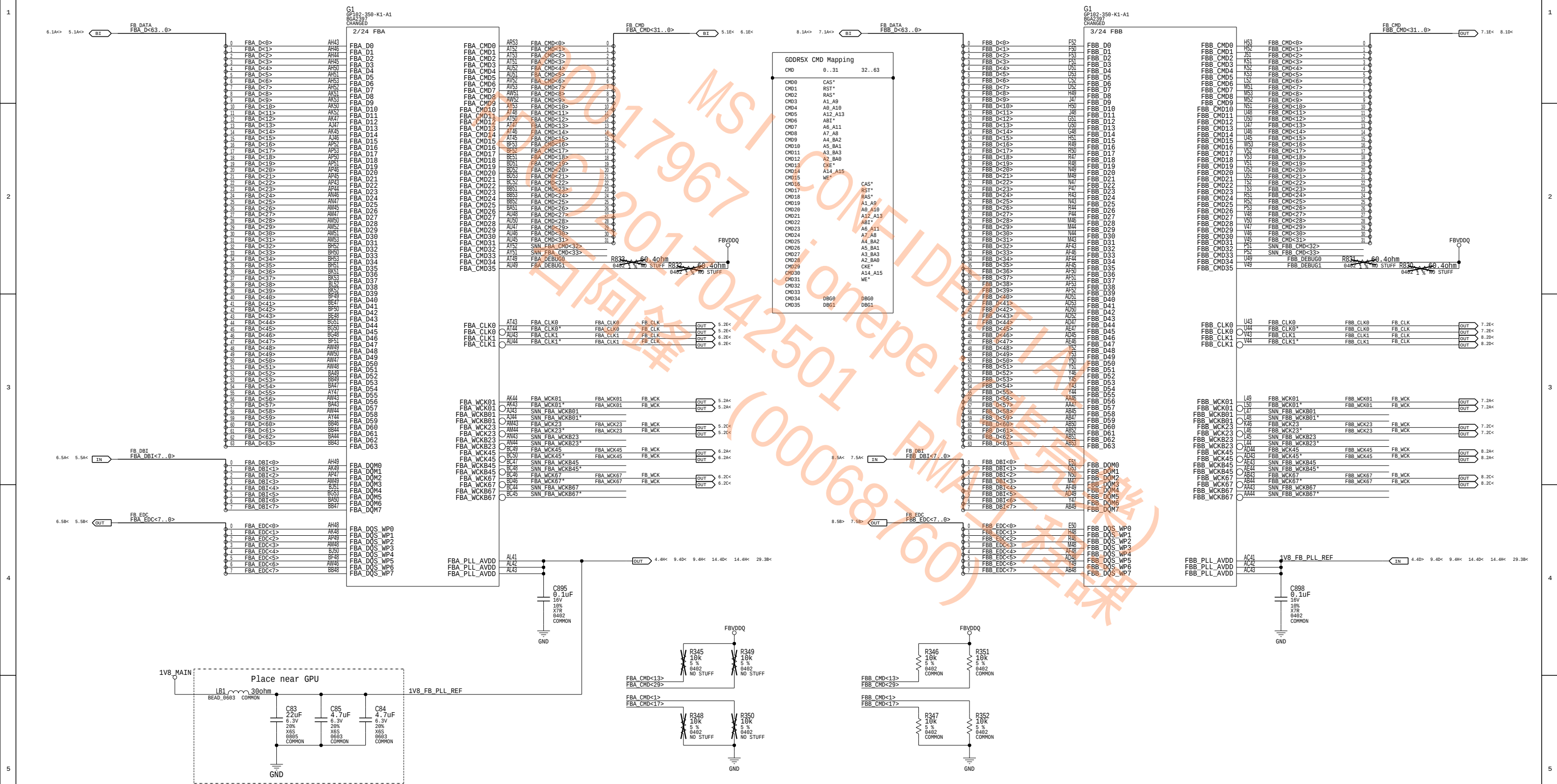


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Page4: MEMORY: GPU Partition A/B



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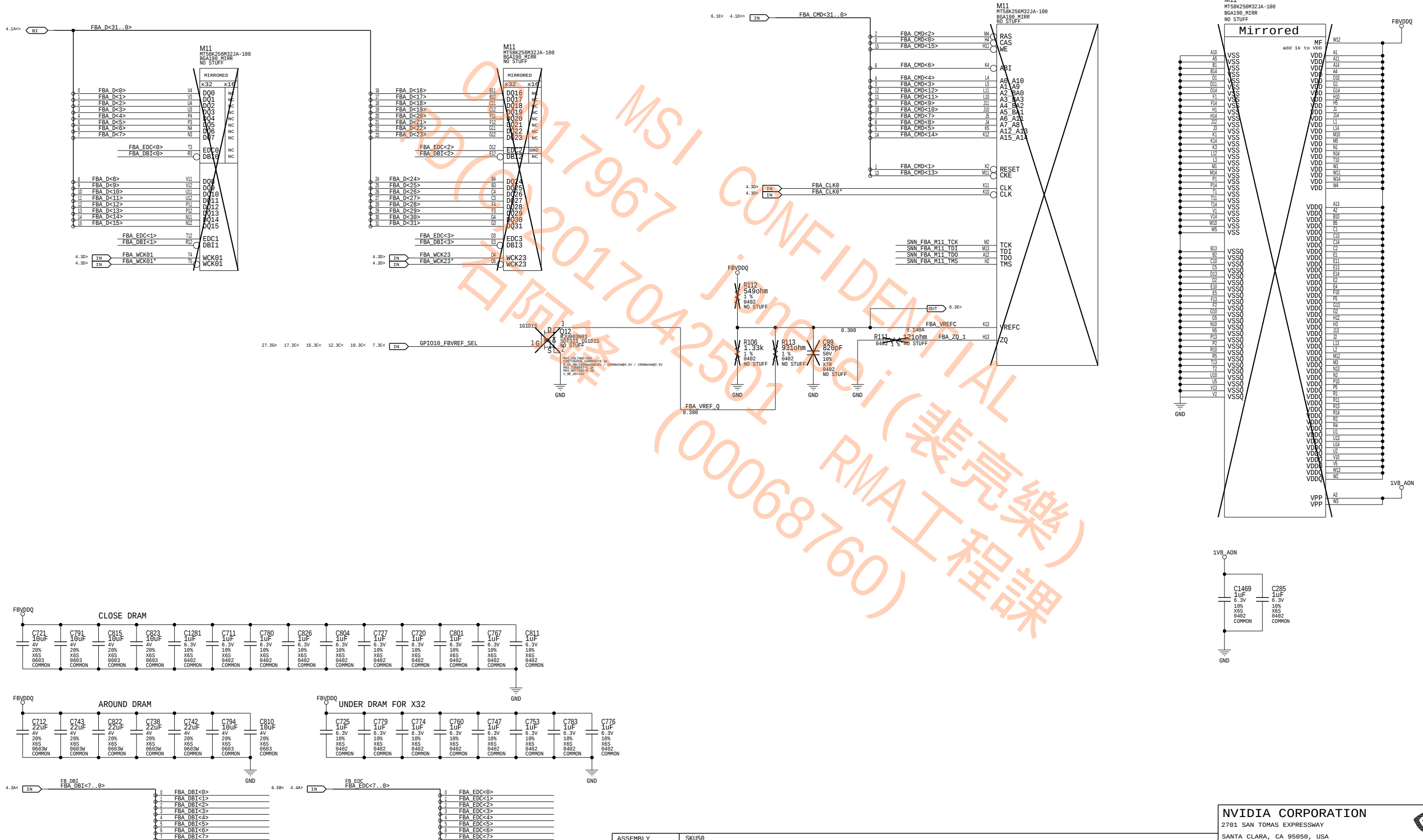
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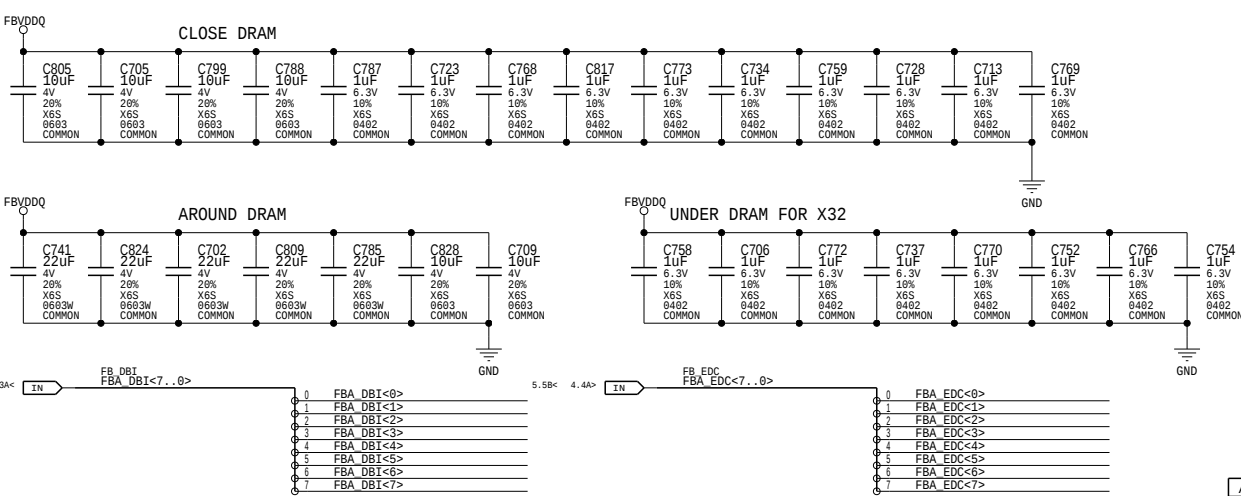
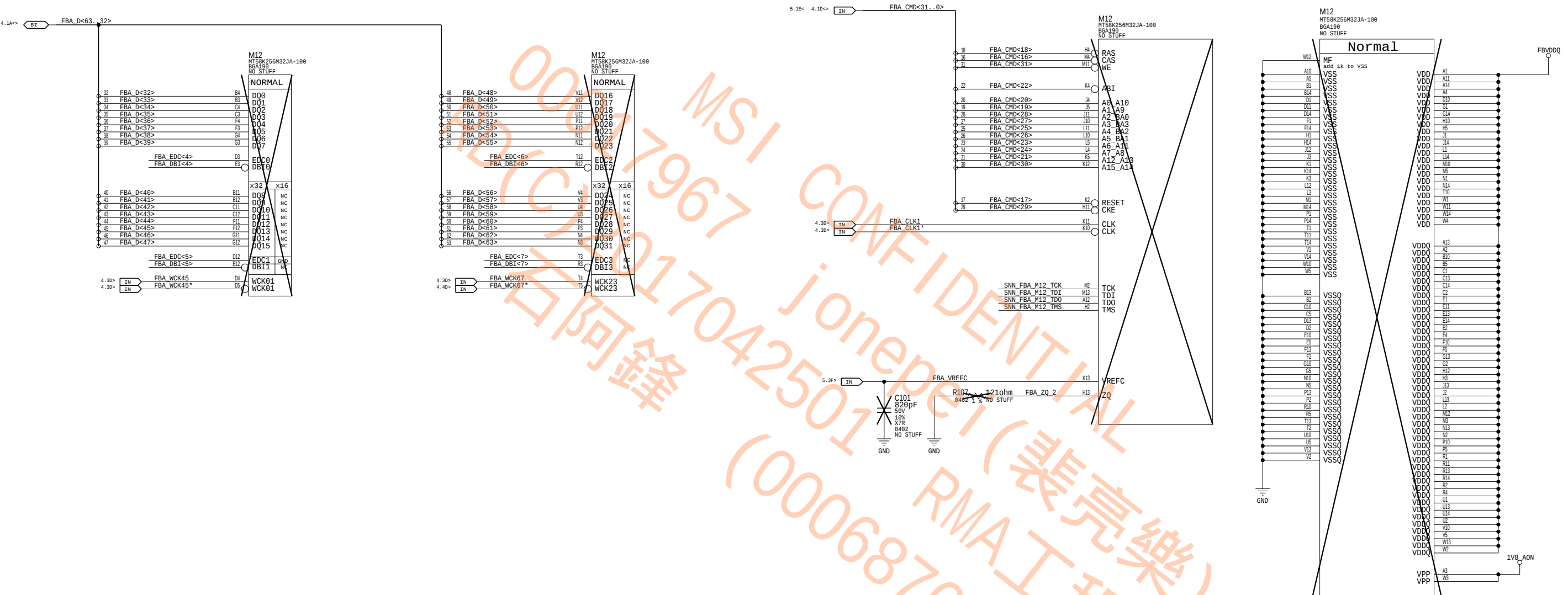
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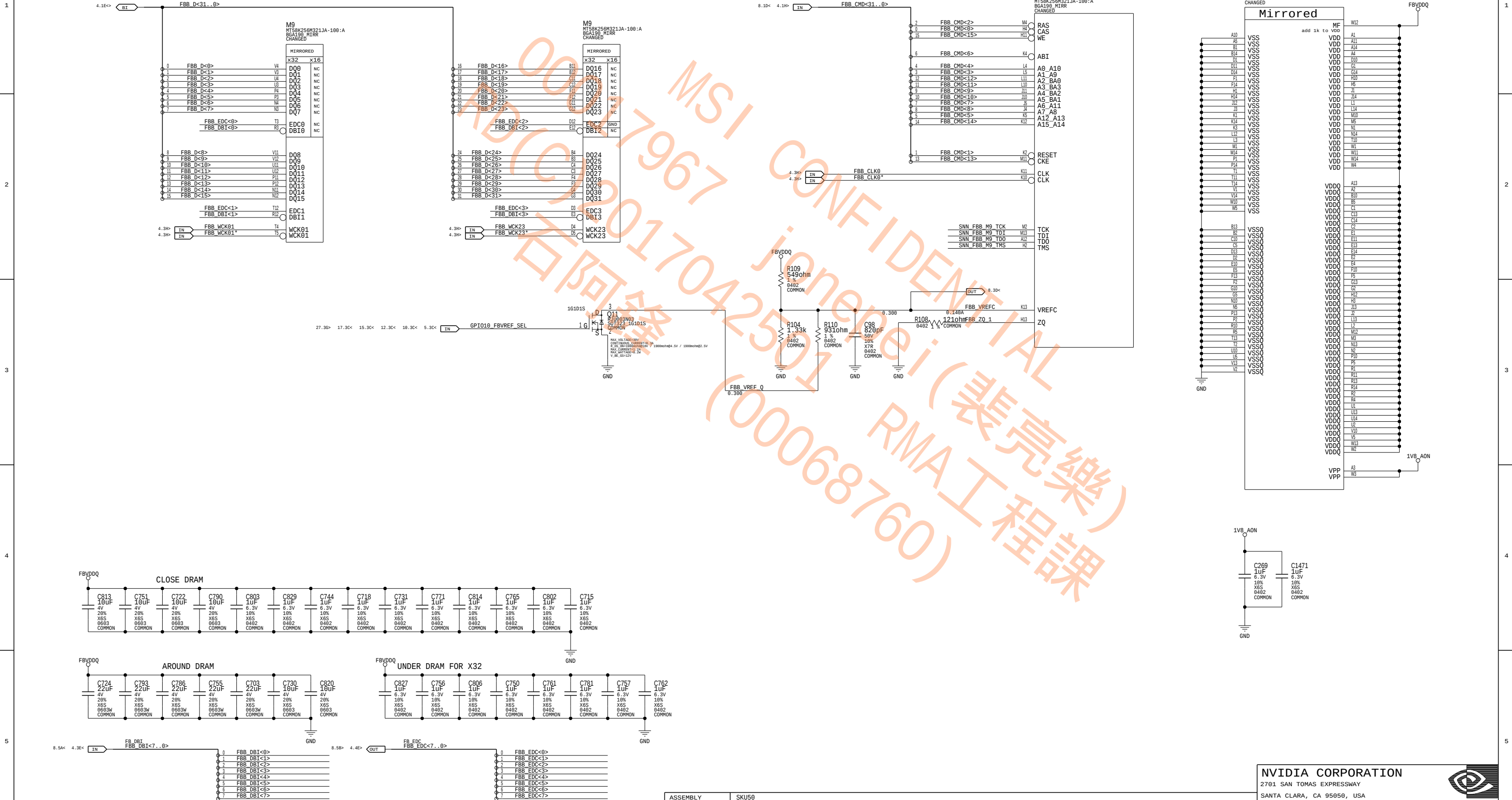
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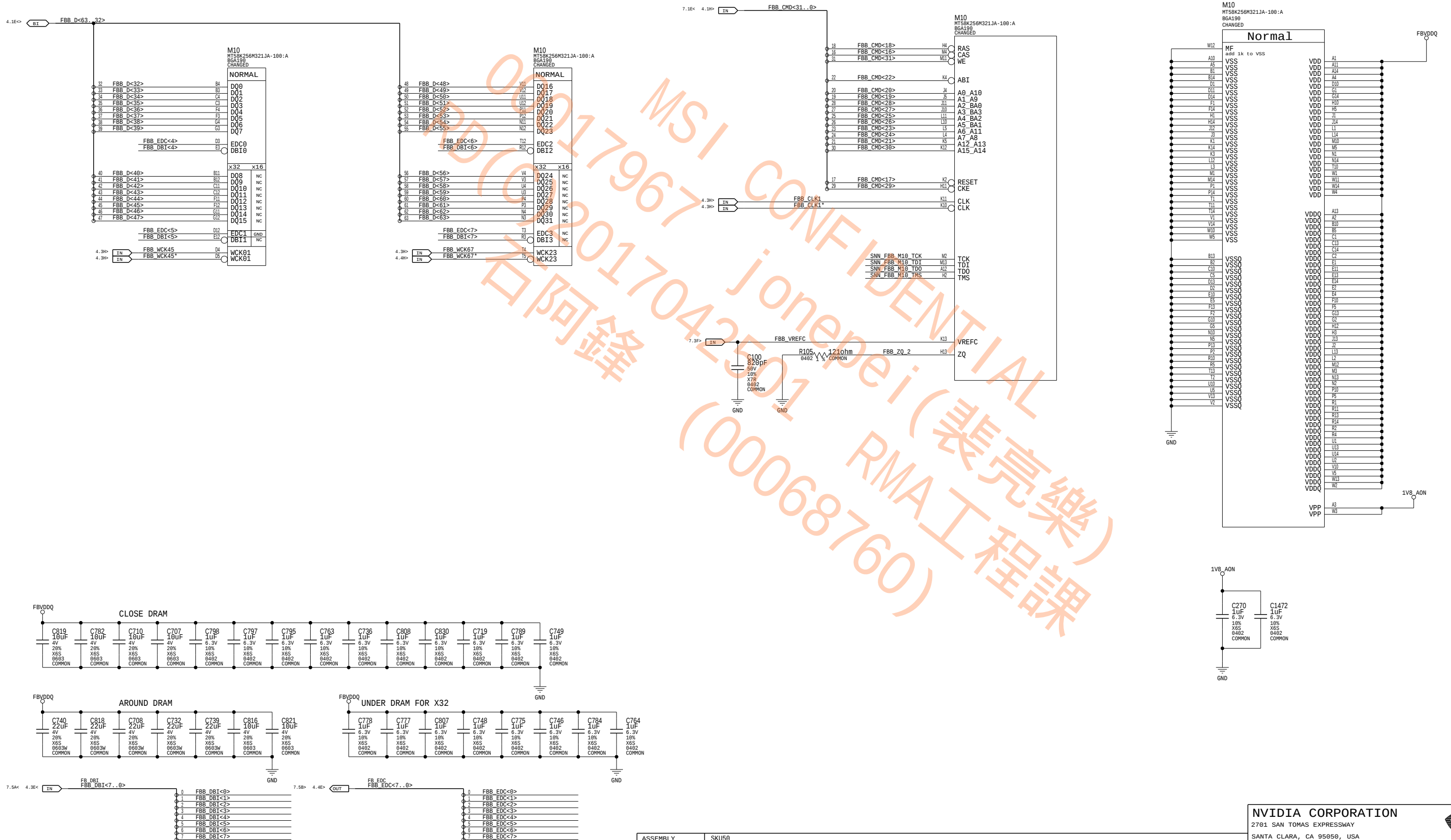


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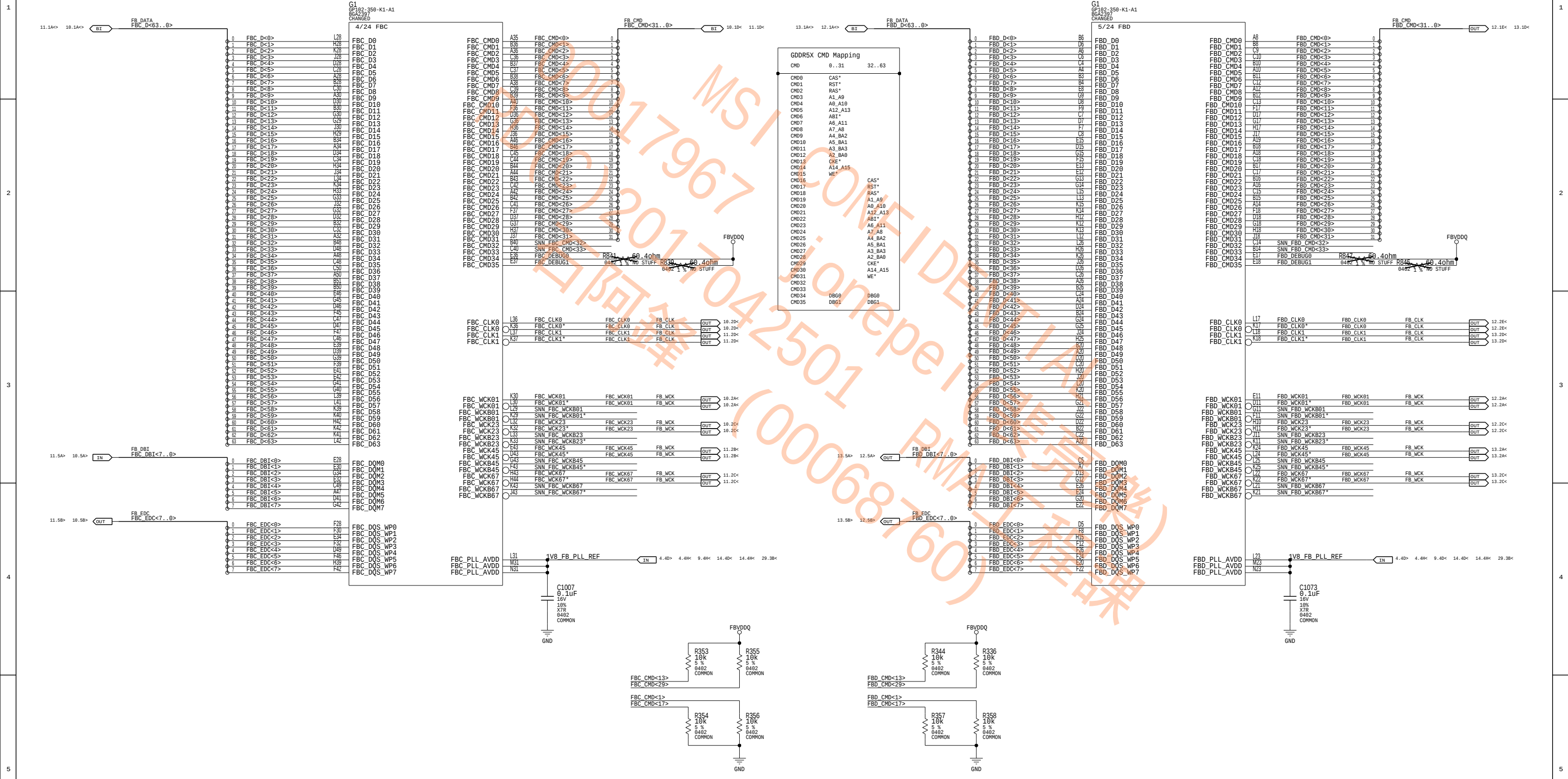


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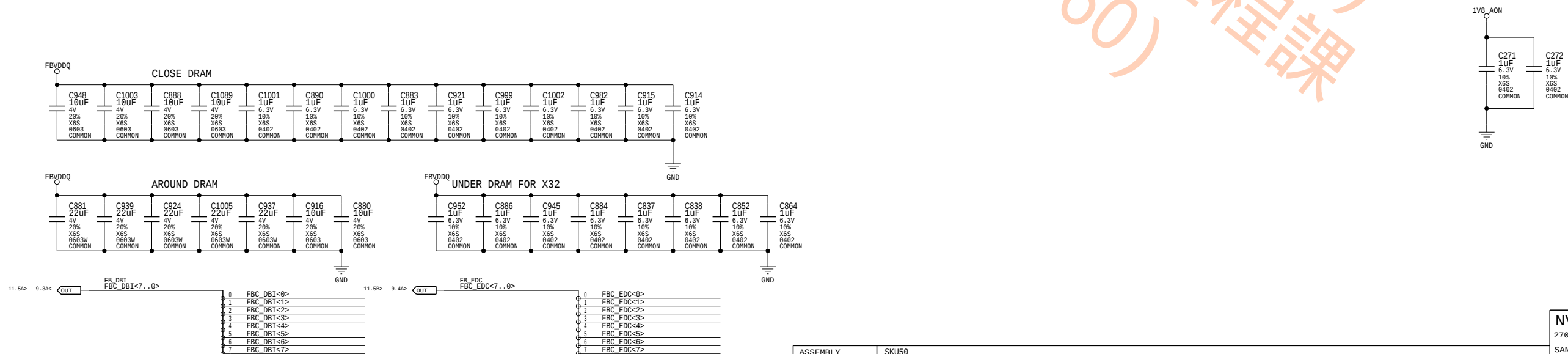
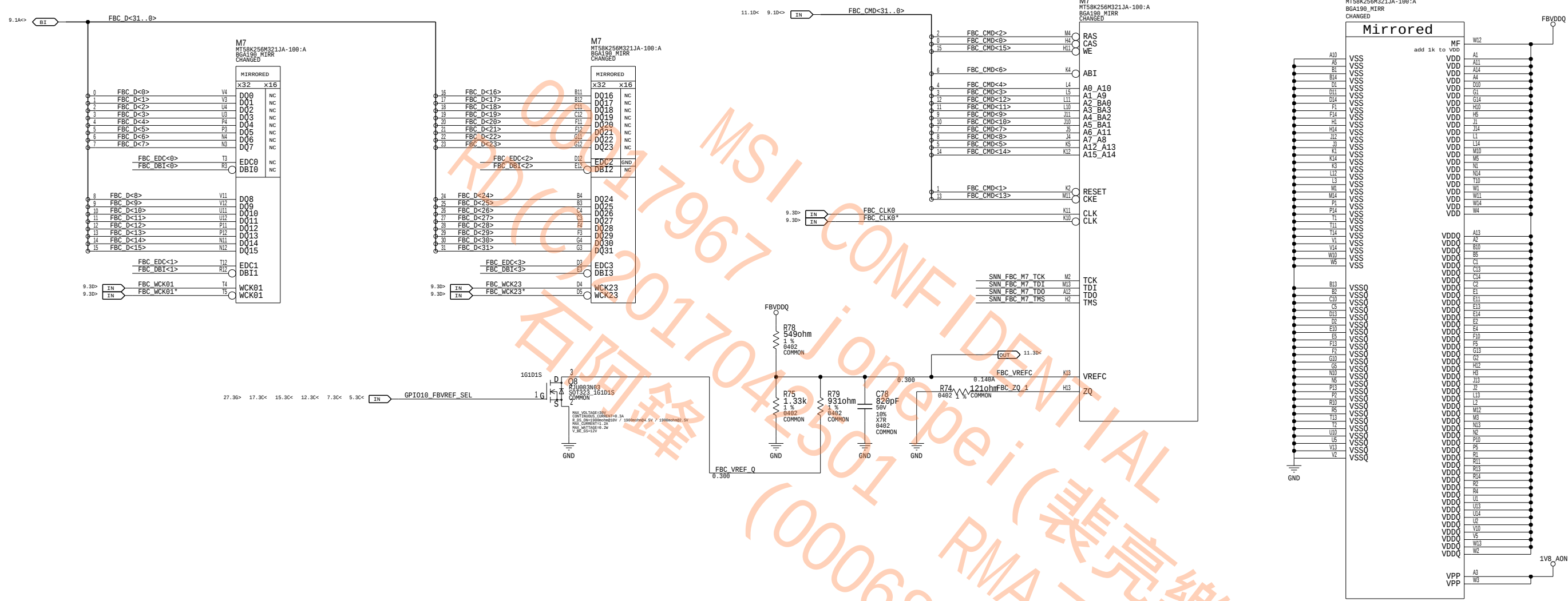


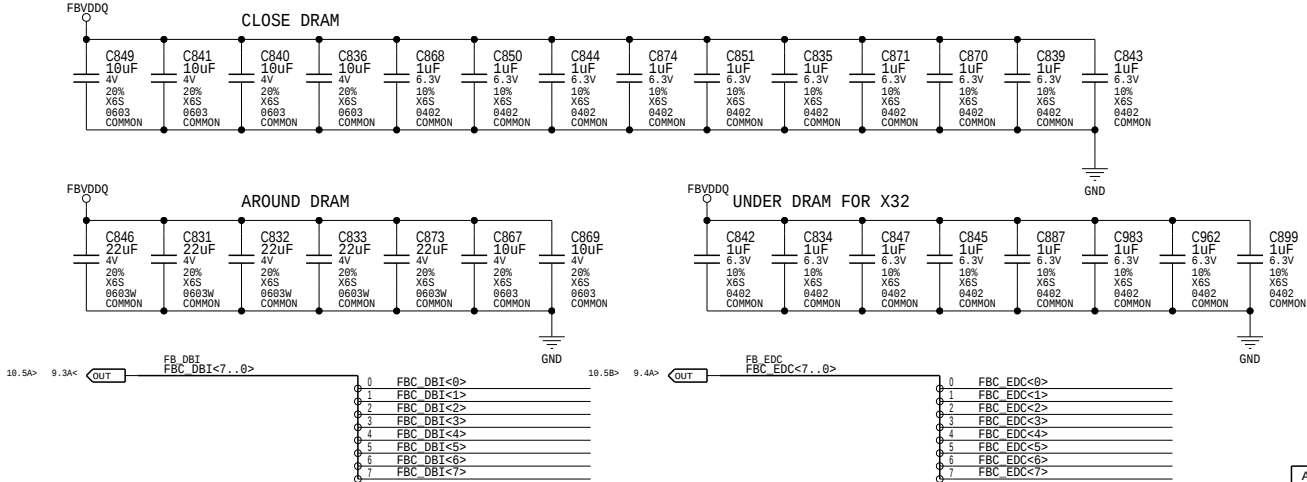
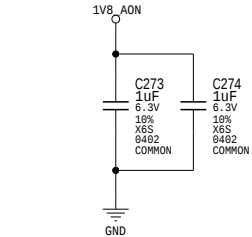
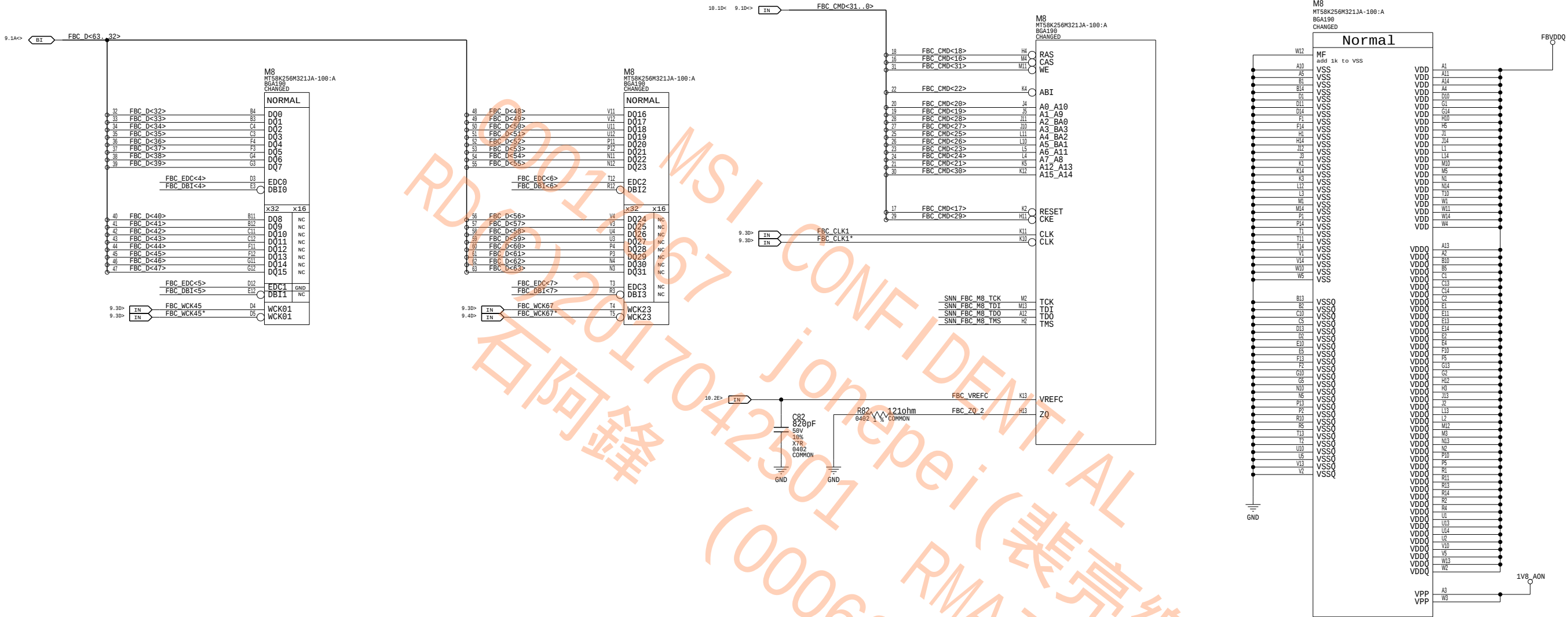
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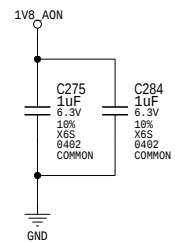
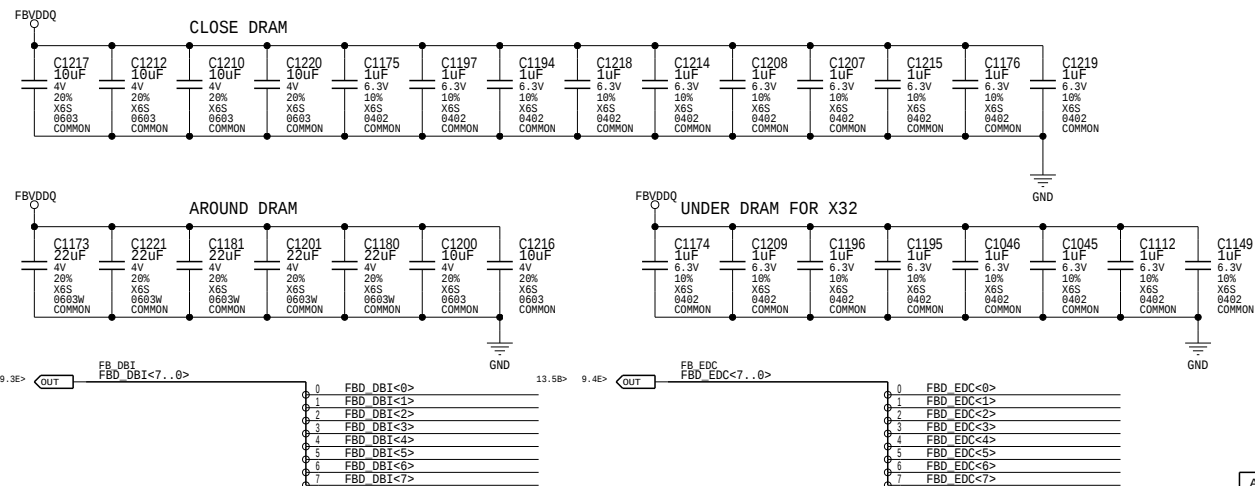
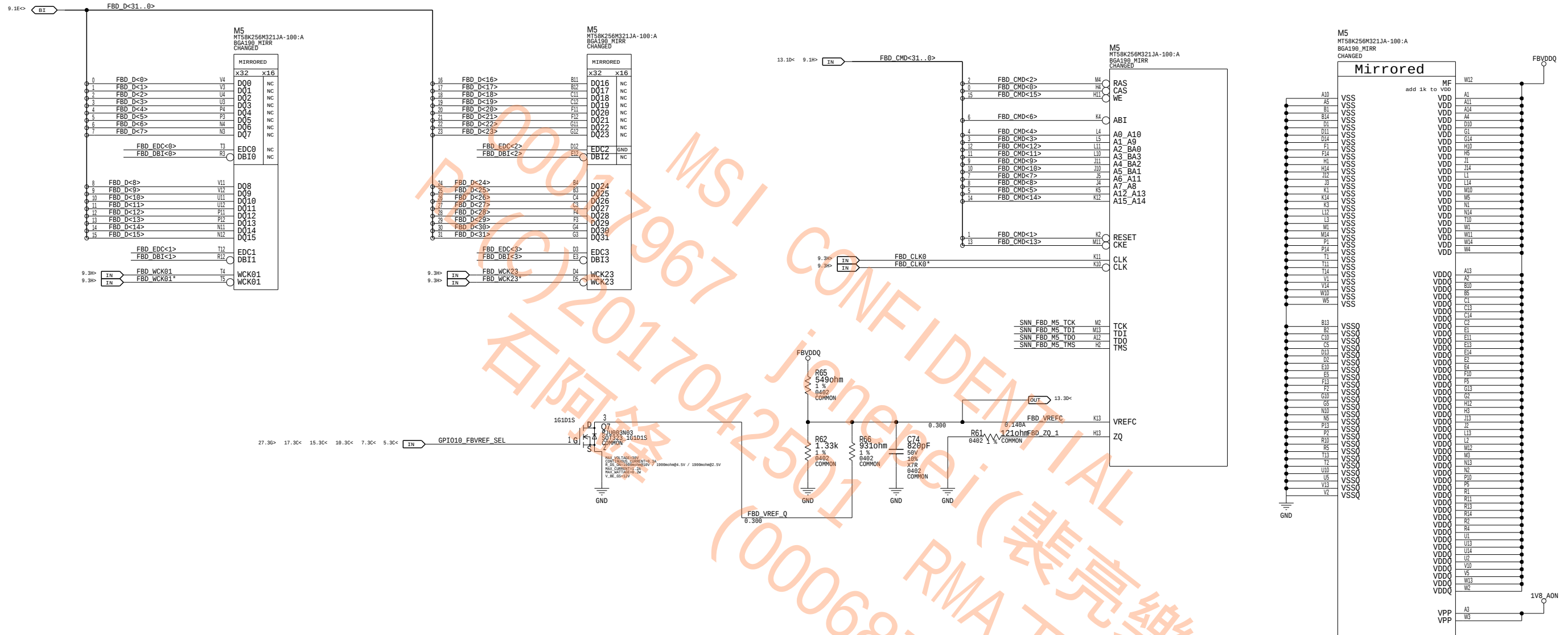
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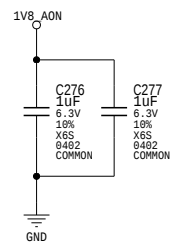
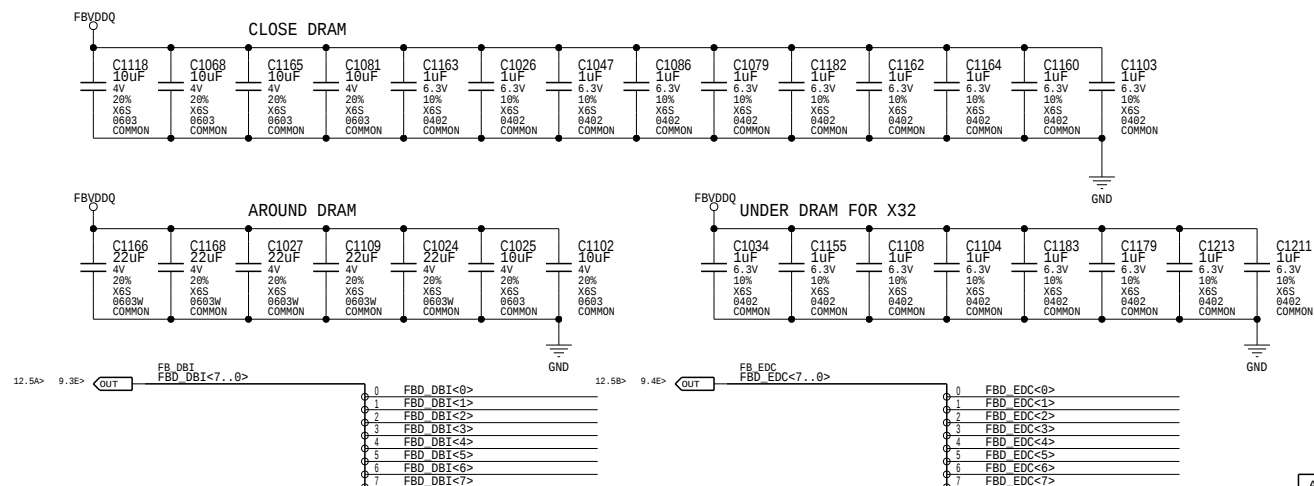
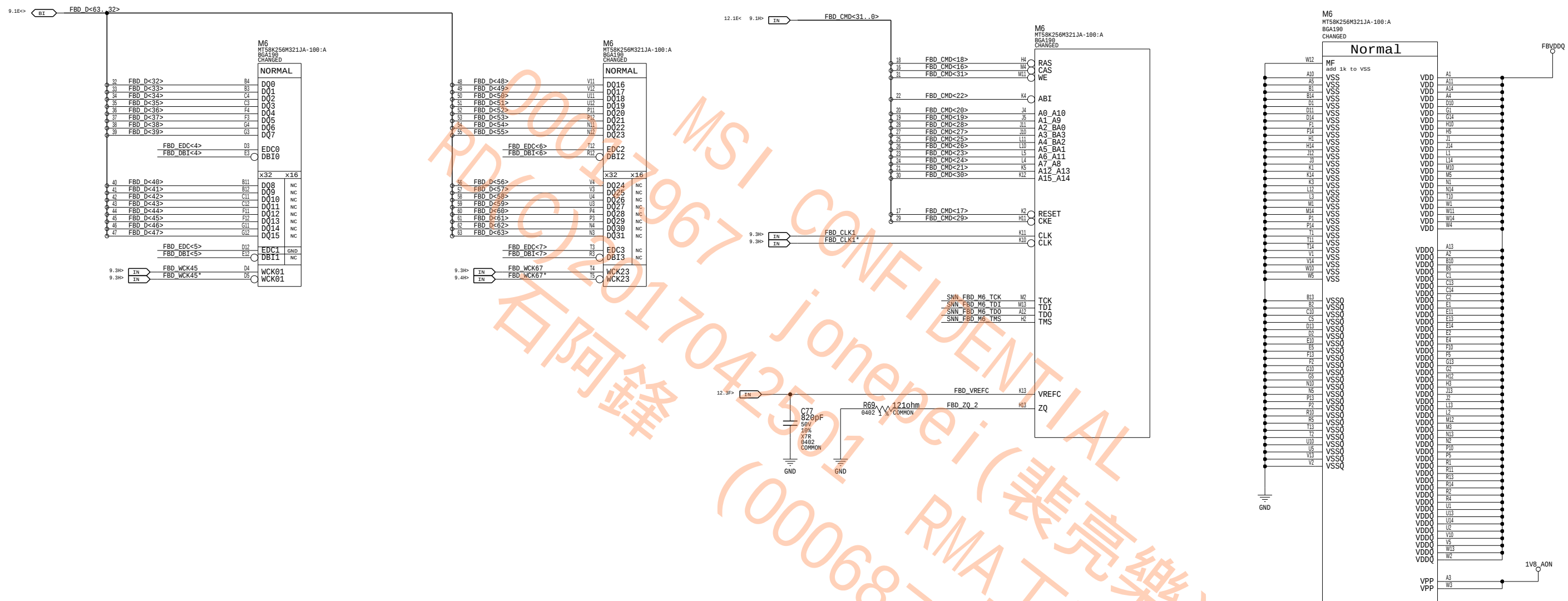
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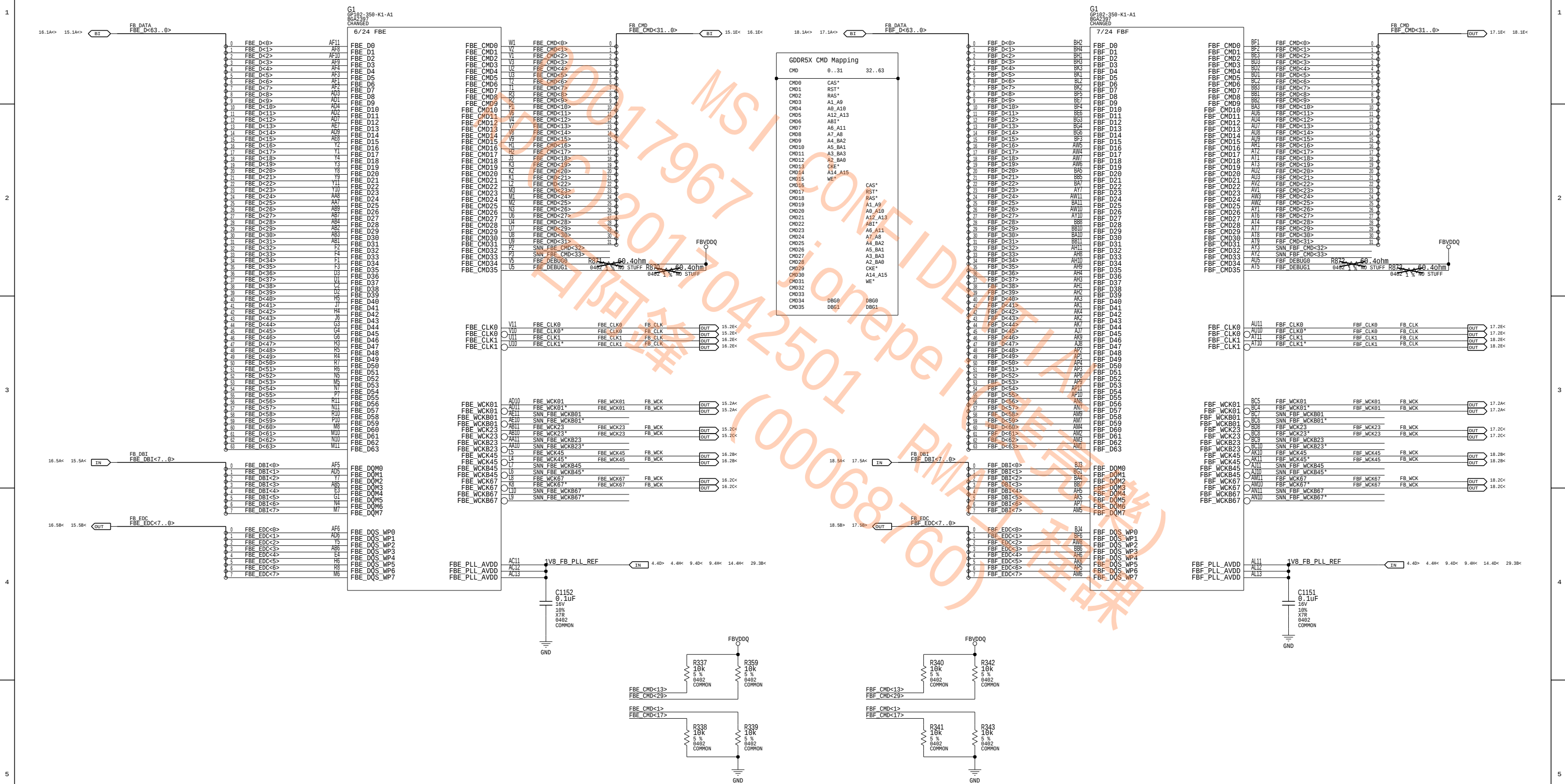
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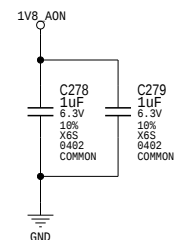
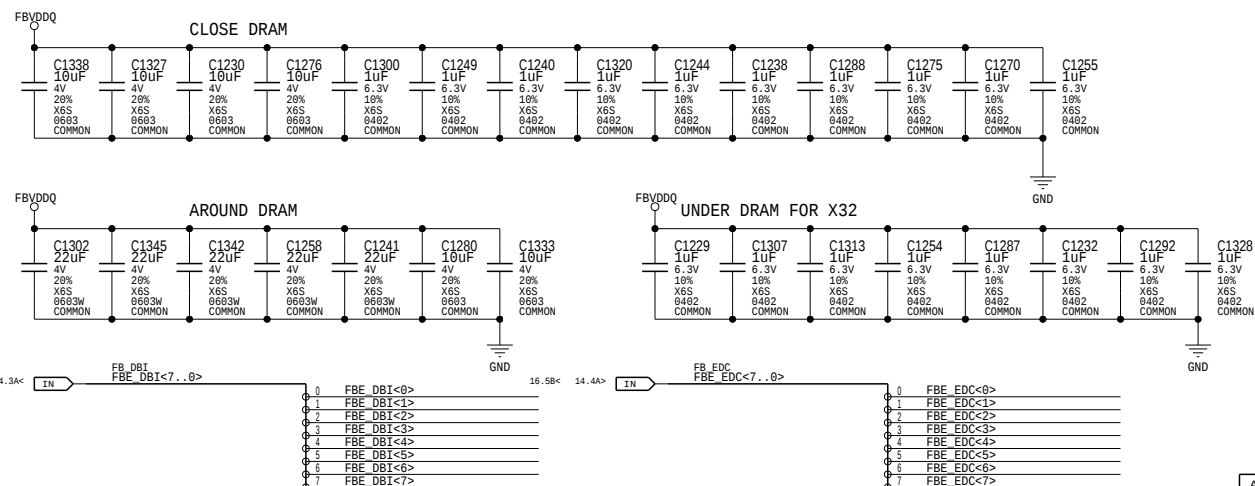
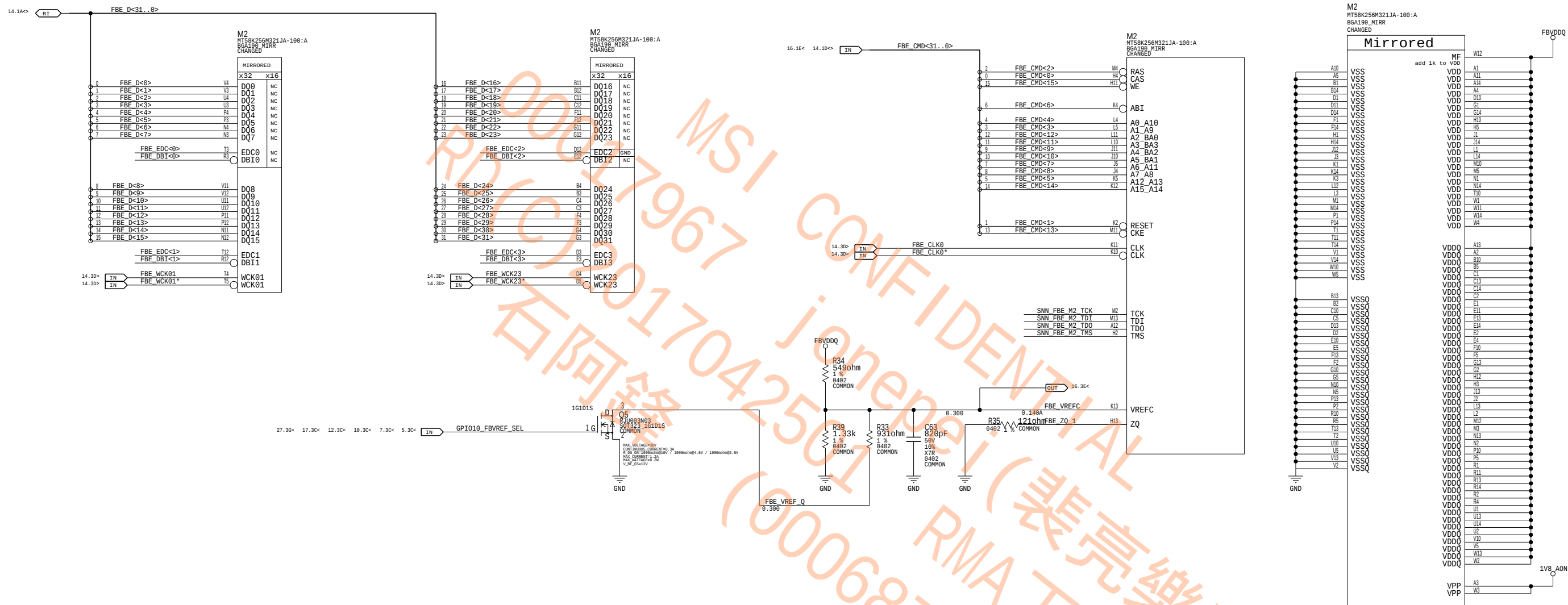


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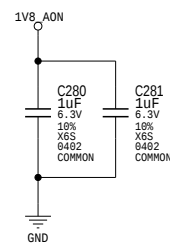
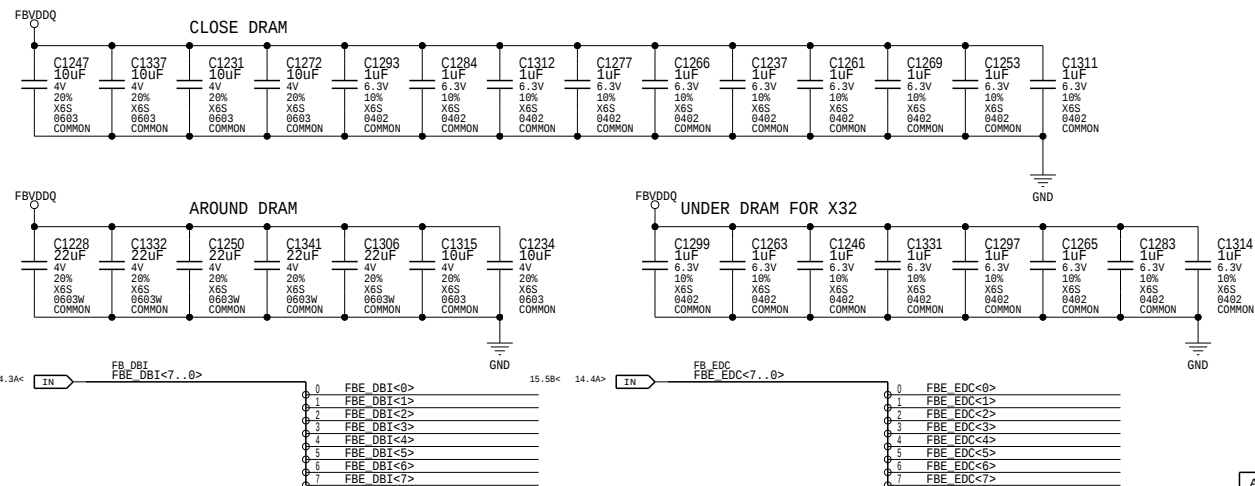
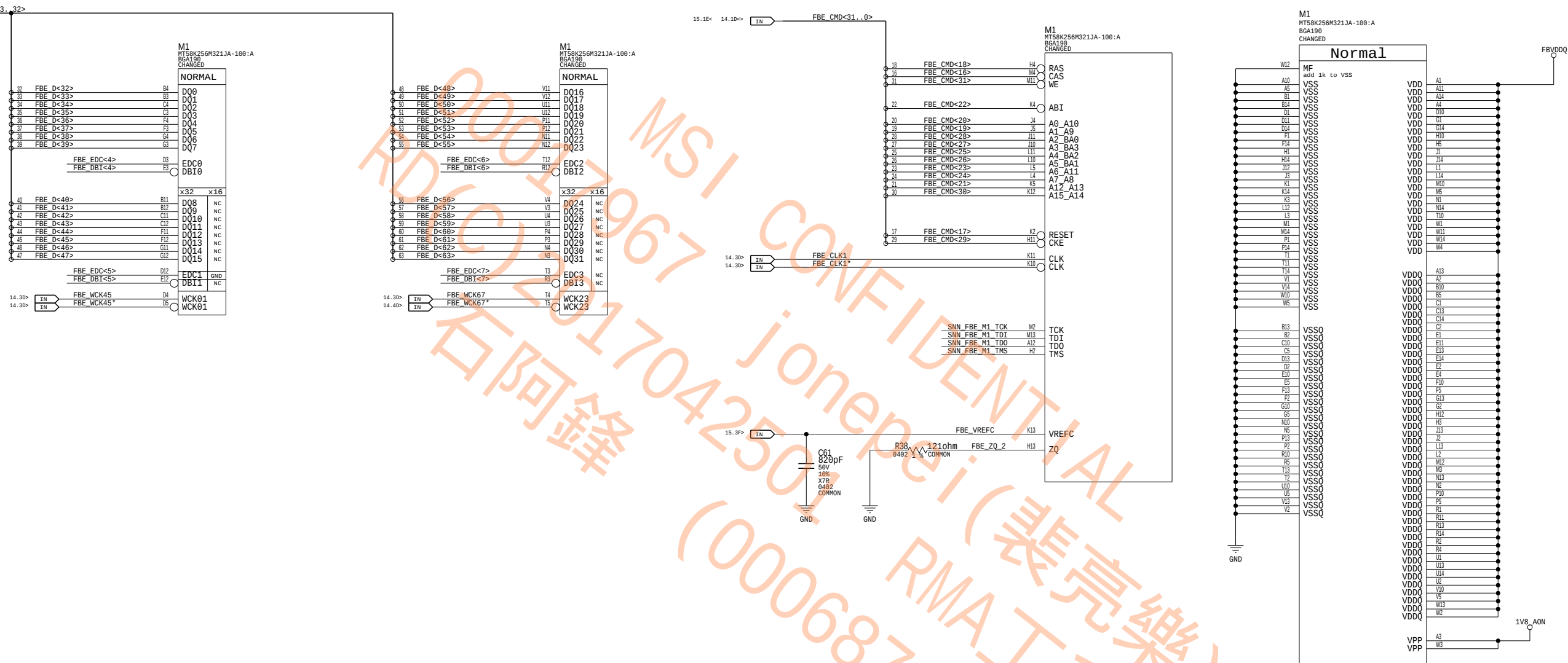
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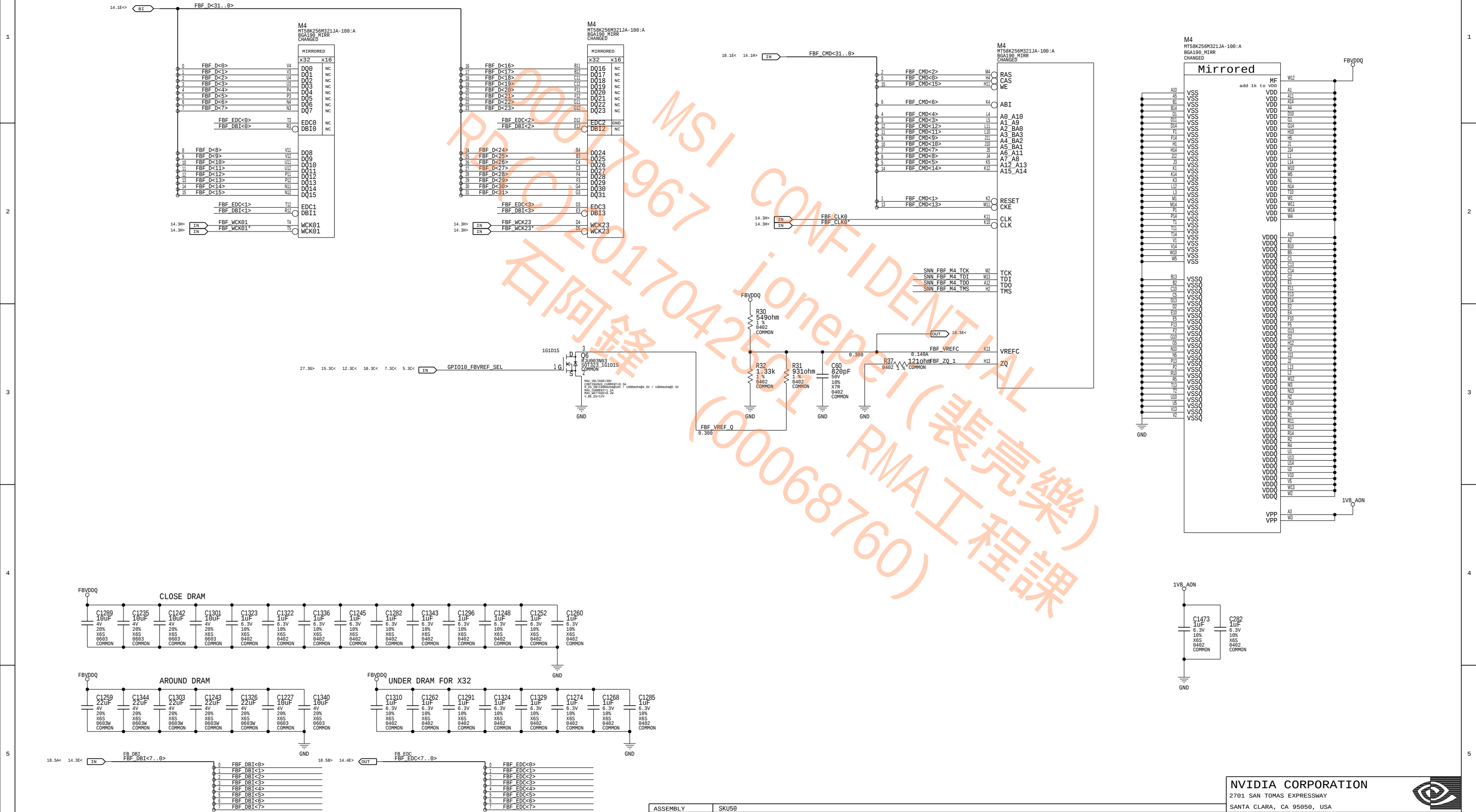
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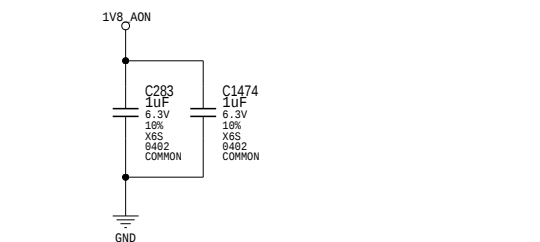
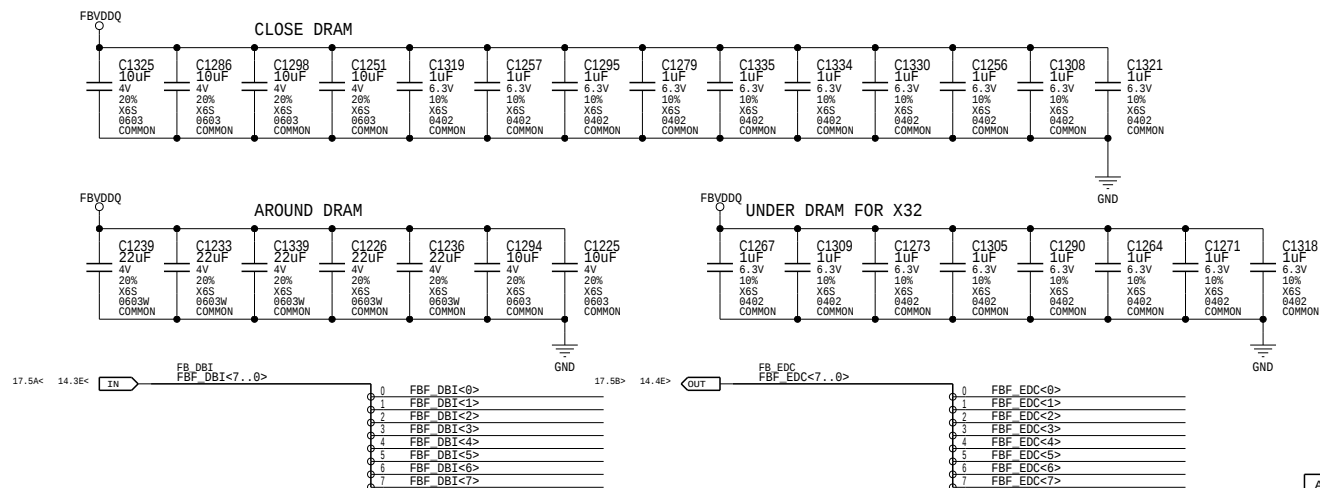
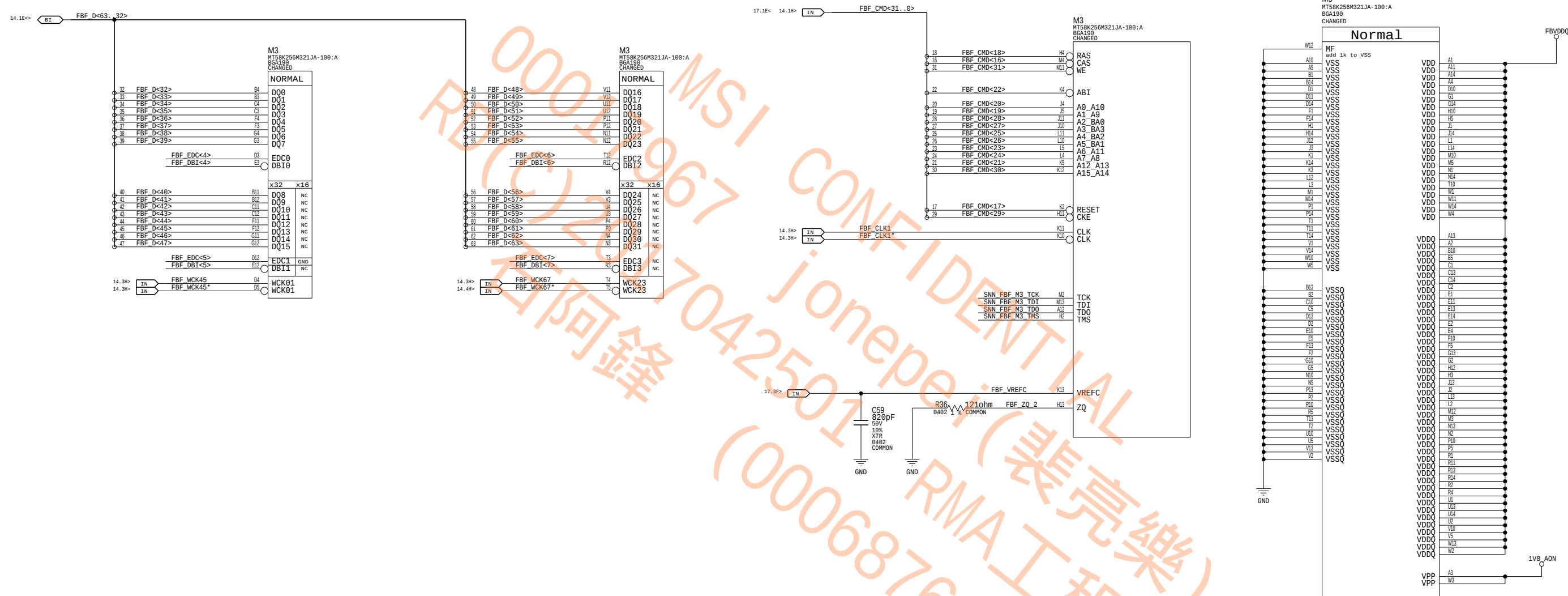
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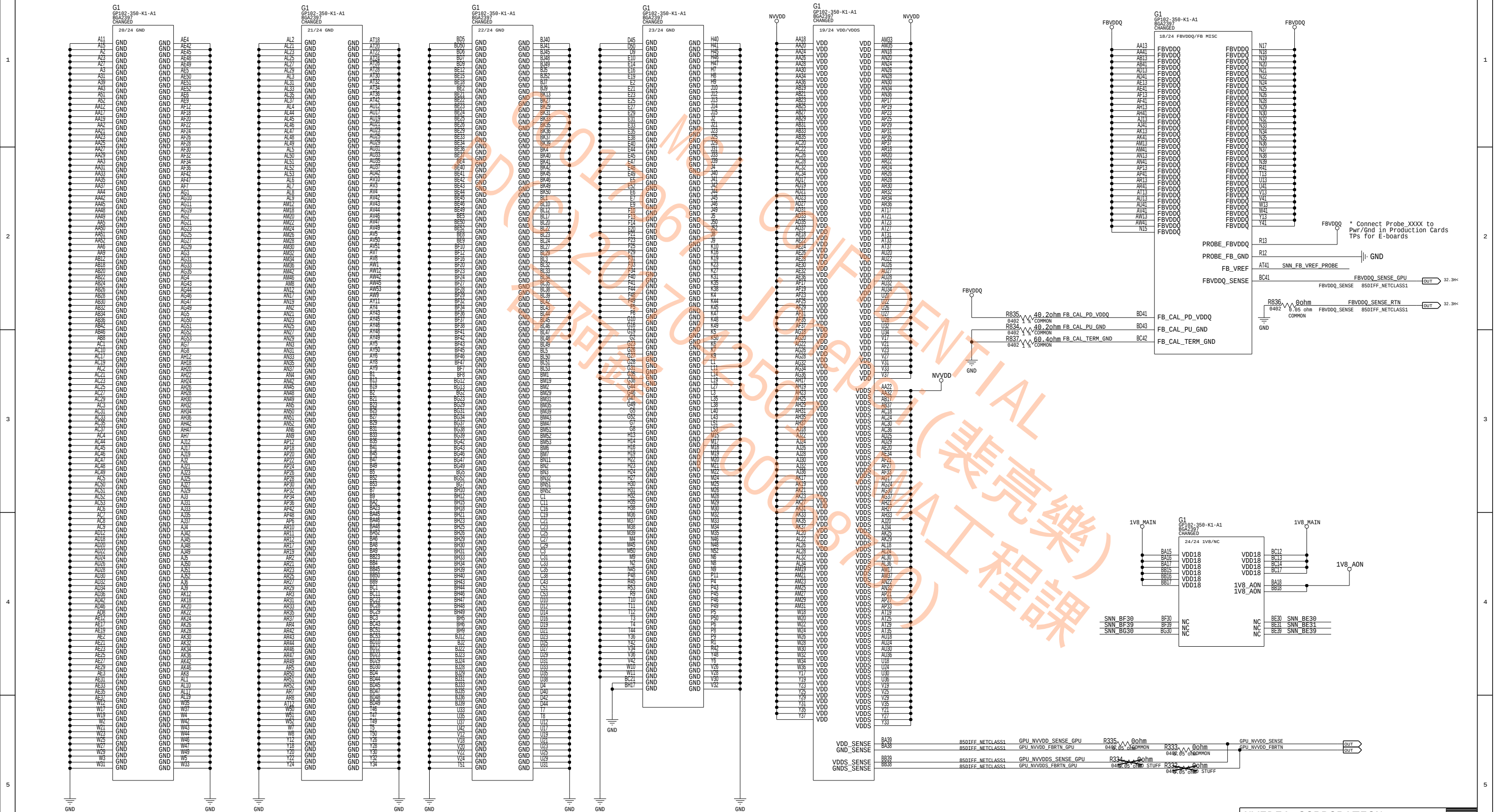
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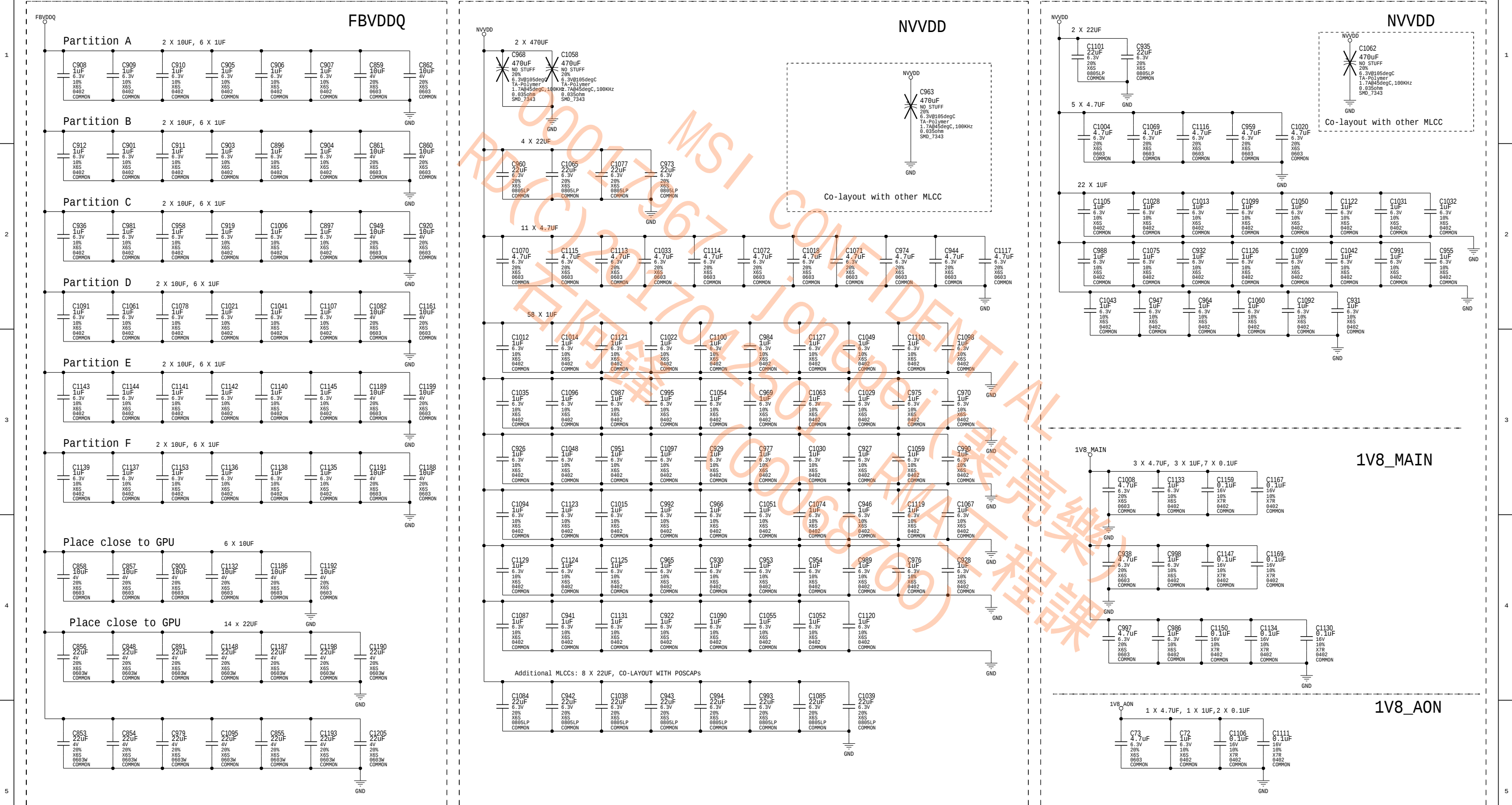


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Page19: GPU PWR and GND





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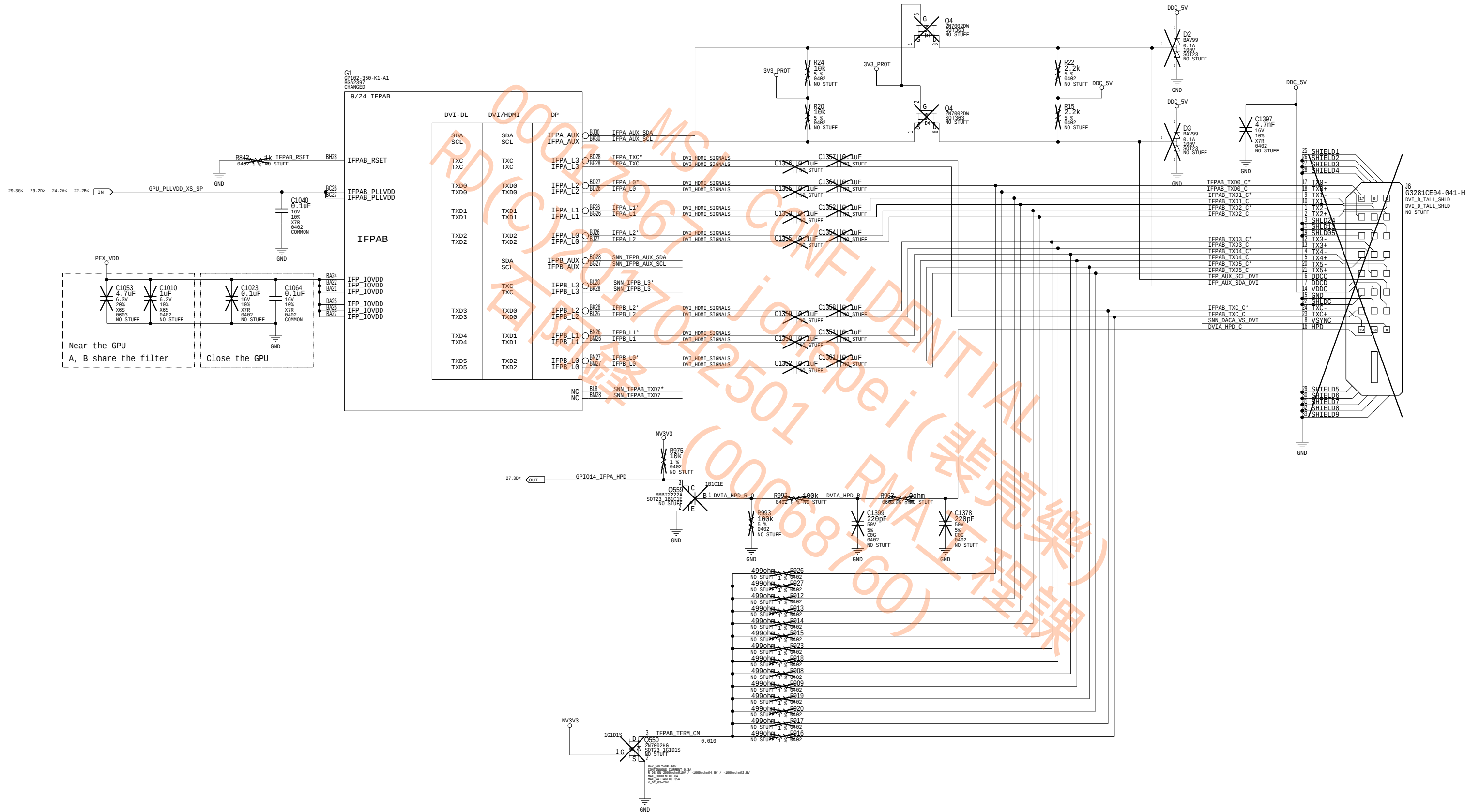
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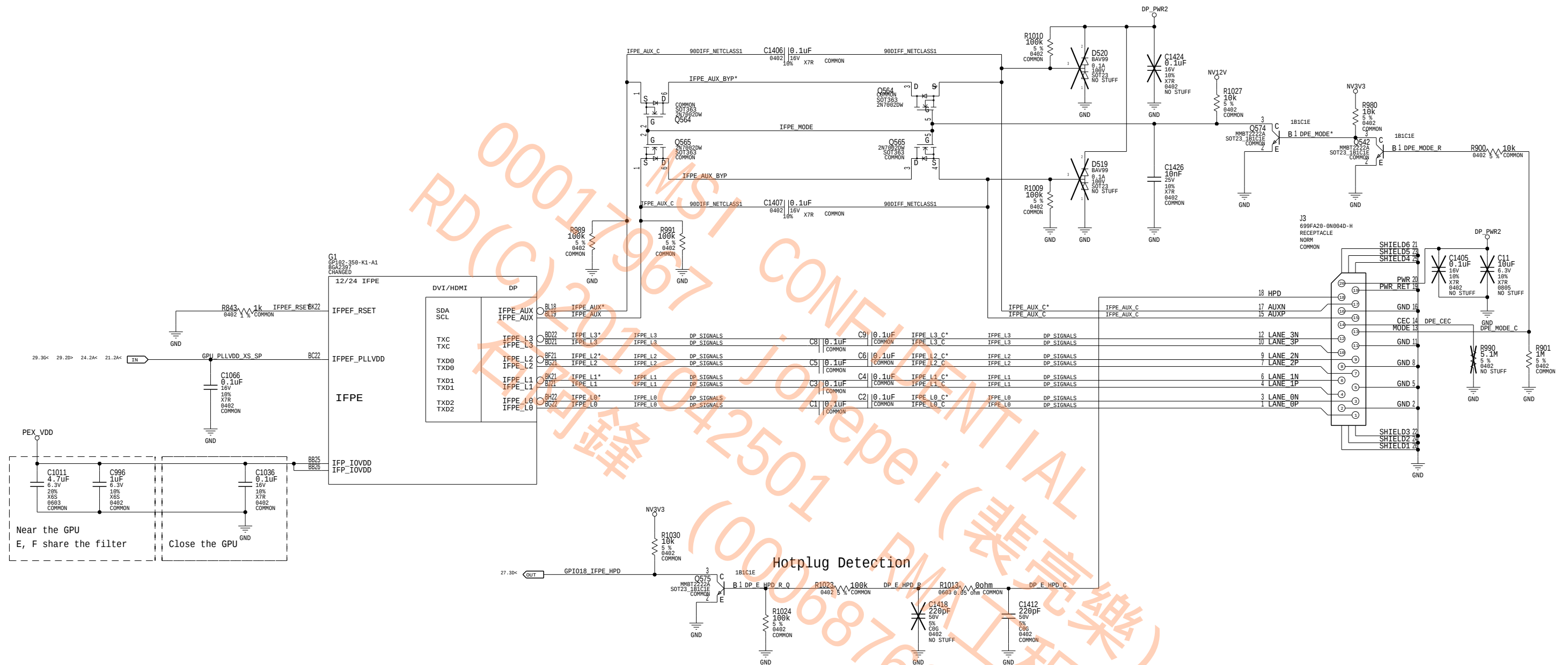
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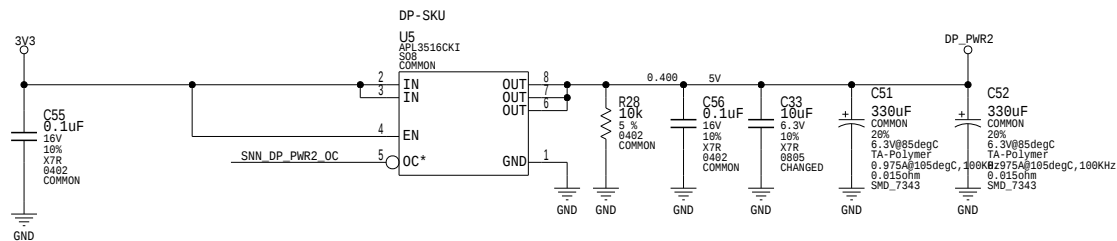
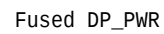
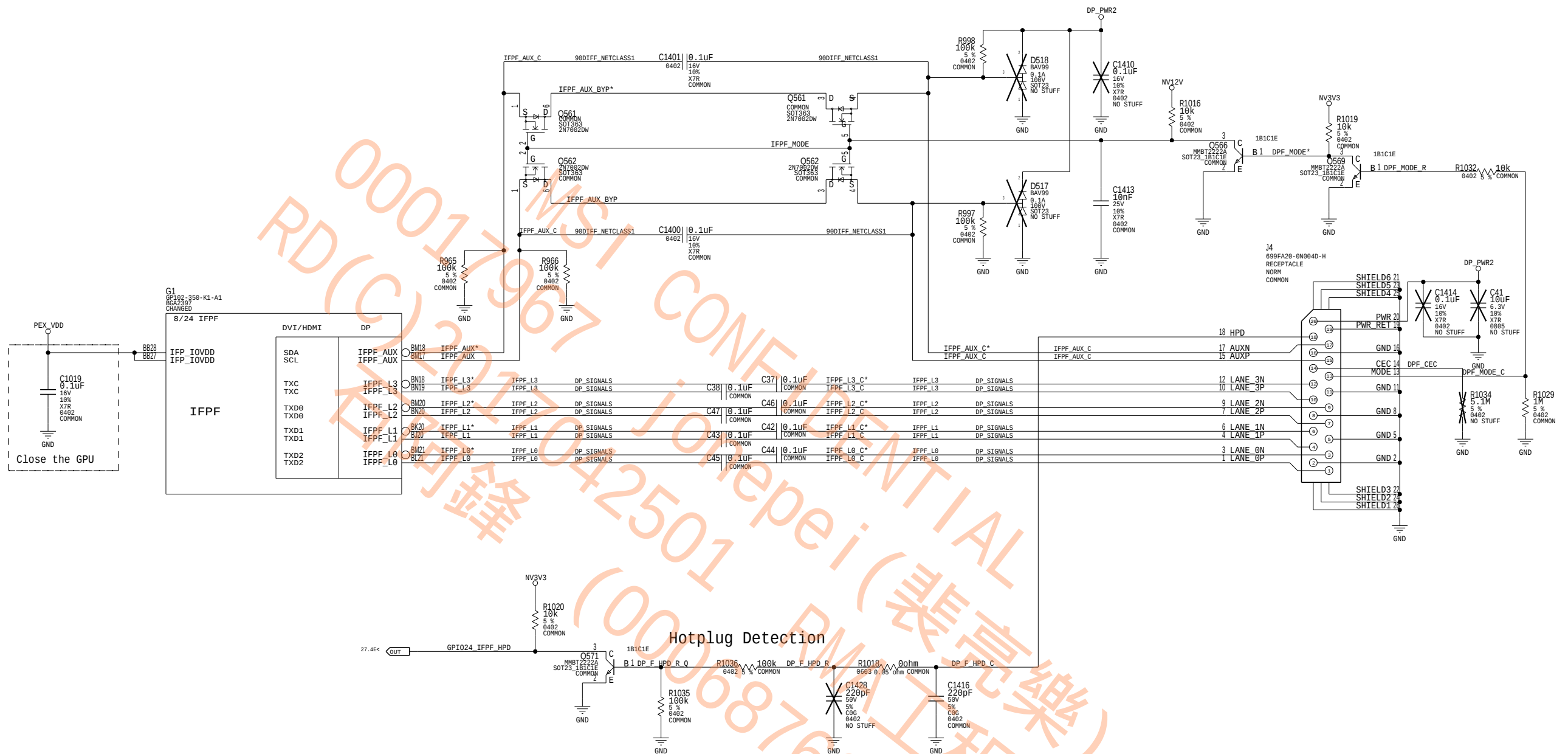
DATE 15-FEB-2017



ASSEMBLY	SKU50
PAGE DETAIL	IFPAB DVI-D-DL

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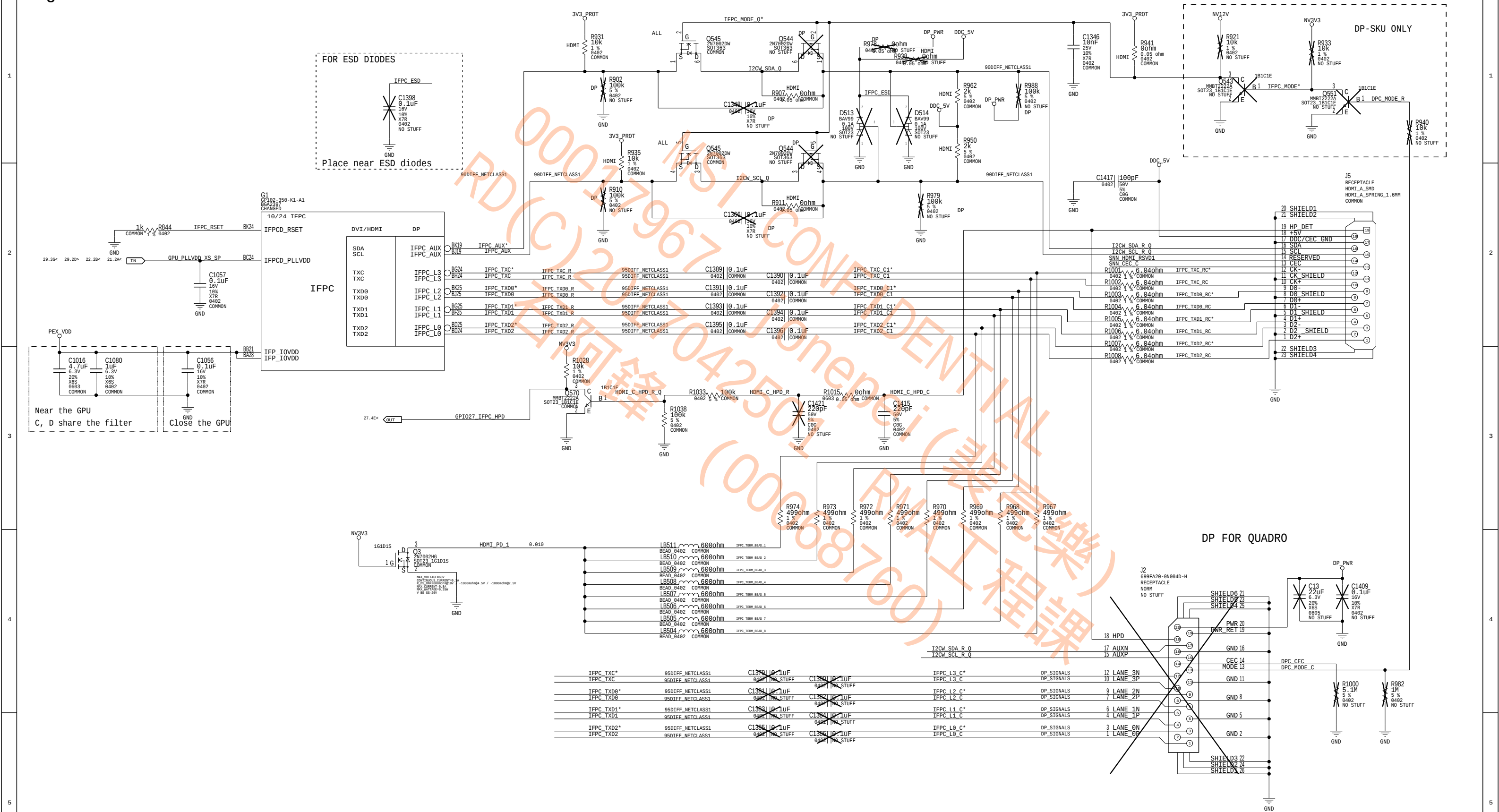


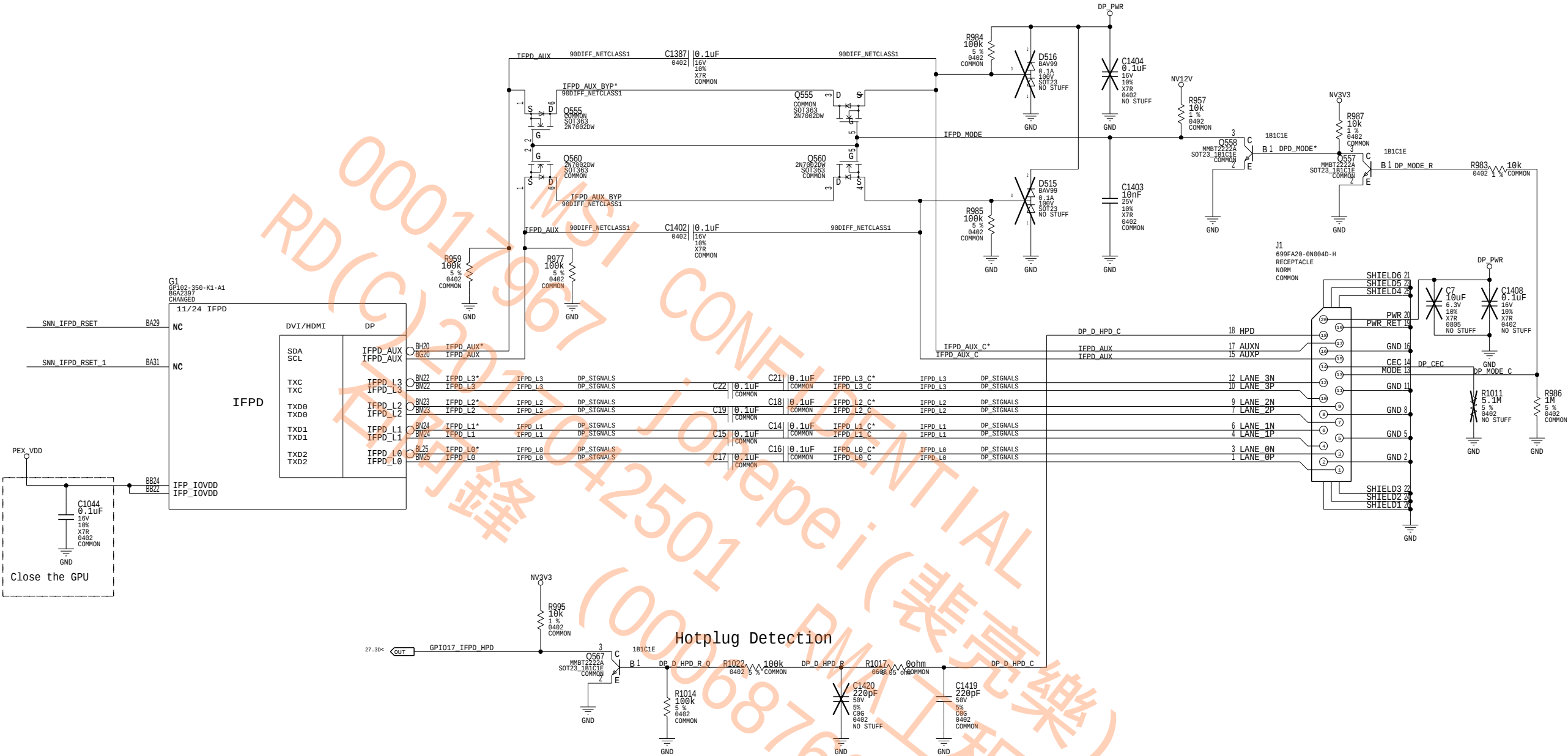


ASSEMBLY	SKU50
PAGE DETAIL	IFPF DP

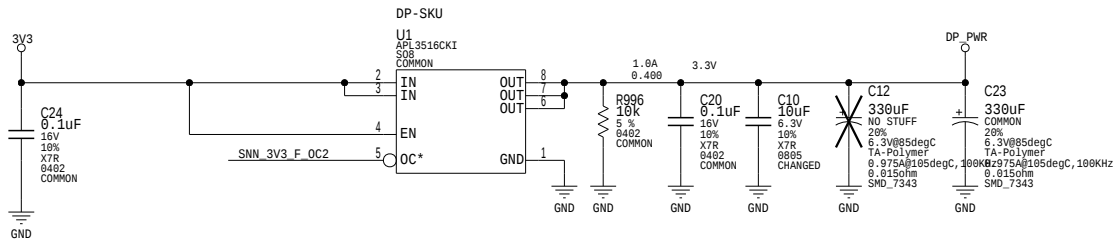
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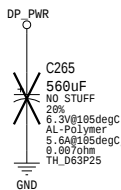




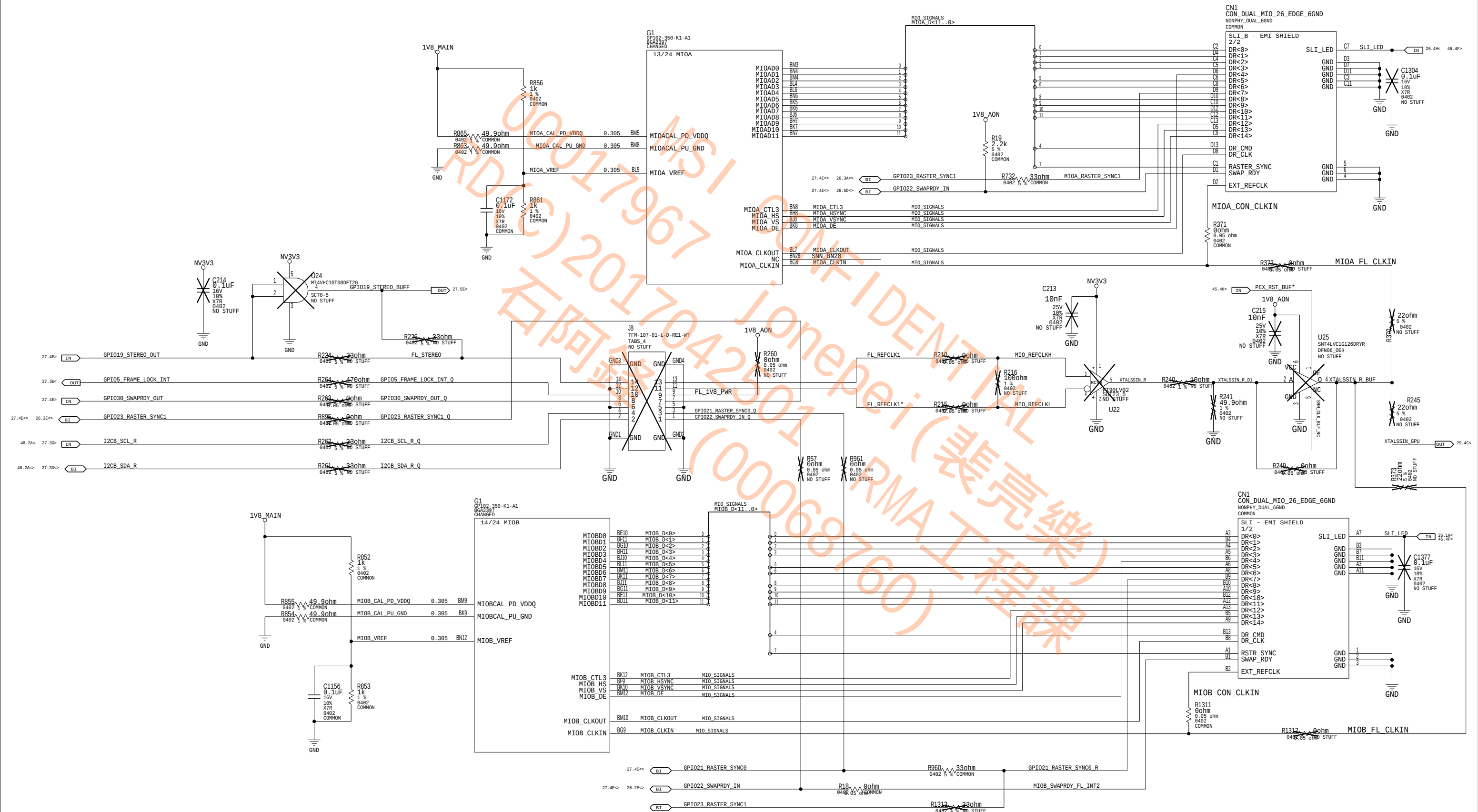
Fused DP_Pwr

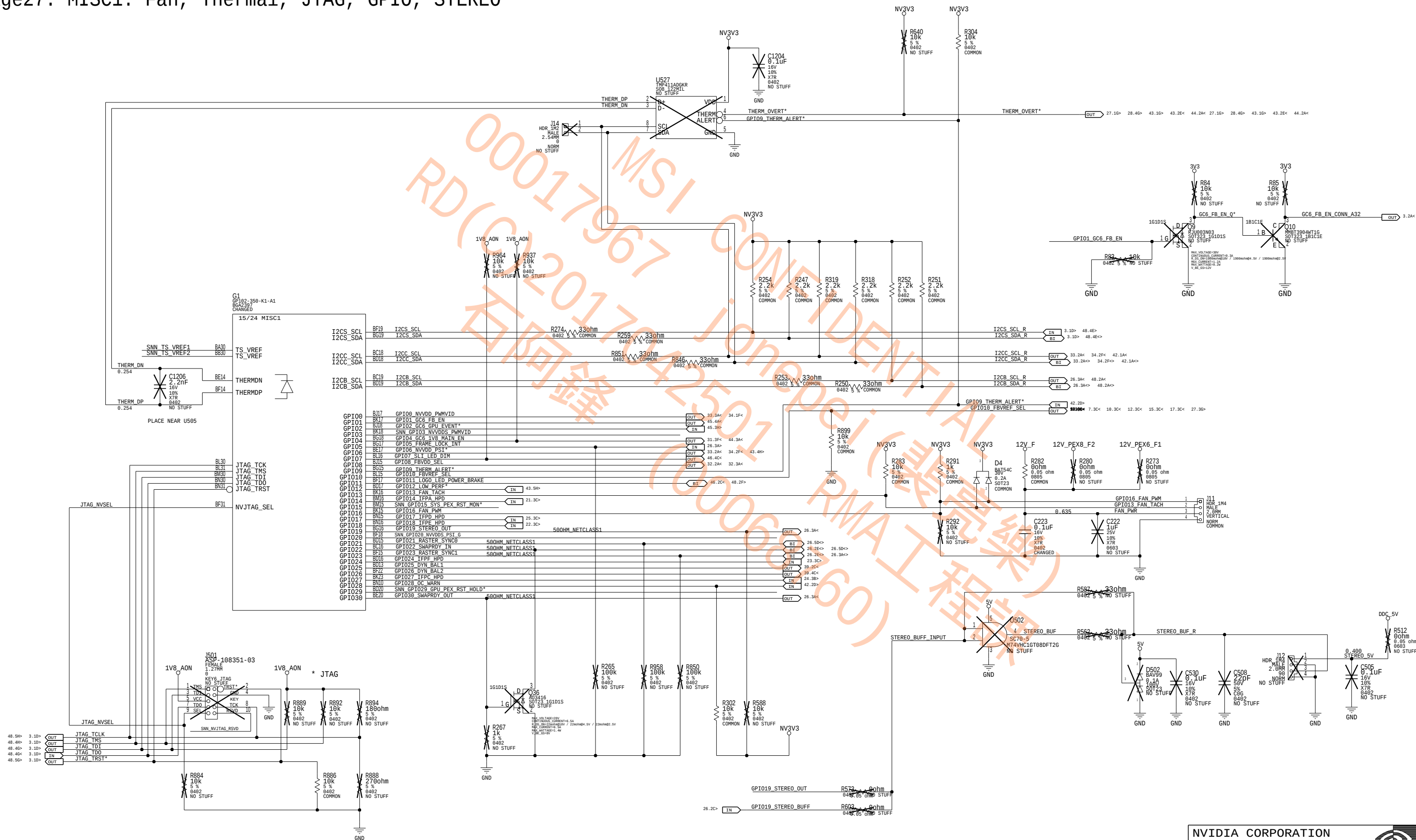


OPTION



Page26: MIOA/B Interface and FRAME LOCK





ASSEMBLY	SKU50
PAGE DETAIL	MISC: FAN, THERMAL, JTAG, GPIO, STEREO

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MSI COMEPEI (00068) BMA (亮亮工程)

1:ENABLE 0:DISABLE
SOR0/1/2/3 ENABLE

DEVID_SEL	PCIE_CFG	VGA_DEVICE
1	1	1
1	1	0
1	0	1
1	0	0
0	1	1
0	1	0
0	0	1
0	0	0
1	1	1
1	1	0
1	0	1

G1
GP102-356-K1-A1
BGA2397
CHANGED

17/24 MISC2

- STRAP0 BG14
- STRAP1 BL14
- STRAP2 BL14
- STRAP3 BK14
- STRAP4 BK14
- STRAP5 BC15

ROM_CS BM13

ROM_SI BN14

ROM_SO BM14

ROM_SCLK BL13

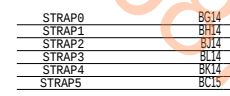
OVERT BJ16

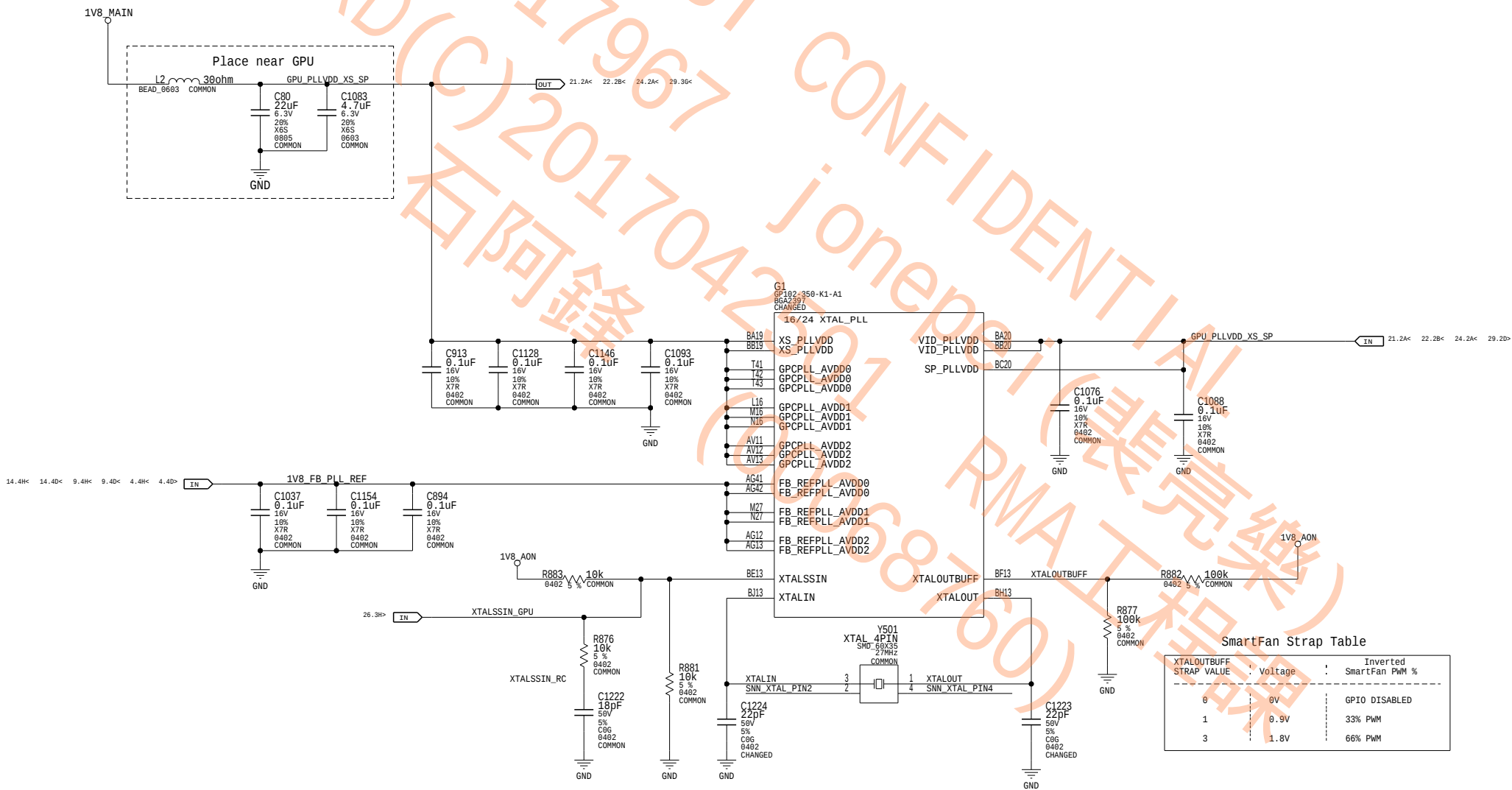
BUFRST BD14

THERM_OVER

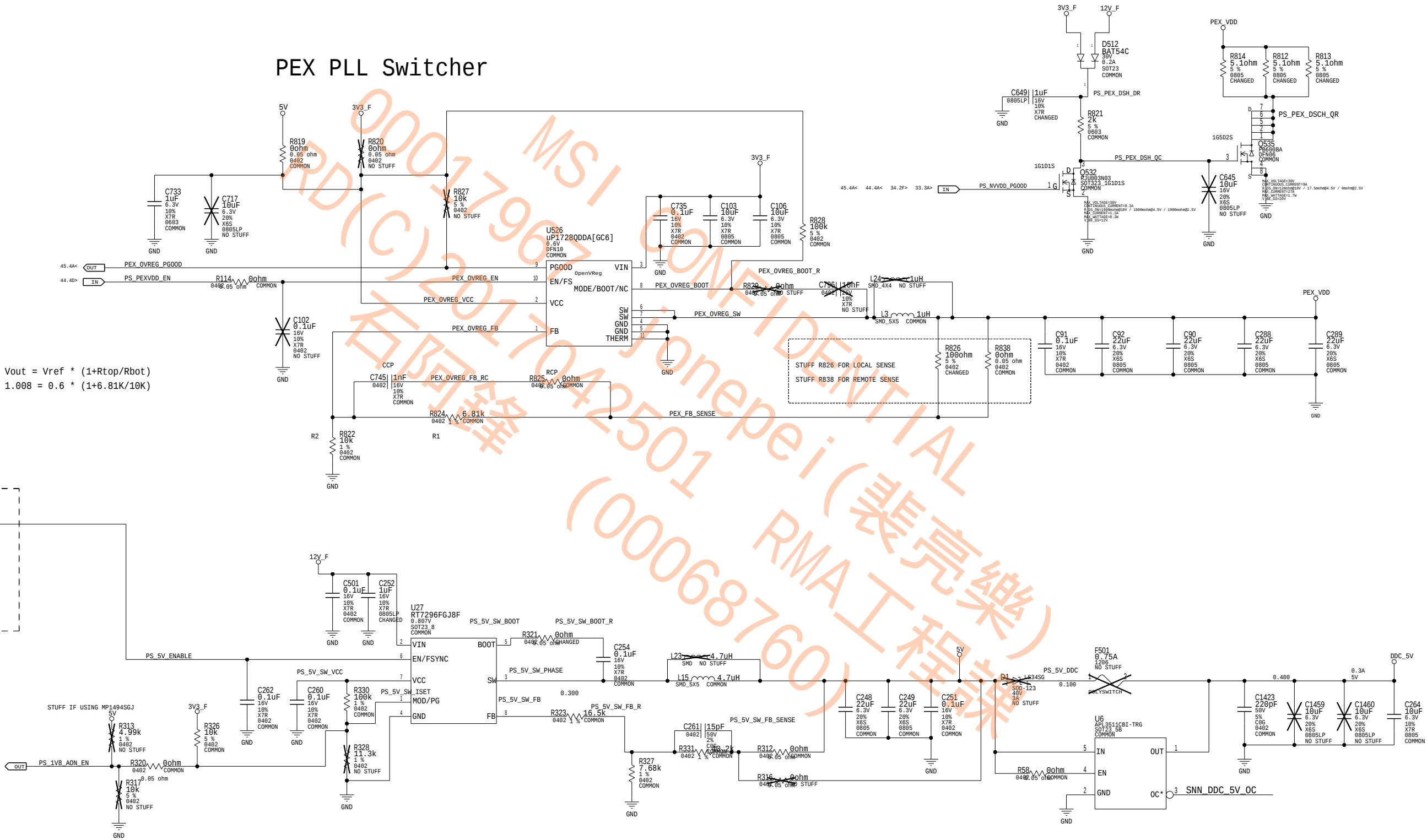
SNN_GPU_E

```
1:VGA_DEVICE  ENABLE
0:VGA_DEVICE  DISABLE
```





PEX PLL Switcher



ASSEMBLY	SKU50
PAGE DETAIL	PS: 5V, PEXVDD

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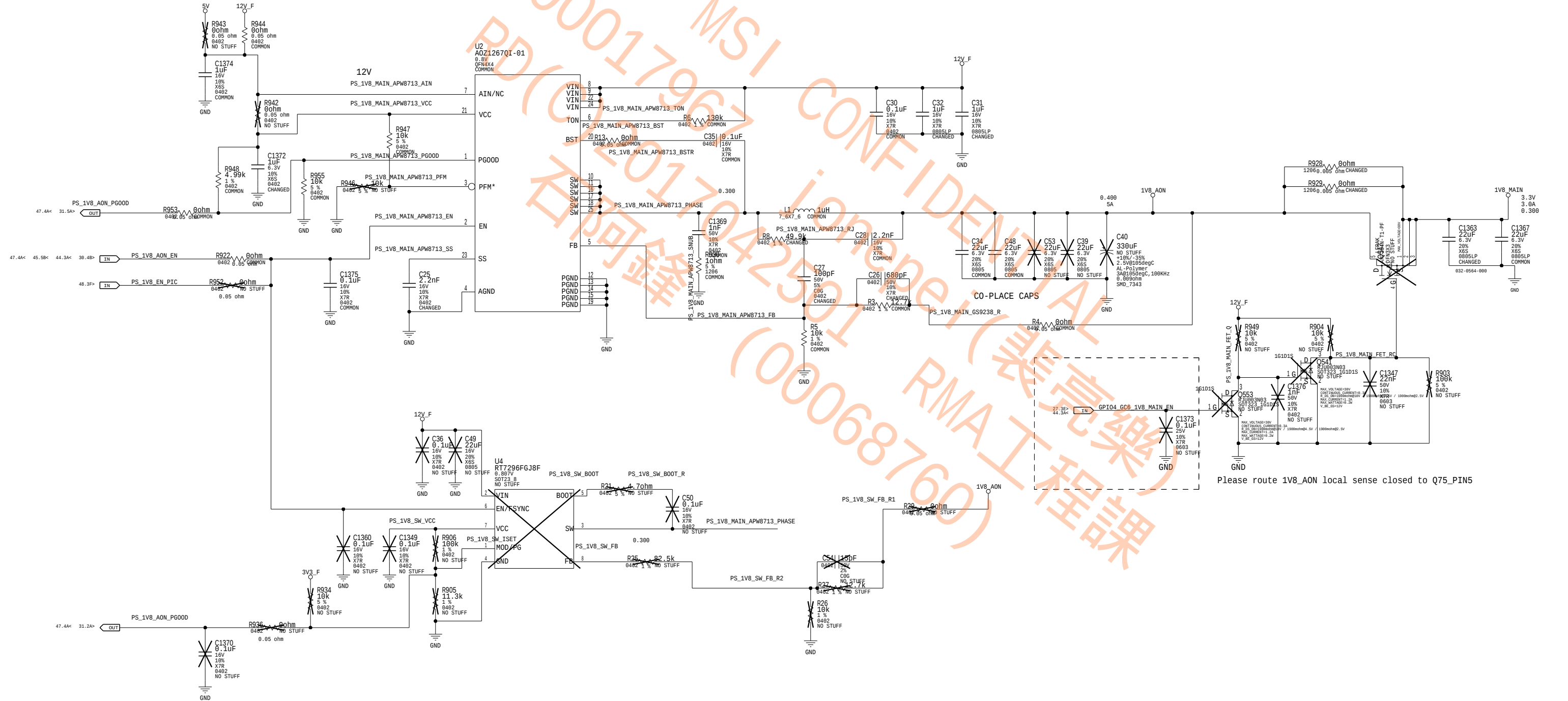
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NV_PN	600-1G611-0050-QS1
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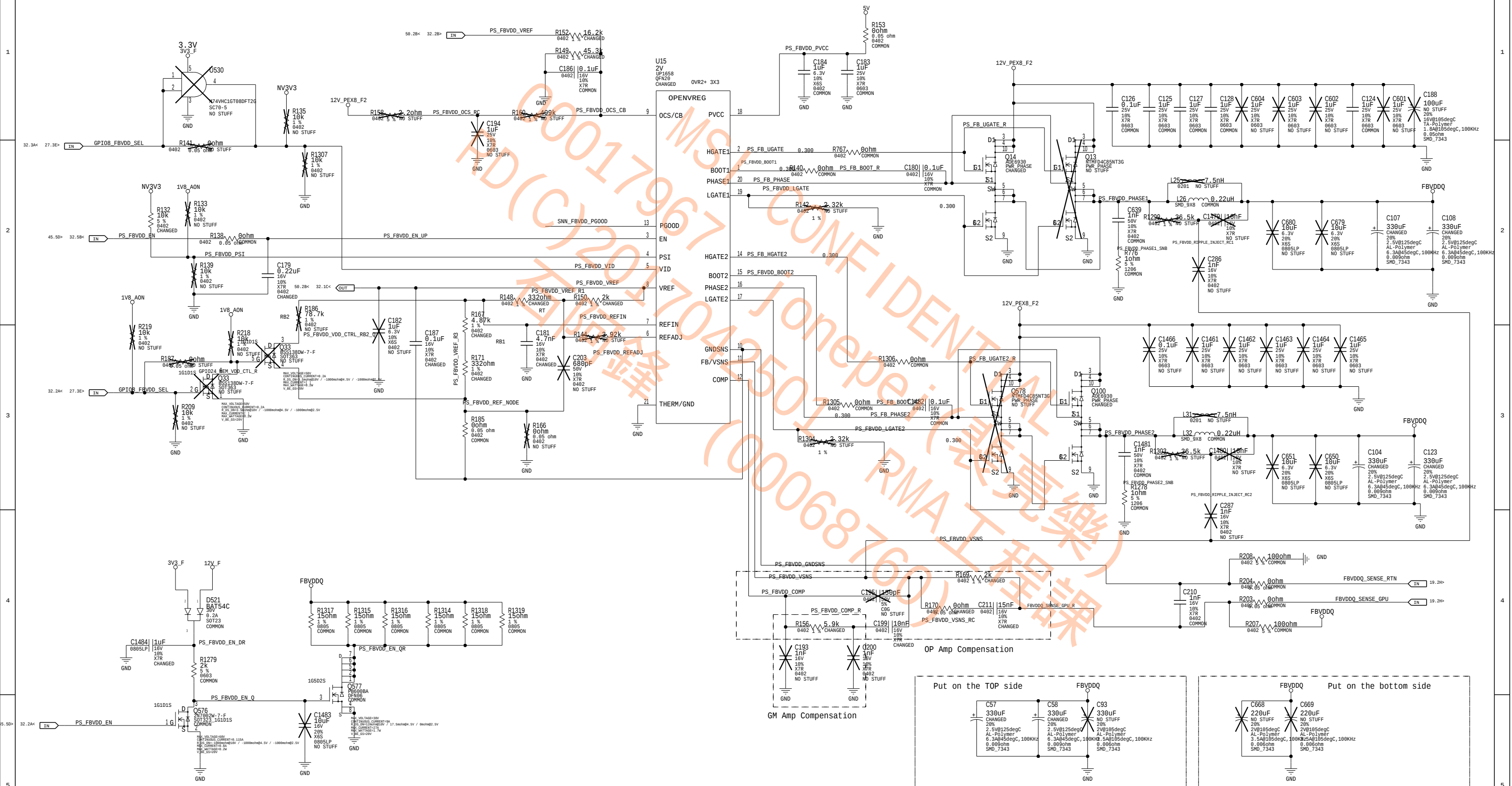




1.8 = 0.8 * (1+18.7K/15K)
Vout = Vref * (1+Rtop/Rbot)

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ASSEMBLY	SKU50
PAGE DETAIL	PS: FBVDDQ

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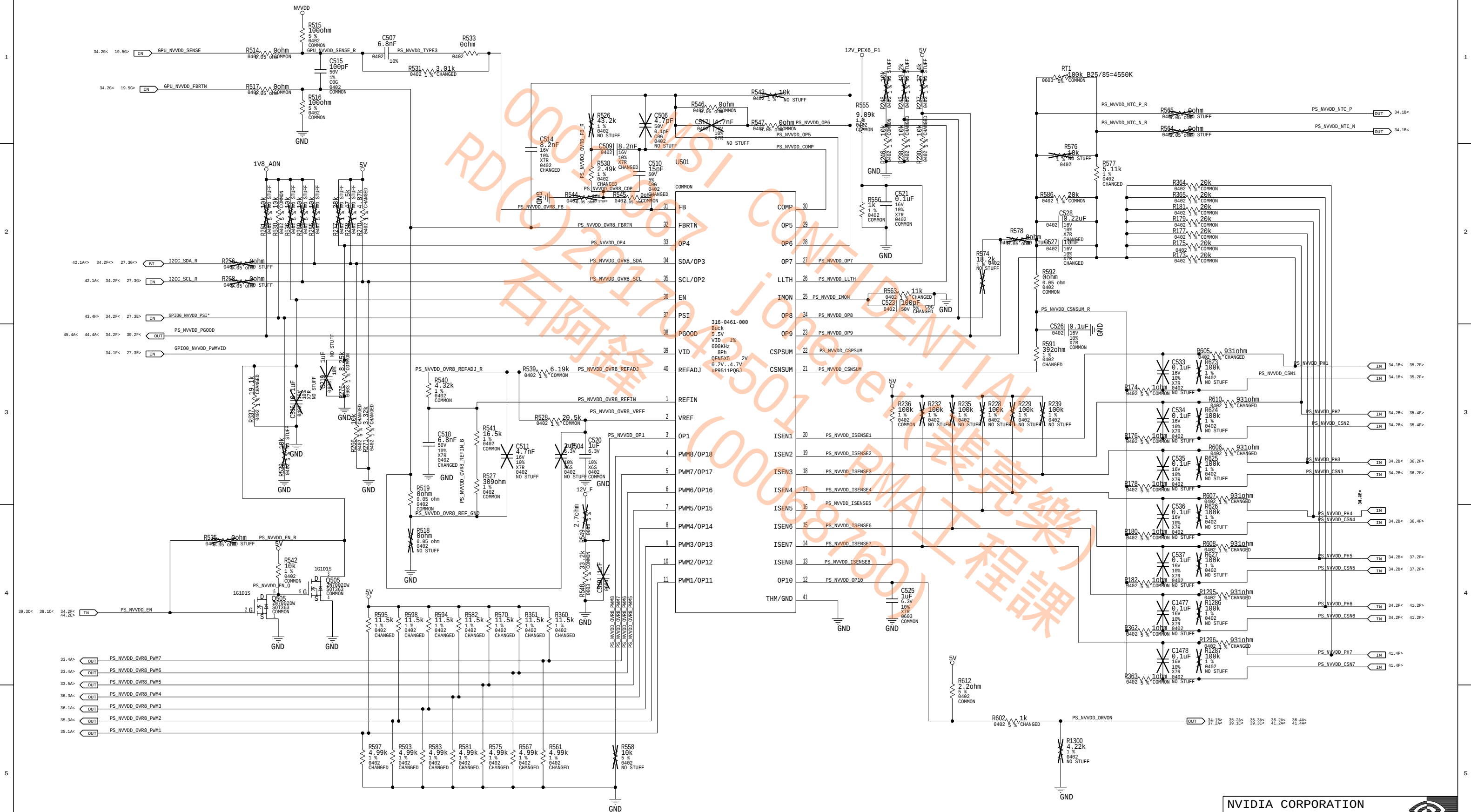
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Page33: PS: NVVDD Controller_OVR8



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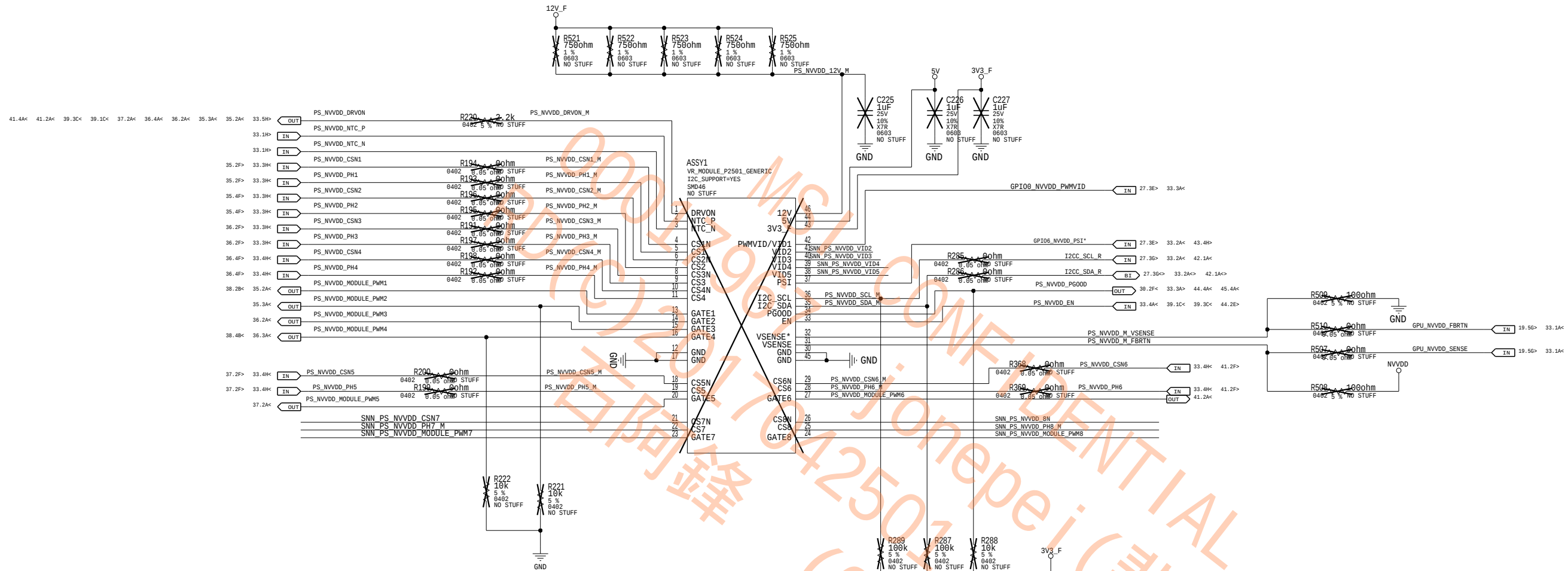
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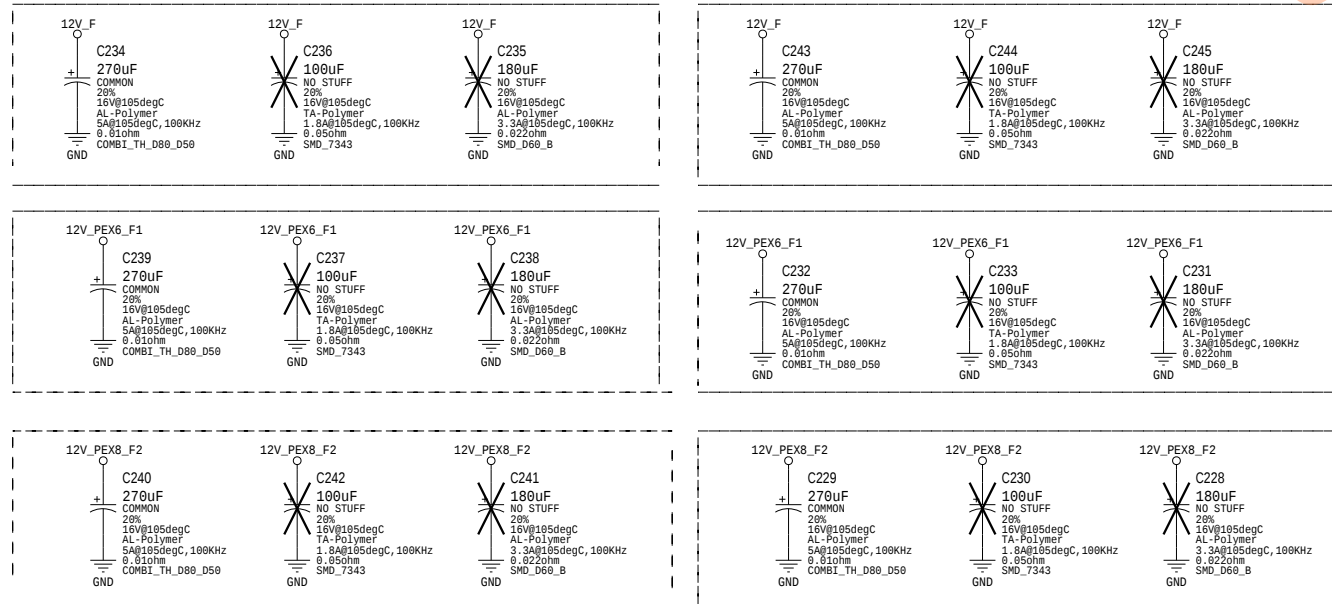
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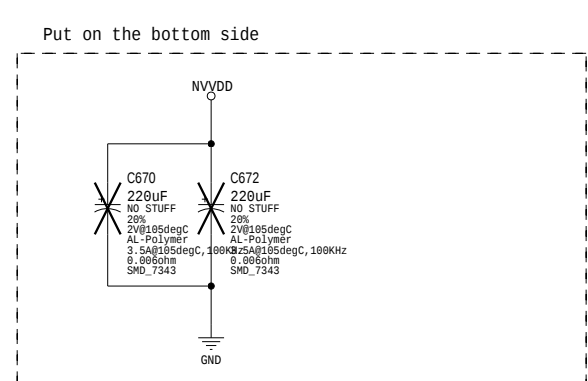
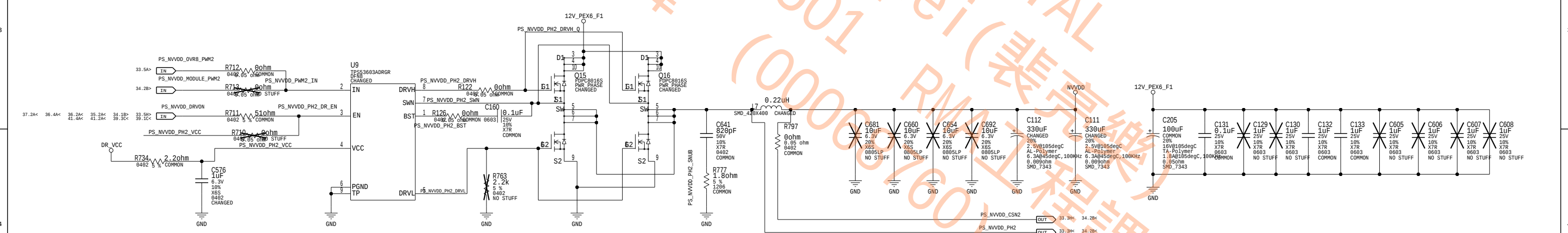
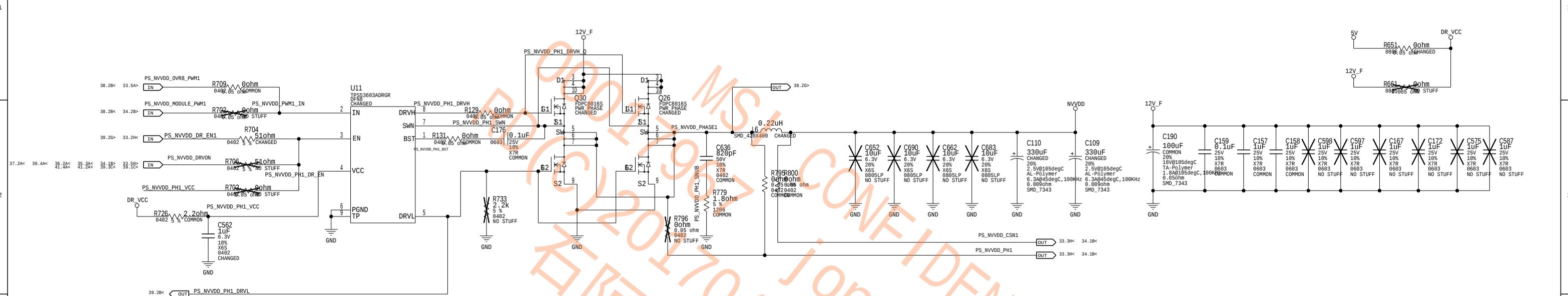


CO-LAYOUT: CHOOSE ONE ONLY



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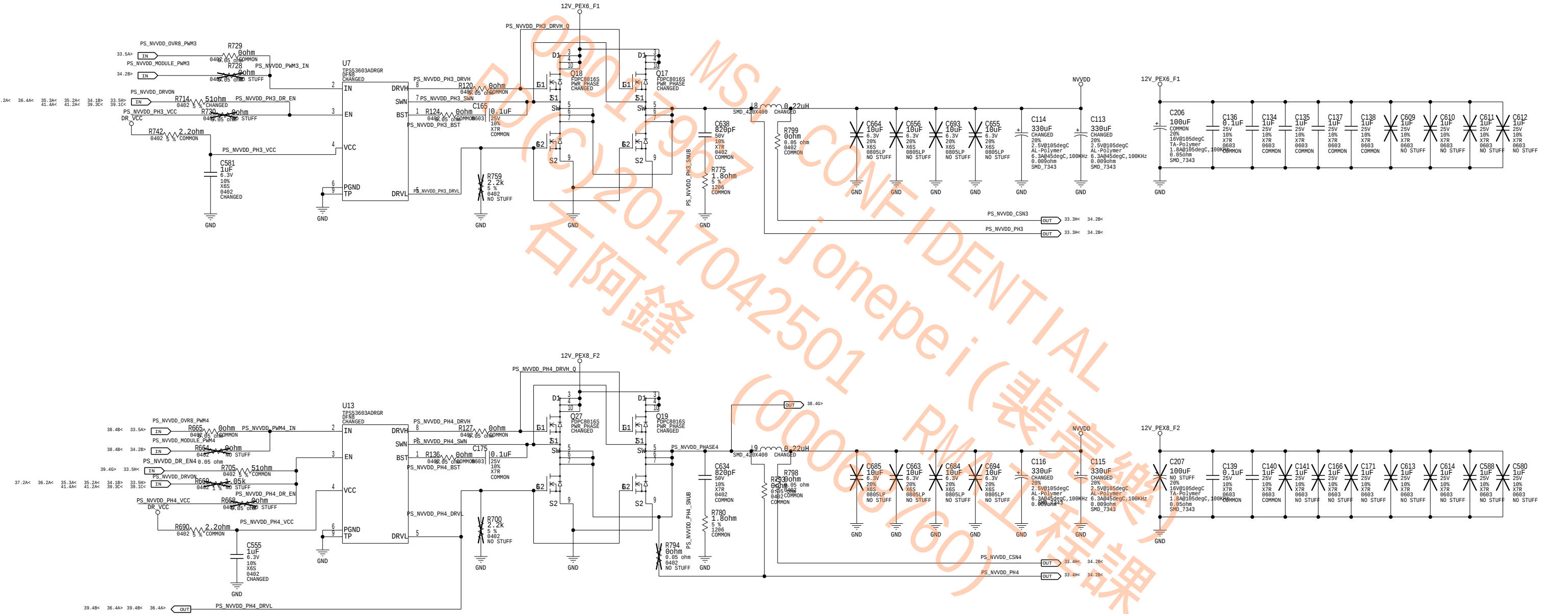
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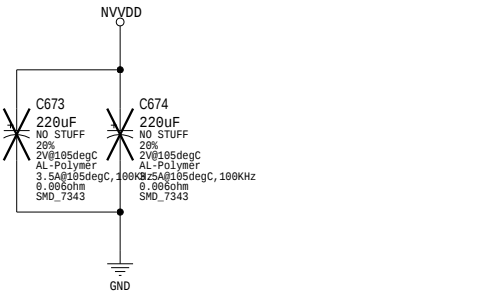
ASSEMBLY	SKU50
PAGE DETAIL	PS: NVVDD Phase 1, 2

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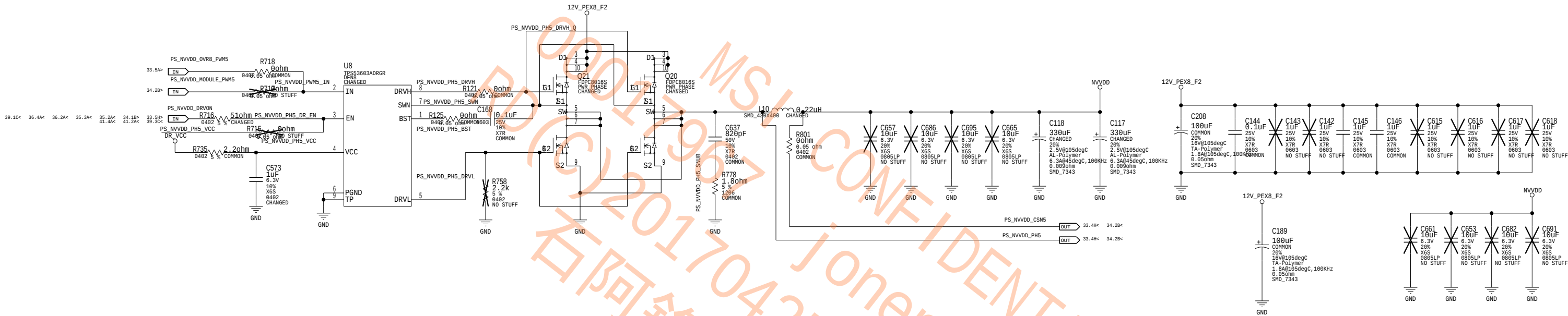
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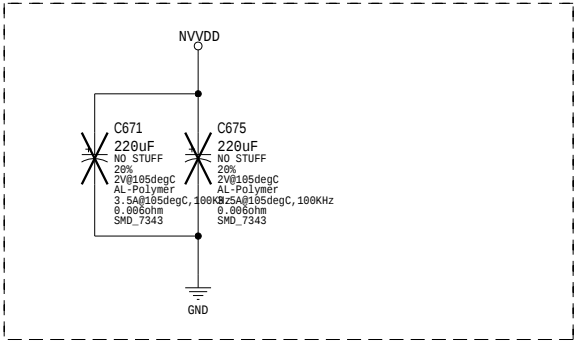
Put on the bottom side



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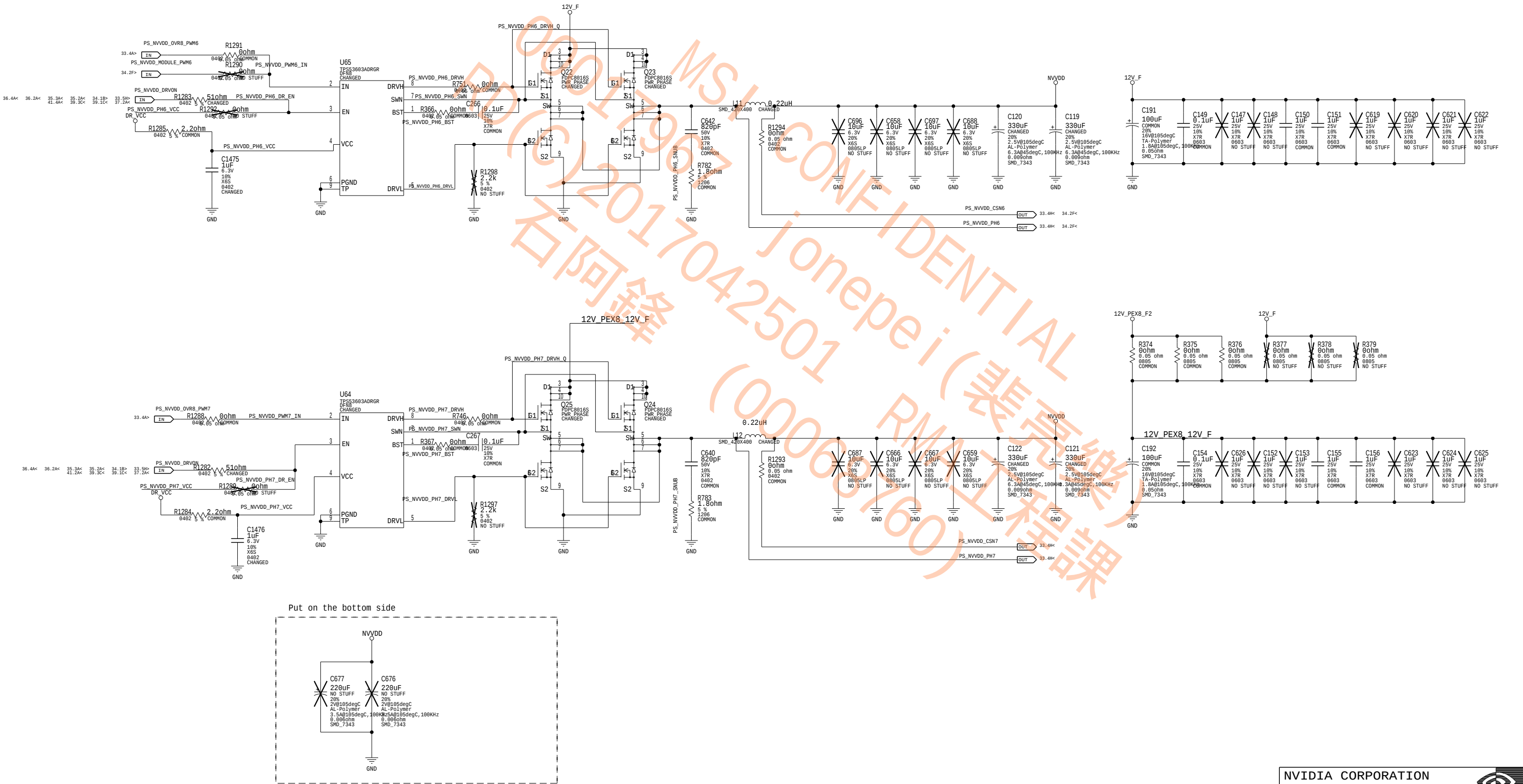
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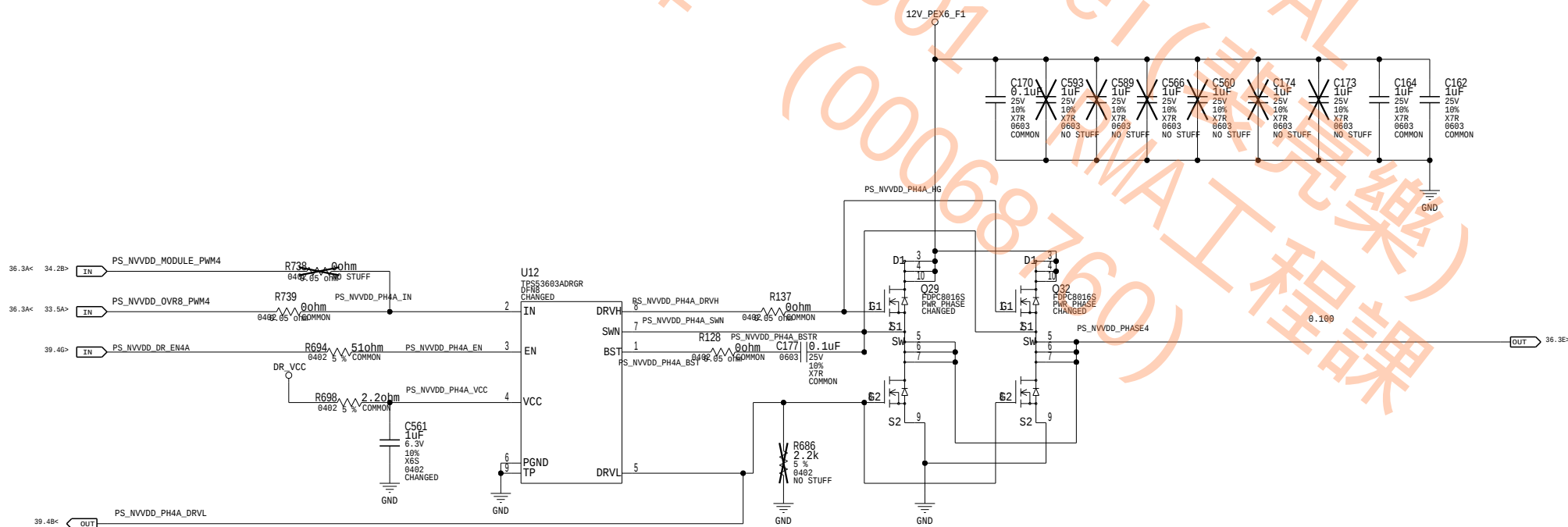
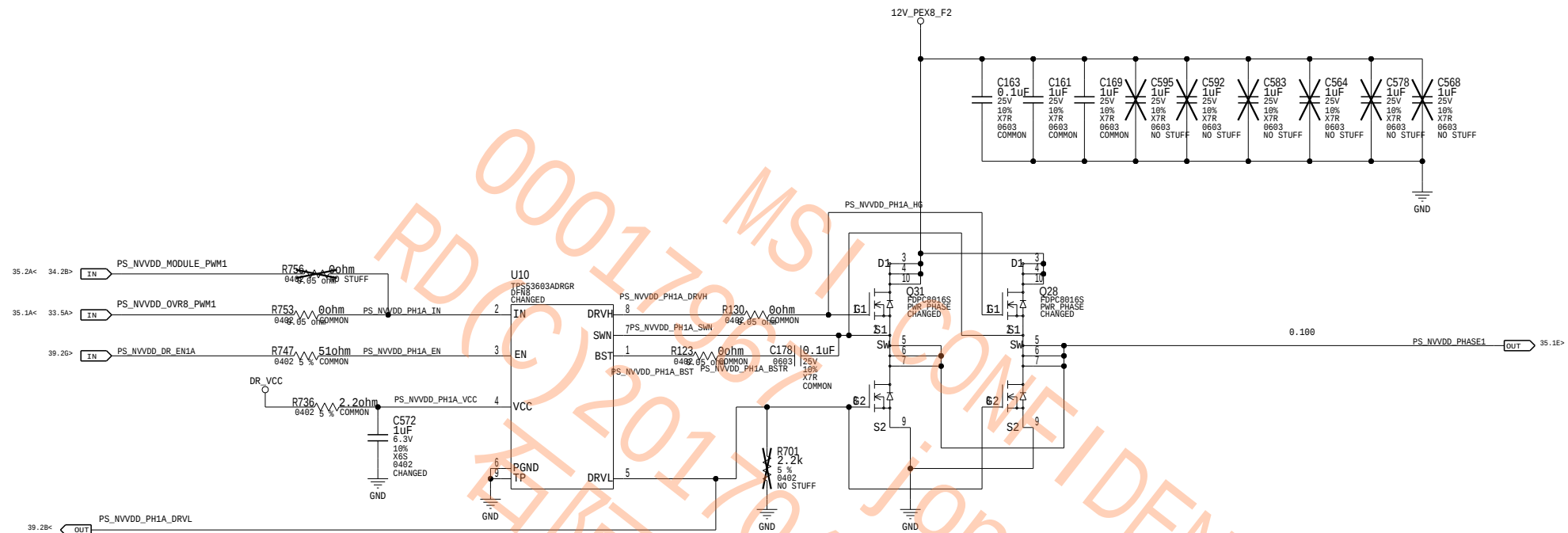
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DATE

15-FEB-2017



Page39: PS: Dynamic Power Balance Phases

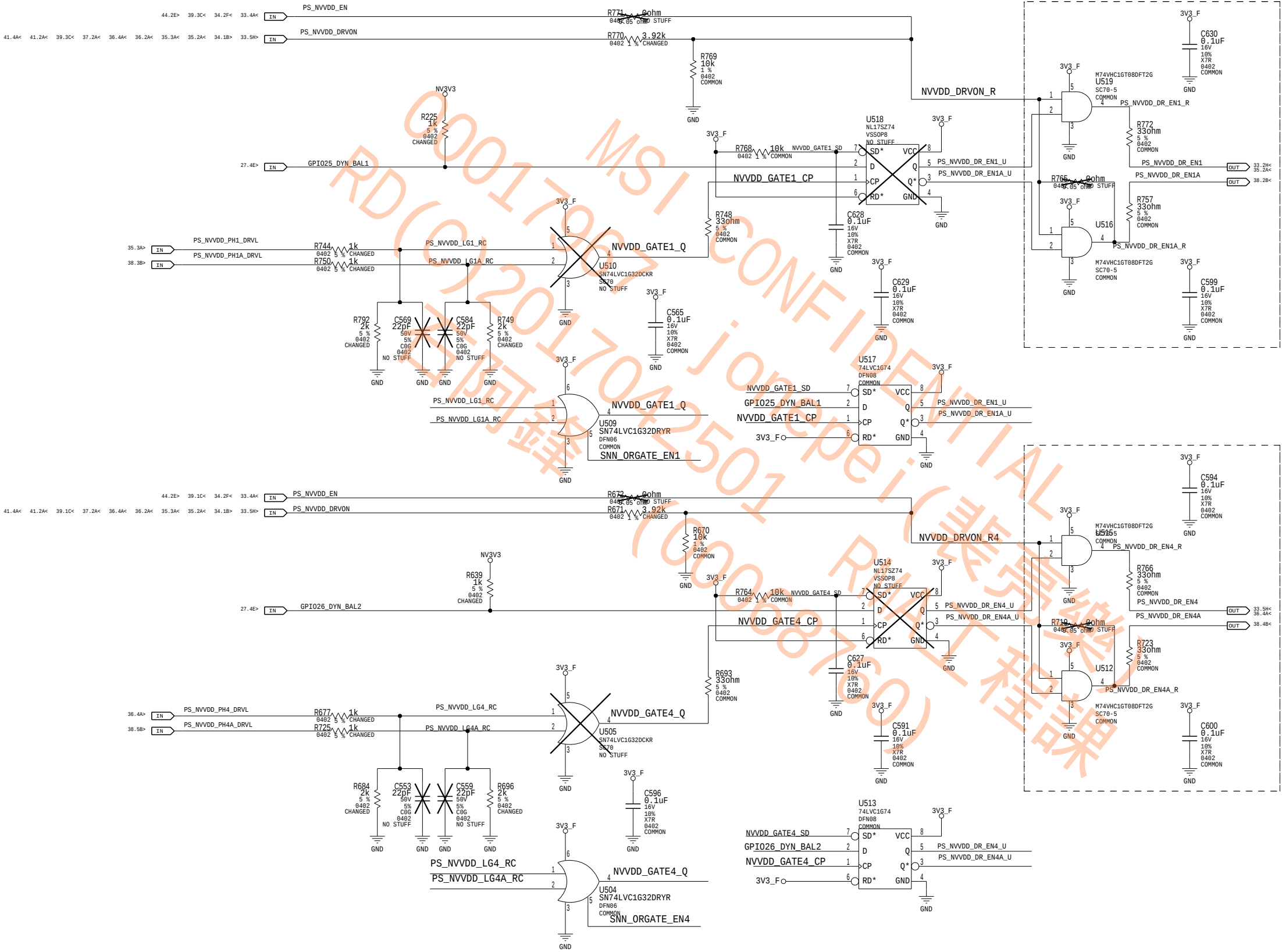


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ASSEMBLY	SKU50
PAGE DETAIL	PS: Dynamic power balance phase

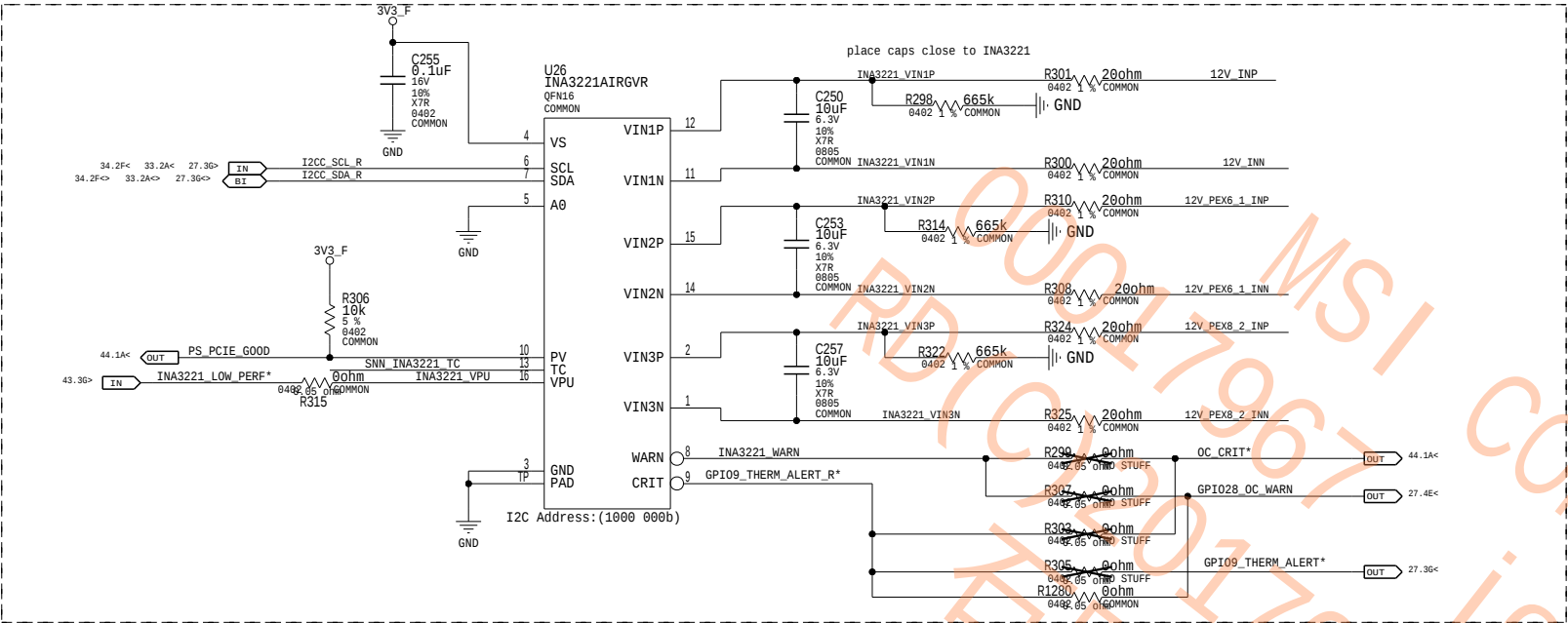
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Page40: PS: Dynamic Power Balance Logic

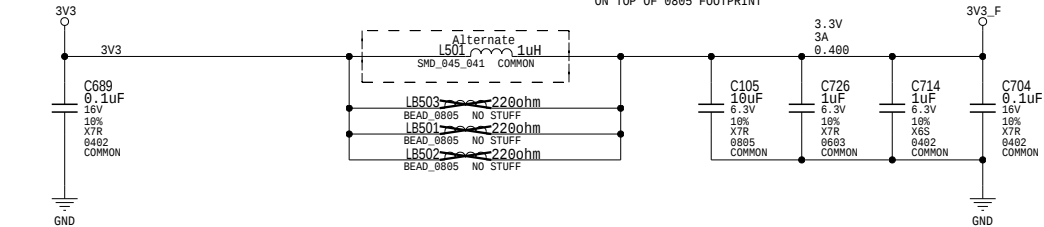


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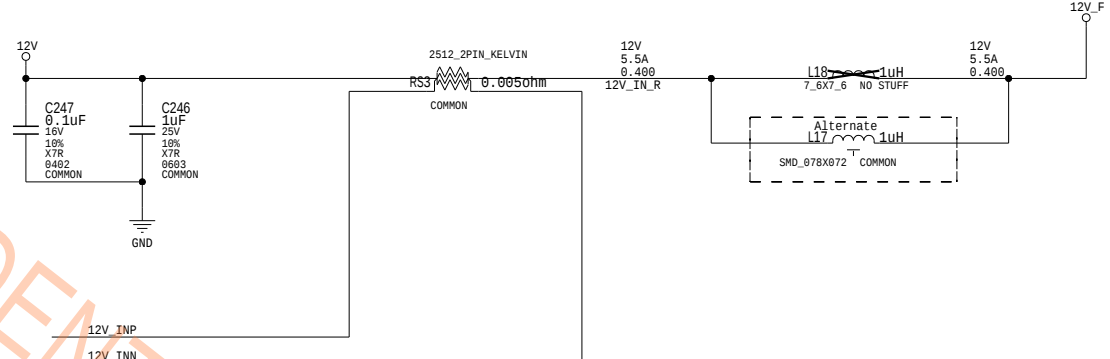
Page41: PS: Inputs, Filtering, and Monitoring



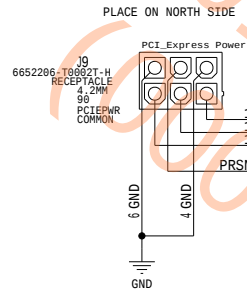
PEX 3V3 INPUT - 10W



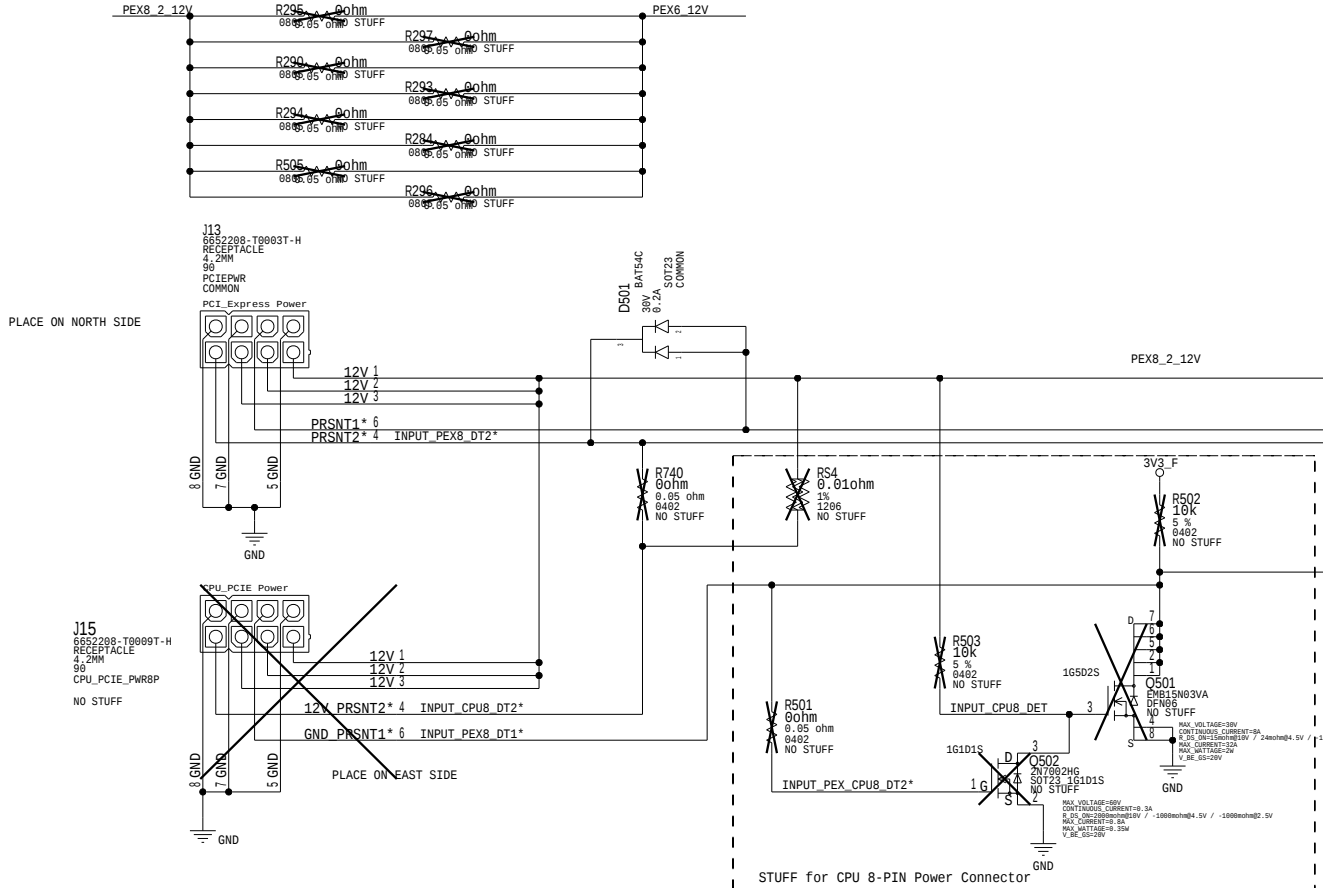
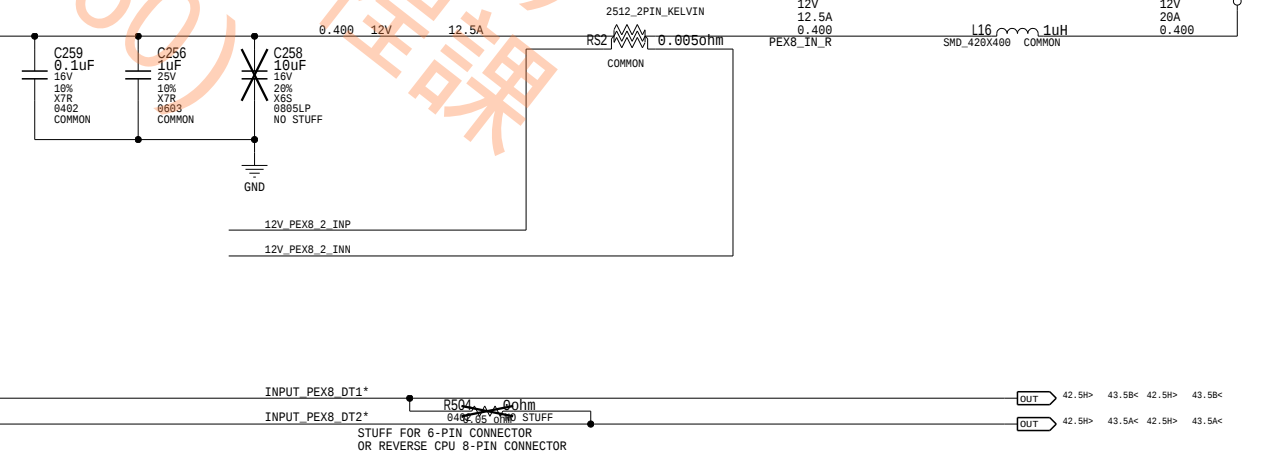
PEX_12V INPUT - 66W



PEX6 INPUT 1 - 2x3 PCIE CON 75W



PEX8 INPUT 2 - 2x4 PCIE CON 150W



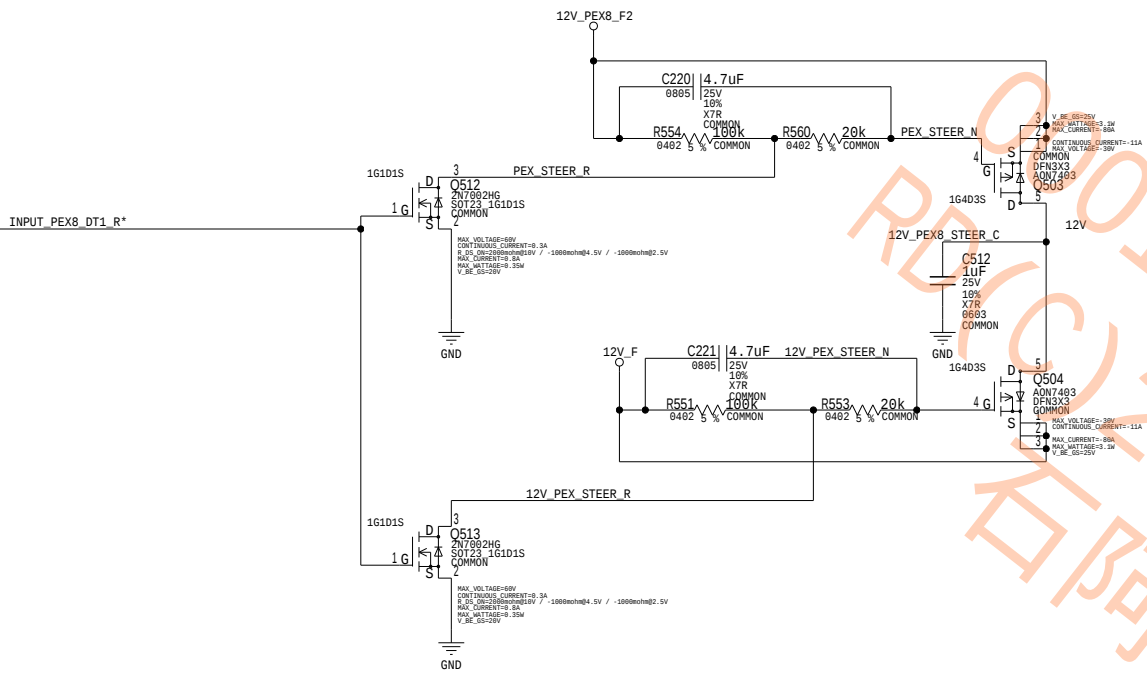
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Page42: PS: 12V Current Steering & Hot Unplug Detect

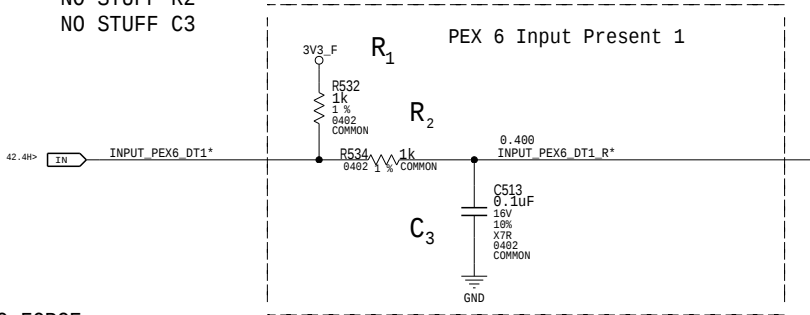
QUADRO/TESLA:
12V CURRENT STEERING (UNDER POWER BOOT):
GUIDES CURRENT FROM PEX EDGE TO PEX 8 PIN INPUT AREA

For OpenVreg Type4 + Phase Doubler, 2 phase PSI mode



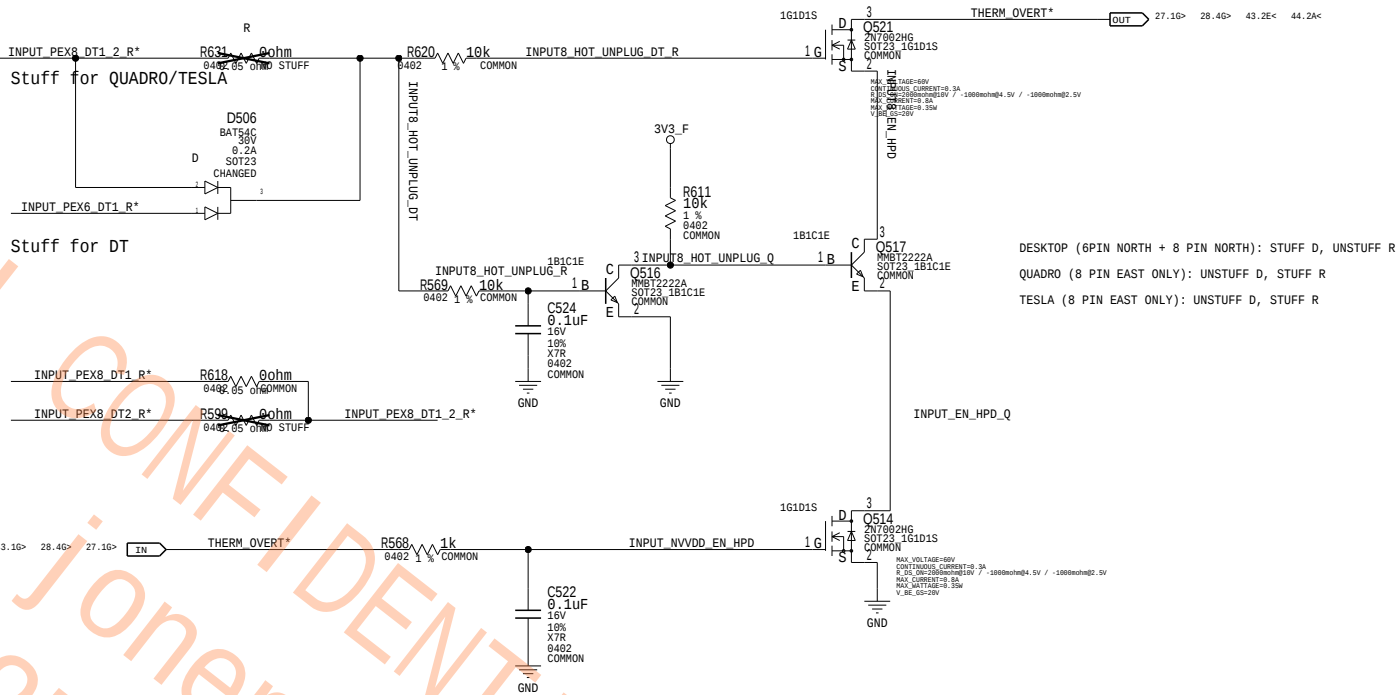
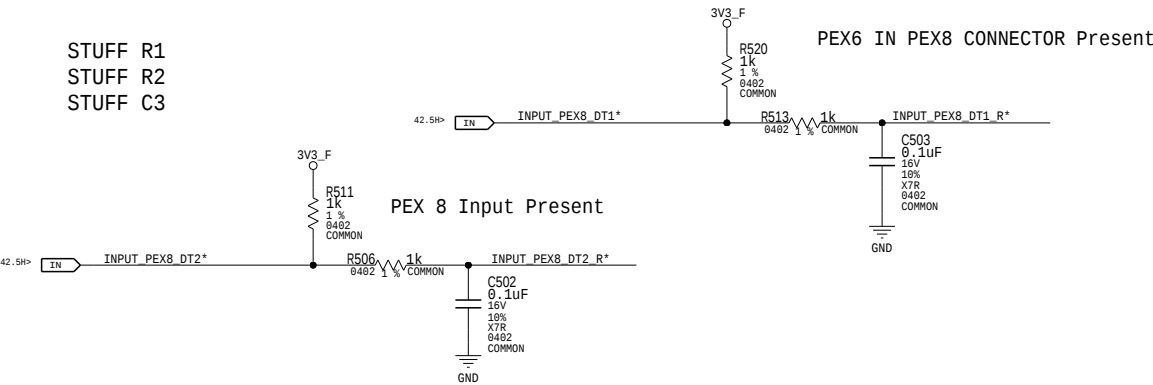
PEX 6 INPUT CONNECTOR IS NOT LOADED
PEX 6 INPUT DETECT MUST BE DISABLED
PEX 8 INPUT DETECT MUST BE USED TO CONTROL STEERING

NO STUFF R1
NO STUFF R2
NO STUFF C3



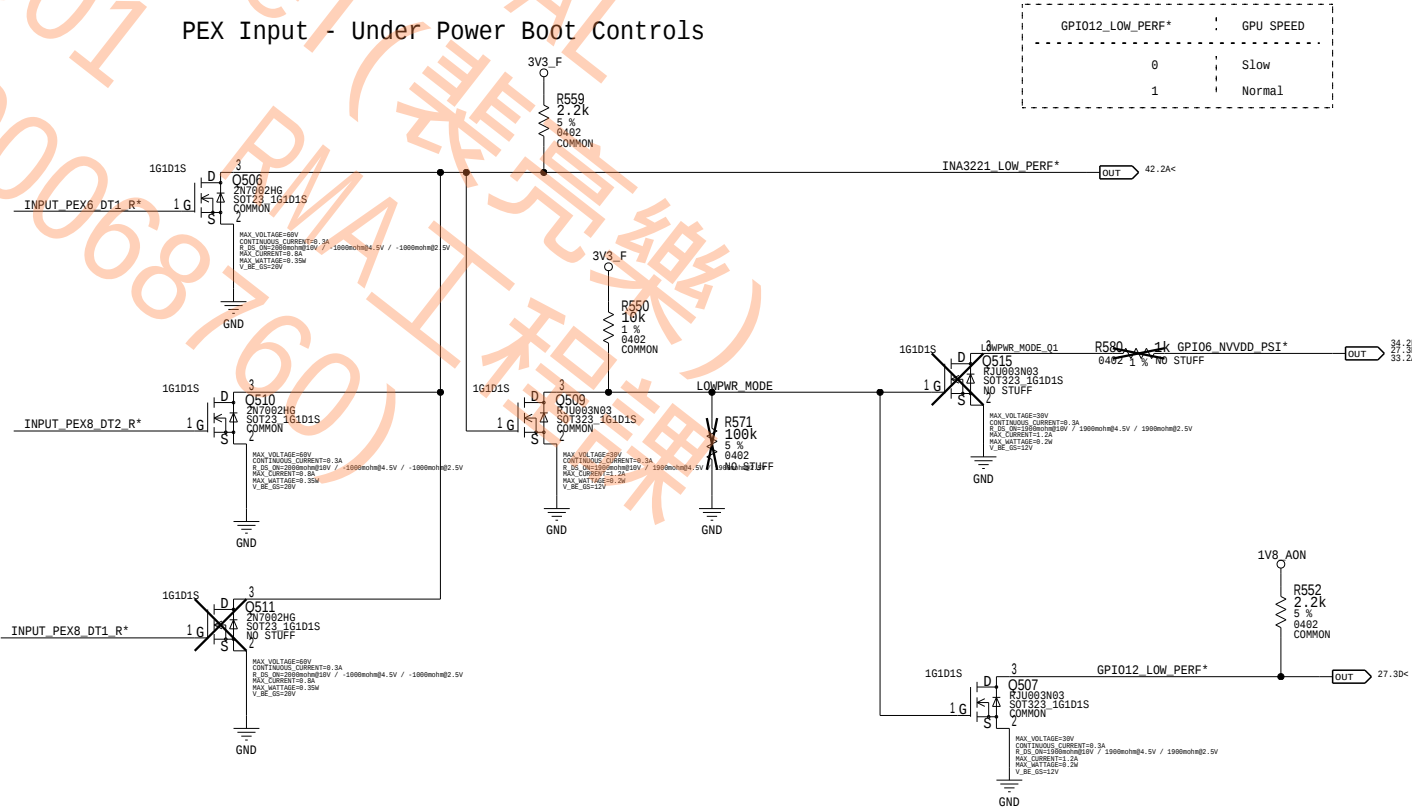
GeFORCE:
PEX 6 INPUT CONNECTOR IS LOADED
PEX 6 INPUT DETECT MUST BE USED TO CONTROL STEERING

STUFF R1
STUFF R2
STUFF C3



DESKTOP (6PIN NORTH + 8 PIN NORTH): STUFF D, UNSTUFF R
QUADRO (8 PIN EAST ONLY): UNSTUFF D, STUFF R
TESLA (8 PIN EAST ONLY): UNSTUFF D, STUFF R

PEX Input - Under Power Boot Controls



A 2 x 4 auxiliary power connector plug from the power supply unit must not use the 75 W sense coding (Sense1=Open and Sense0=Ground) to avoid end-user confusion.

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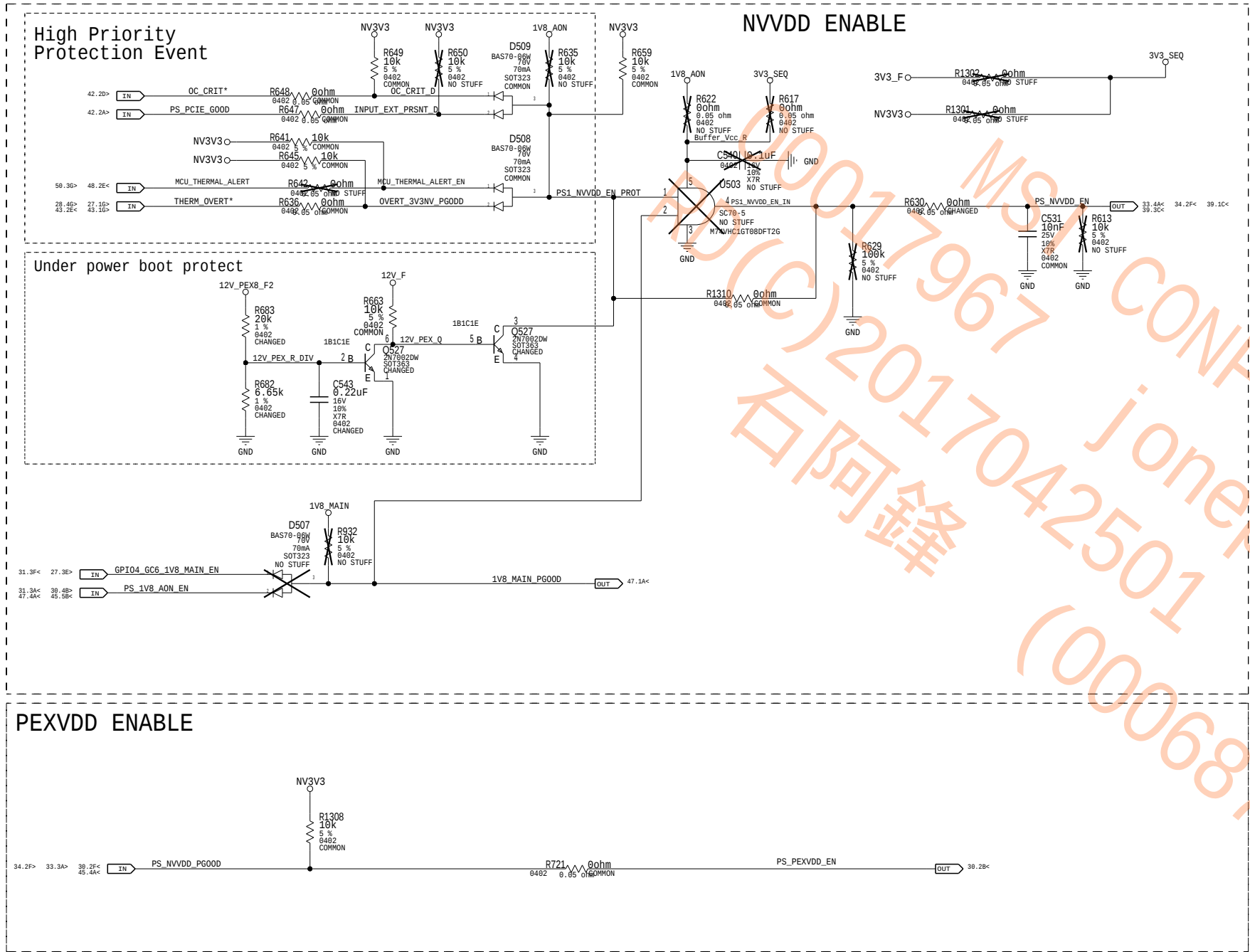
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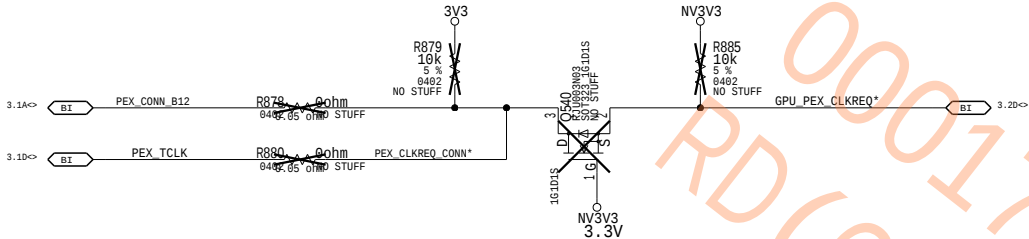
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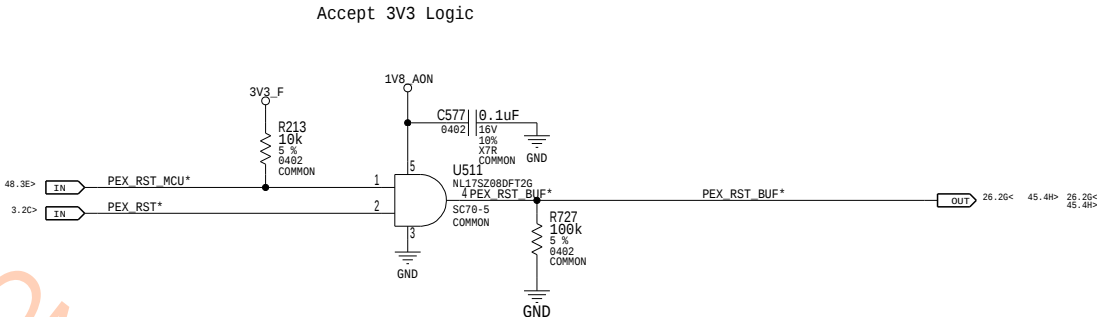
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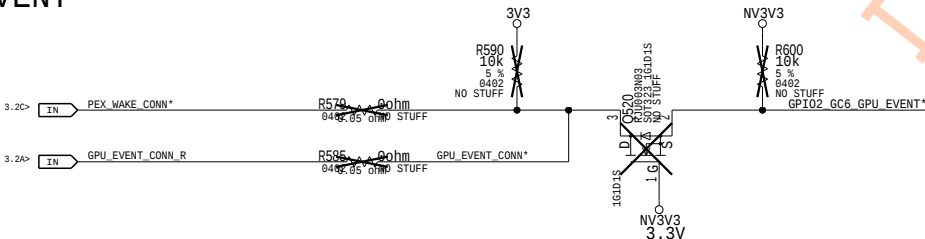
PEX_CLKREQ*



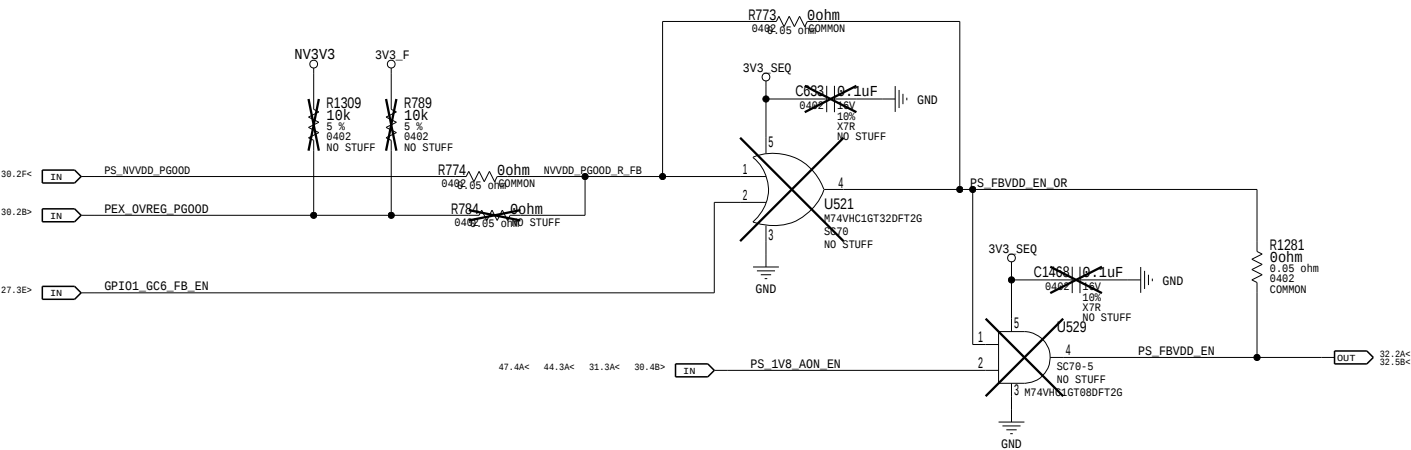
PEX_RST# LOGIC



GPU_EVENT*



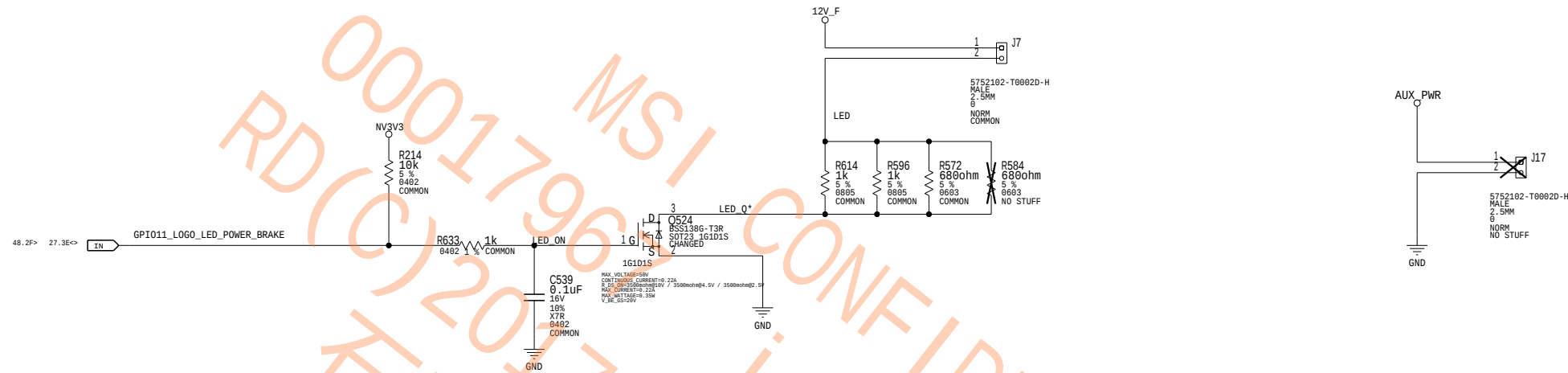
FBVDD/Q ENABLE



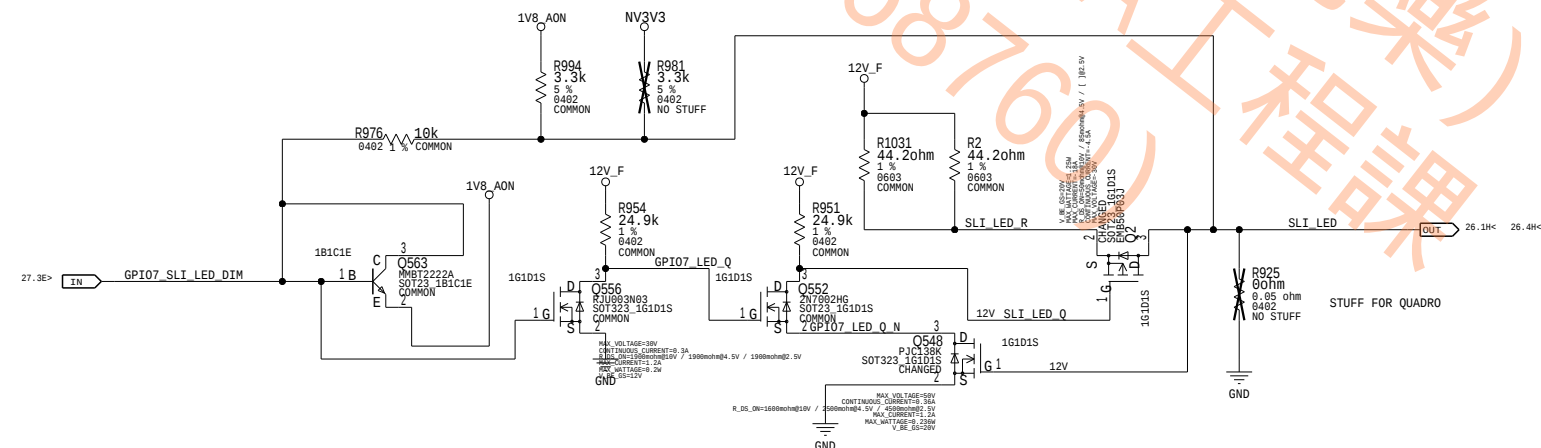
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LED HEADER(GEFORCE ONLY)



SLI LED (GEFORCE ONLY)



ASSEMBLY	SKU50
PAGE DETAIL	GEFORCE LED AND SLI LED

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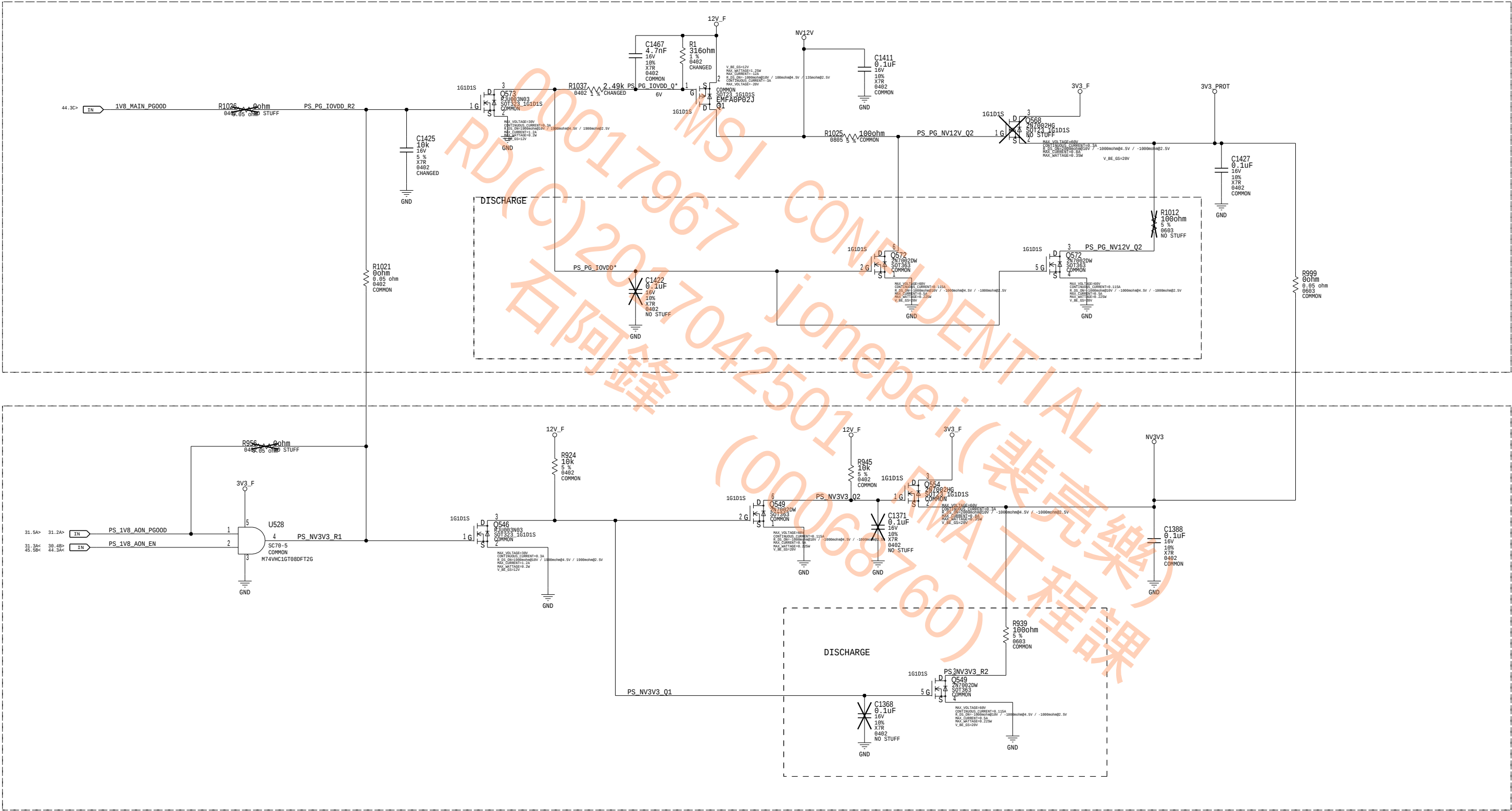
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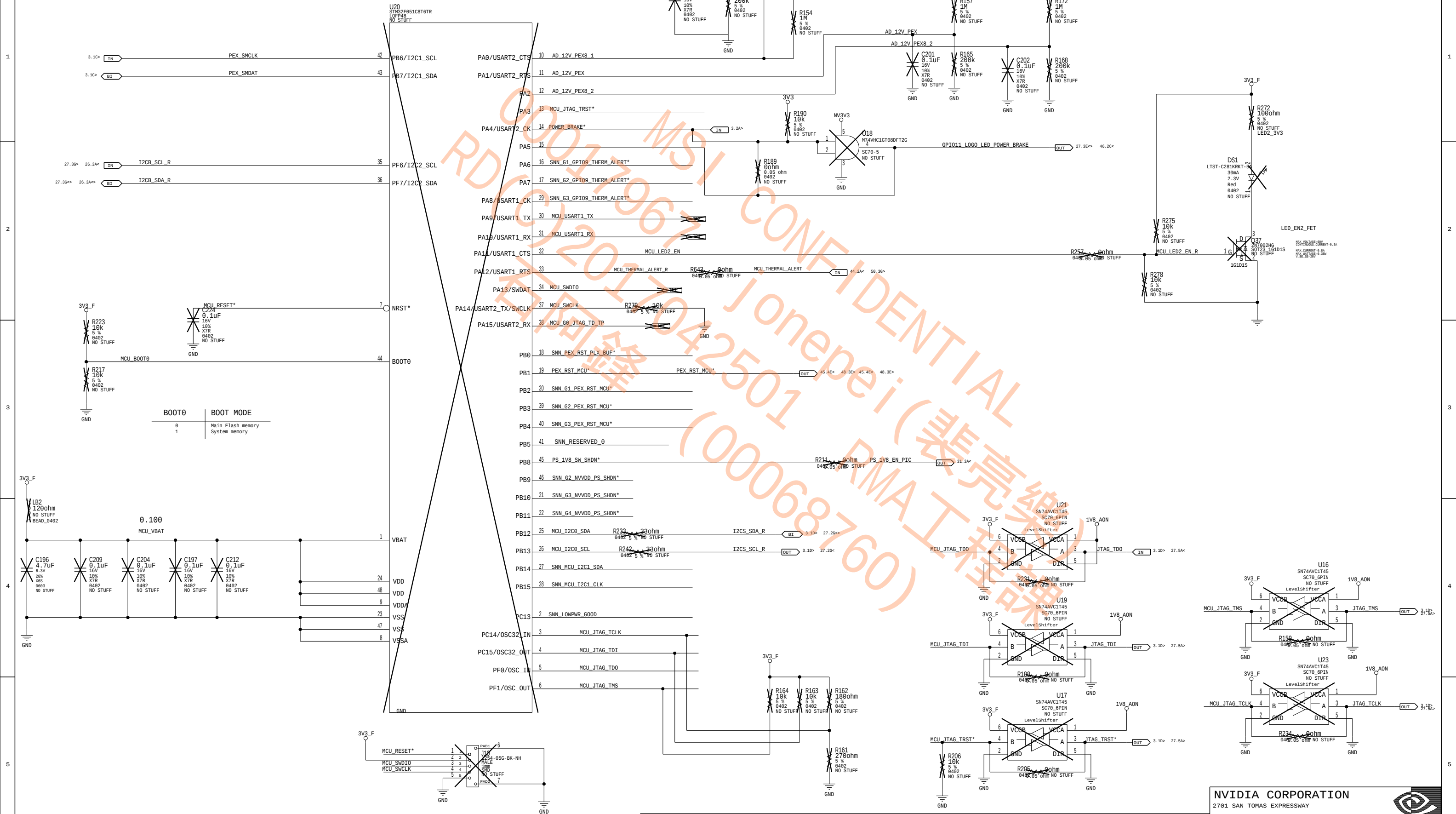
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Page47: MCU



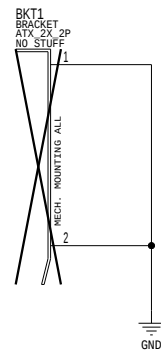
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ASSEMBLY	SKU50
PAGE DETAIL	PS: MCU

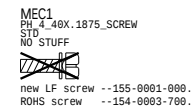
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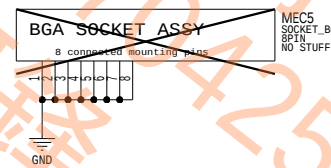
Brackets:



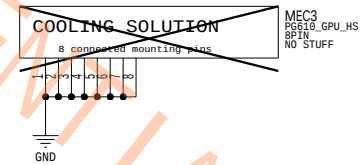
Bracket Screw



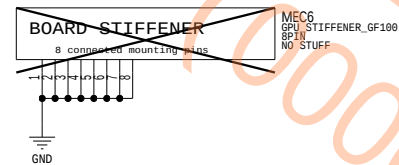
GPU SOCKET



GPU HEATLINK



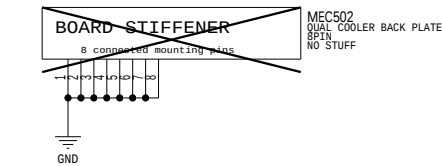
GPU Stiffener (BRING-UP COOLER)



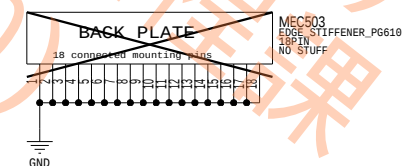
STIFFENER



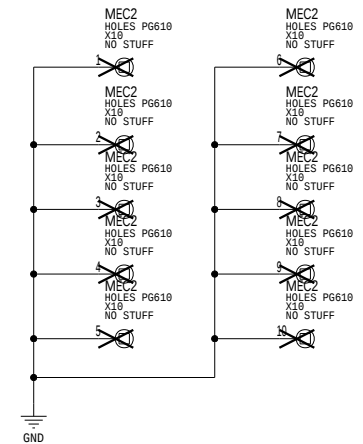
QUAL COOLER STIFFENER



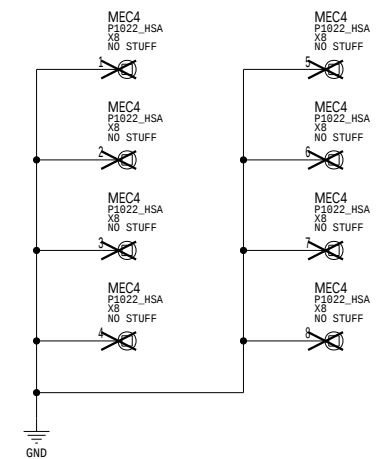
BOARD BACK PLATE



Mechanical Holes Symbol



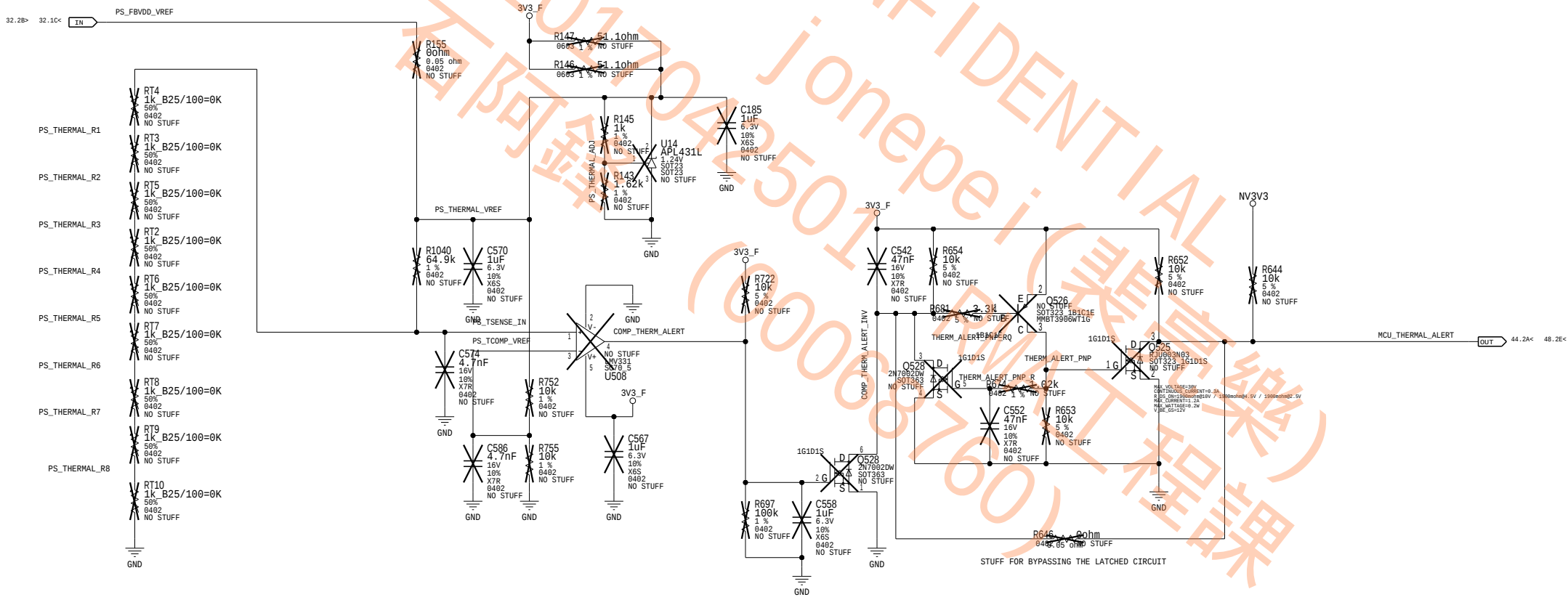
GPU HOLES SYMBOL



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PAGE DETAIL	MECH

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