

PG180-A02

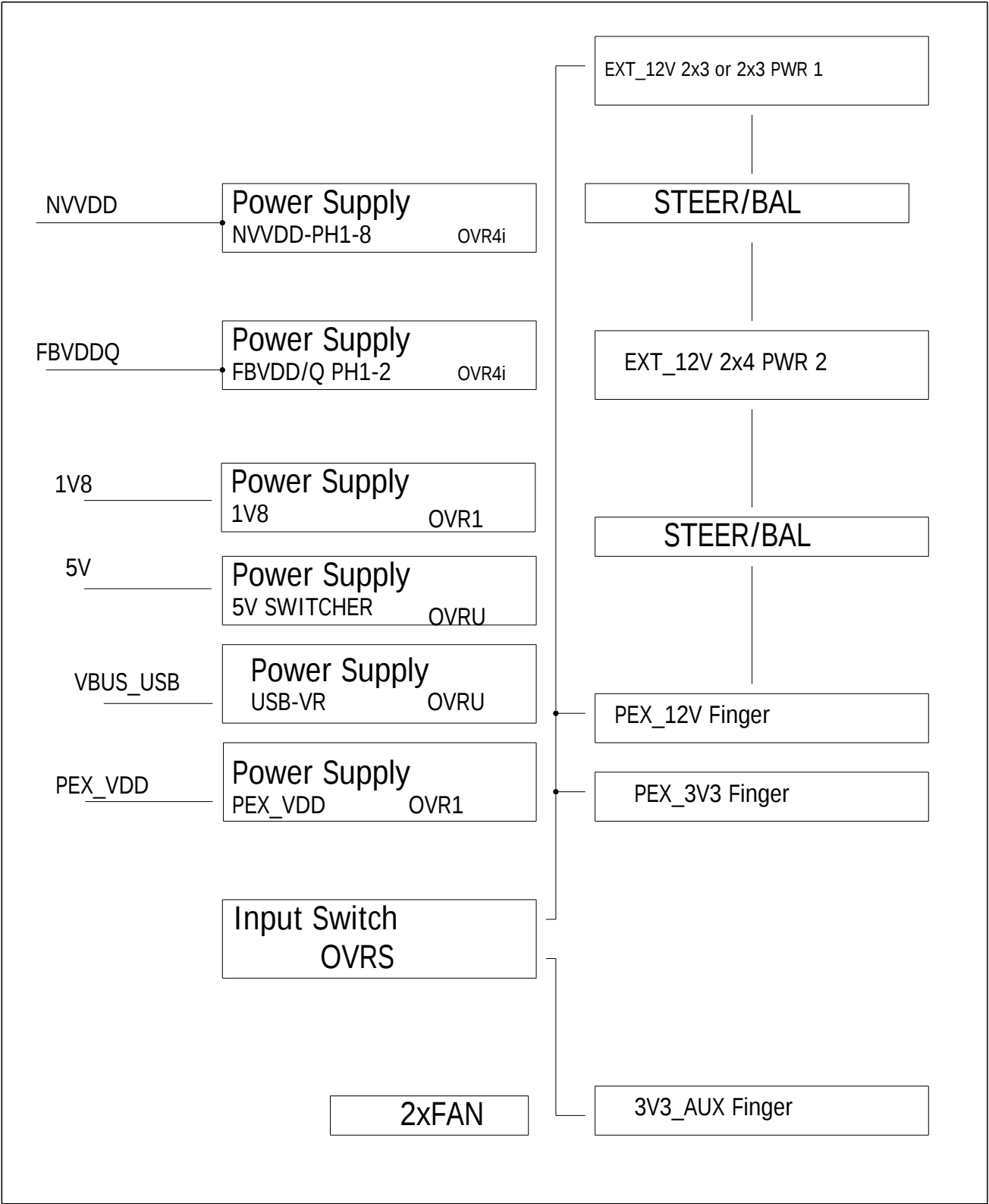
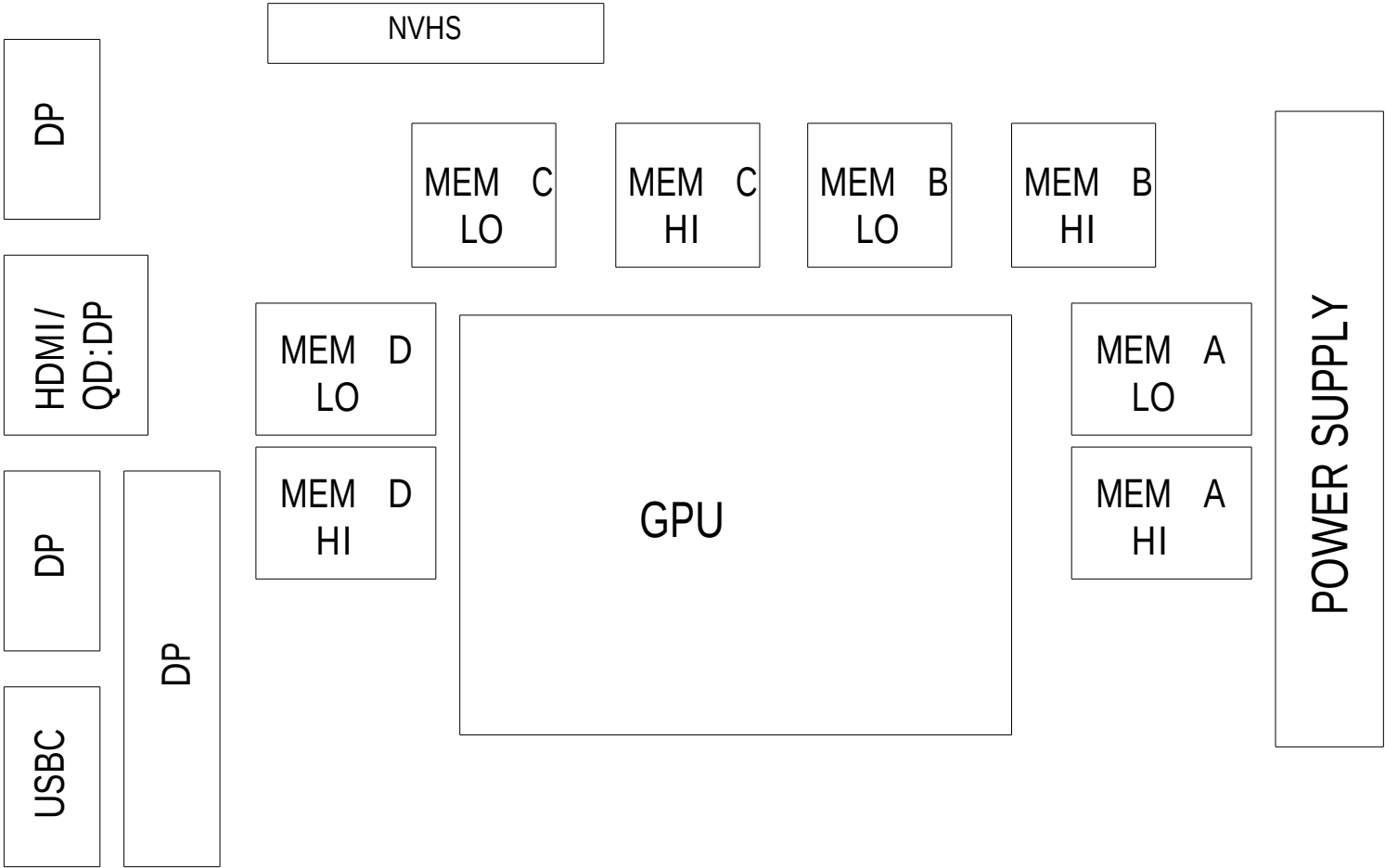
256b GDDR6 x16

TALL DP + DP + DP + HDMI/DP + USB

TABLE OF CONTENTS

Page	Description	Page	Description	Page	Description
1	Table of Contents	26	MISC: XTAL, PLL	51	SEQUENCE: MISC
2	Block Diagram	27	MISC: USB PPC	52	LED & FAN HEADERS
3	PCI Express	28	PS: USB VR	53	LED 2
4	PCI Termination	29	PS: 5V, 5V BACKUP	54	Mechanical: Bracket/Thermal Solution
5	MEMORY: GPU Partition A/B	30	PS: PEXVDD, 1V8		
6	MEMORY: FBA Partition 31..0	31	PS: FBVDD CONTROLLER		
7	MEMORY: FBA Partition 63..32	32	PS: FBVDD CONTROLLER OVR3		
8	MEMORY: FBB Partition 31..0	33	PS: FBVDD PHASE 1, 2		
9	MEMORY: FBB Partition 63..32	34	PS: NVVDD CONTROLLER OVR8		
10	MEMORY: GPU Partition C/D	35	PS: NVVDD Phase 1, 2		
11	MEMORY: FBC Partition 31..0	36	PS: NVVDD Phase 3, 4		
12	MEMORY: FBC Partition 63..32	37	PS: NVVDD Phase 5, 6		
13	MEMORY: FBD Partition 31..0	38	PS: NVVDD Phase 7, 8		
14	MEMORY: FBD Partition 63..32	39	PS: INPUT SWITCH RTD3		
15	GPU PWR and GND	40	PS: INPUT SWITCH RTD3 USB		
16	GPU Decoupling	41	PS: INPUT SWITCH RAIL BALANCE		
17	GPU DECOUPLING	42	PS: 12V CURRENT STEERING		
18	IFPAB TALL-DP	43	PS: VR THERMAL PROTECTION		
19	IFPE DP	44	PS: INPUTS, FILTERING AND MONITORING		
20	IFPF USBC	45	PS: CURRENT STEERING,HOT UNPLUG DETECT		
21	IFPC HDMI 2.0/DP	46	PS: PRE-FILTER		
22	IFPD DP	47	SEQUENCE: 5V, 1V8, NV3V3 ENABLE		
23	NVHS INTERFACE	48	SEQUENCE: NV, PEX, FB ENABLE		
24	MISC: FAN,THERMAL,JTAG,GPIO,STEREO	49	SEQUENCE: PCIE VOLTAGE MONITOR		
25	MISC3: ROM, STRAPS	50	SEQUENCE: DISCHARGE		

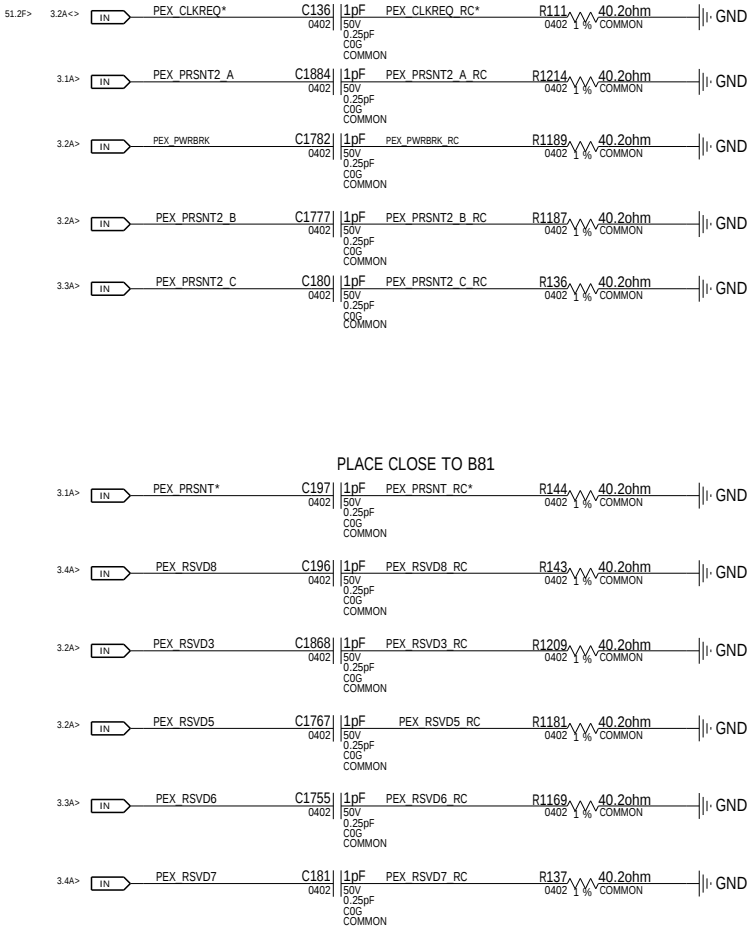
Page2: Block Diagram



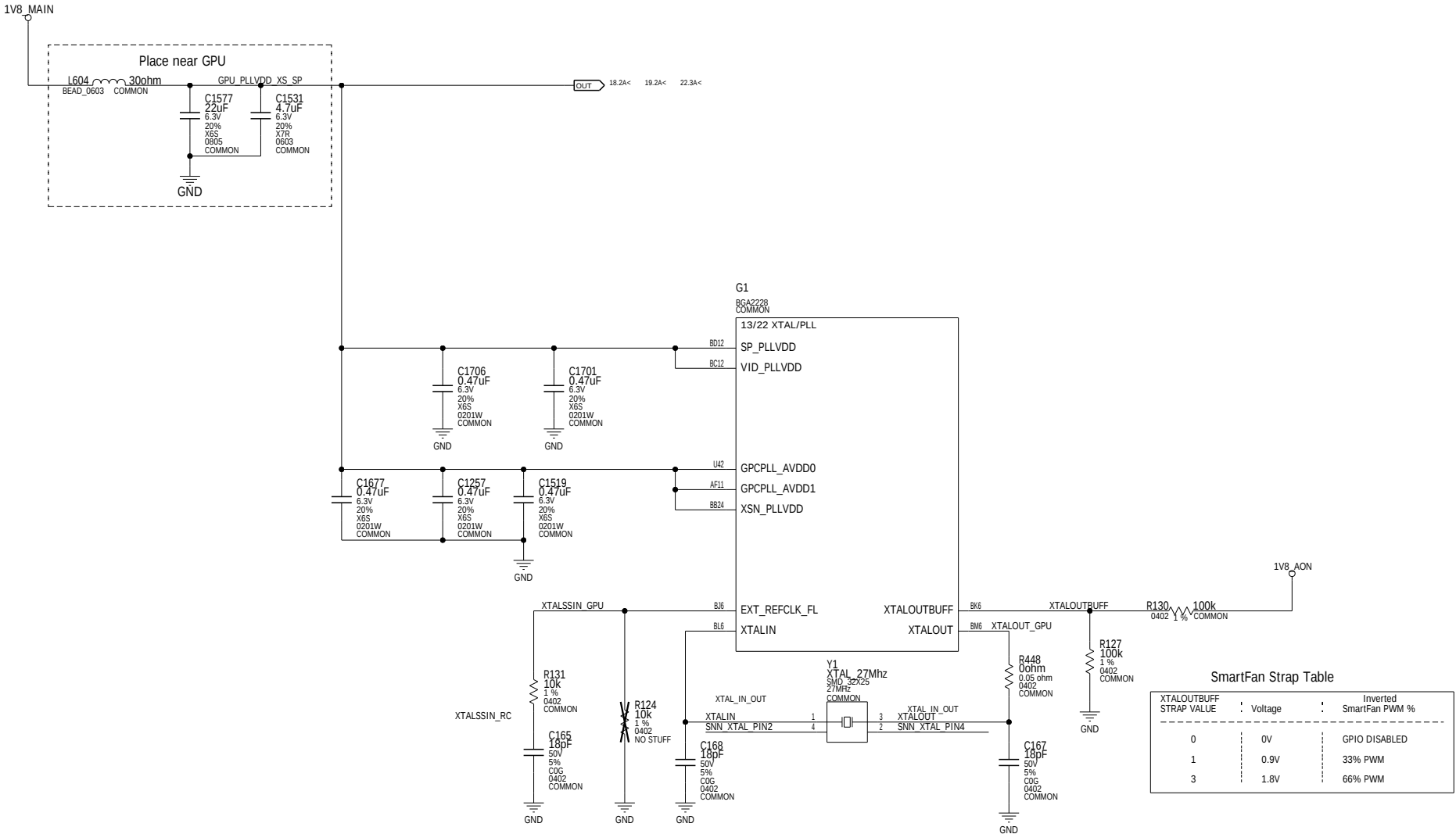
ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, 'MATERIALS') ARE BEING PROVIDED 'AS IS'. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

Page3

Page4: PCI Termination



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, 'MATERIALS') ARE BEING PROVIDED 'AS IS'. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.



ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, 'MATERIALS') ARE BEING PROVIDED 'AS IS'. THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NONINFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

