

PG160-B02

8GB GDDR6, 256b, X16
Tall DVI-D + USBC/DP + DP + HDMI/DP + DP

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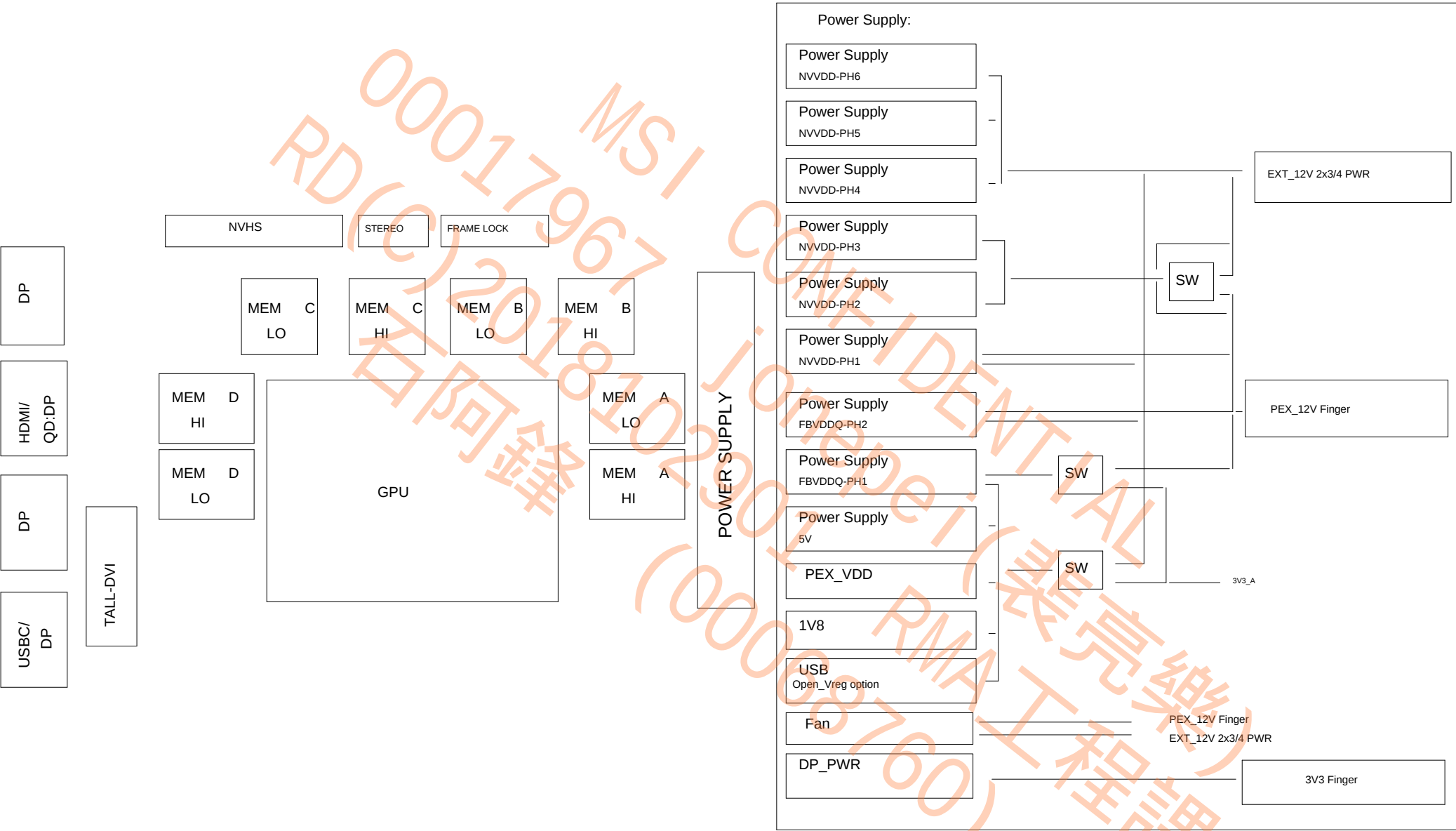
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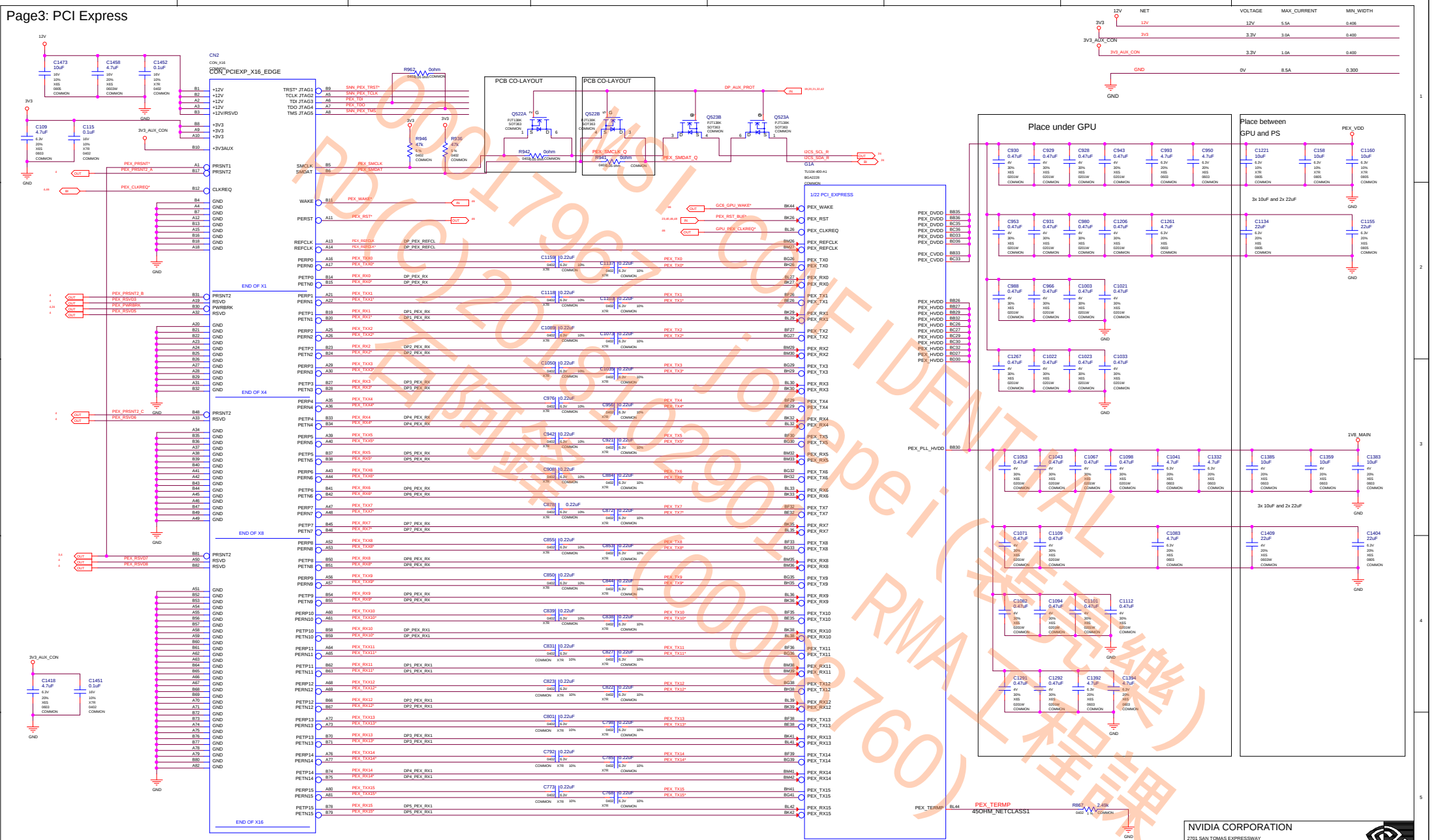
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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE DETAIL	Block Diagram

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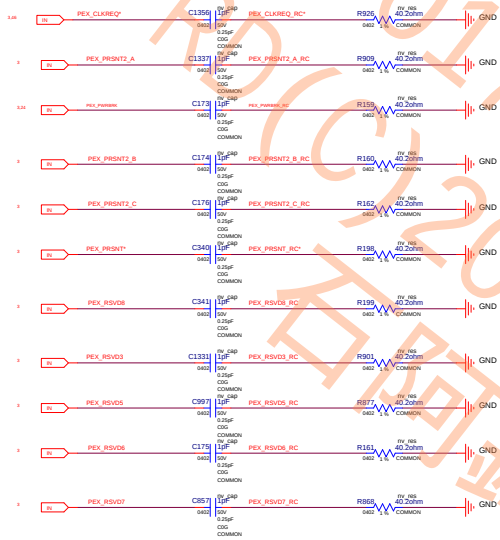
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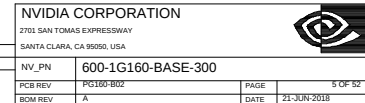
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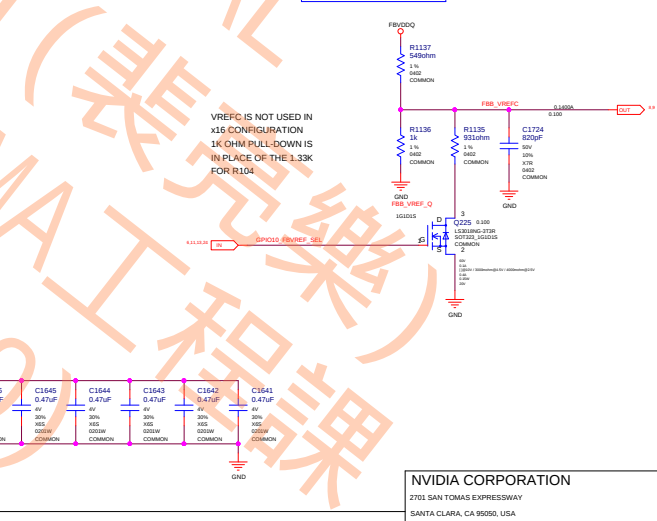
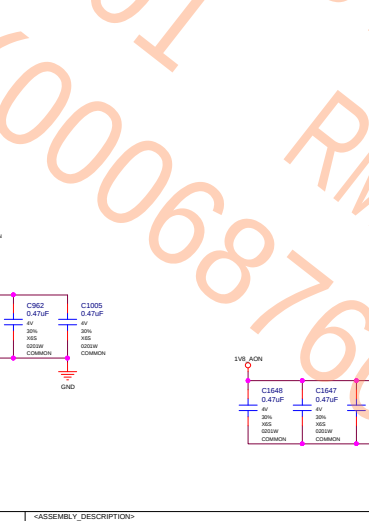
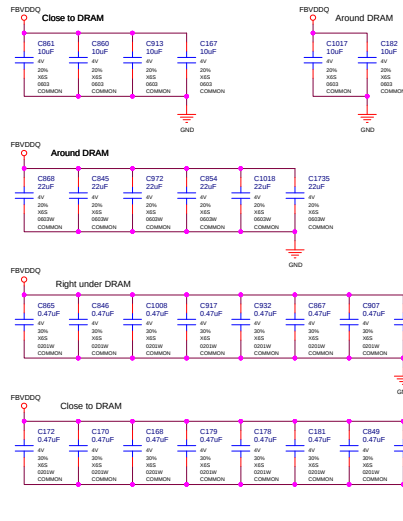
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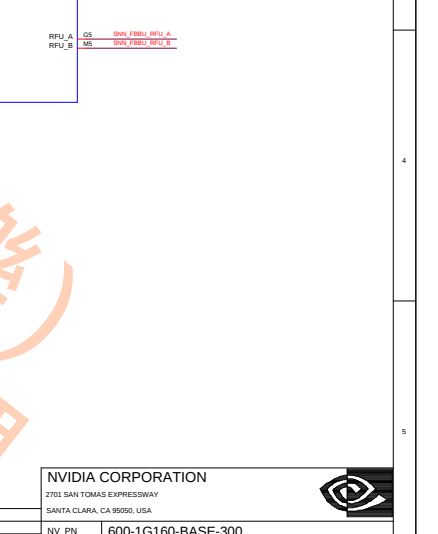
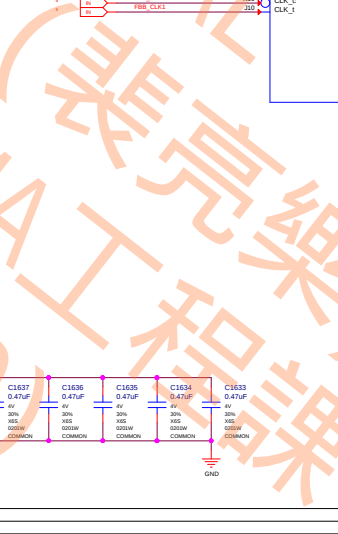
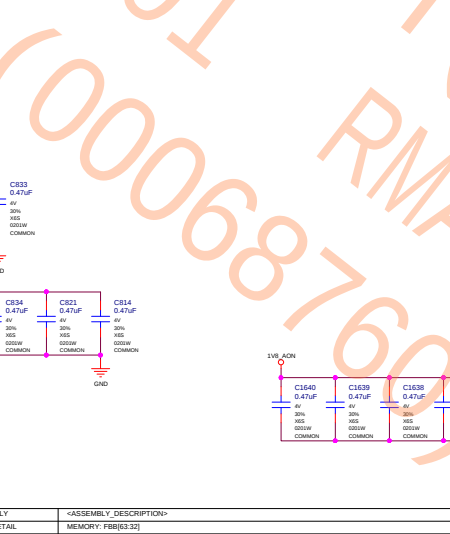
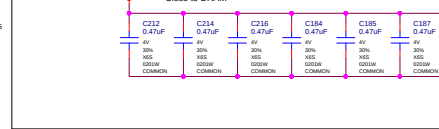
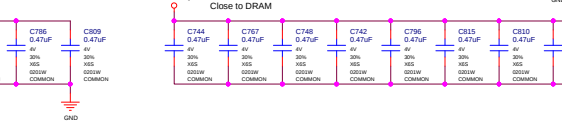
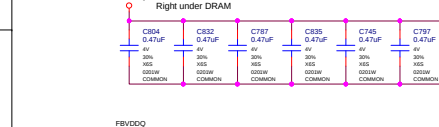
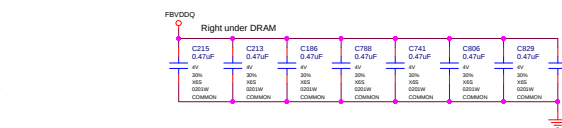
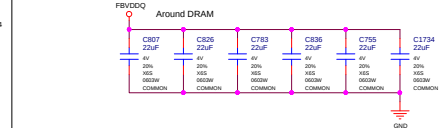
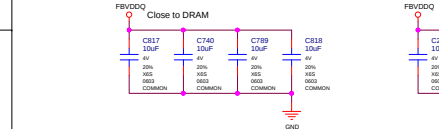
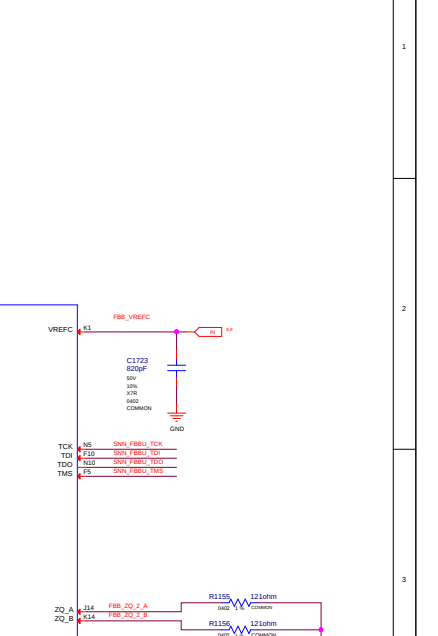
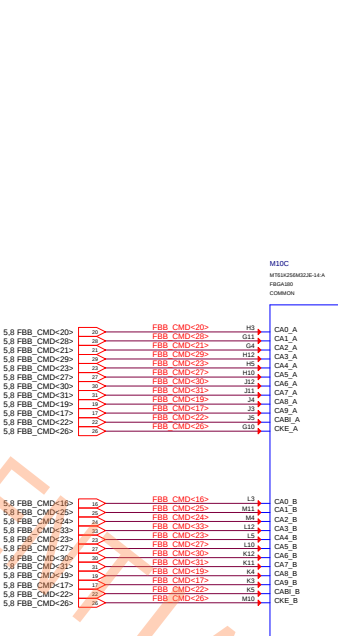
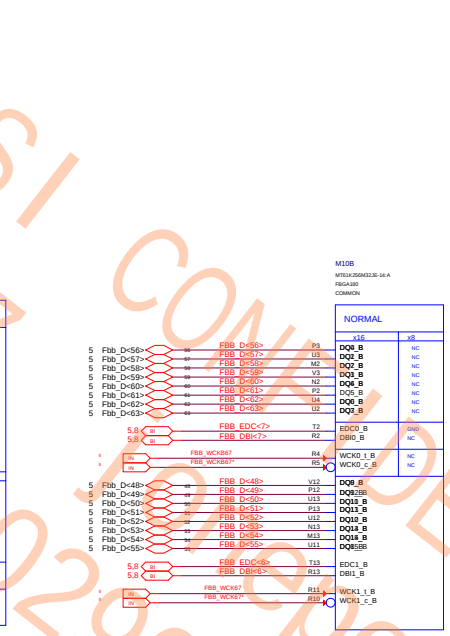
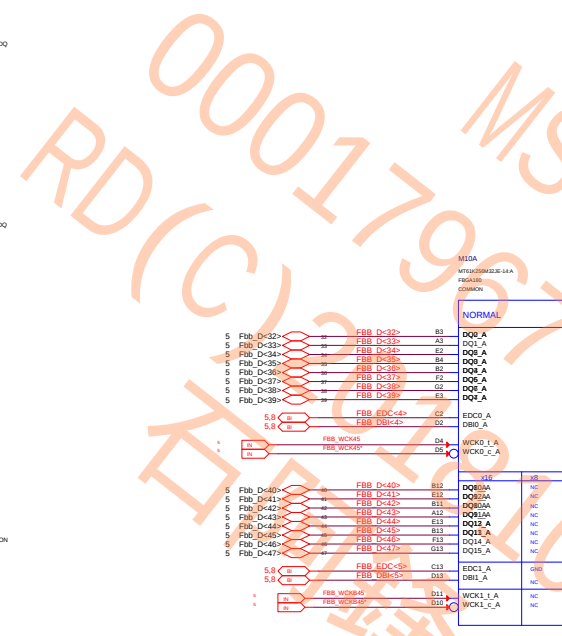
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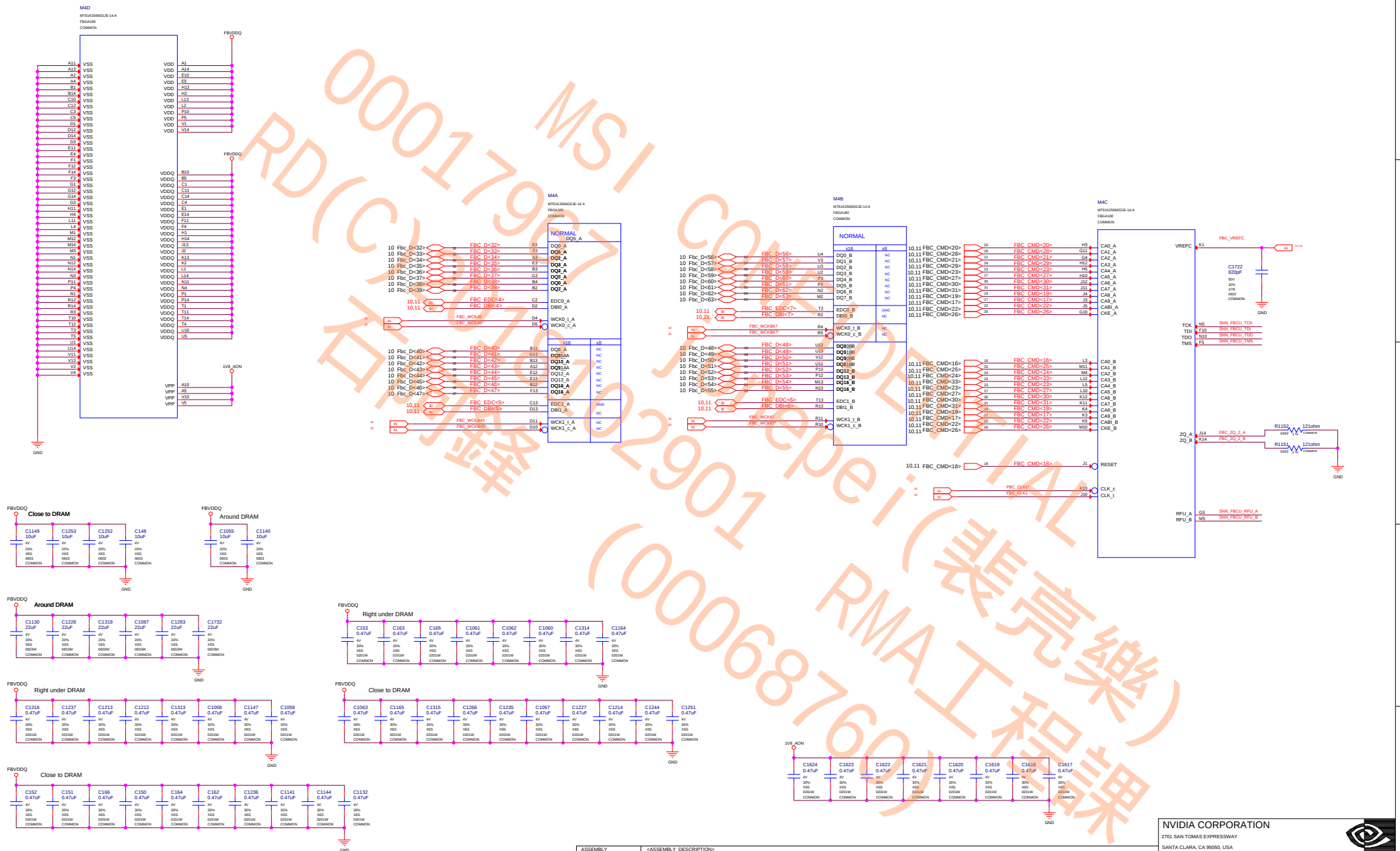
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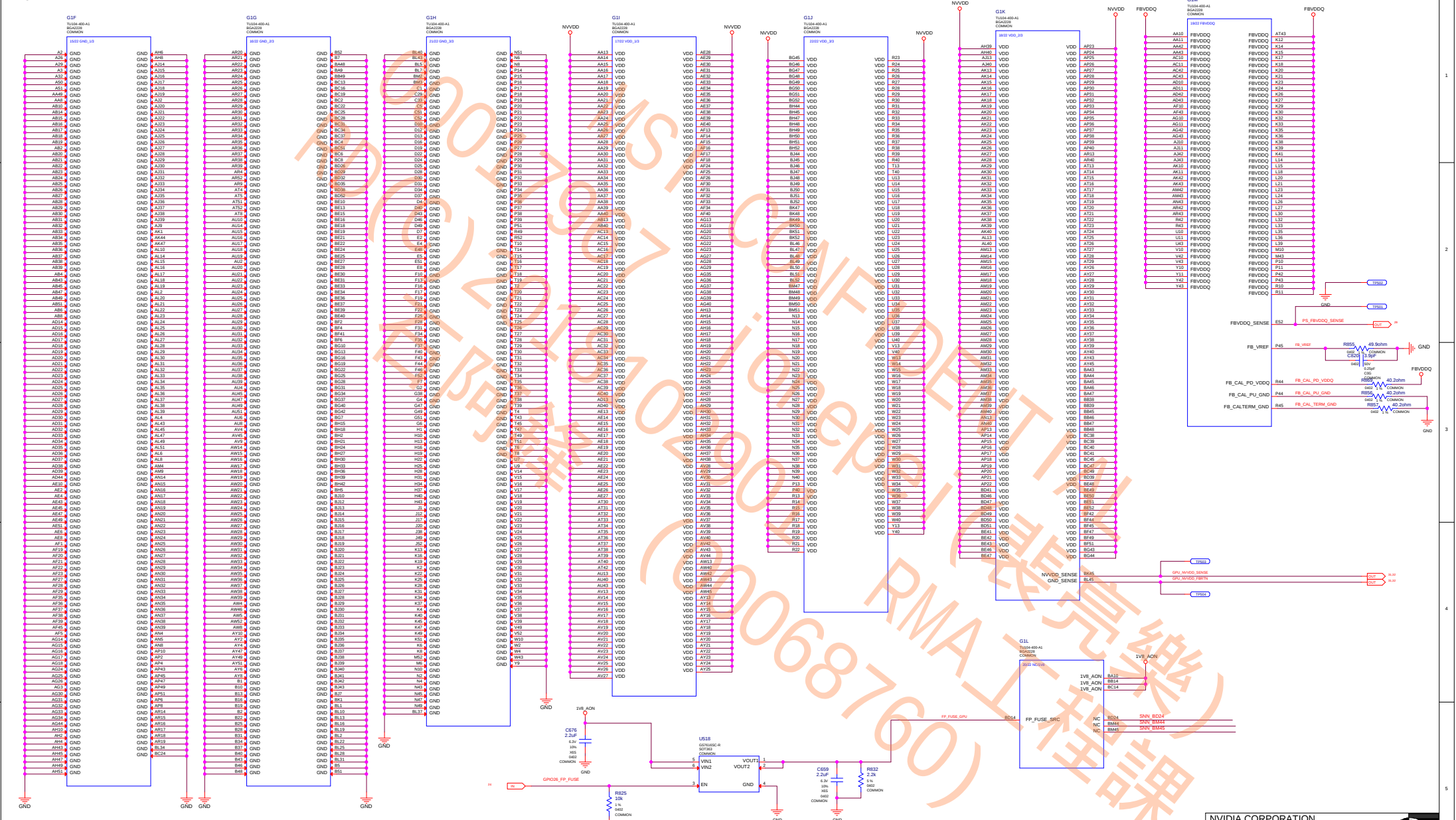
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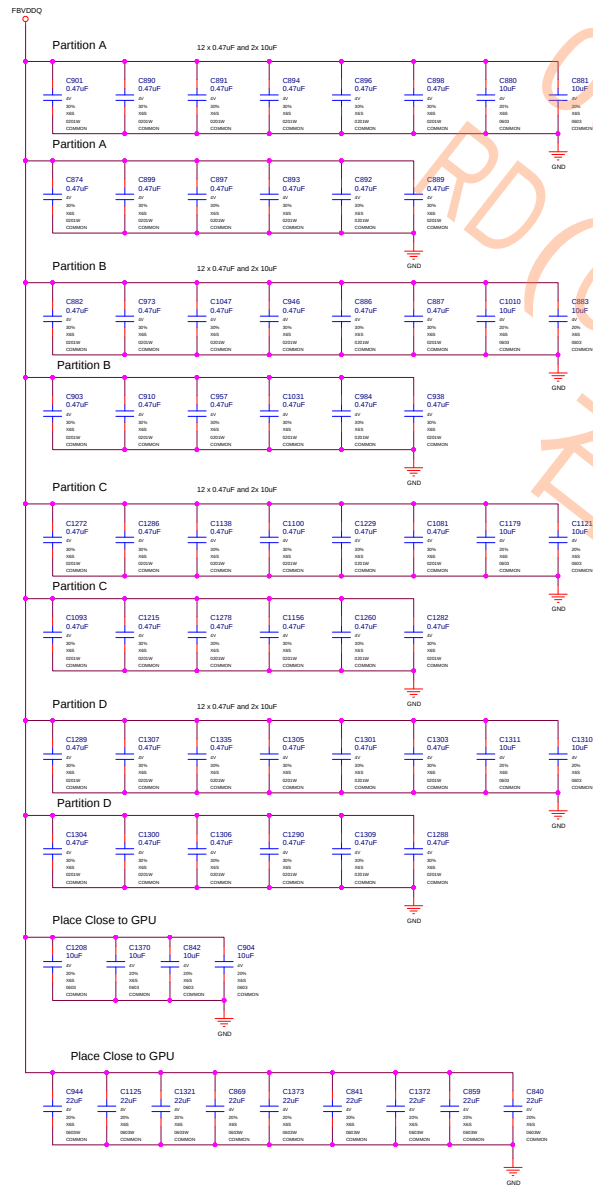
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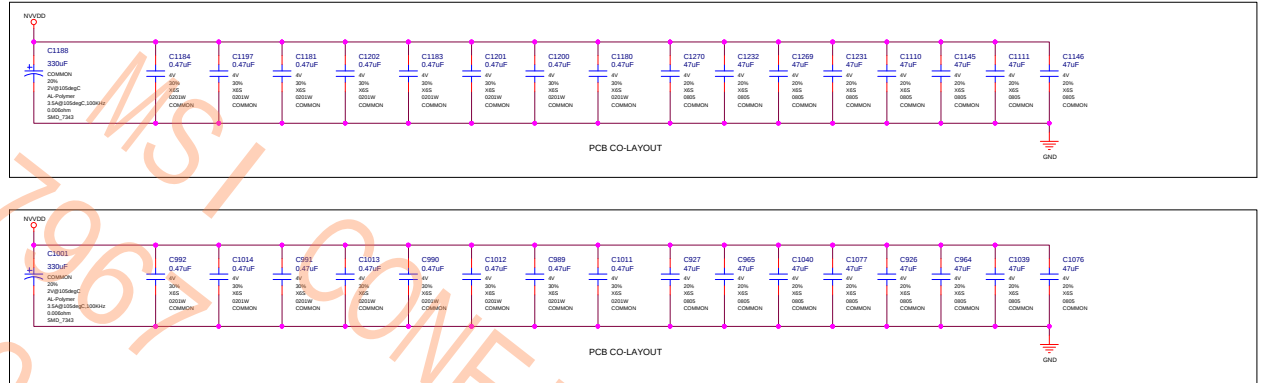
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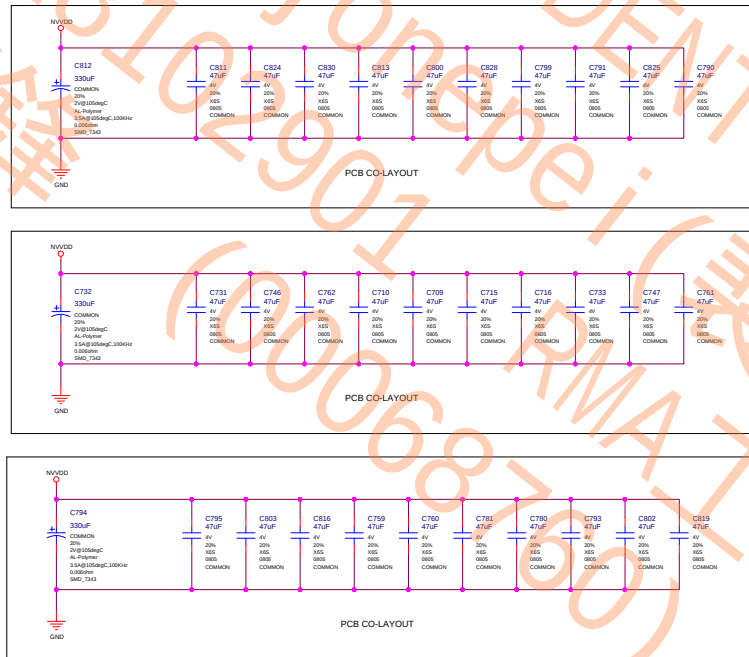
FBVDDQ



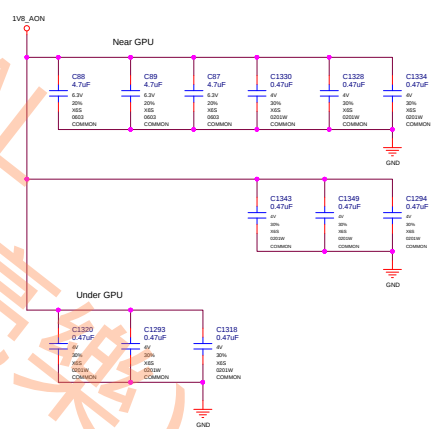
Under GPU



Near GPU



1V8_AON



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ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	GPU Decoupling

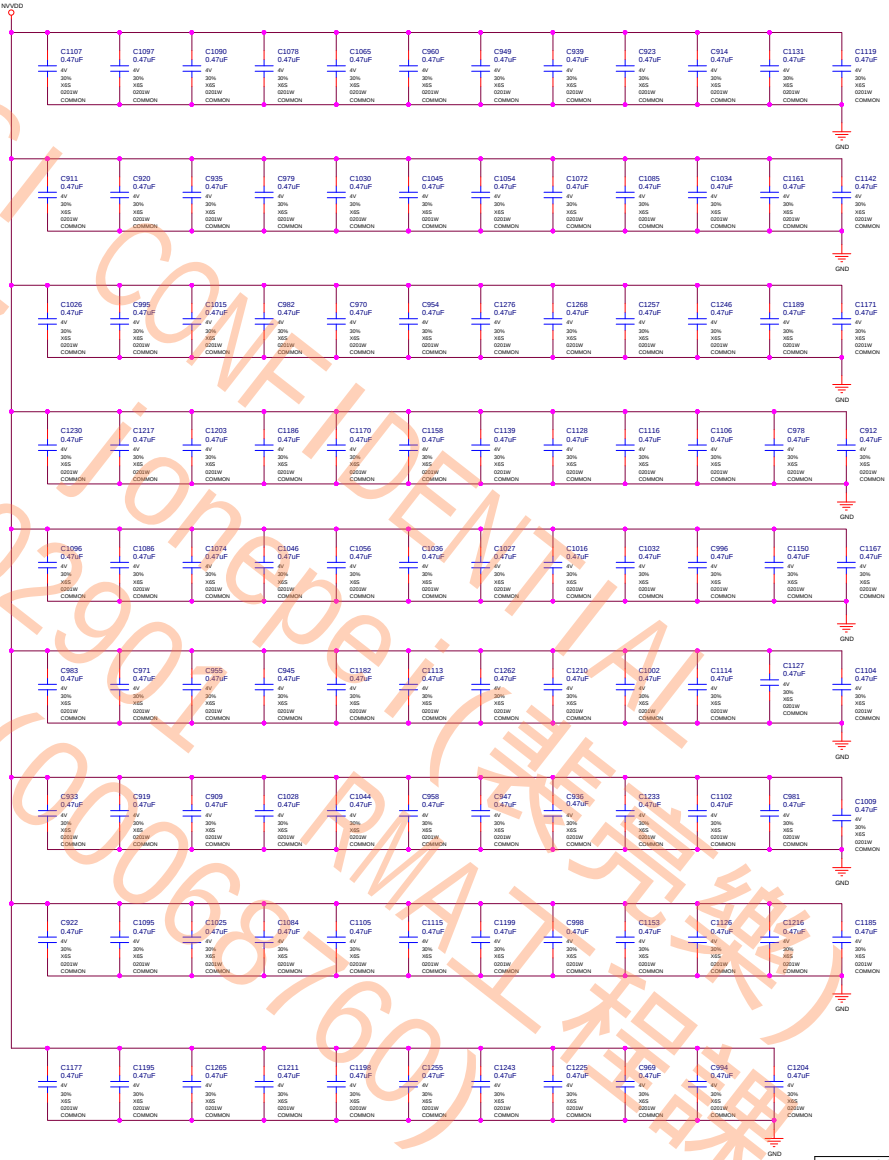
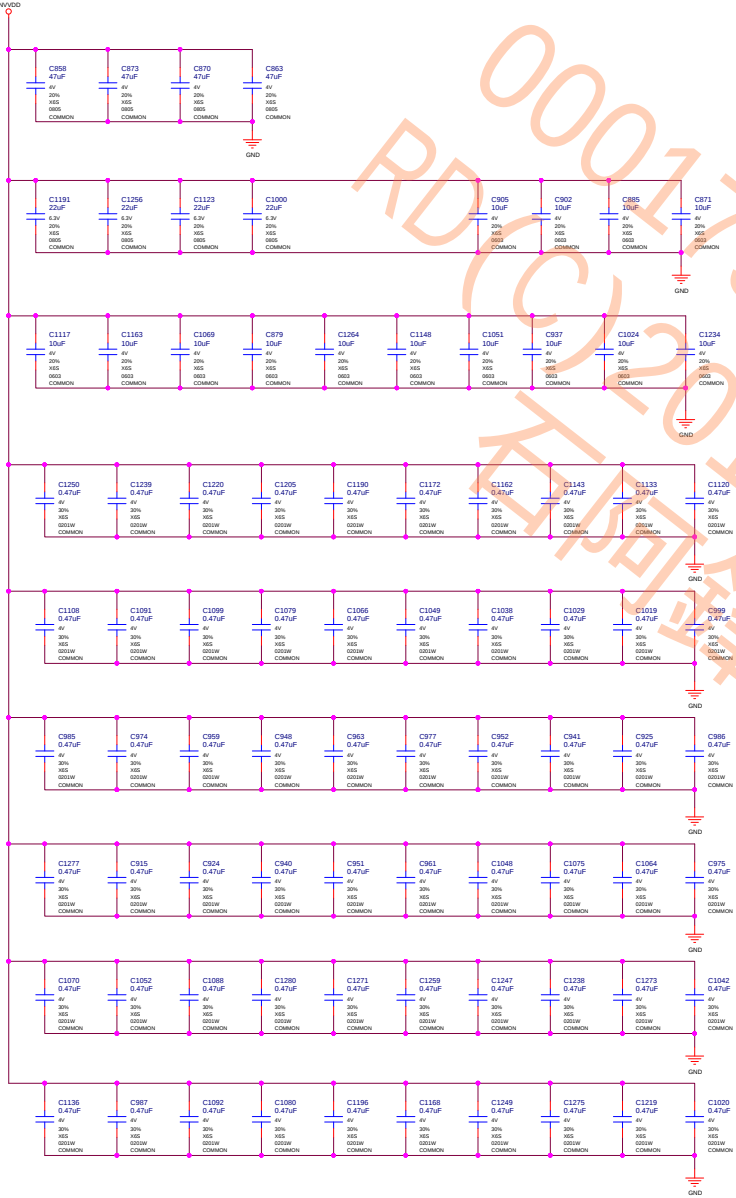
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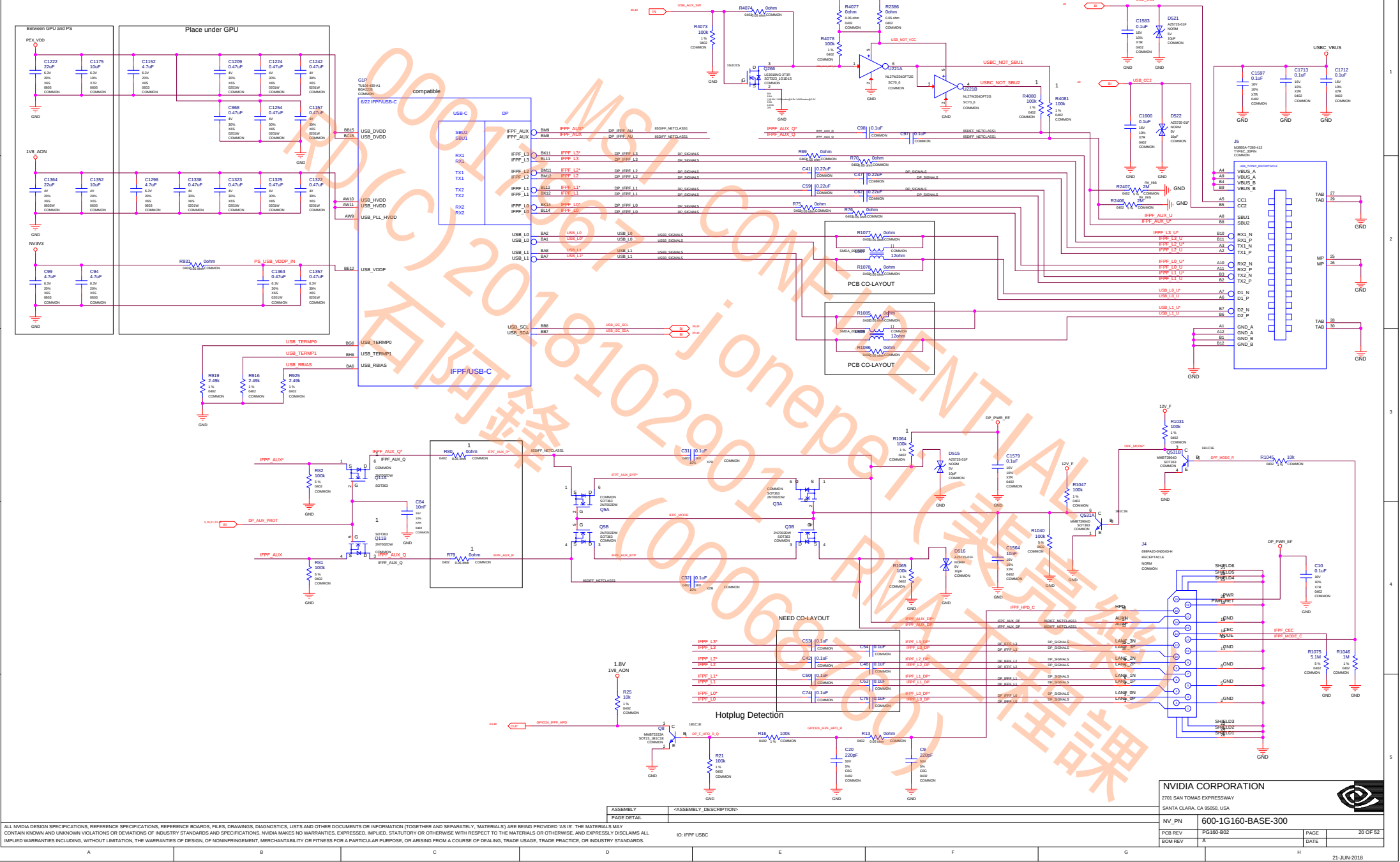


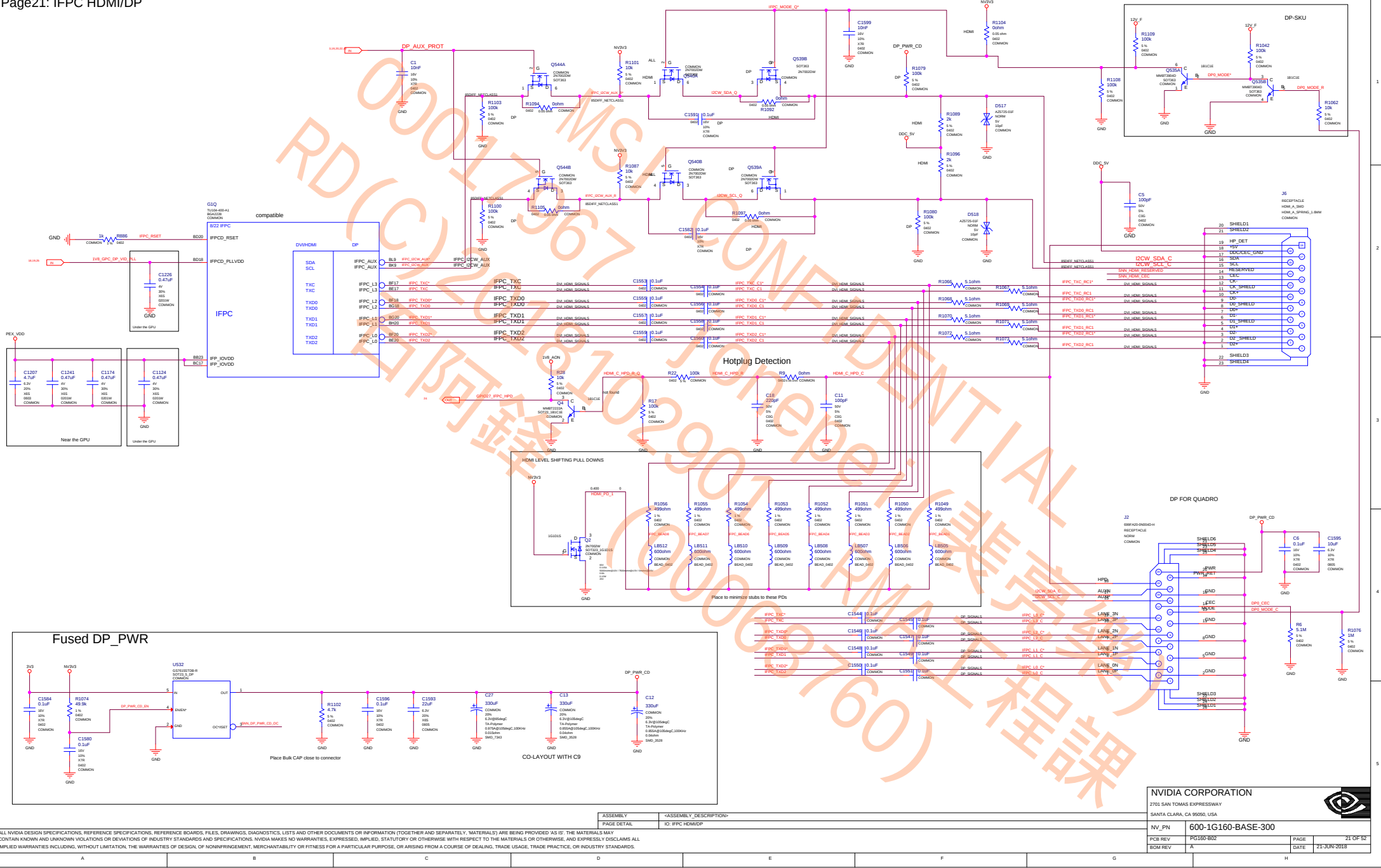
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NVDD



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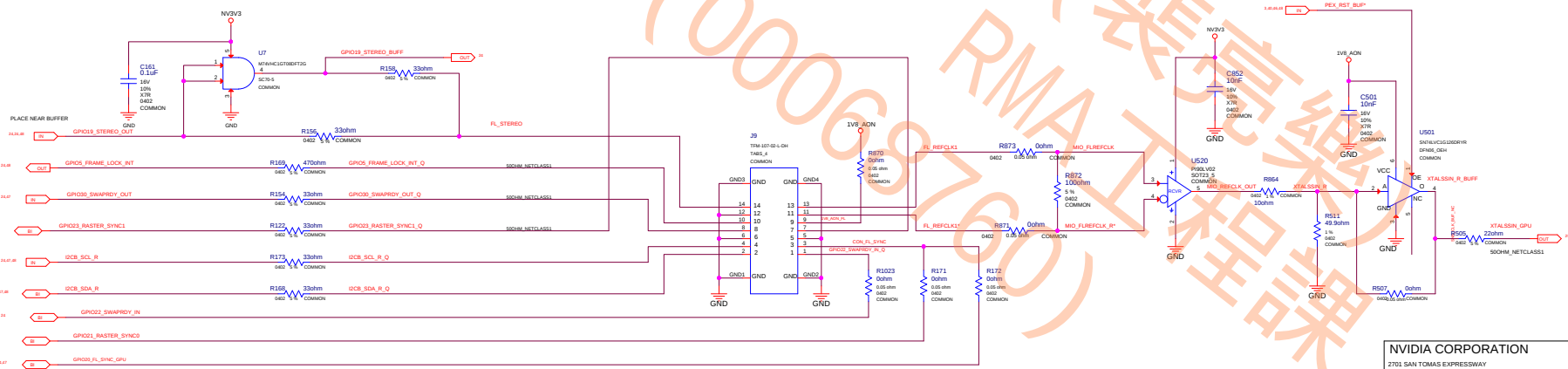




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PAGE DETAIL	ID: IFPC HDMI/DP

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IO: NVHS Interface and Frame Lock

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NV_PN	600-1G160-BASE-300
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PCB REV	PG160-B02
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STRAP2	STRAP1	STRAP0	RAMCFG[4:0]
L	L	L	00000
L	L	H	00001
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111
L	L	M	01000

RAMCFG[4:0]	DENSITY	WIDTH	VENDOR	DIE
00000	8Gb	256-bit	Samsung	C
00001	8Gb	256-bit	Micron	A
00010	8Gb	256-bit	Hynix	M
00011	8Gb	256-bit	Samsung	C
00100	8Gb	256-bit	Micron	A
00101	8Gb	256-bit	Hynix	M
00110	16Gb	256-bit	Samsung	M
00111				

ROM_SO	ROM_SI	ROM_SCLK	DUMMY[2:0].FS_OVERT	1:ENABLE 0:DISABLE	
L	L	L	XXX1	FS_OVERT ENABLE	DEFAULT
L	L	M	XXX0	FS_OVERT DISABLE	

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
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L	L	L	0	0	0	0

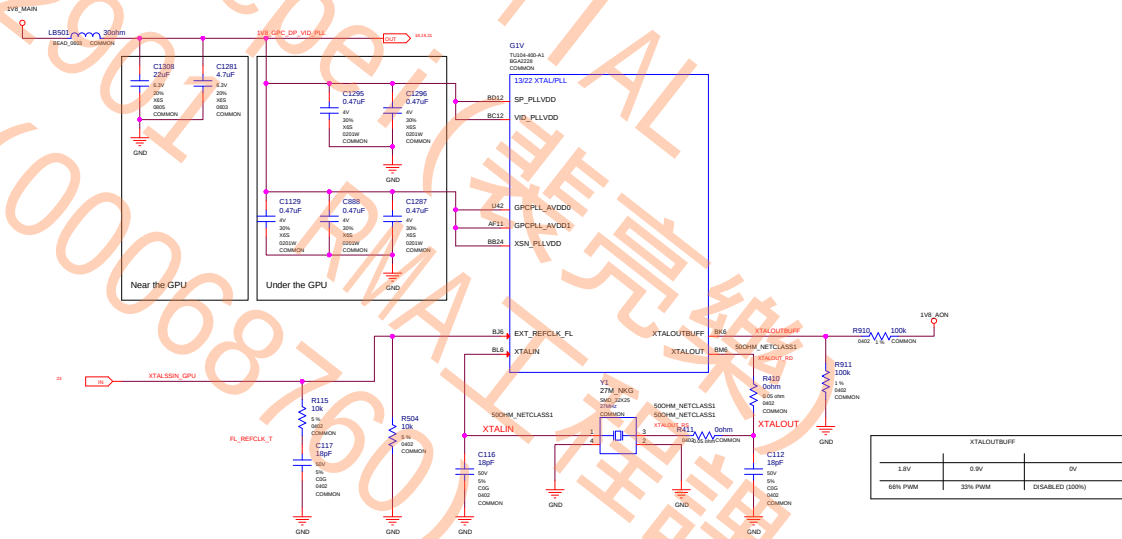
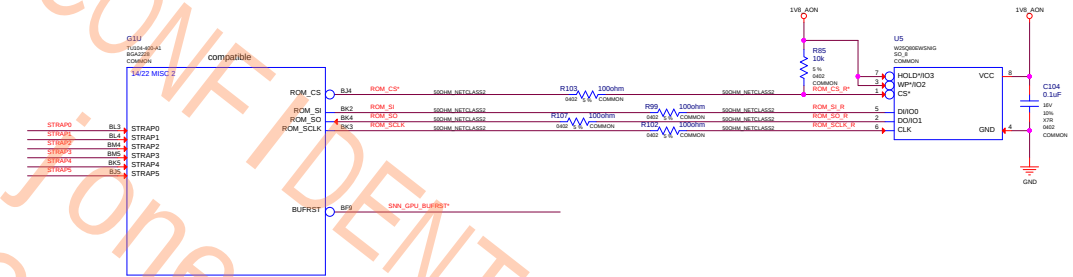
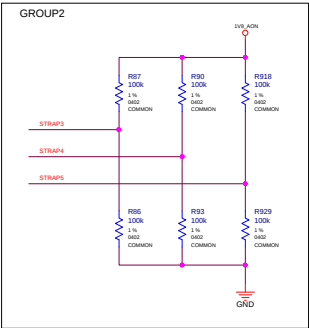
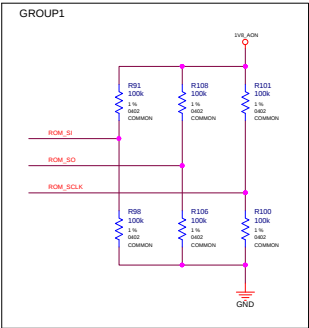
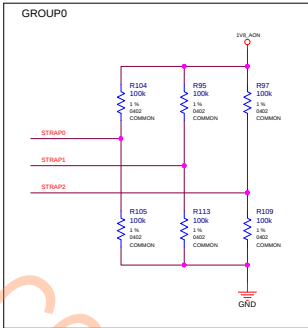
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M=Middle:Tied to 0.9V
L=Low :Tied to 0V

1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL

1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE



XTALOUTBUFF		
1.8V	0.9V	0V
66% PWM	33% PWM	DISABLED (00%)

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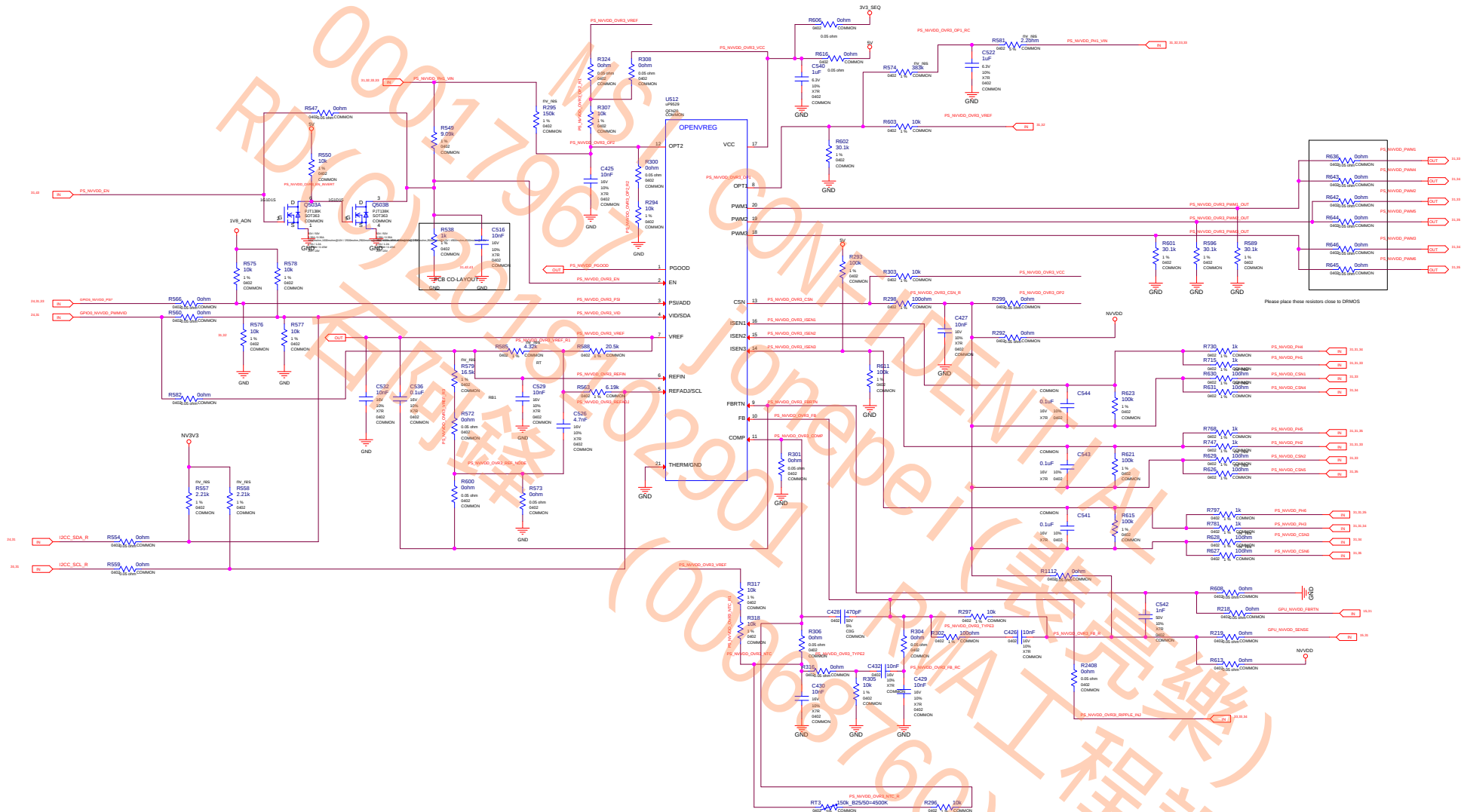
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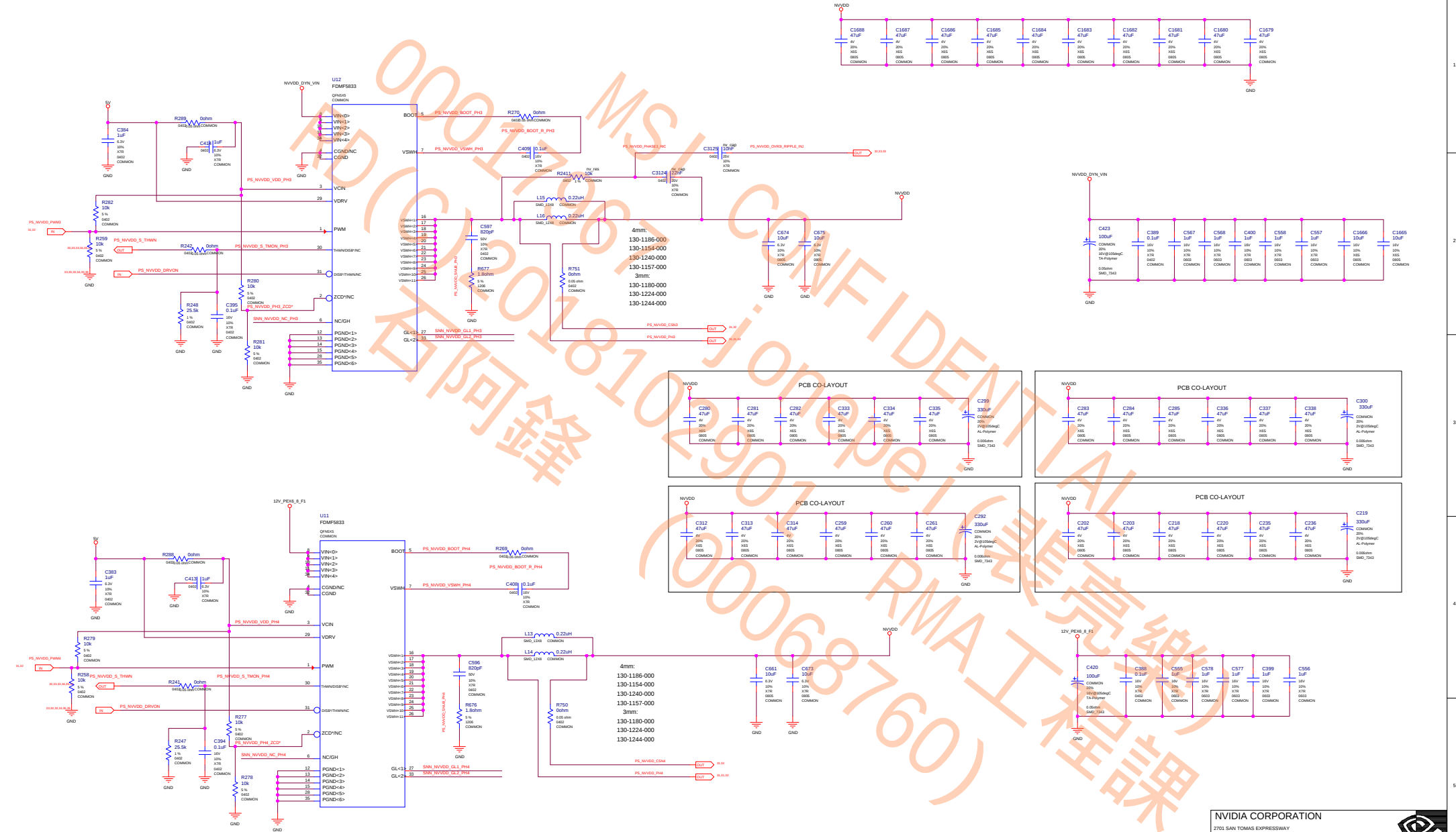
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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: 1V8_PLL, 1V8_AON

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PAGE DETAIL		PS: NVVDD PH3&4	

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PCB REV

PG160-802

DATE

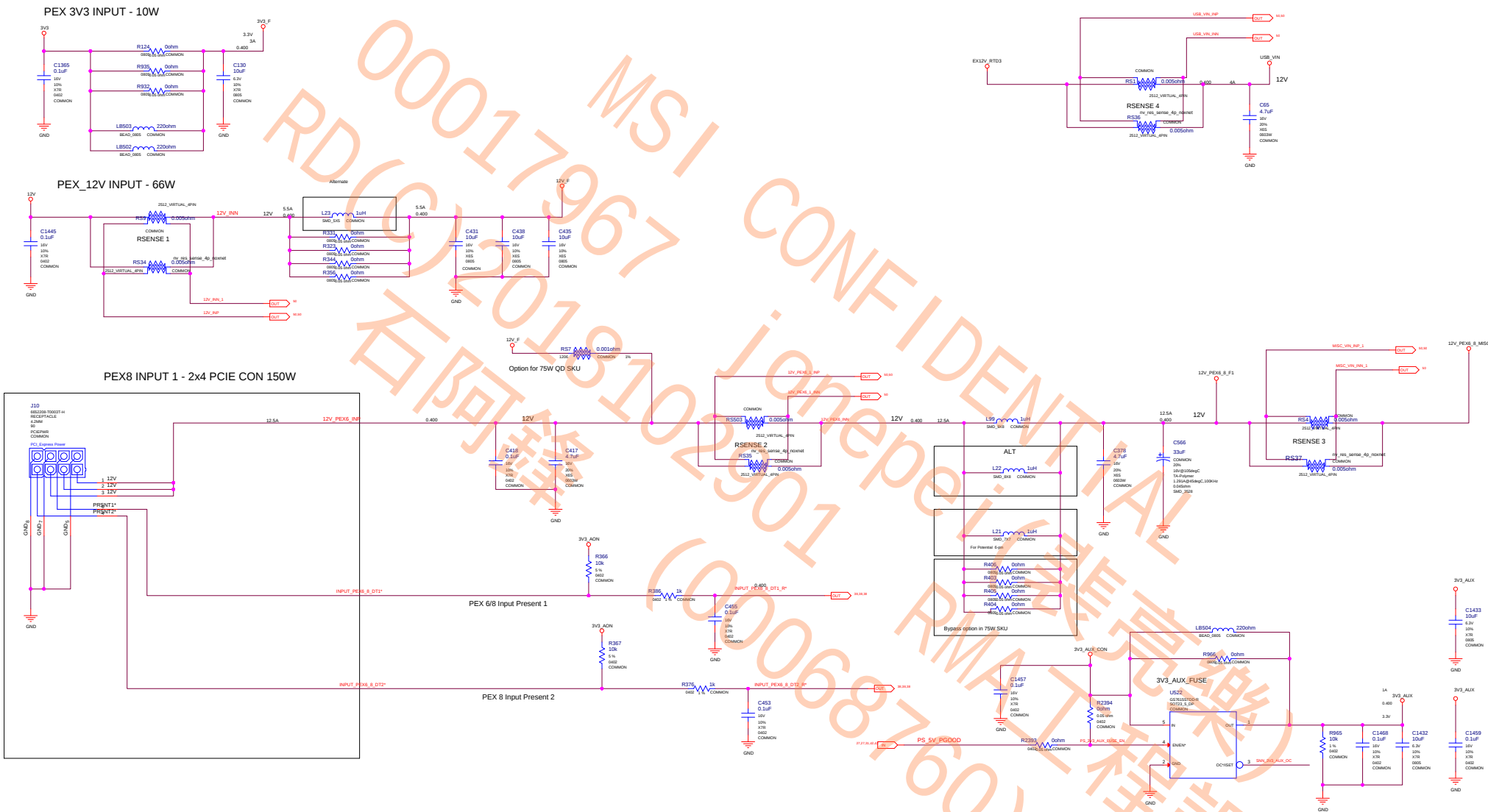
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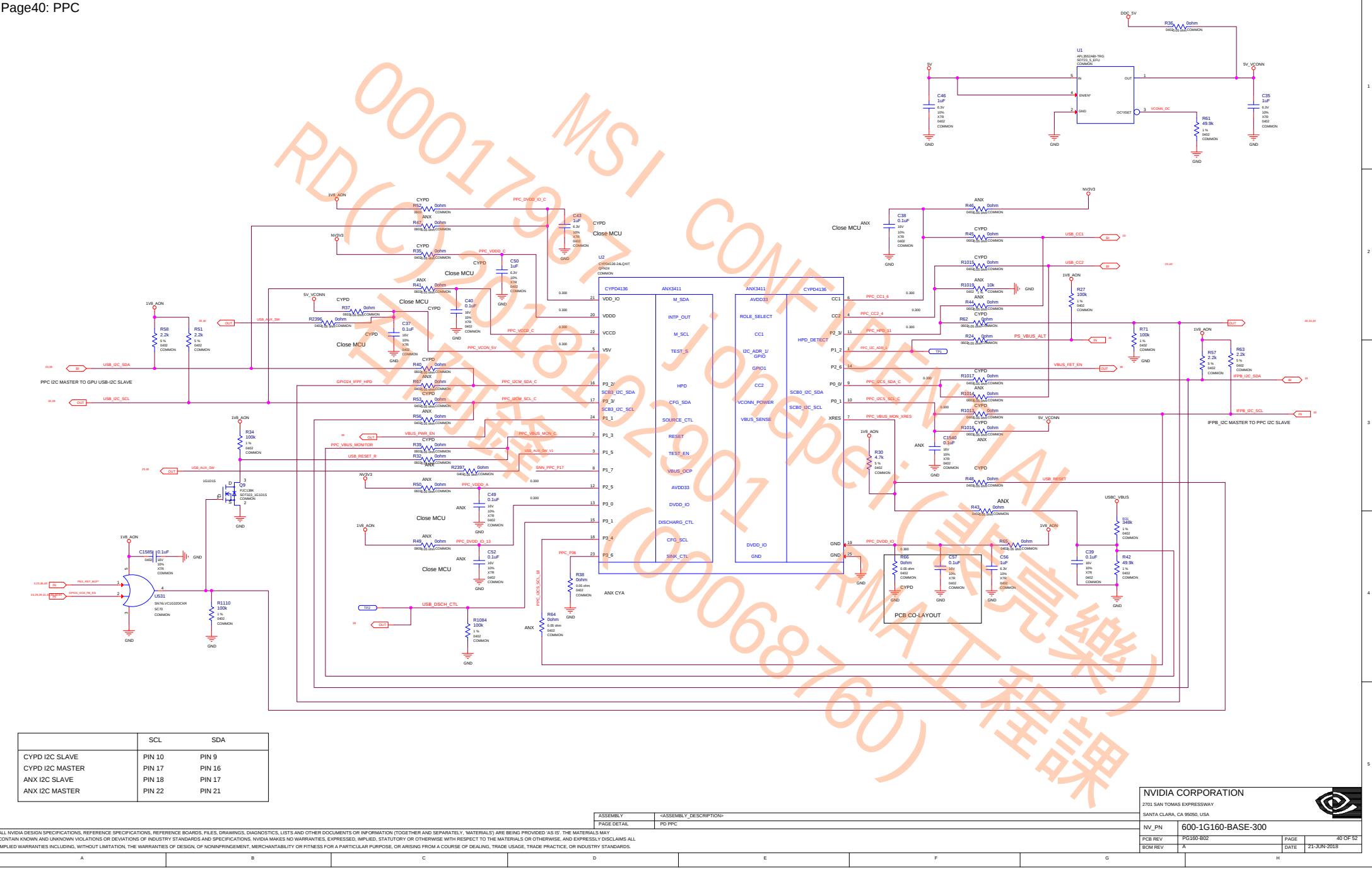
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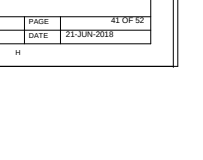
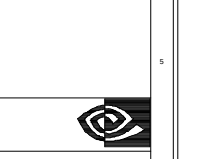
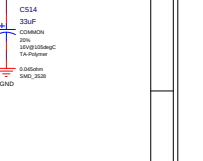
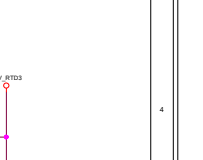
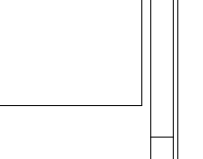
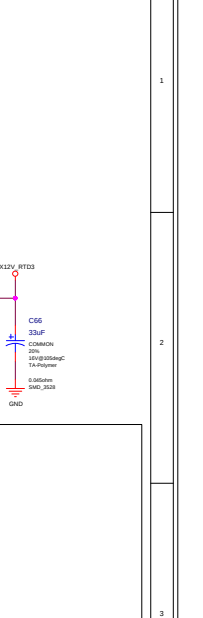
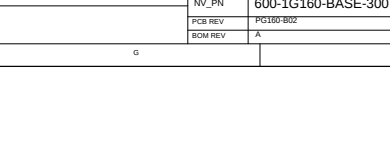
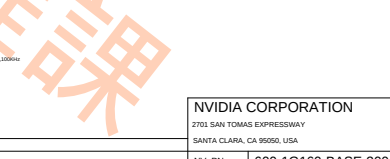
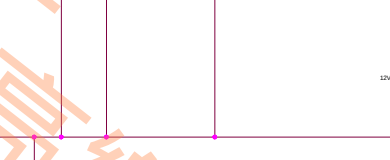
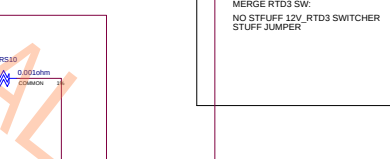
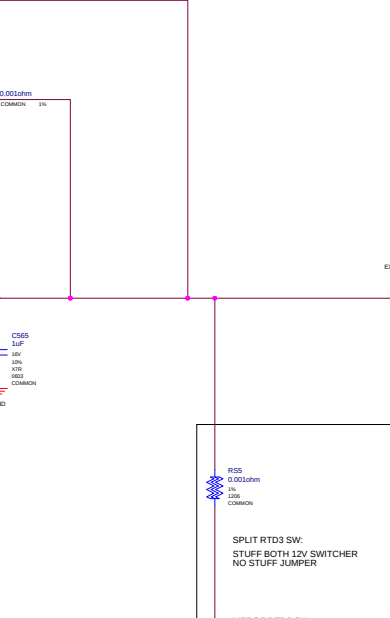
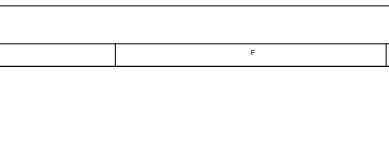
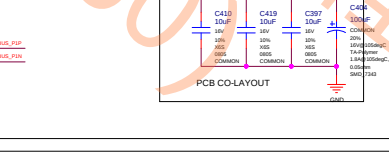
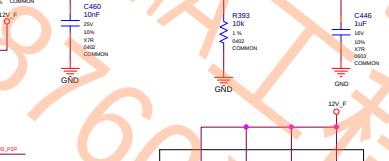
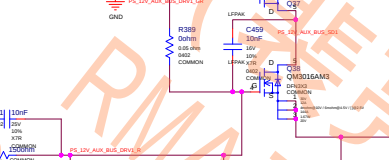
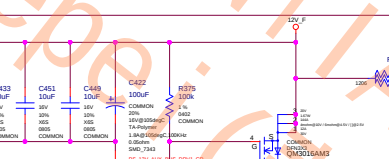
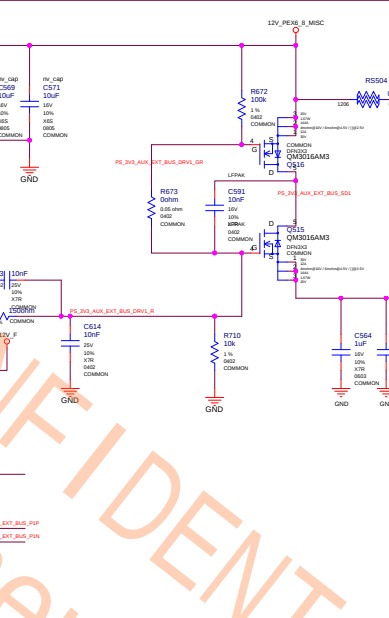
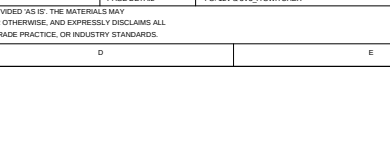
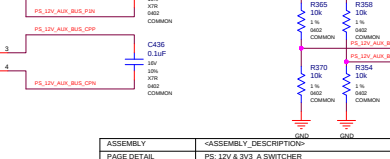
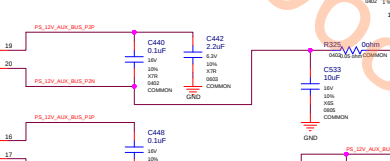
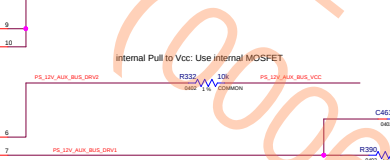
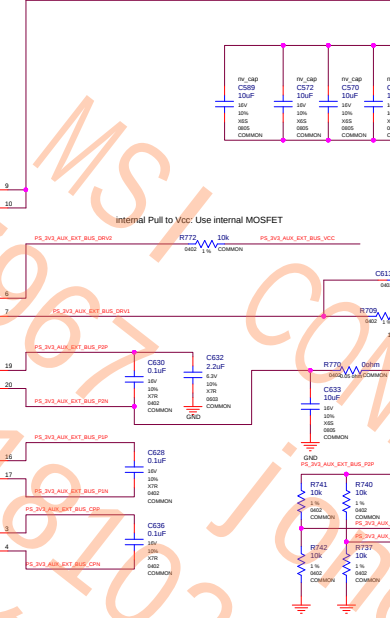
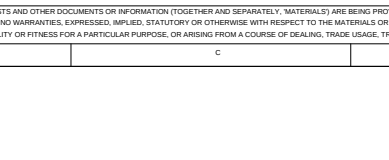
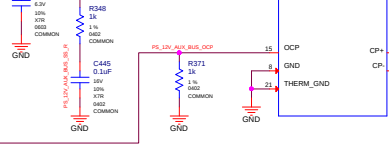
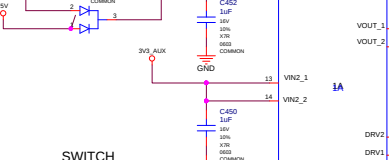
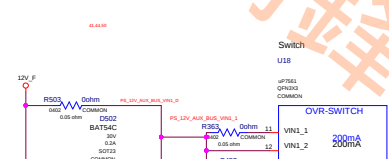
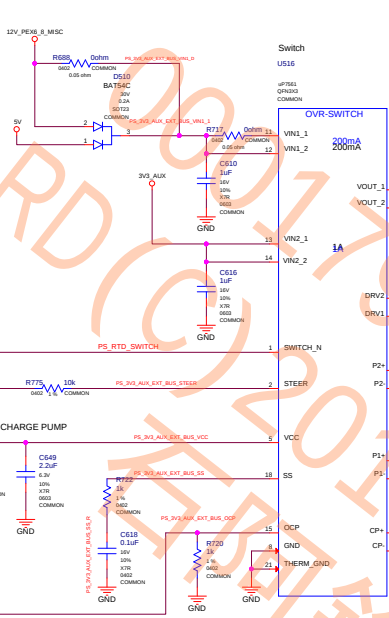
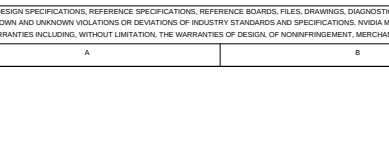
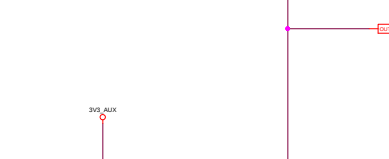
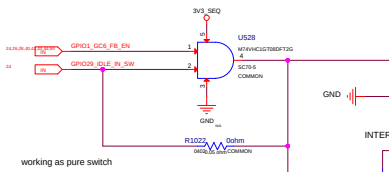
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SWITCH		VOUT	
0		VOUT = VIN1	
1		VOUT = VIN2	

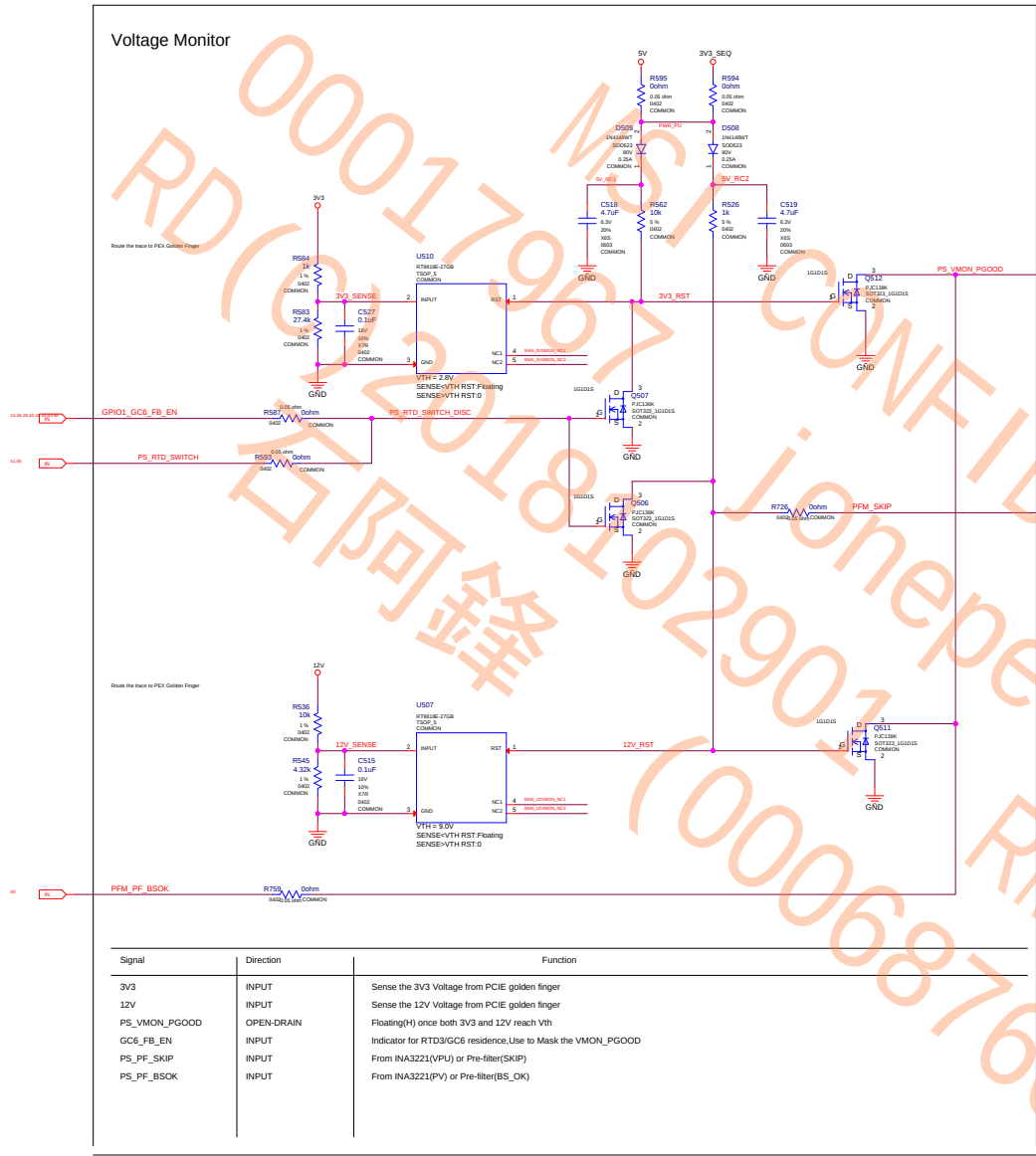
AND GATE LOGIC			
GPIO1	GPIO29	SWITCH	VOUT
0	0	0	12V
0	1	0	12V
1	0	0	12V
1	1	1	3V3A



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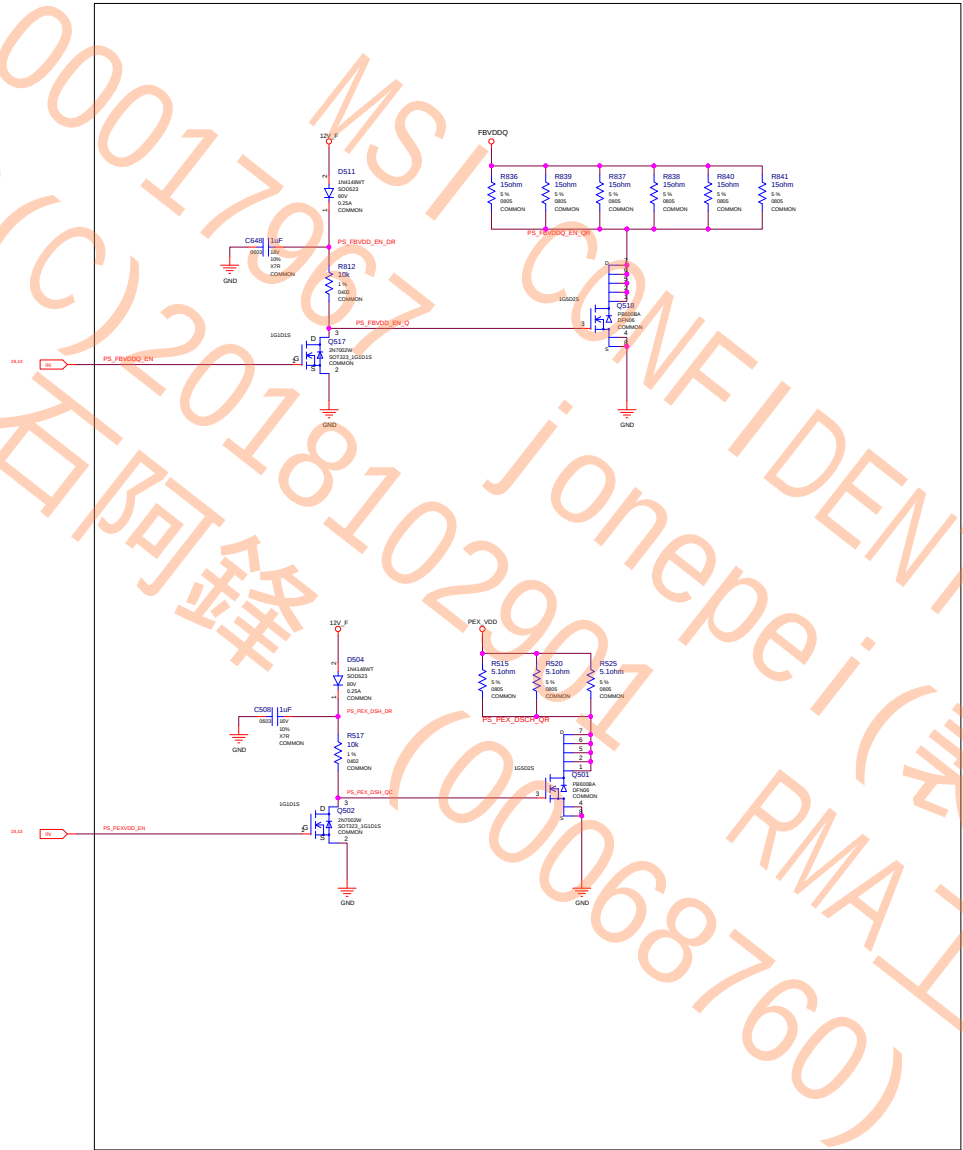
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BOM Configuration

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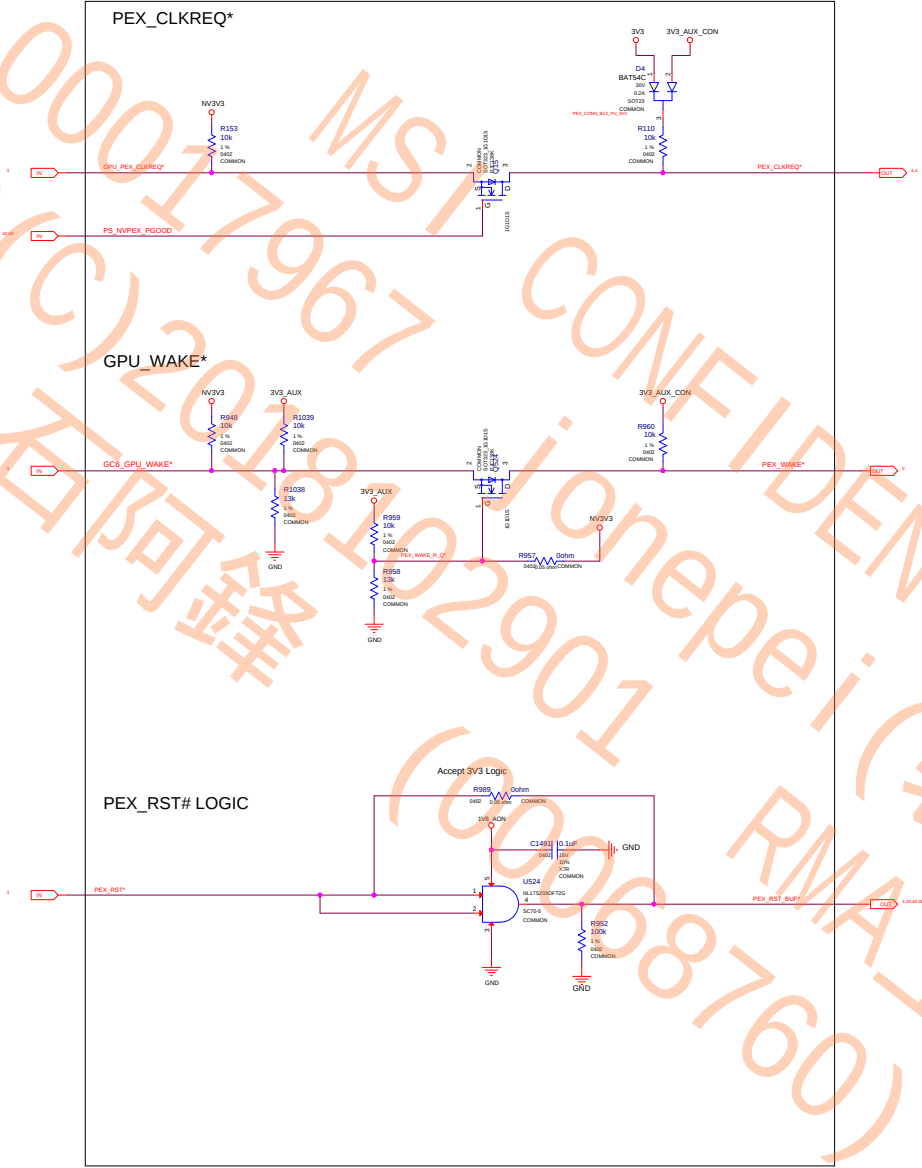


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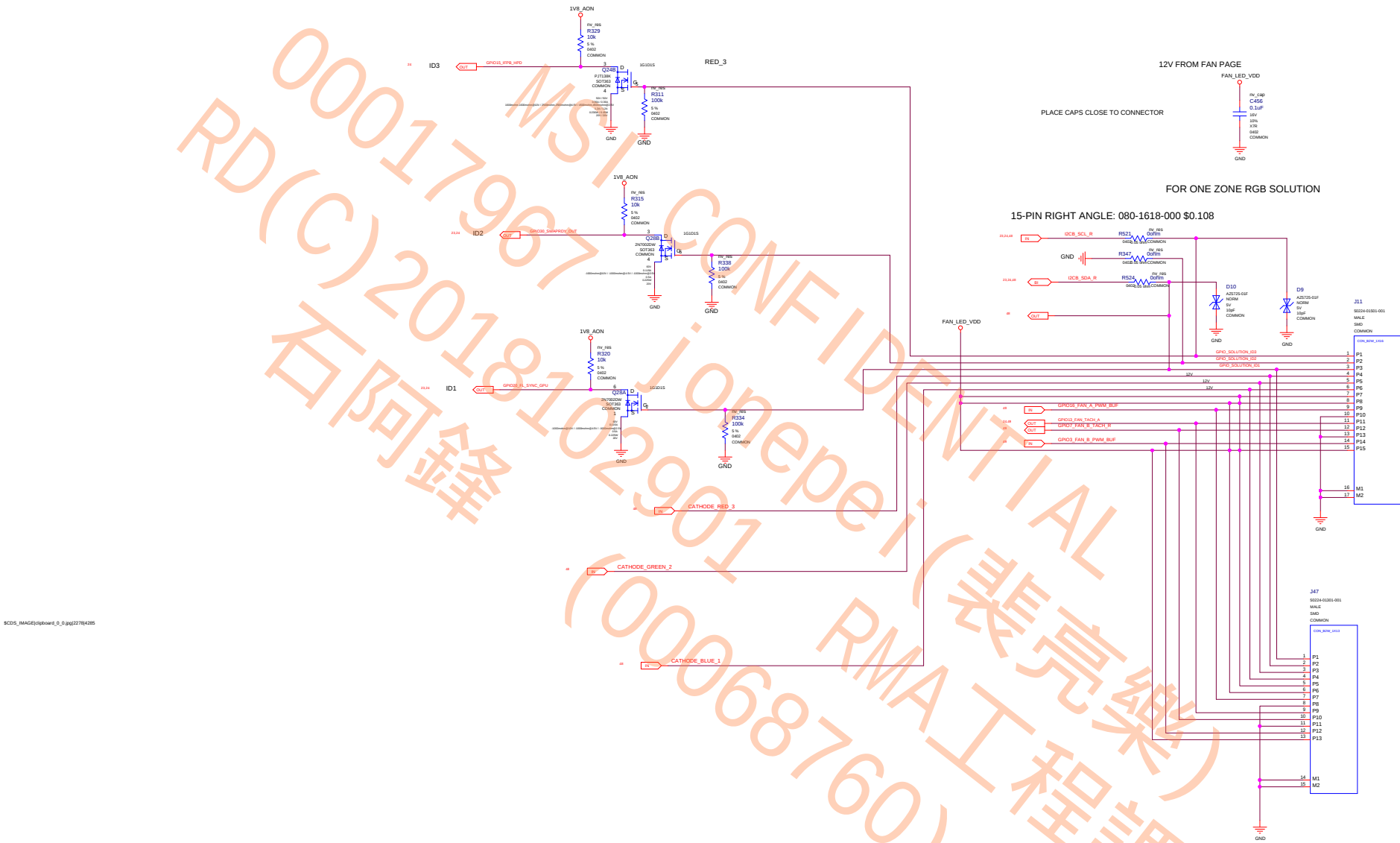


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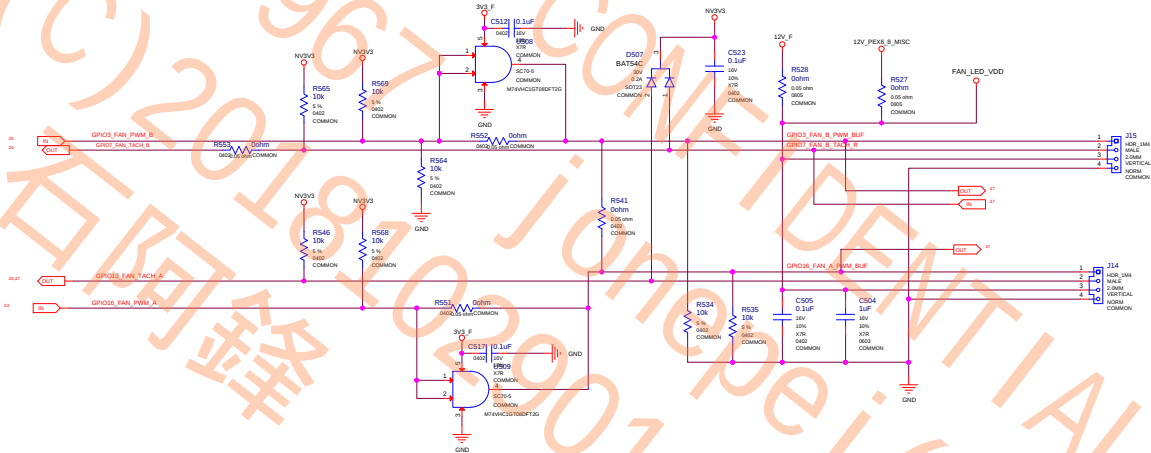
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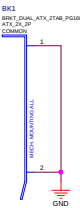
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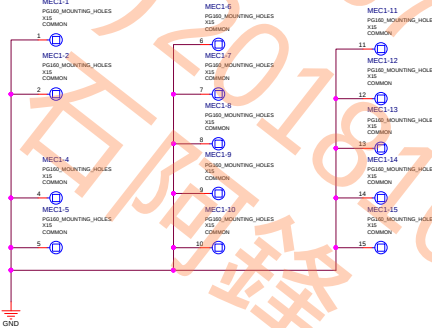
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Brackets:



MOUNTING HOLES FOR HS:



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
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