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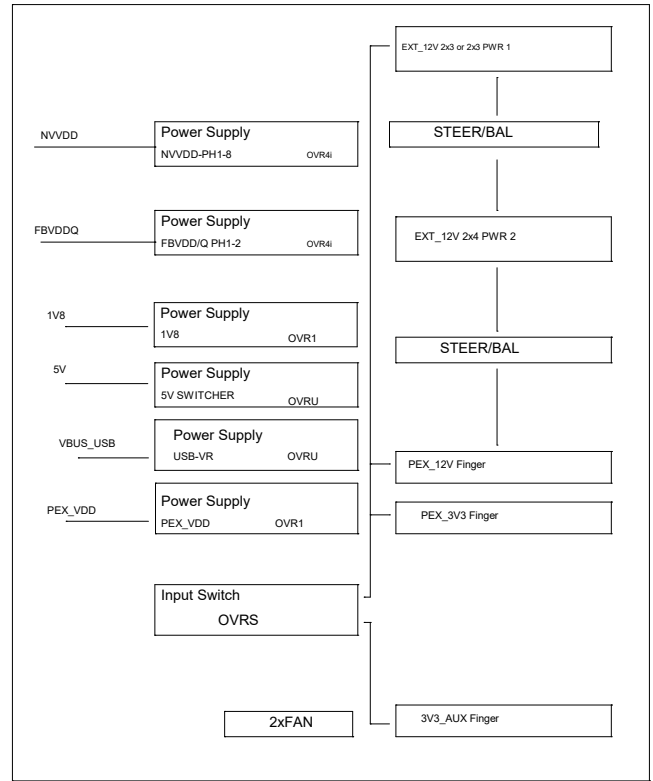
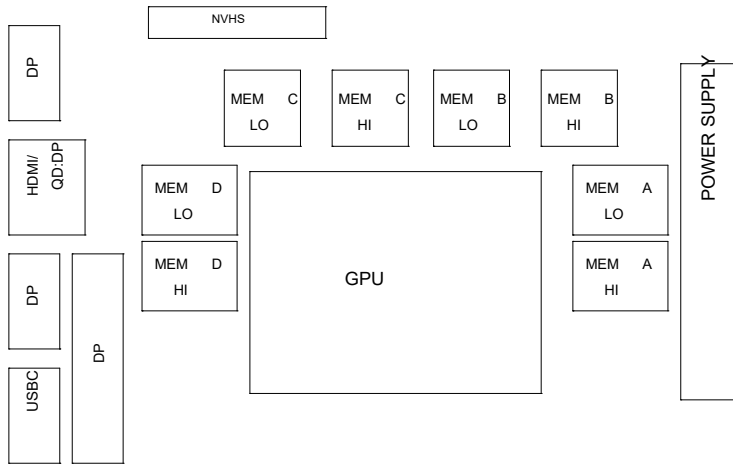
MS-V372

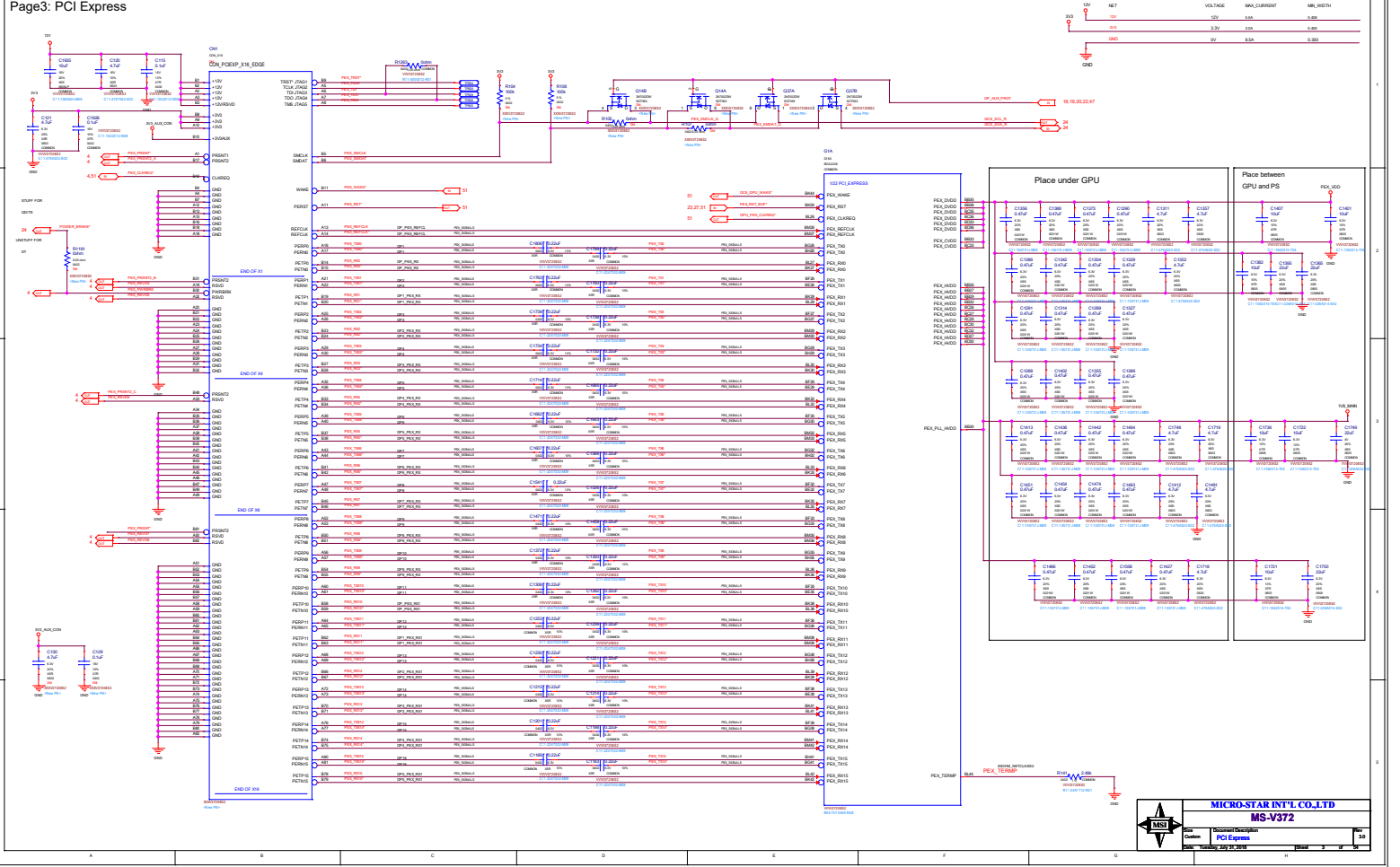
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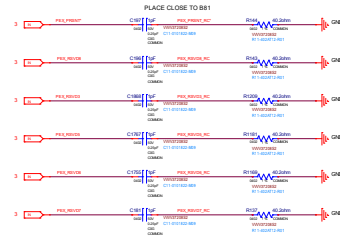
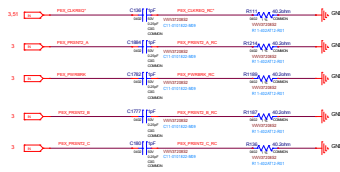
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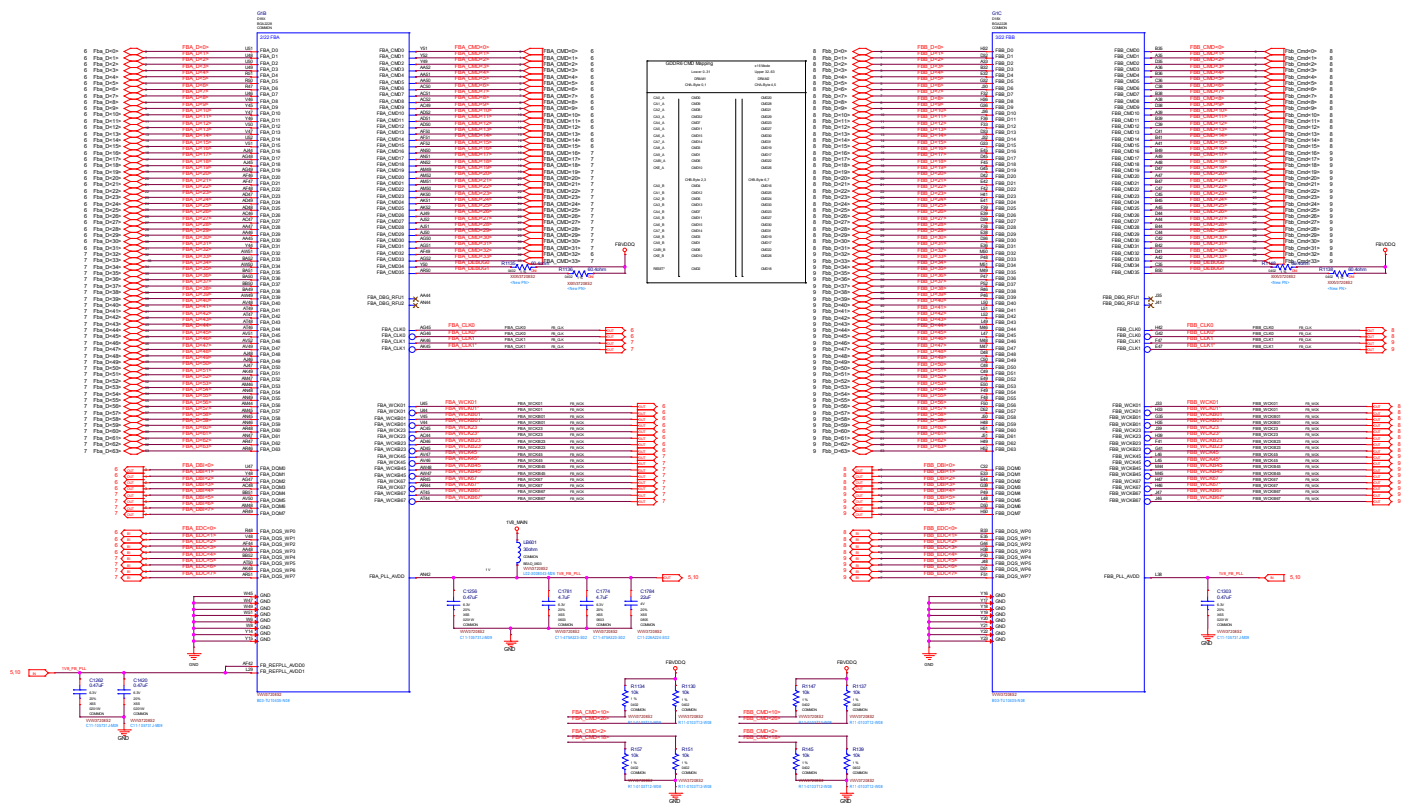
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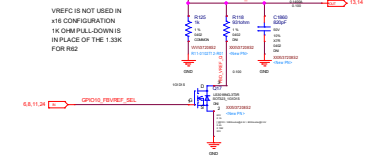
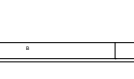
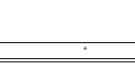
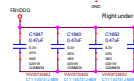
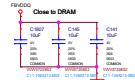
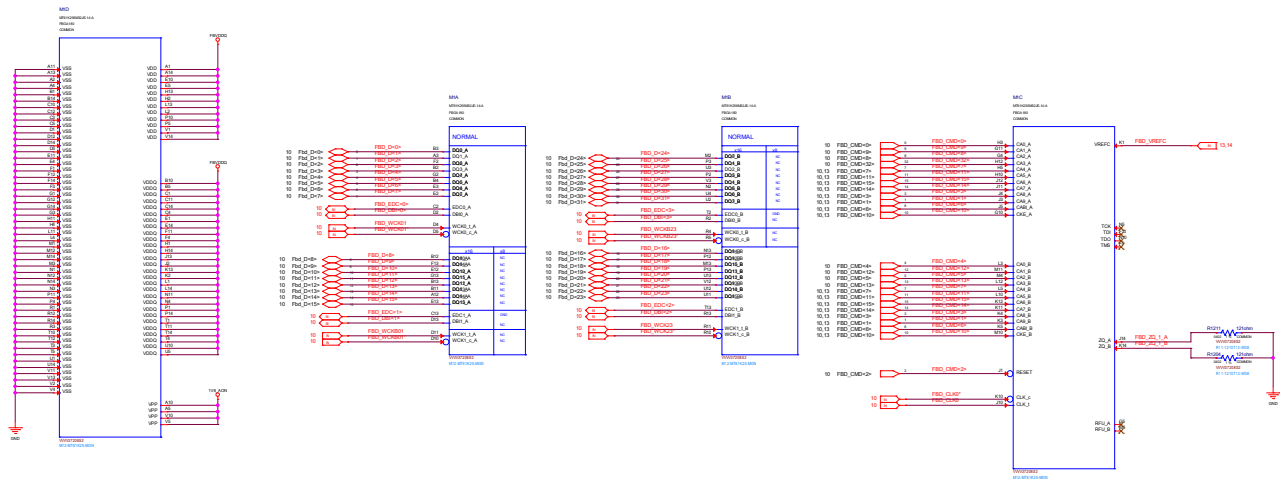
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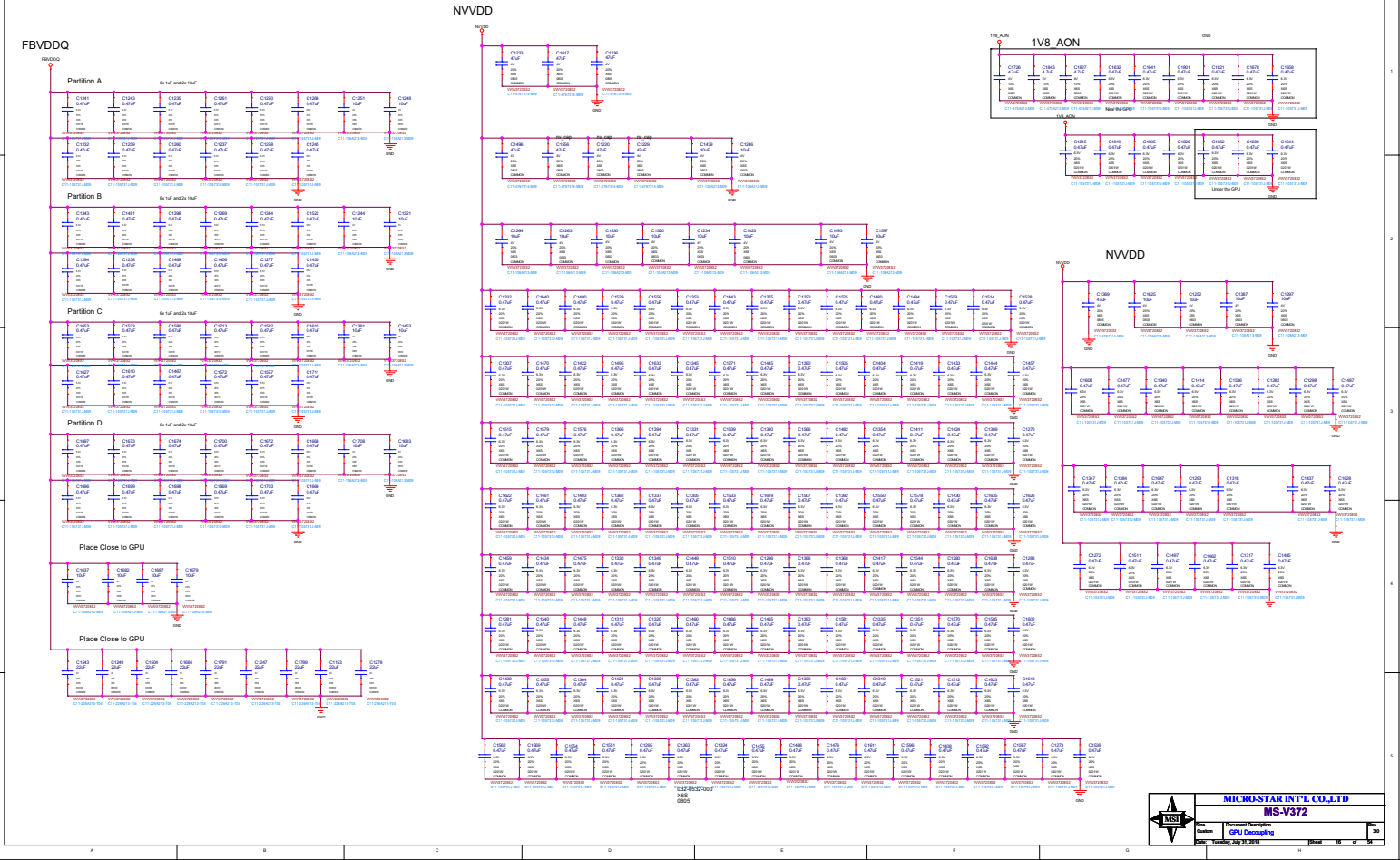


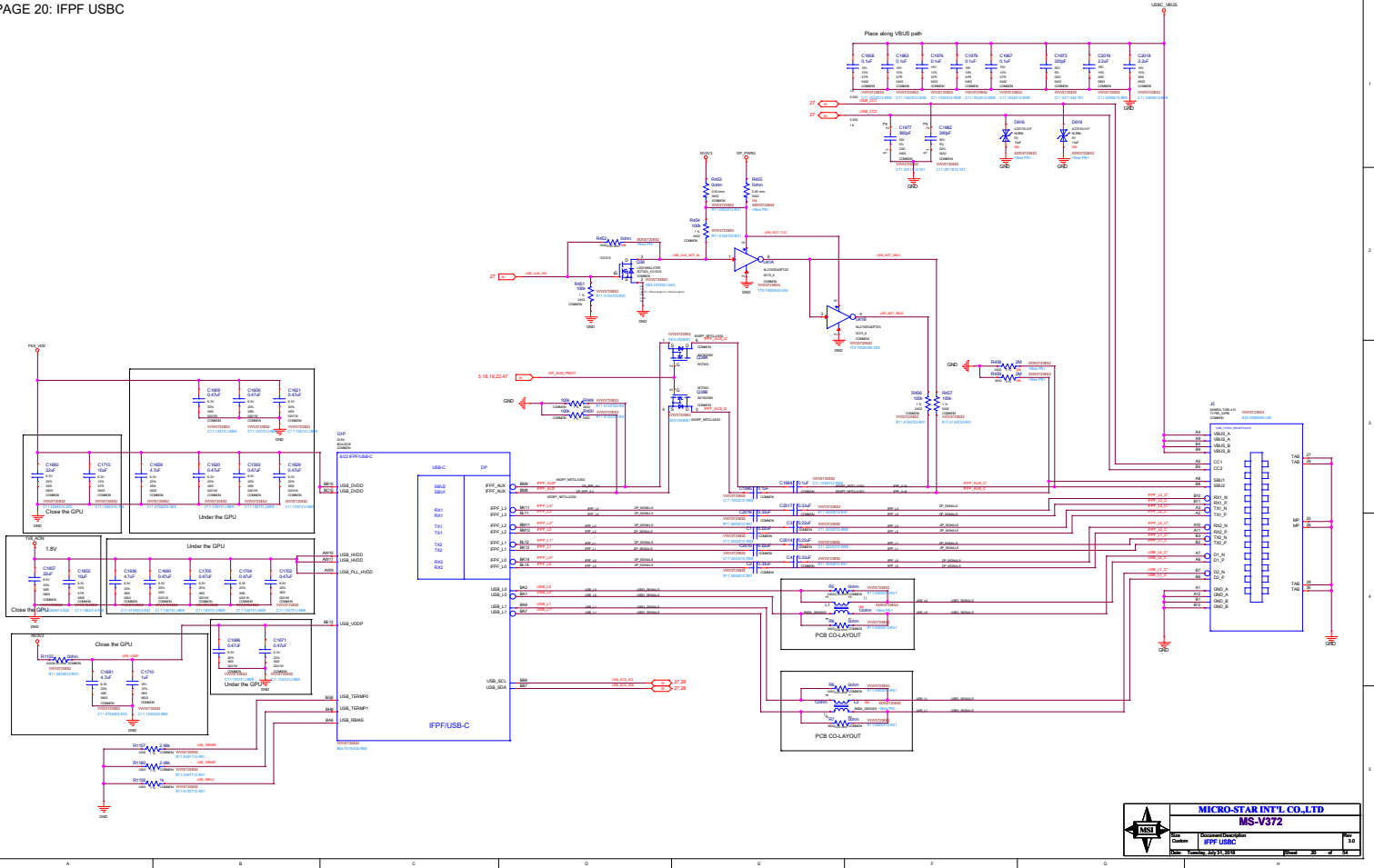




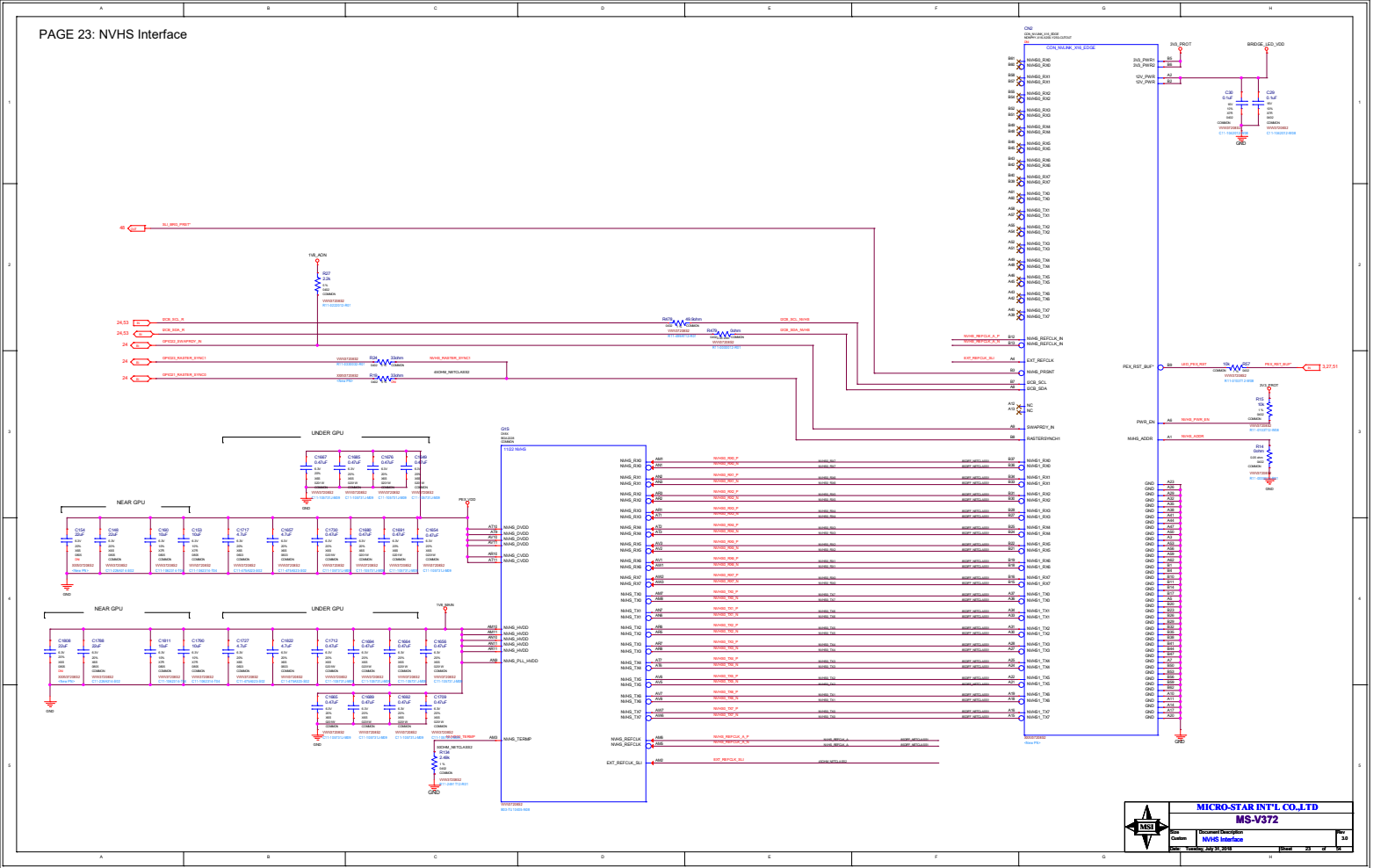












H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

STRAP2	STRAP1	STRAP0	RAMCFG[4:0]	
L	L	L	00000	RAMCFG TBD
L	L	H	00001	RAMCFG TBD
L	H	L	00010	RAMCFG TBD
L	H	H	00011	RAMCFG TBD
H	H	L	00110	RAMCFG TBD
H	H	H	00111	RAMCFG TBD

DEFAULT

ROM_SO	ROM_SI	ROM_SCLK	DUMMY[2:0]FS_OVERT	1:ENABLE 0:DISABLE
L	L	L	XXX1	FS_OVERT ENABLE
L	L	M	XXX0	FS_OVERT DISABLE

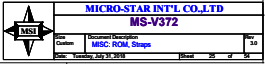
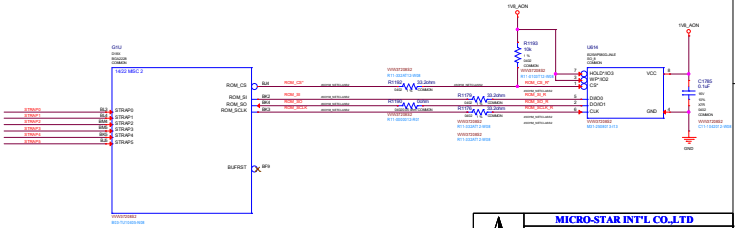
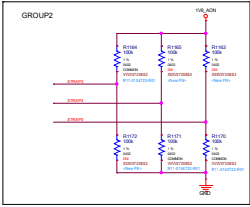
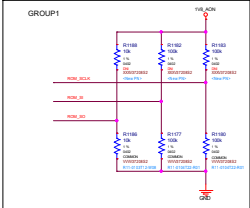
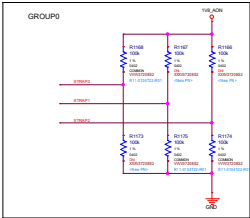
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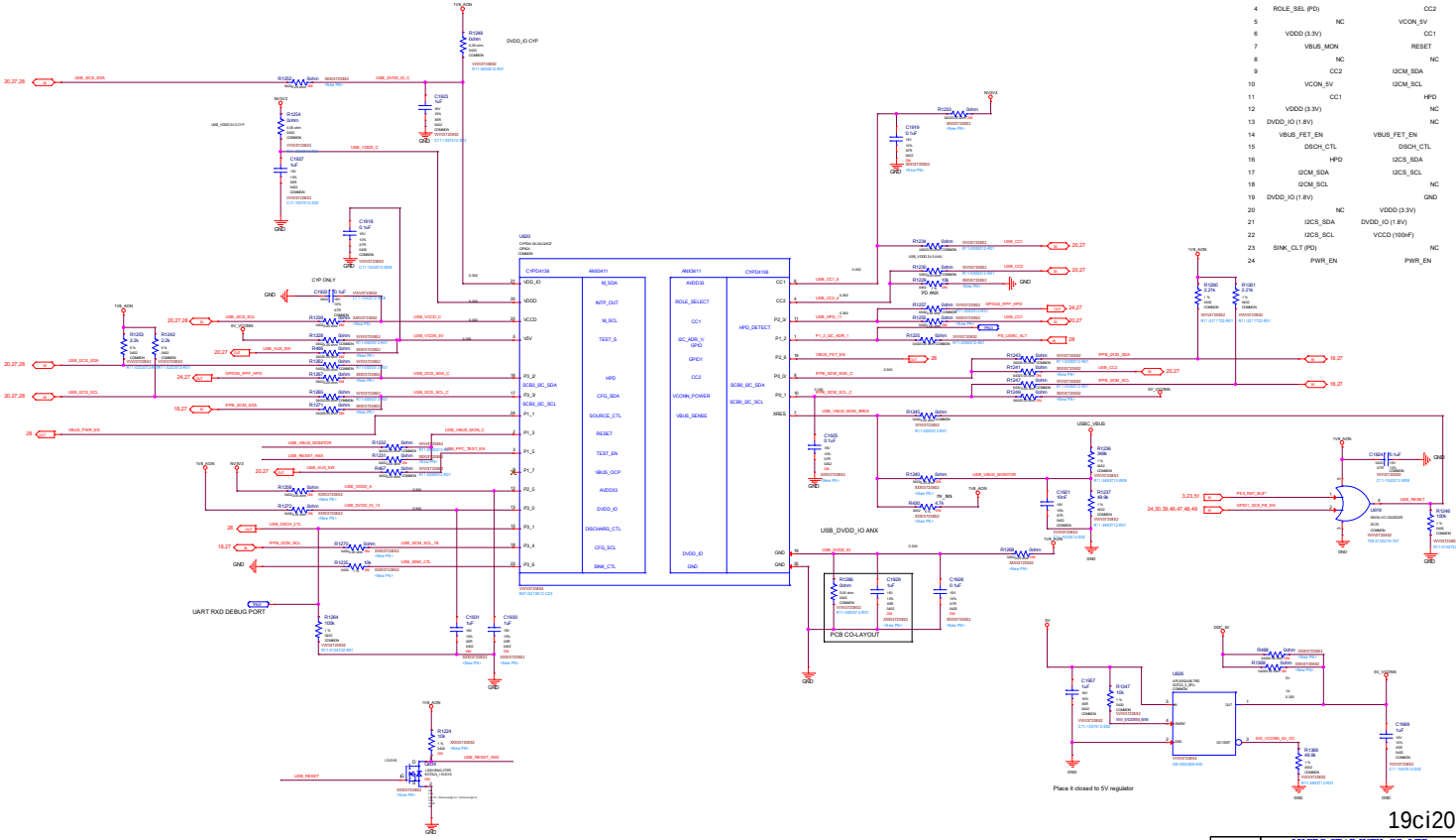
STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1
L	L	L	0	0	0	0

1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE
1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL
1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER
1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE

Default

RAMCFG[4:0]	DENSITY	WIDTH	VENDOR
00000	8Gb	256-bit	Samsung
00001	8Gb	256-bit	Micron
00010	8Gb	256-bit	Hynix
00110	16Gb	256-bit	Samsung
00111	16Gb	256-bit	Samsung



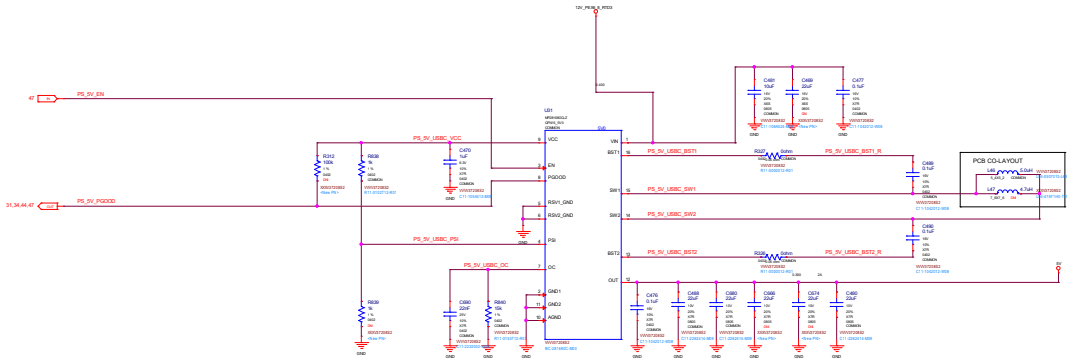


PIN	ANX	CYP
1	TP	TP
2	RESET	VBUS_MON
3	NC	NC
4	ROLE_SEL_P0	CC2
5	NC	VCON_SV
6	VDDD (3.3V)	CC1
7	VBUS_MON	RESET
8	NC	NC
9	CC2	DCM_SDA
10	VCON_SV	DCM_SCL
11	CC1	HPD
12	VDDD (3.3V)	NC
13	DVDD_IO (1.8V)	NC
14	VBUS_FET_EN	VBUS_FET_EN
15	DISCH_CTL	DISCH_CTL
16	HPD	DCS_SDA
17	DCM_SDA	DCS_SCL
18	DCM_SCL	NC
19	DVDD_IO (1.8V)	GND
20	NC	VDDD (3.3V)
21	DCS_SDA	DVDD_IO (1.8V)
22	DCS_SCL	VDDD (100mV)
23	SINK_CTL_P0	NC
24	PWR_EN	PWR_EN

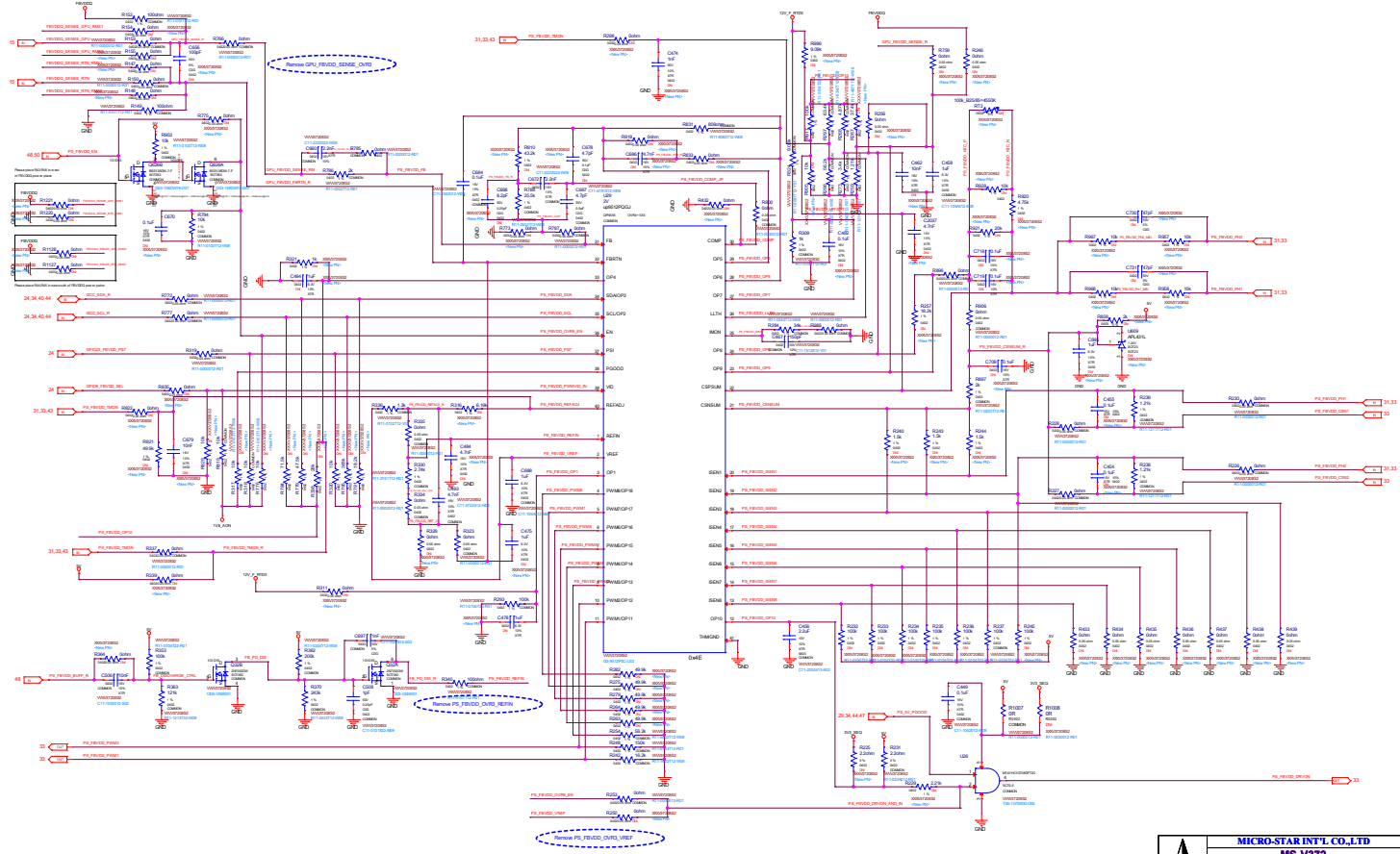
MICRO-STAR INT'L CO., LTD
MS-V372
Revision: 1.0
Date: 2010.05.10
Drawing: 2010.05.10

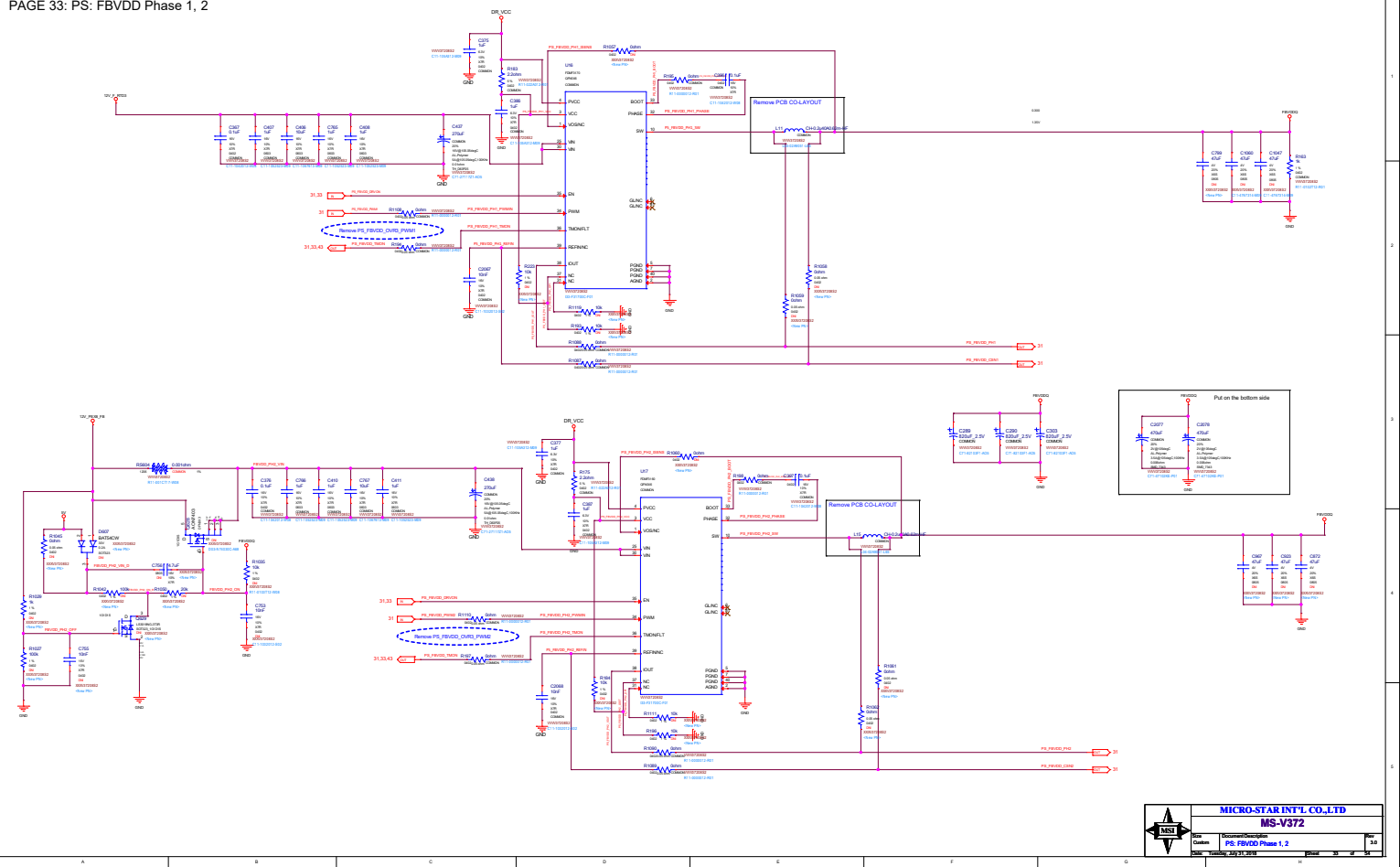
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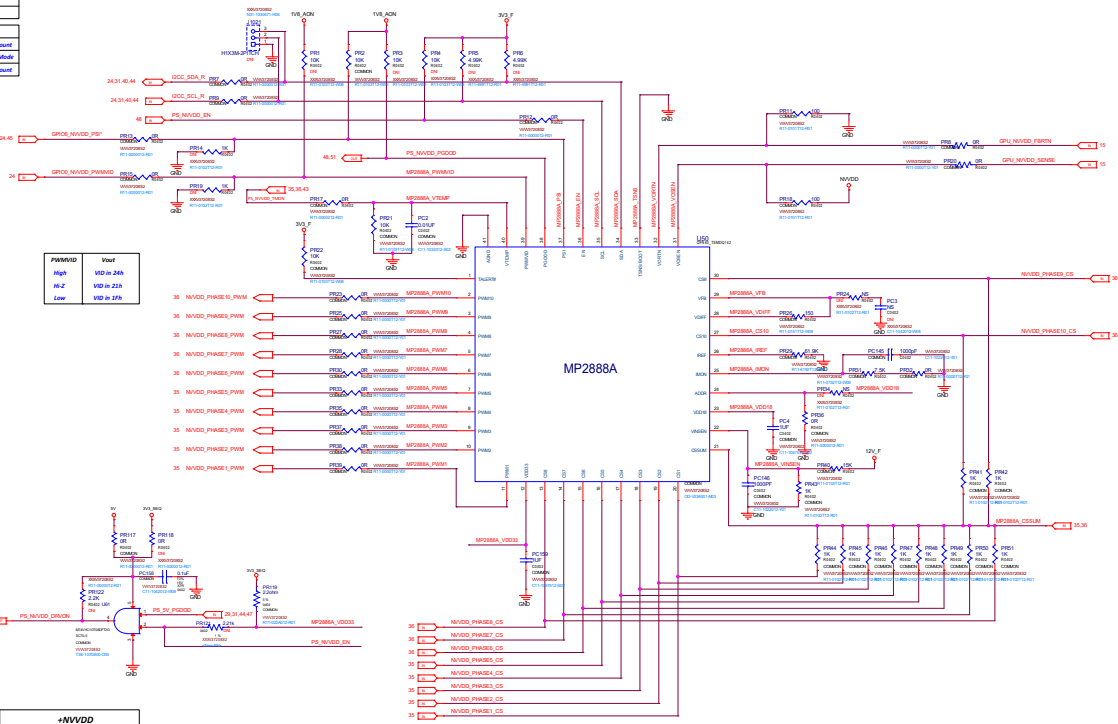
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Remove U32 (RT7296FGJ8F)



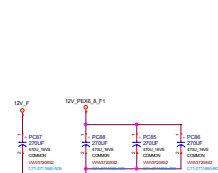
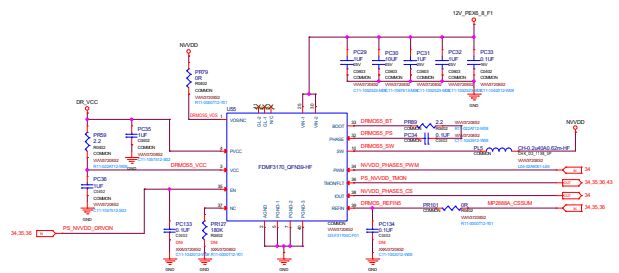
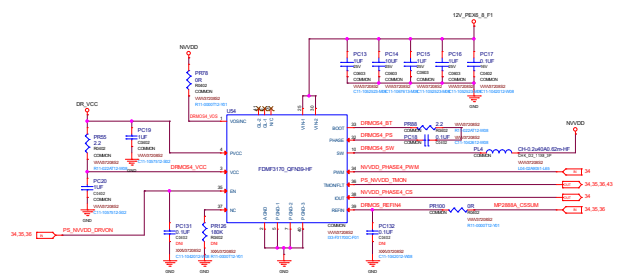


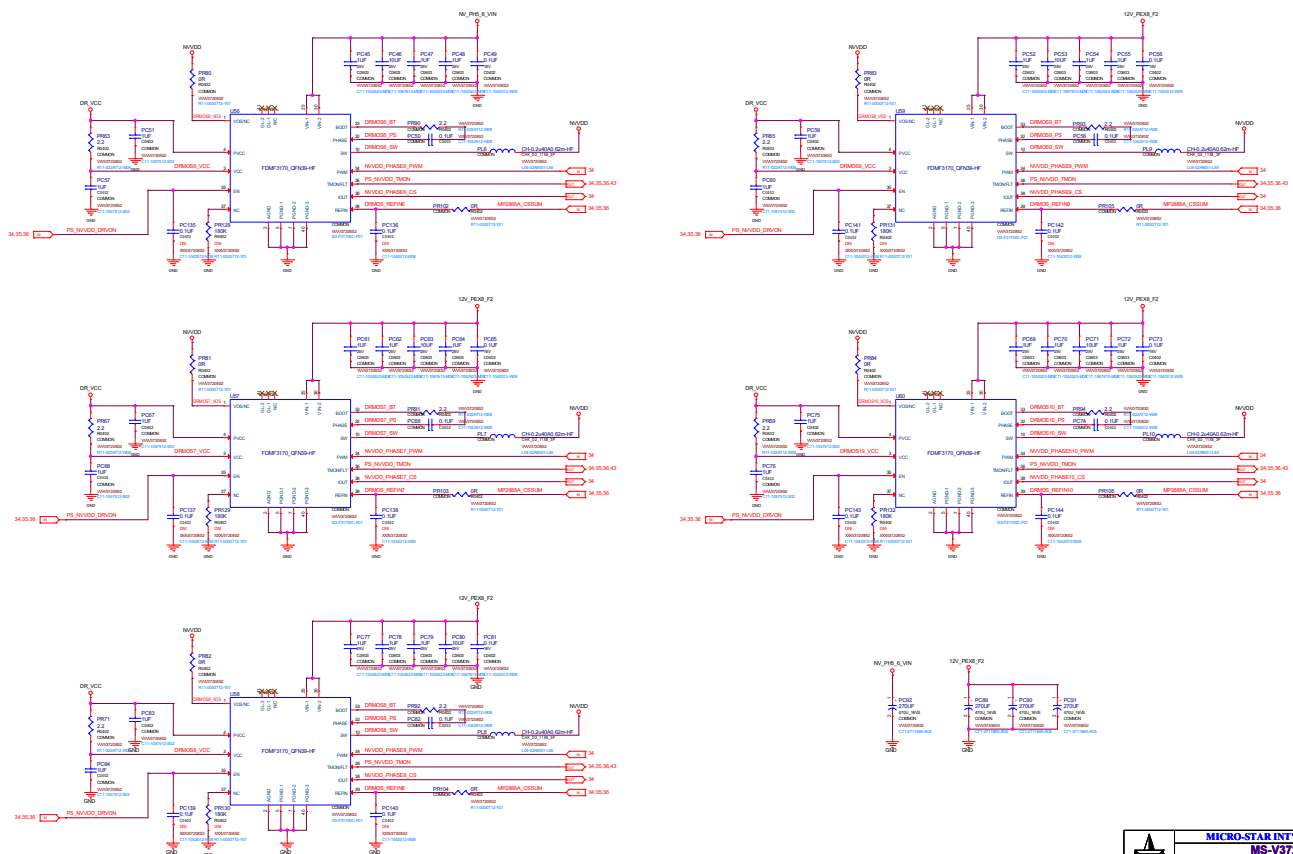


	+NVVDD
TDC	200A
IccMax	330A
Vboot	0.8V
Load Line	0mohm

PMID#s	Setting	R_{mean}	R_{mean}
200n	Point(V)	(95% CI) %	(95% CI) %
21n	0.031	3.32	0.059
22n	0.057	3.32	0.11
23n	0.084	3.32	0.162
24n	0.116	3.32	0.226
25n	0.156	3.32	0.318
26n	0.205	3.32	0.43
27n	0.266	3.32	0.576
28n	0.340	3.32	0.768
29n	0.430	3.32	1.05
30n	0.545	3.32	1.43
31n	0.675	3.32	2.1
32n	0.844	3.32	2.94
33n	1.048	3.32	4.64
34n	1.301	3.32	8.66
35n	1.500	3.32	16.5







NVVDD Output CAP





MICRO-STAR INT'L CO., LTD	
MS-V372	
Rev:	Revised: 08/08/2018
Drawn:	PS:NVVDD (X)
Date:	Tuesday, 20/08/2018

0

10

20

30

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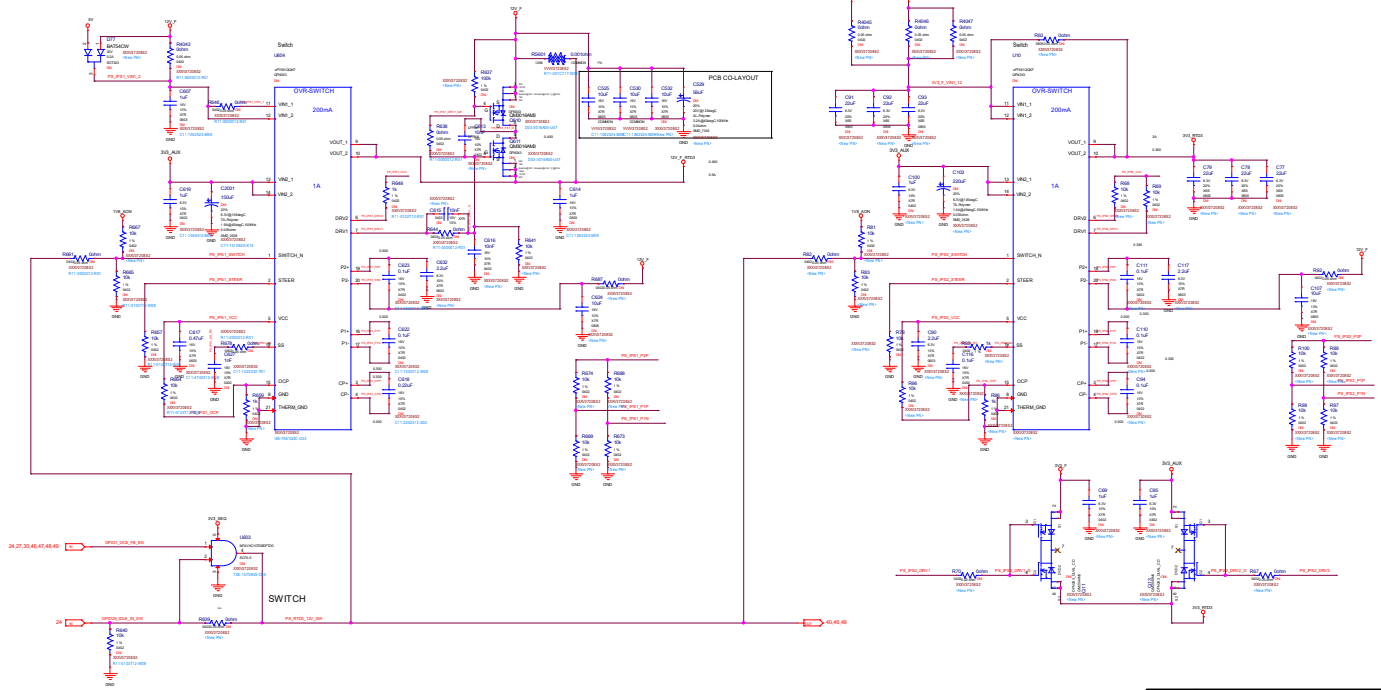
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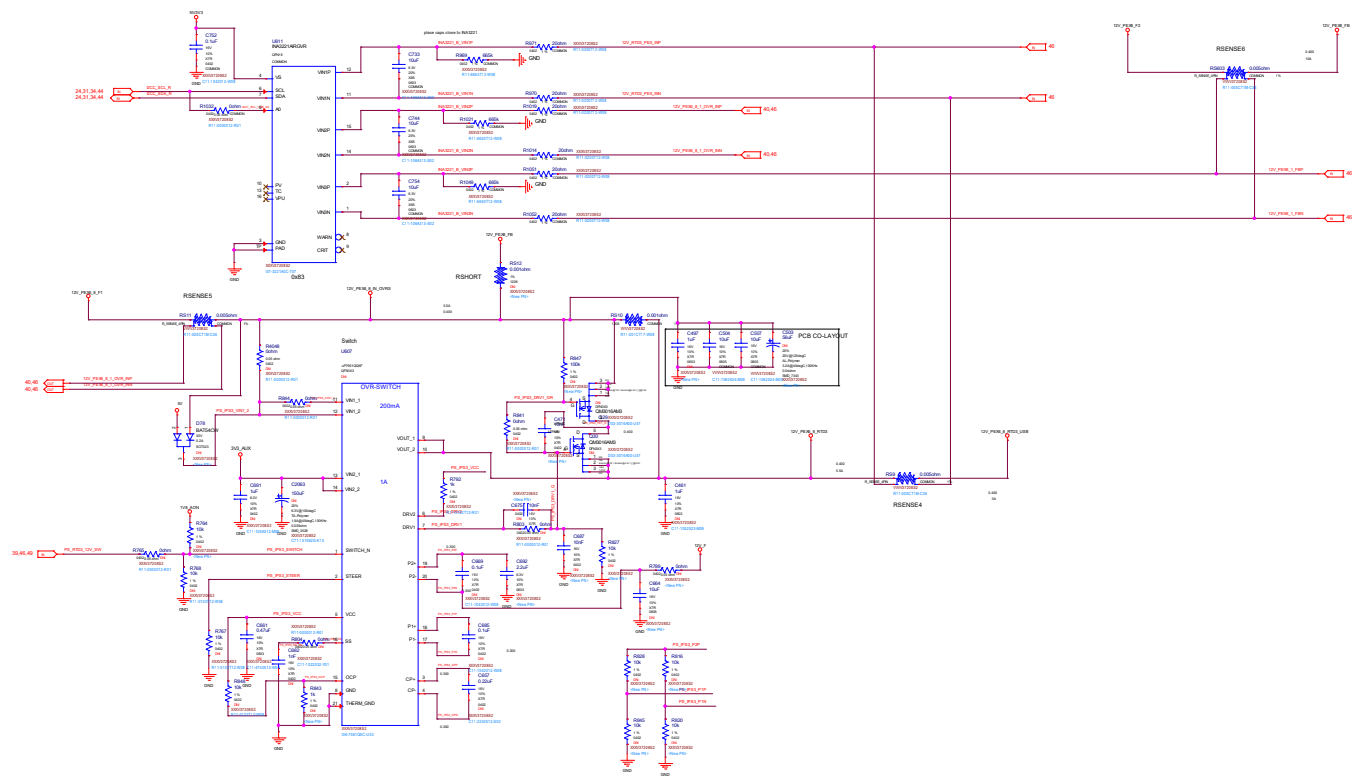
AND GATE LOGIC FOR P-BOARD

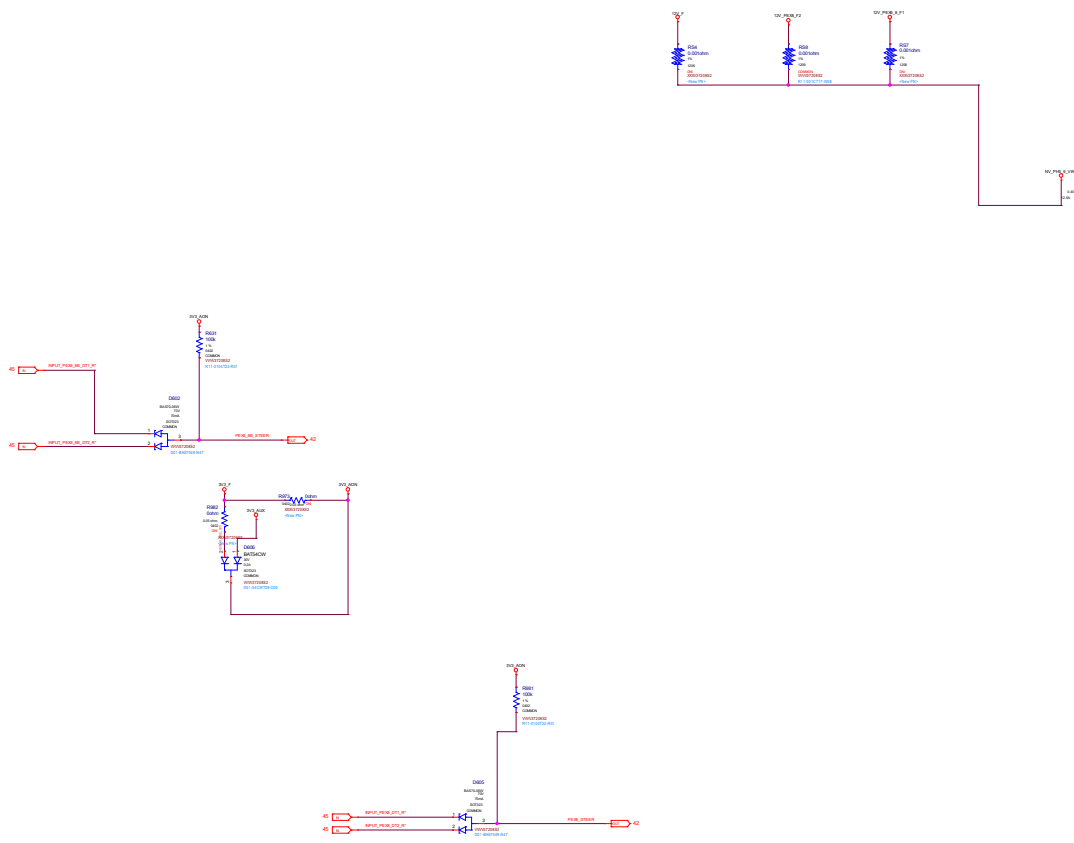
GPI01	GPI029	SWITCH	VOUT
0	0	0	12V_F
0	1	0	12V_F
1	0	0	12V_F
1	1	1	3V3A

AND GATE LOGIC FOR P-BOARD

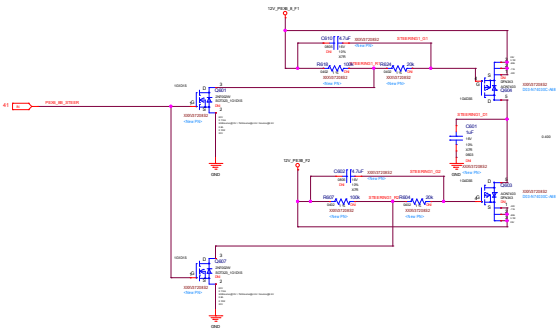
GPI01	GPI029	SWITCH	VOUT
0	0	0	3V3
0	1	0	3V3
1	0	0	3V3
1	1	1	3V3A



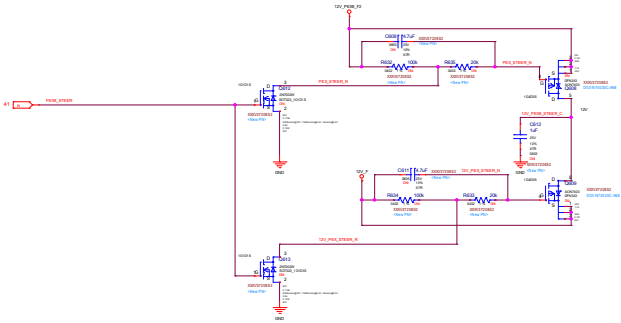


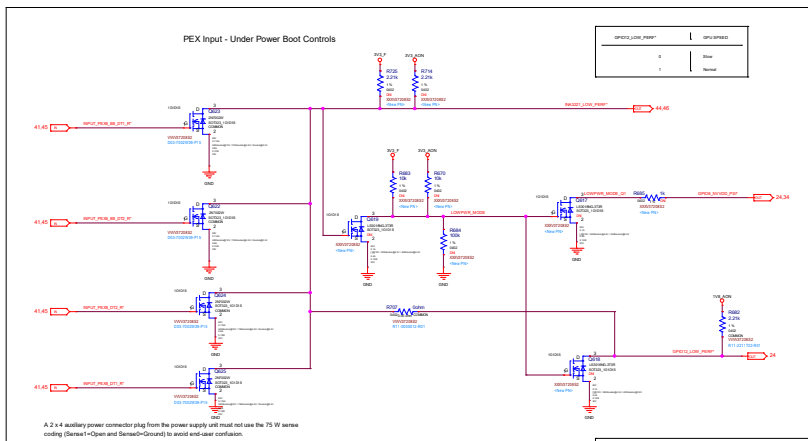
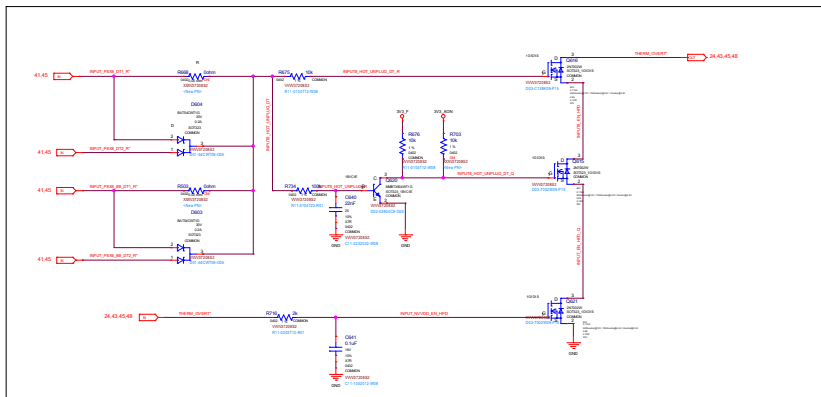
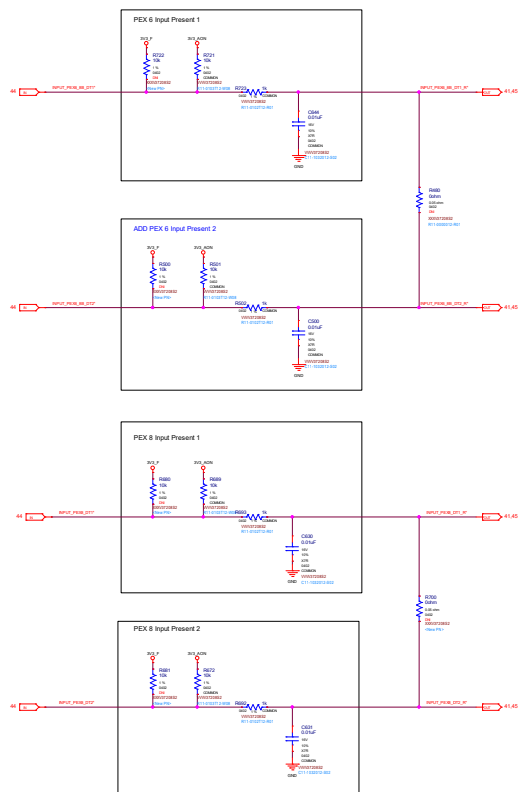


12V CURRENT STEERING (UNDER POWER BOOT):
GUIDES CURRENT FROM PEX EDGE TO PEX 8 PIN INPUT AREA



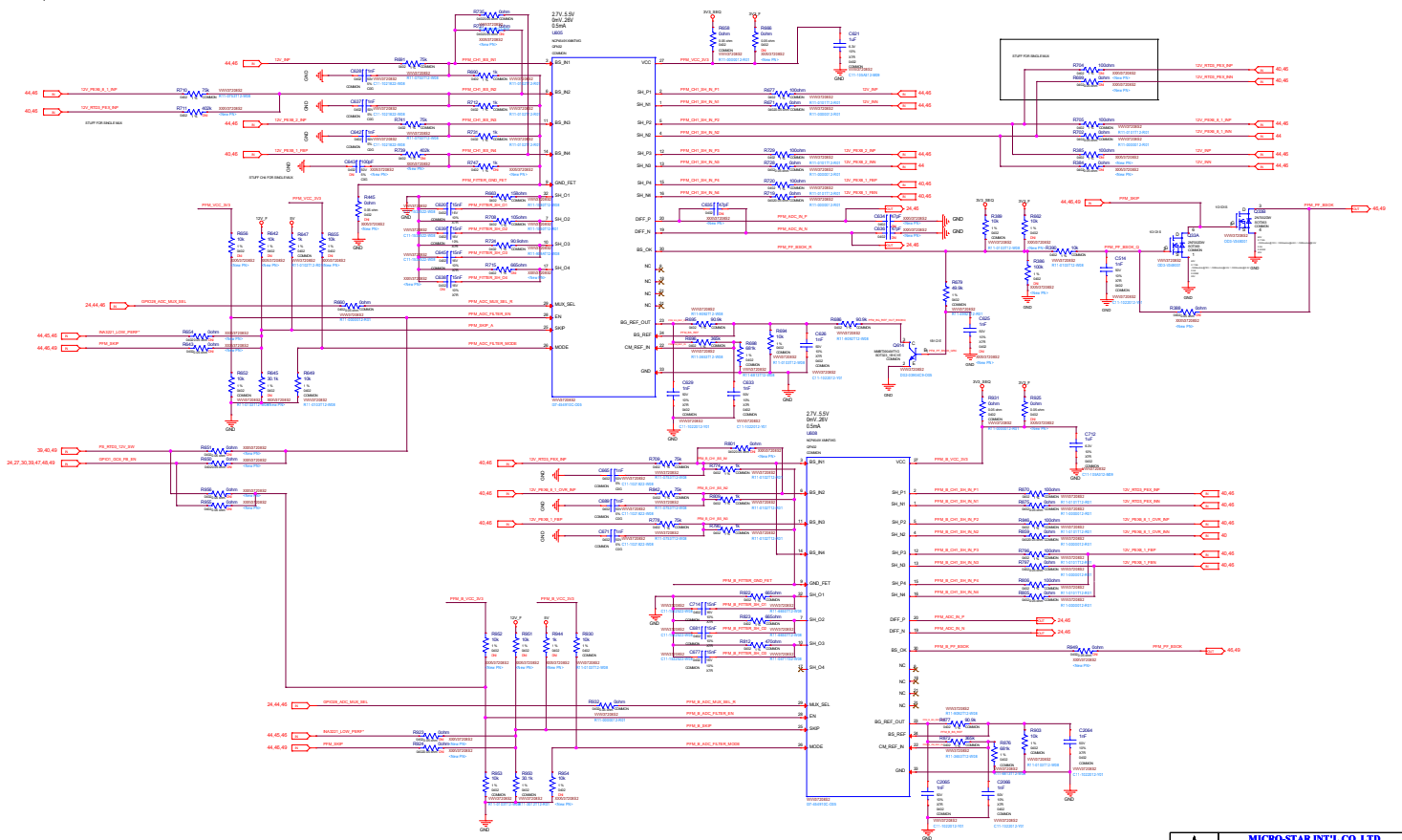
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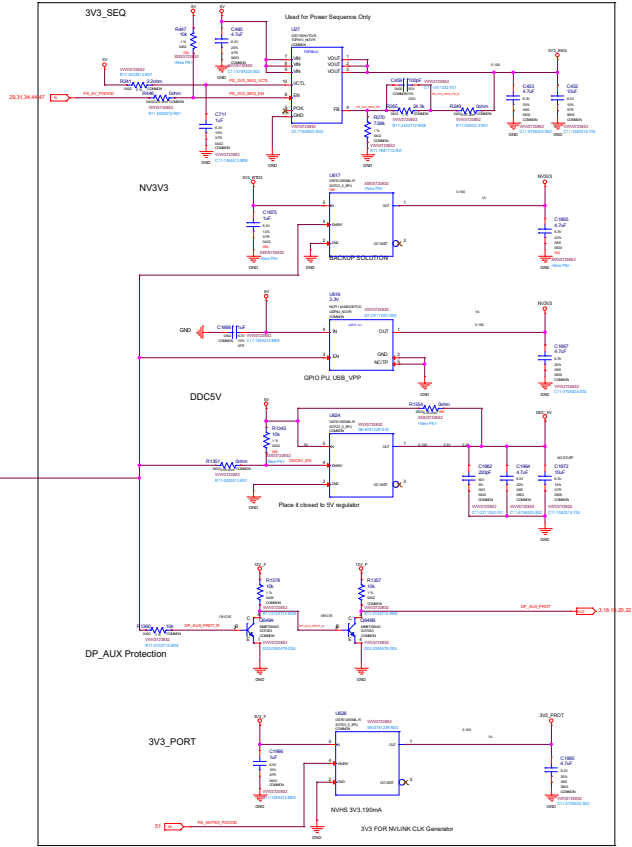
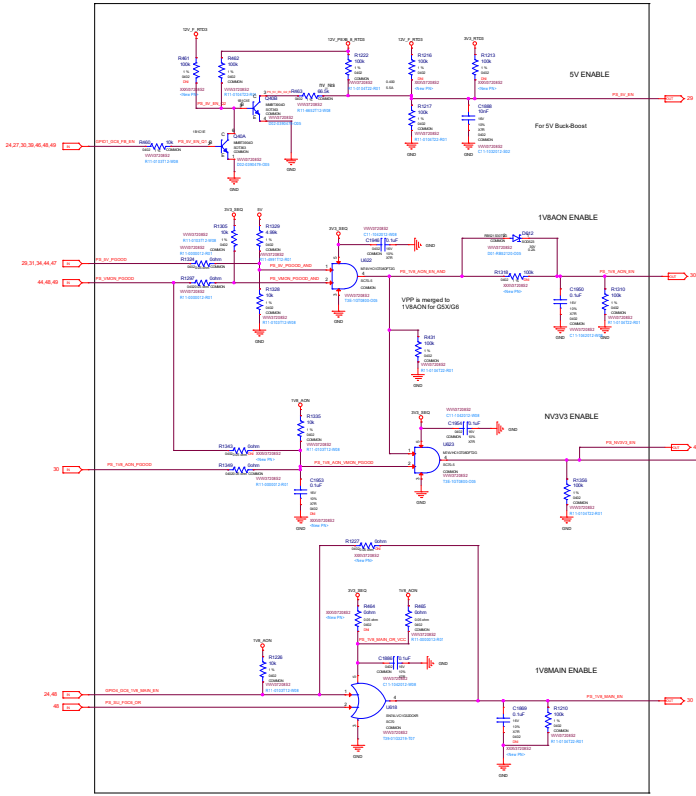


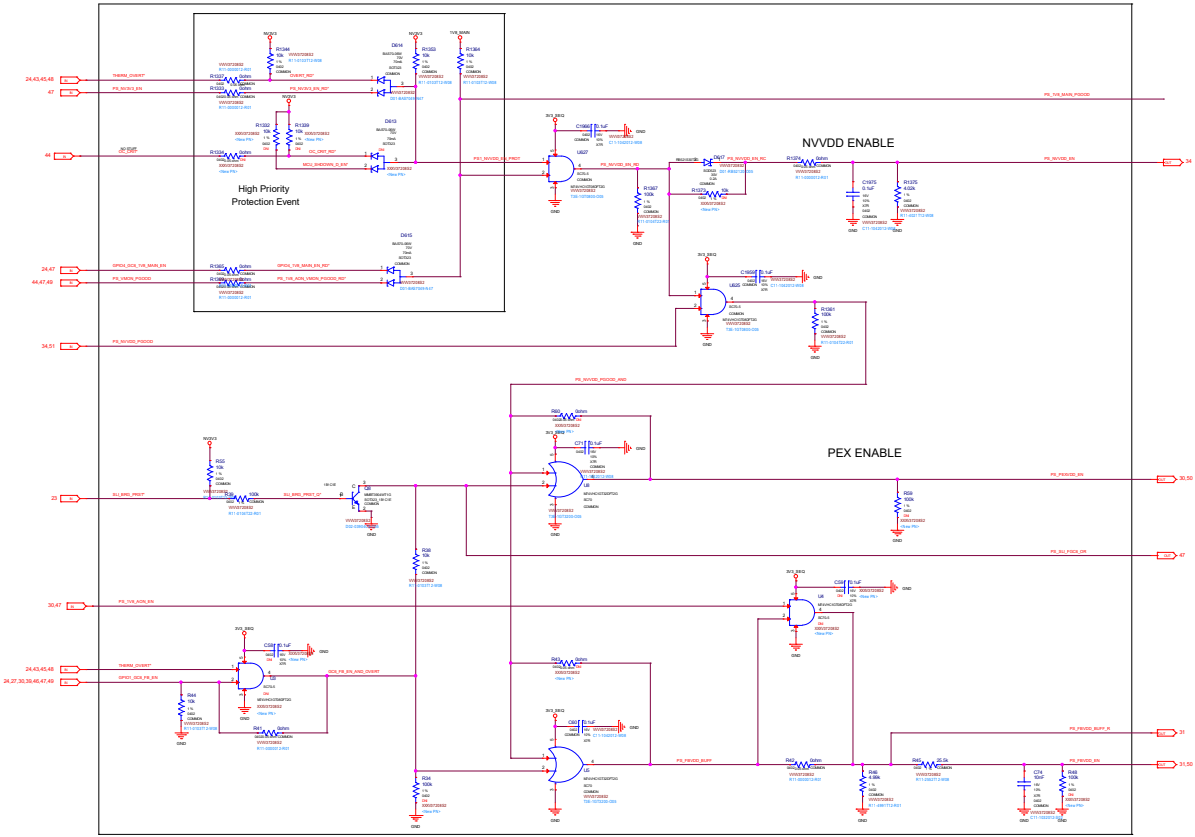


A 2 x 4 auxiliary power connector plug from the power supply unit must not use the 75 W series coding (Sense1+Open and Sense2+Ground) to avoid end-user confusion.

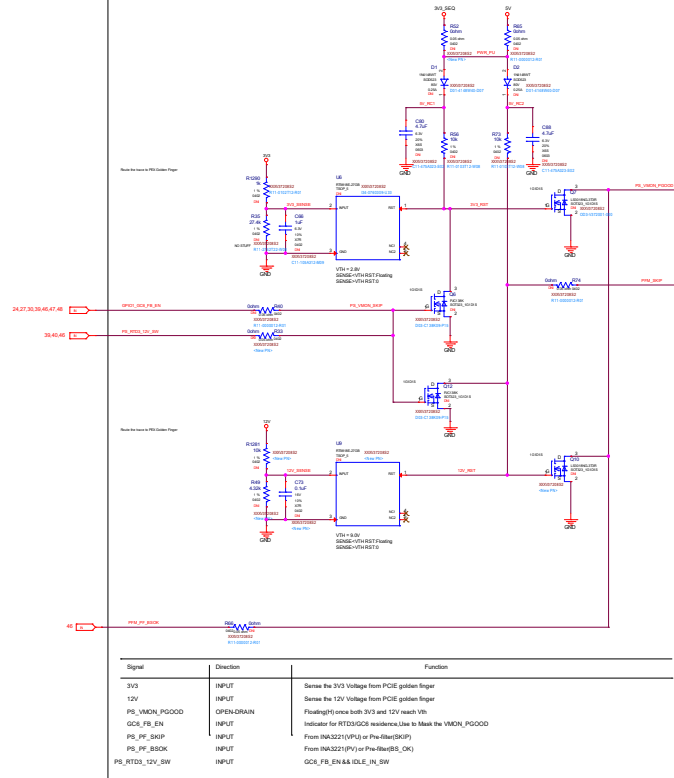
STUFF IF USING 20MX







PCIE Voltage Monitor

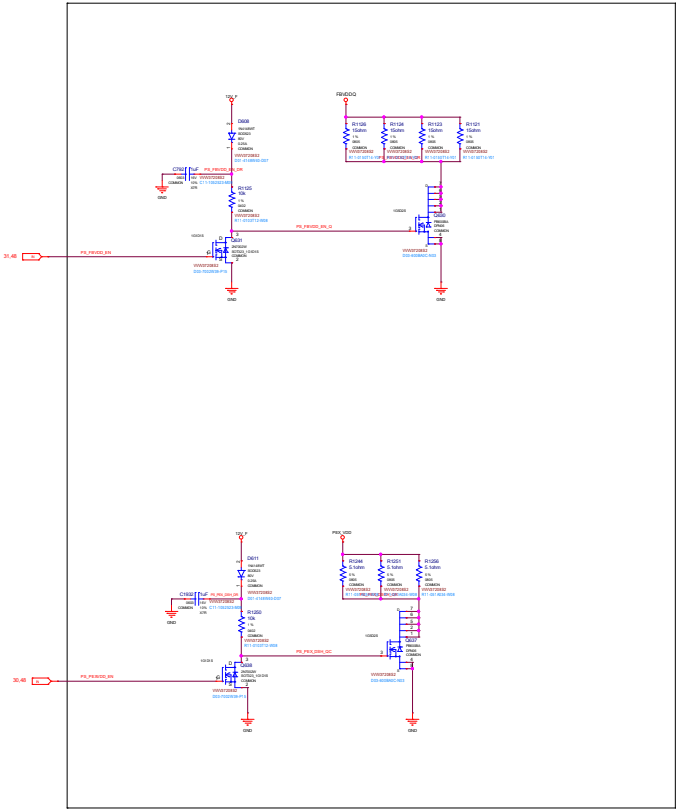


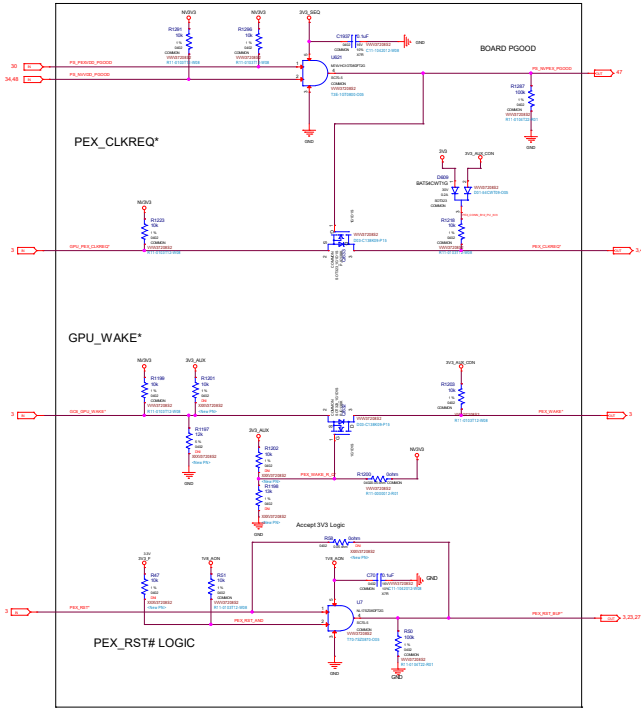
BOM Configuration

OPTIONS	PEX3V3_SENSE	PEX12V_SENSE	OTHER_12V_SENSE
Use Pre-Filter	Pre-Filter NO STUFF U12 NO STUFF Q3.05 NO STUFF D15	Pre-Filter NO STUFF U12 NO STUFF Q4	Pre-Filter
Use INA3221	Voltage_Monitor	INA3221 NO STUFF U12 NO STUFF Q4	INA3221
NO INA3221 NO Pre-Filter	Voltage_Monitor	Voltage_Monitor	N/A

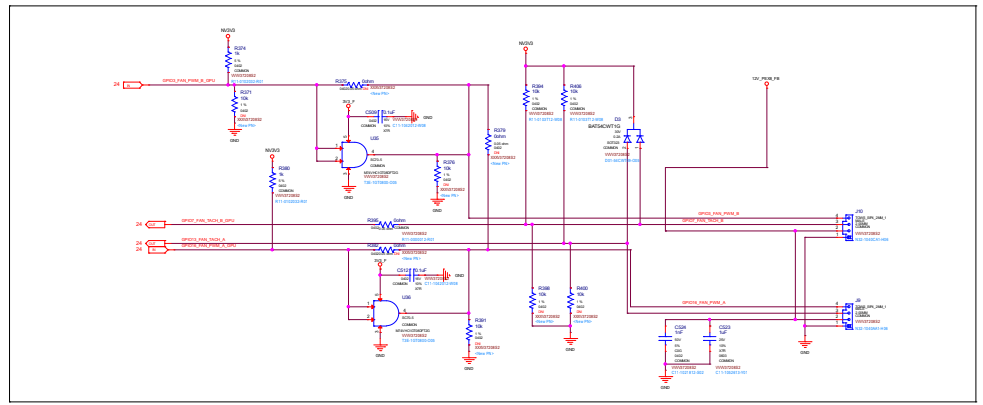
Power Supply
INA3221:3V3_SEQ
Pre_Filter(ADC_MUX):3V3(PEX)

Dual Pre-Filter case:
Only use the Primary Pre-Filter to sense 3V3PEX
and All Input 12Vs



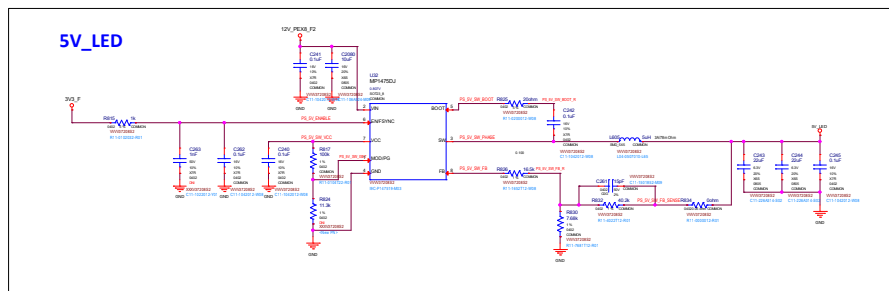
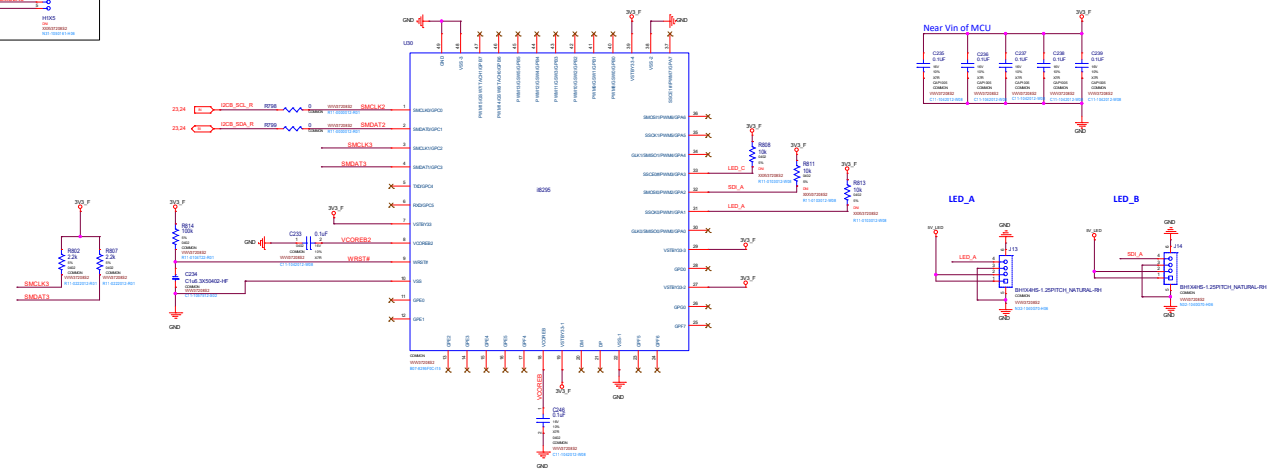
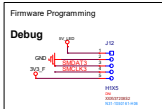


Remove LED

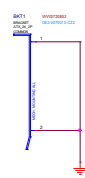


Remove LED BOOST

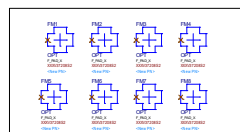
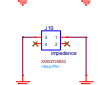
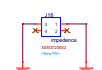




Brackets:



Screw



Mechanical Holes Symbol

