

GA102 DT SKU P-BOARD

350W, FH Std PCB, 384b, GDDR6X x8  
DP + DP + DP + HDMI/DP

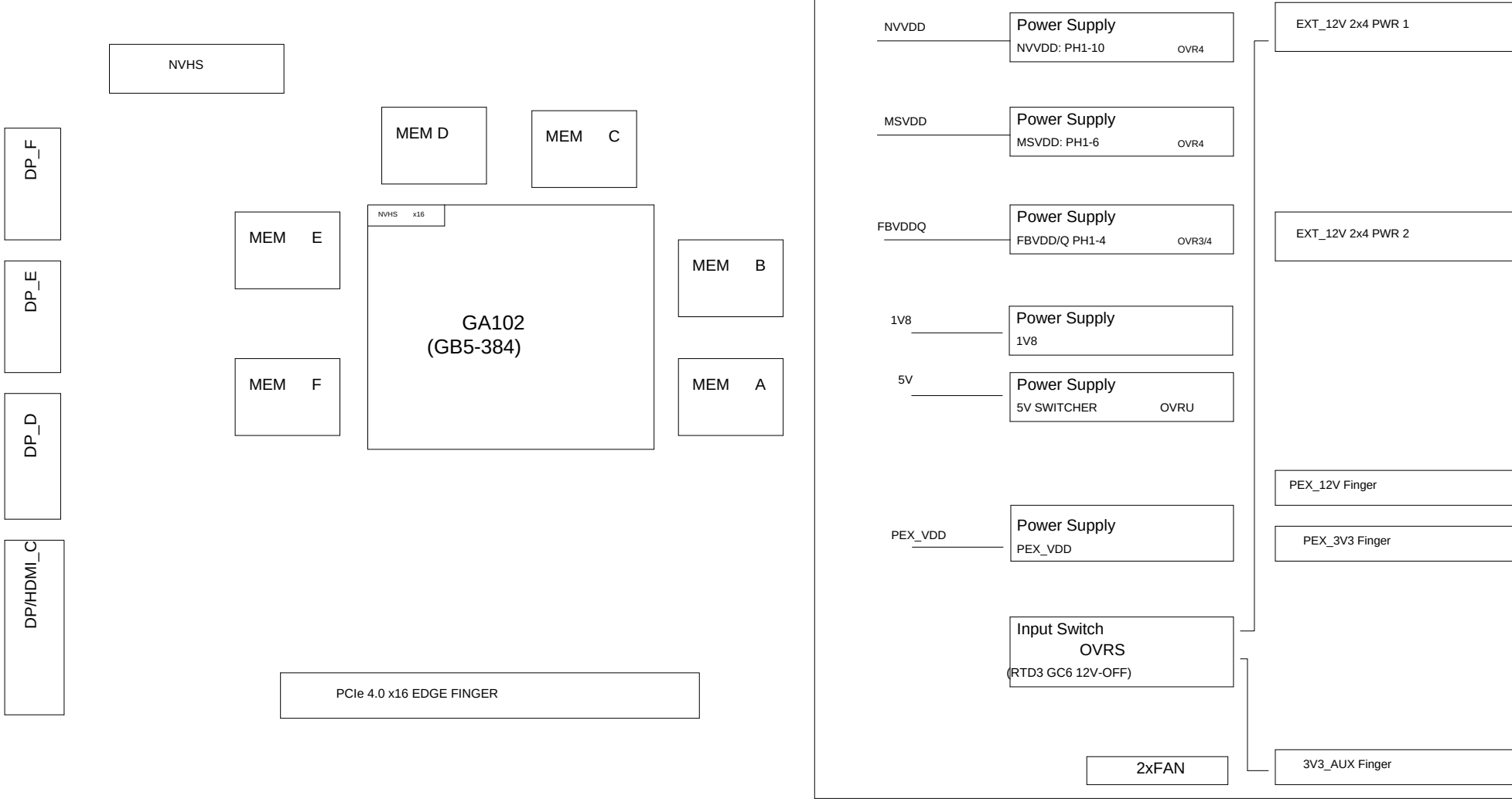
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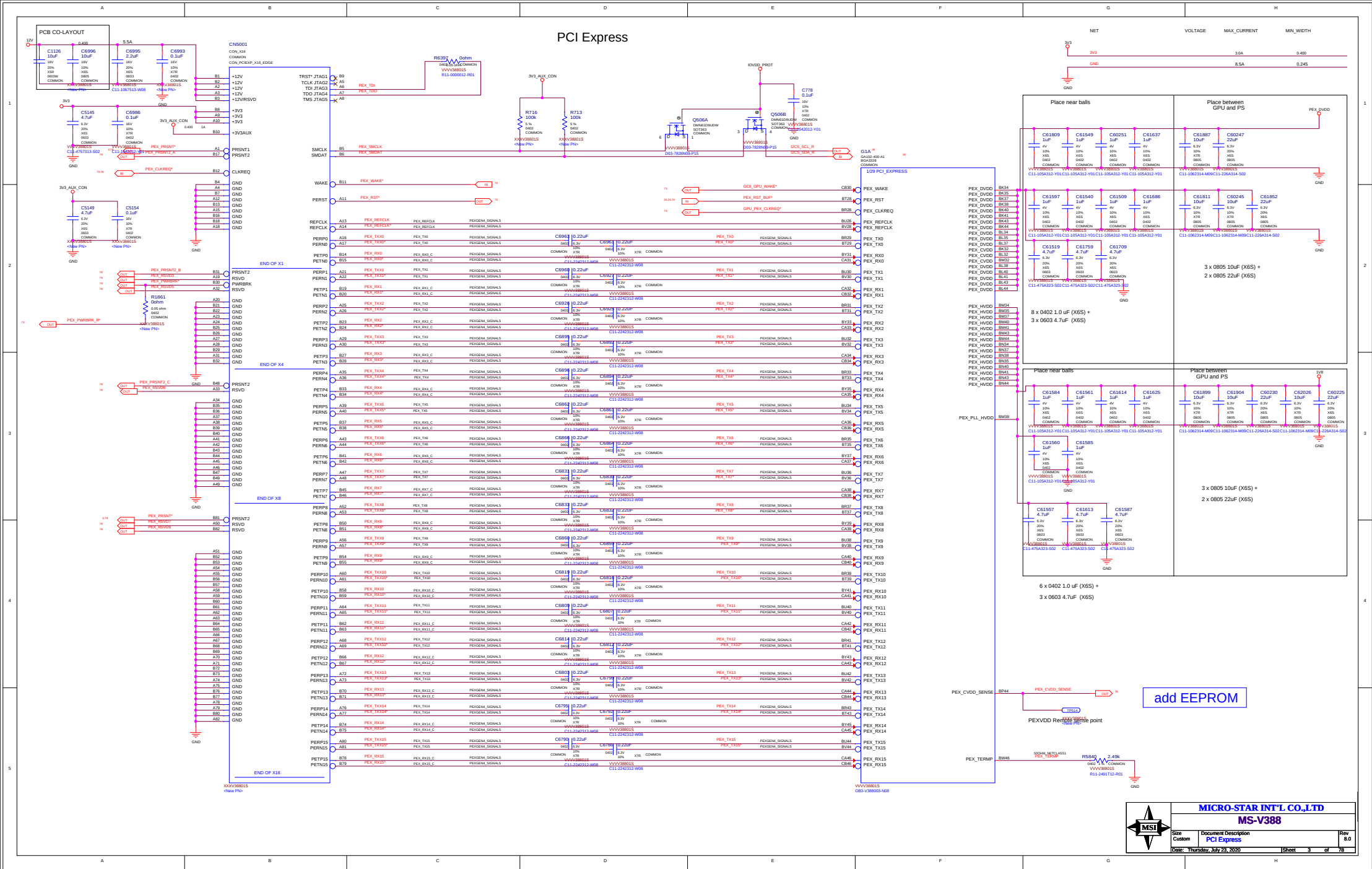
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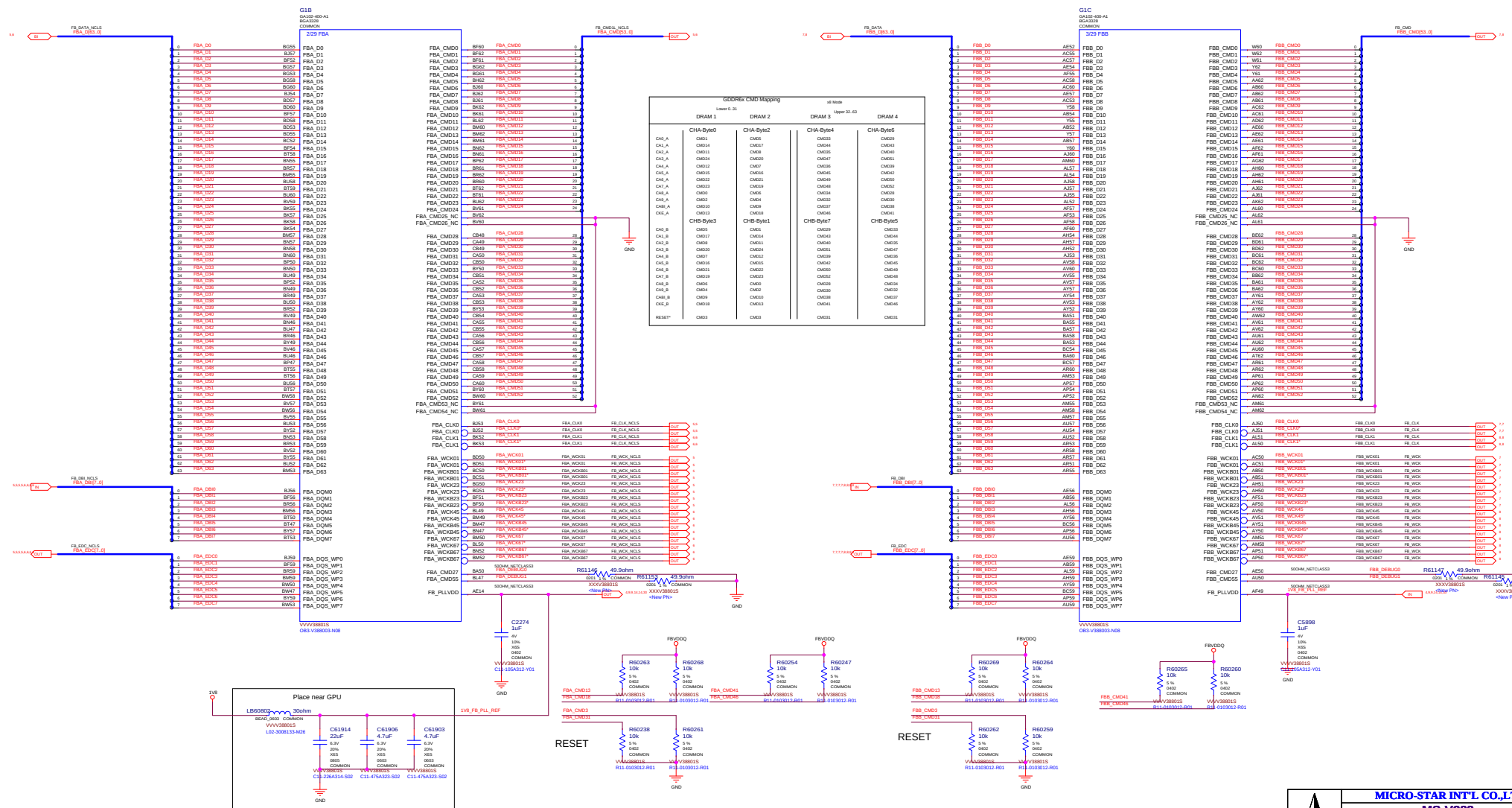
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Block Diagram

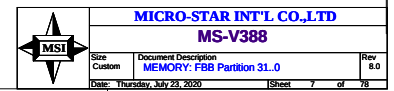


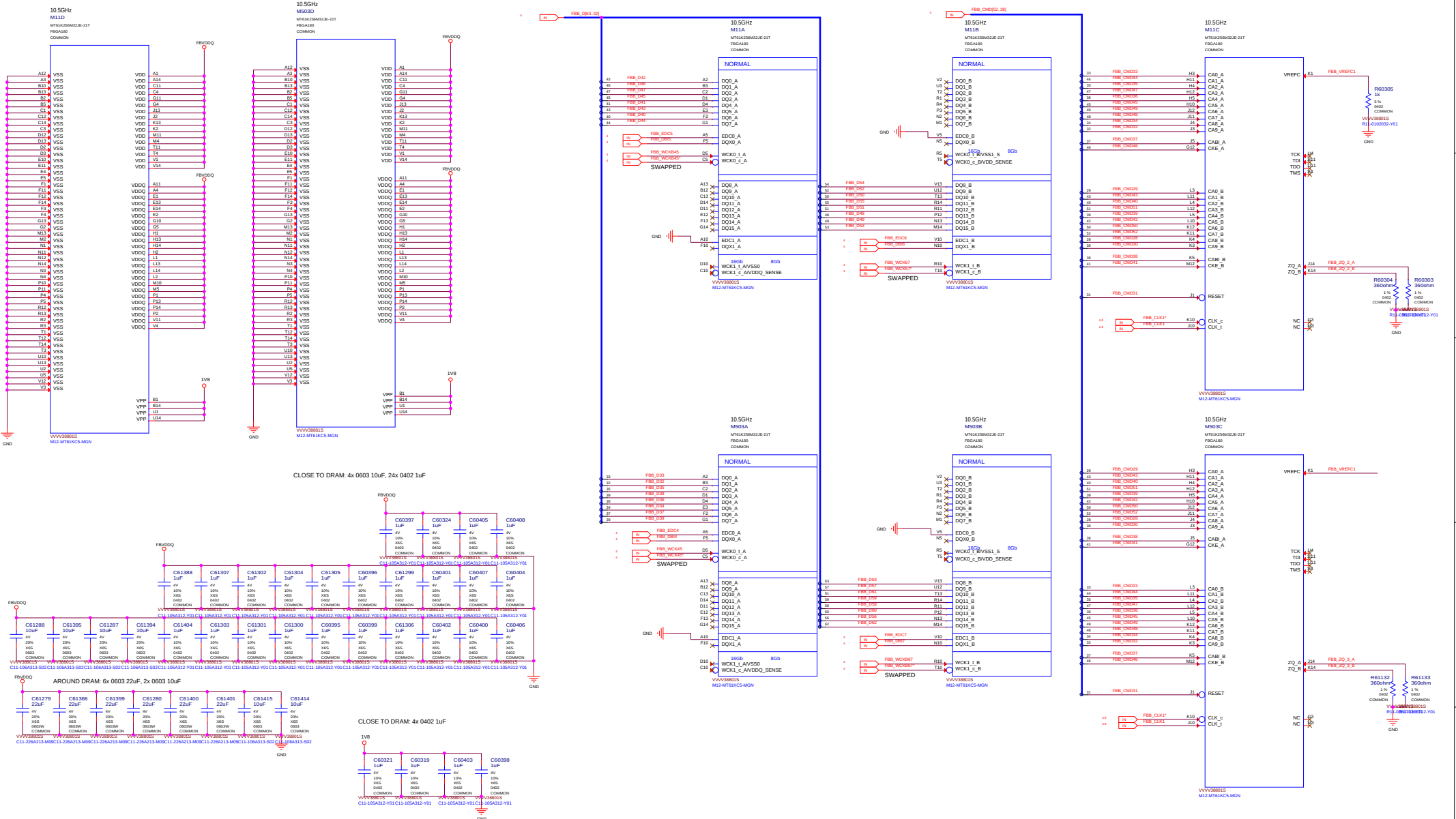






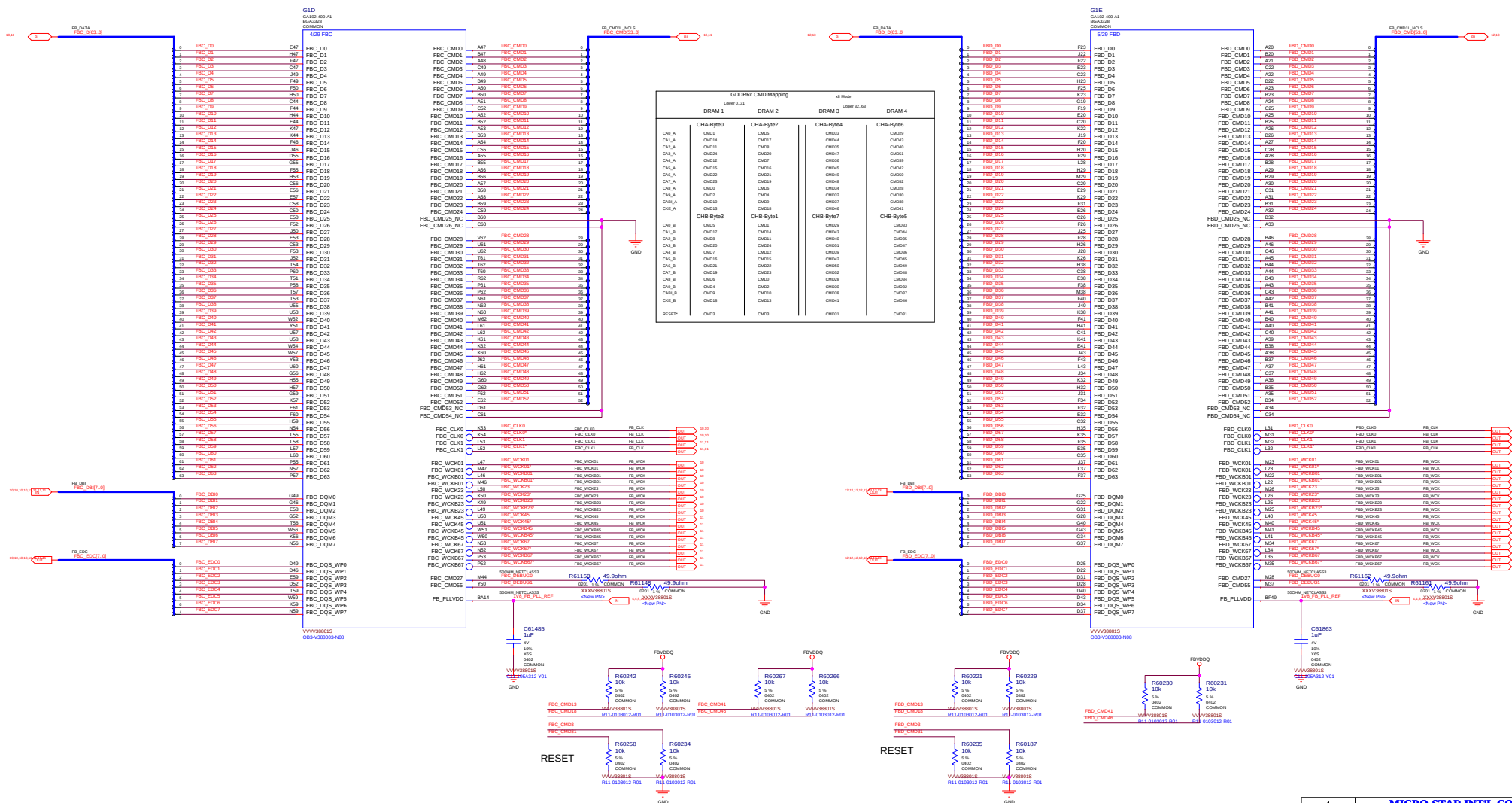


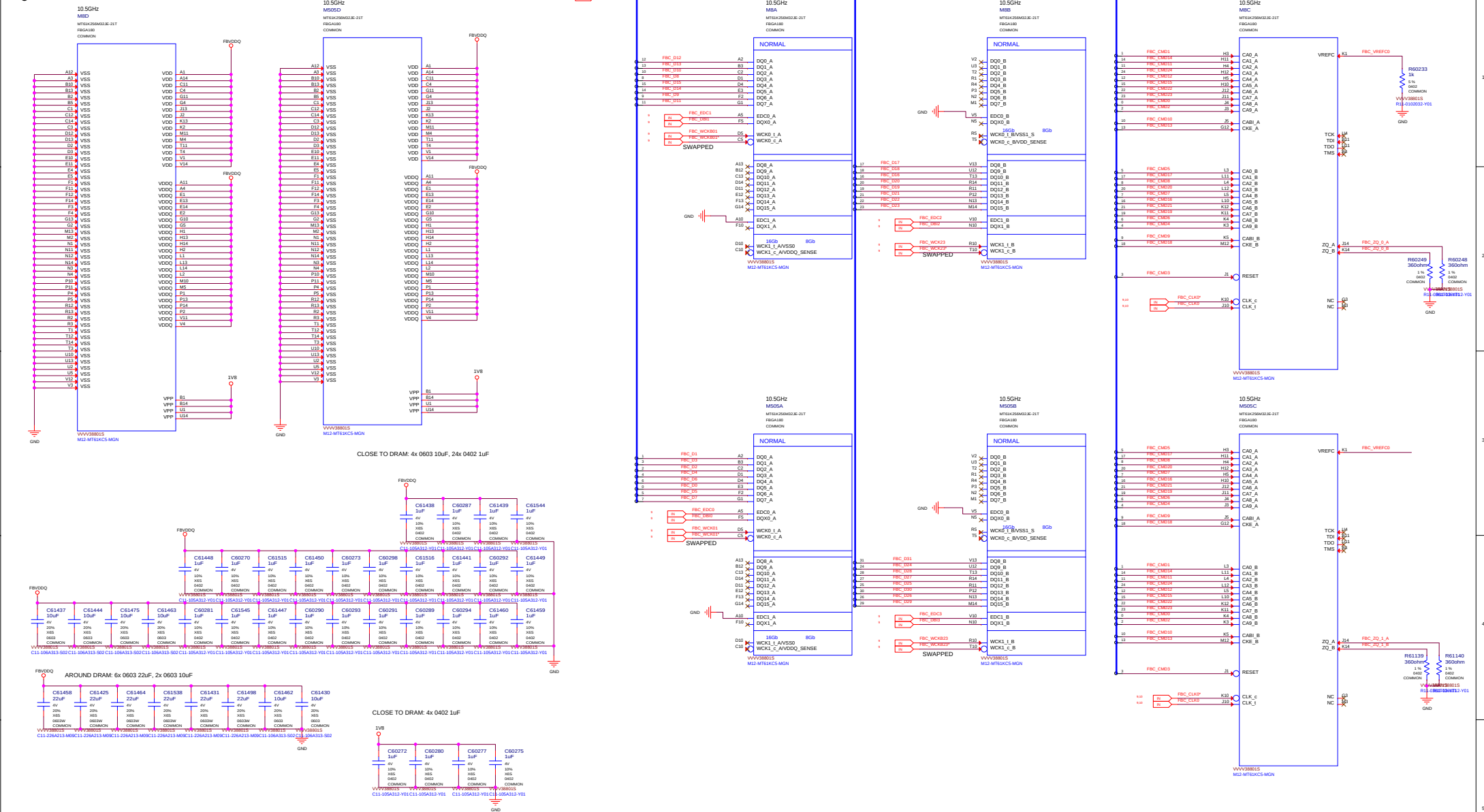






|                           |
|---------------------------|
| MEMORY: GPU Partition C/D |
|---------------------------|





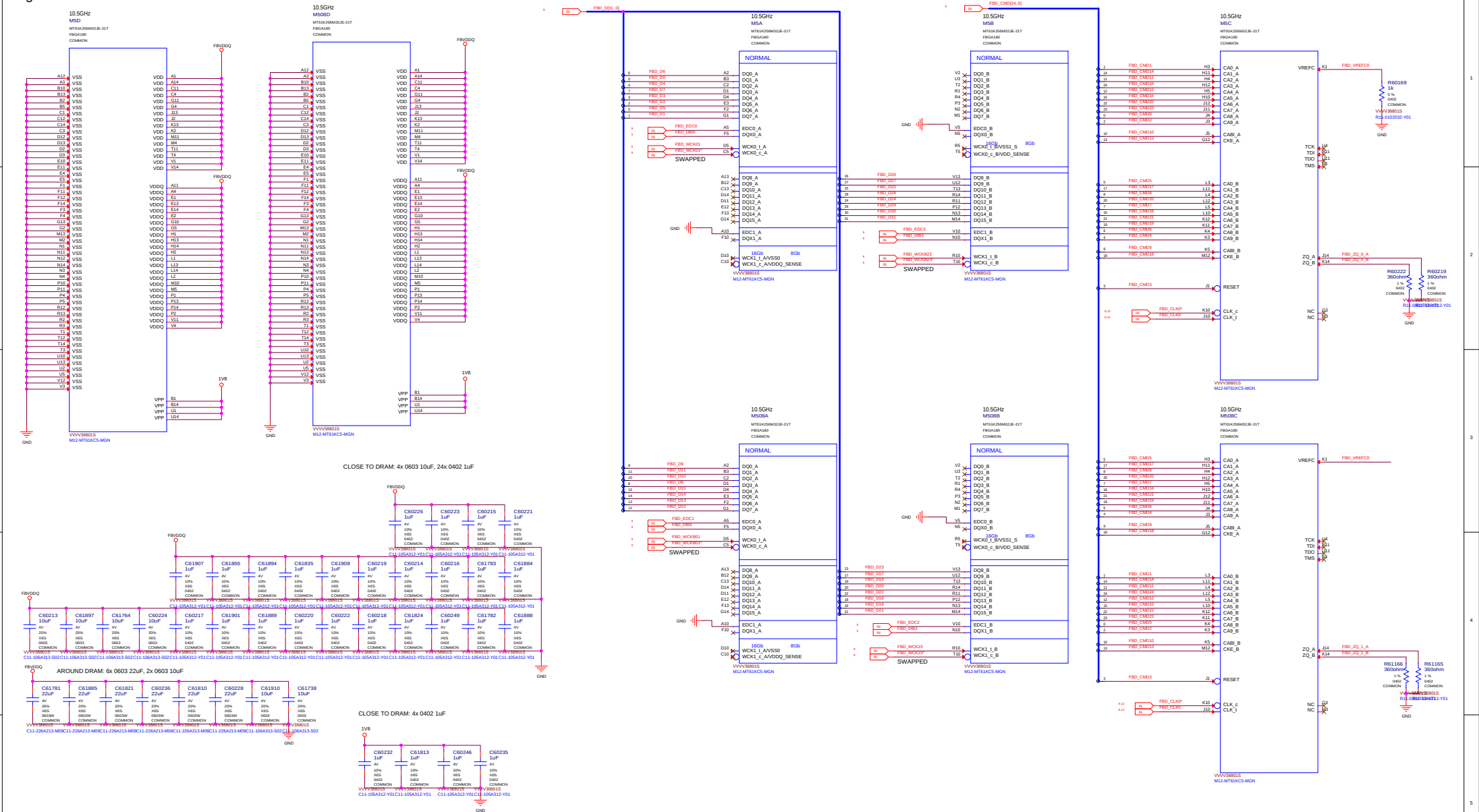
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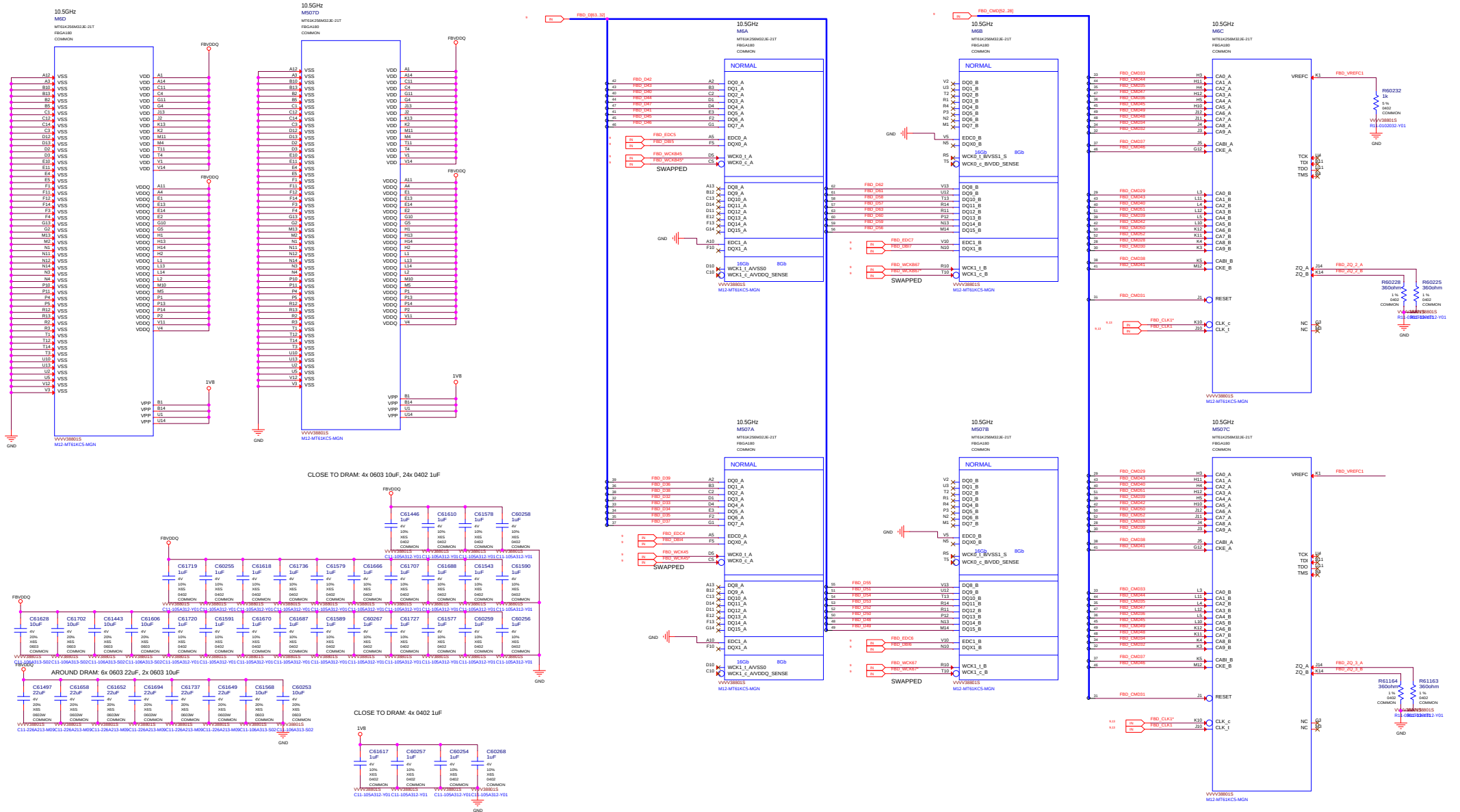
MS-V388

|                               |                                                           |                |
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| Size<br>Custom                | Document Description<br><b>MEMORY: FBC Partition 31.0</b> | Rev<br>8.0     |
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|      |      |          |    |    |    |
|------|------|----------|----|----|----|
| DATE | TIME | LOCATION | TO | BY | TO |
|      |      | H        |    |    |    |

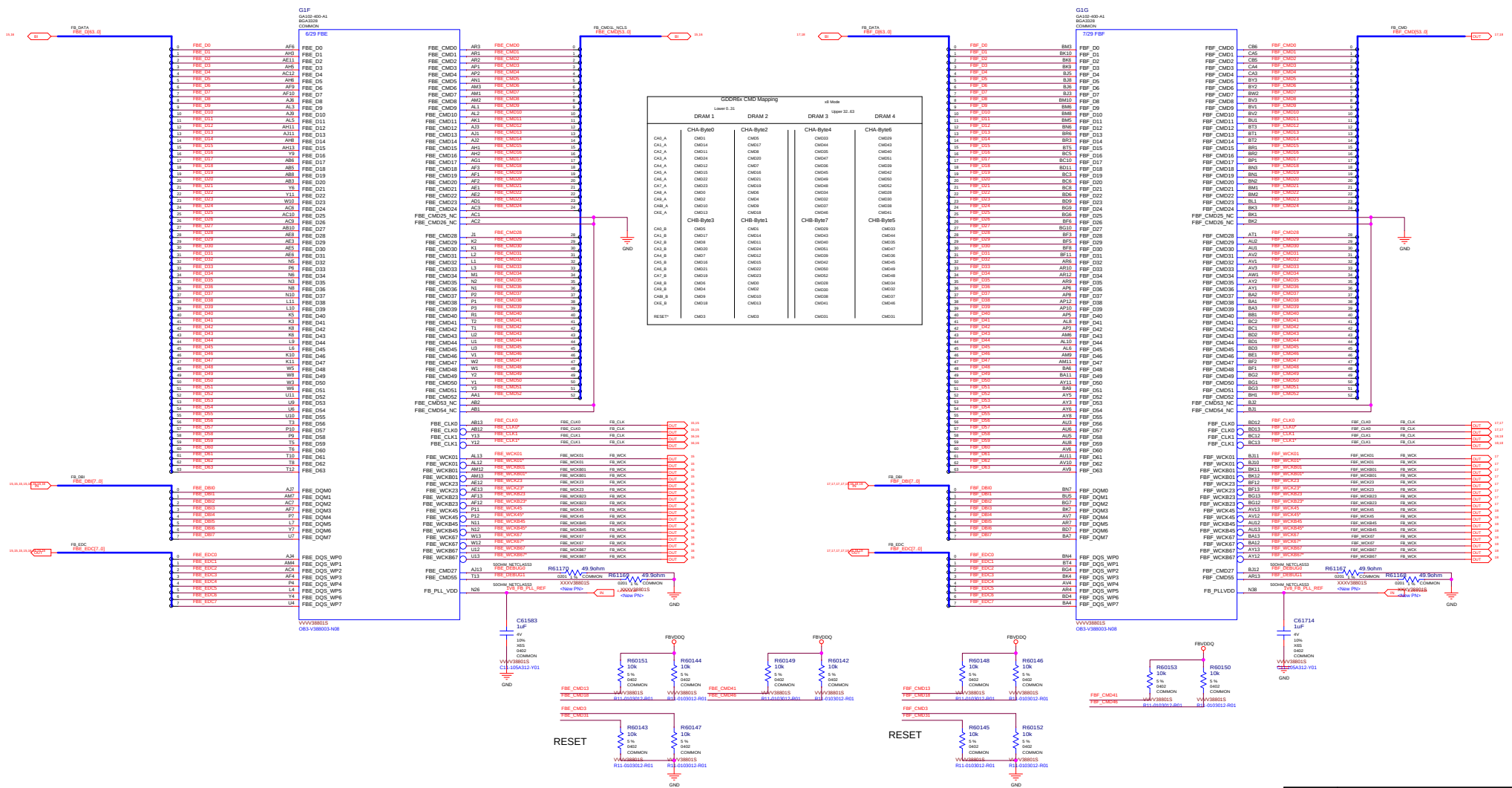


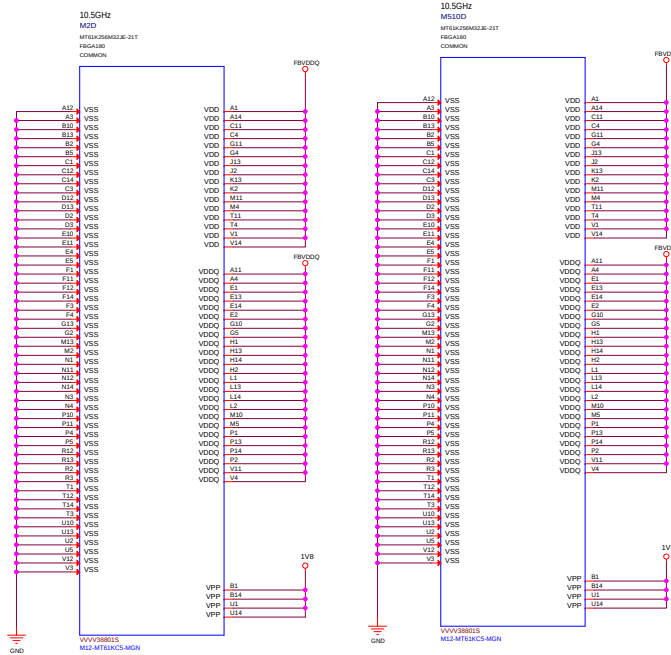




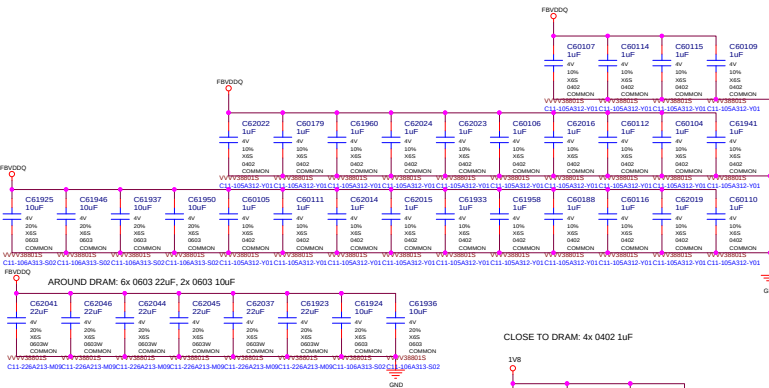
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**MS-V388**

|                               |                                                             |                |
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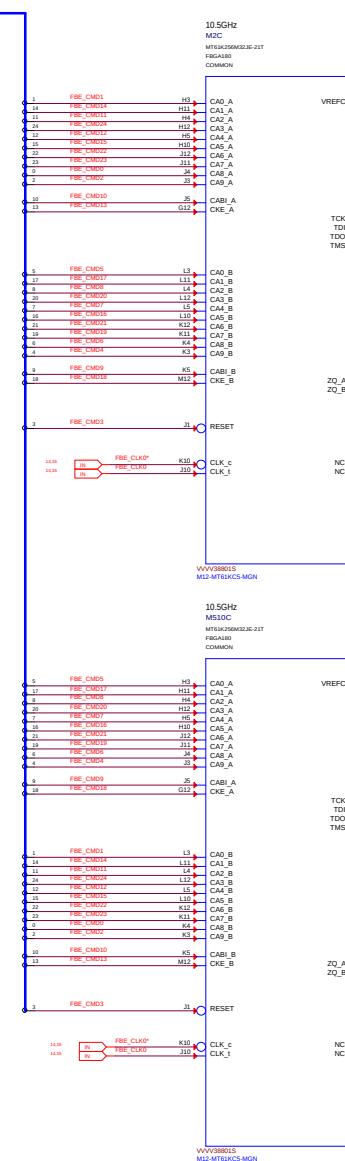
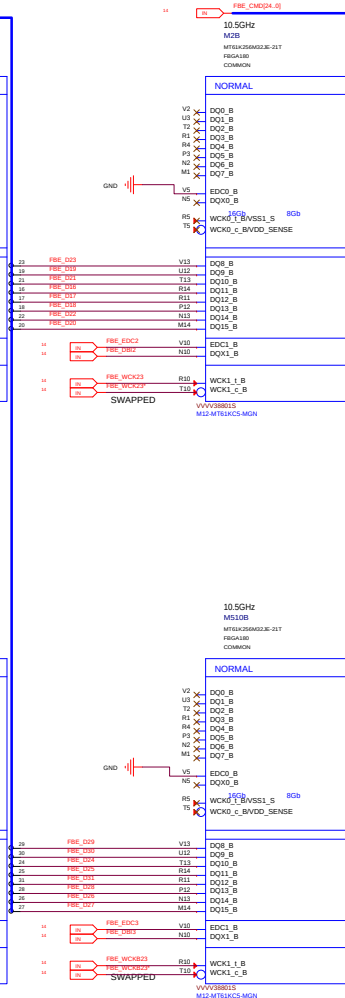
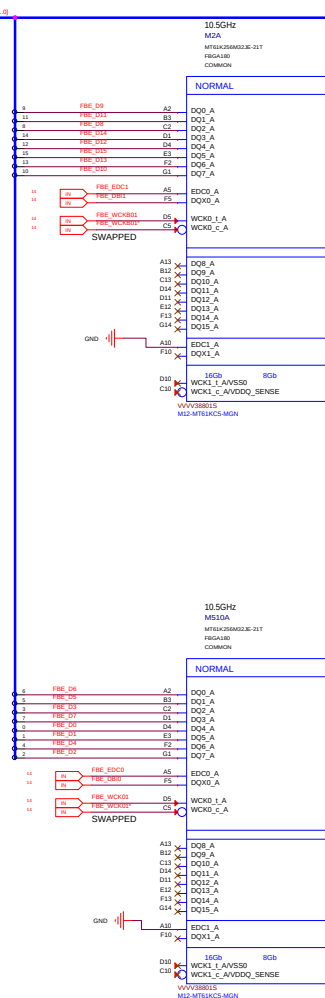
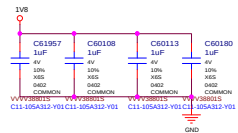




CLOSE TO DRAM: 4x 0603 10uF, 24x 0402 1uF

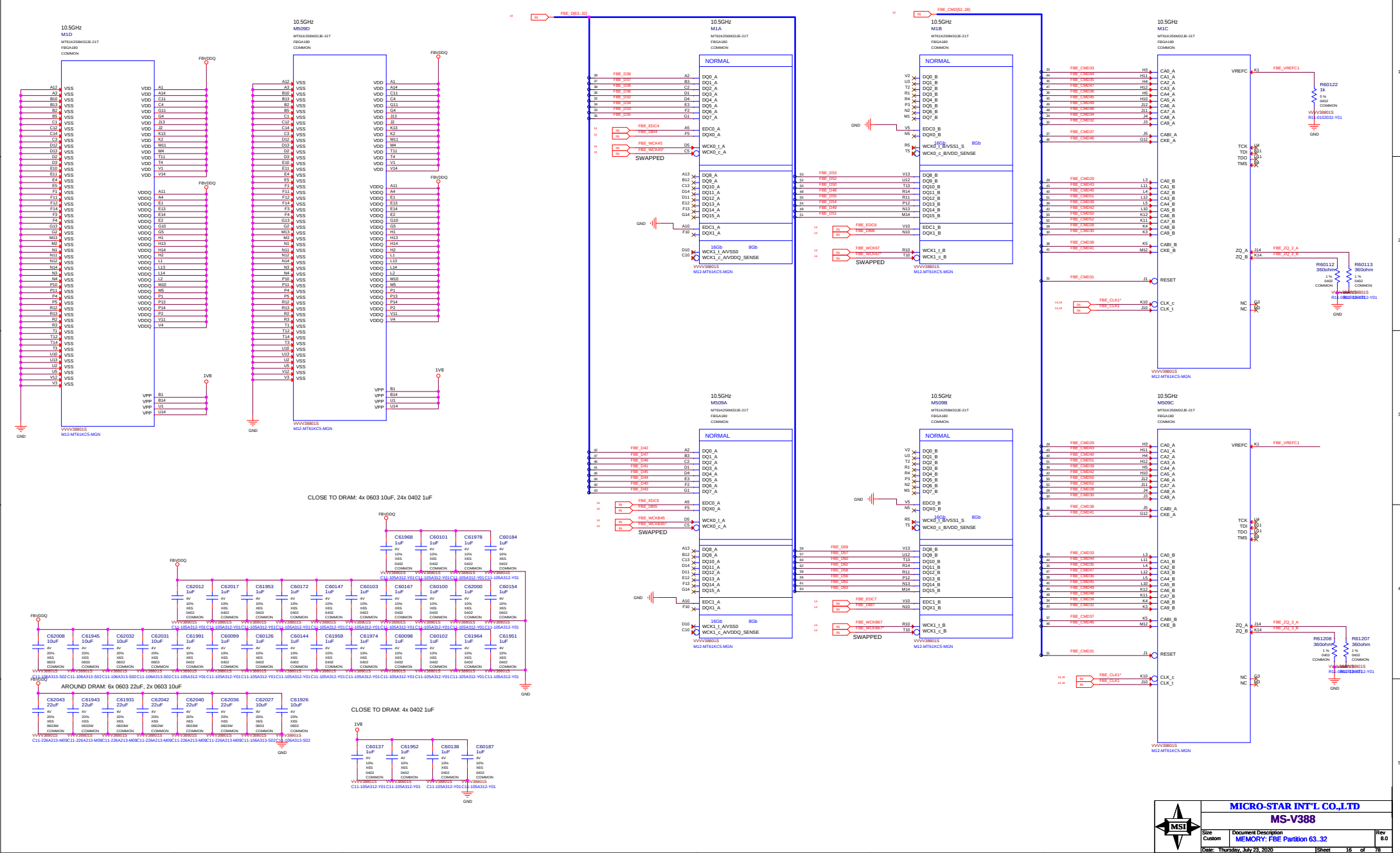


CLOSE TO DRAM: 4x 0402 1uF

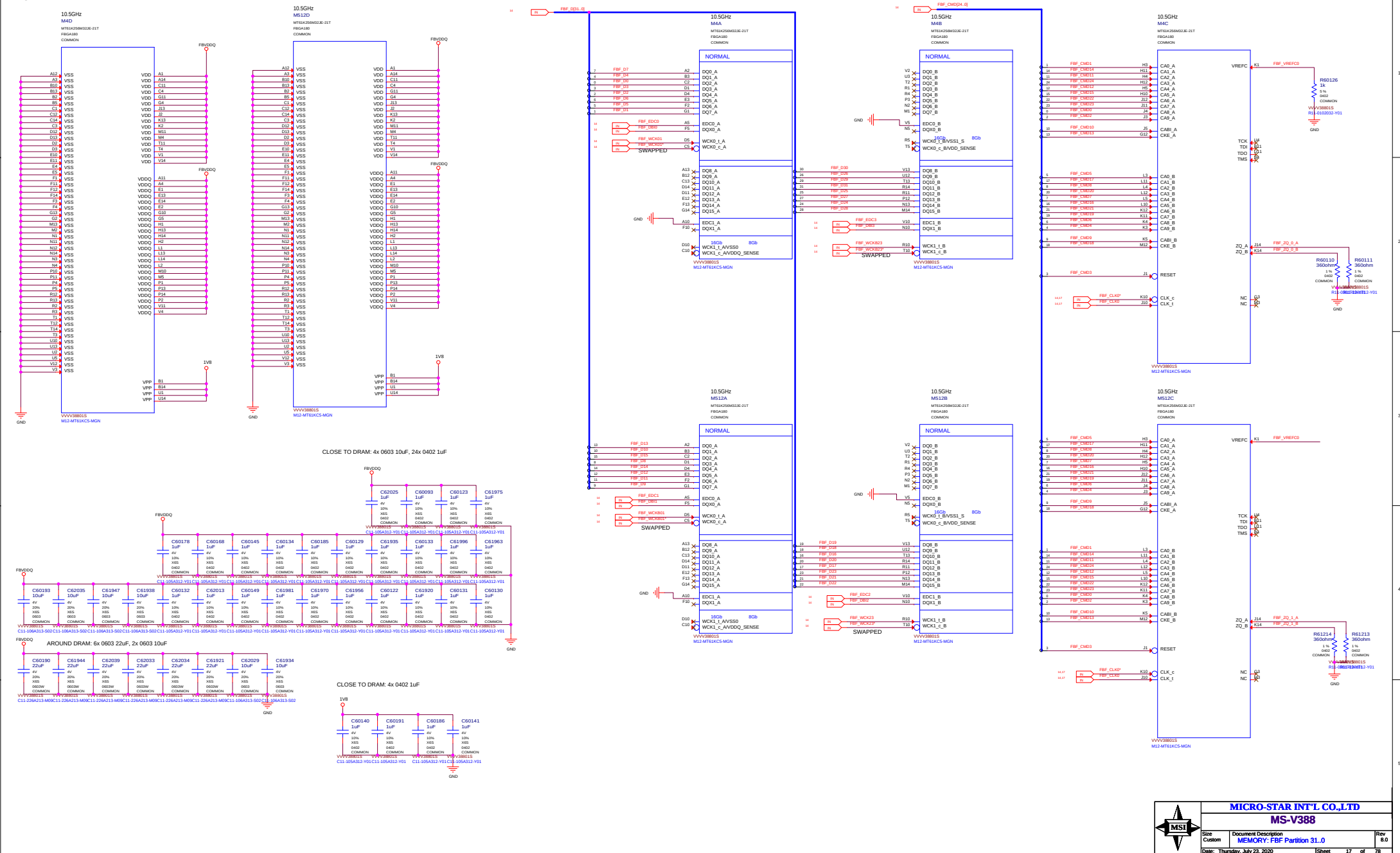


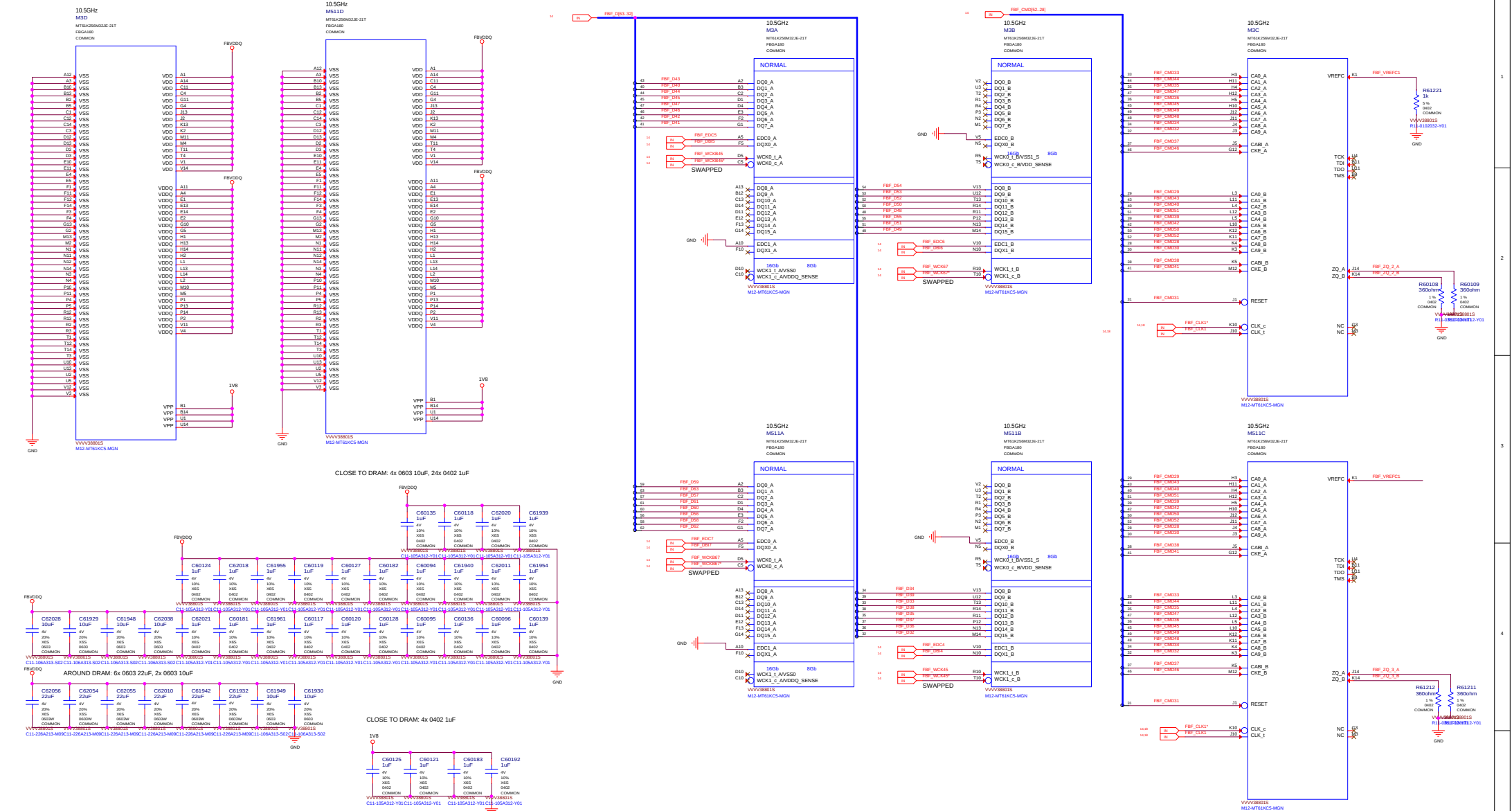
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**MS-V388**

|                               |                                                            |                |
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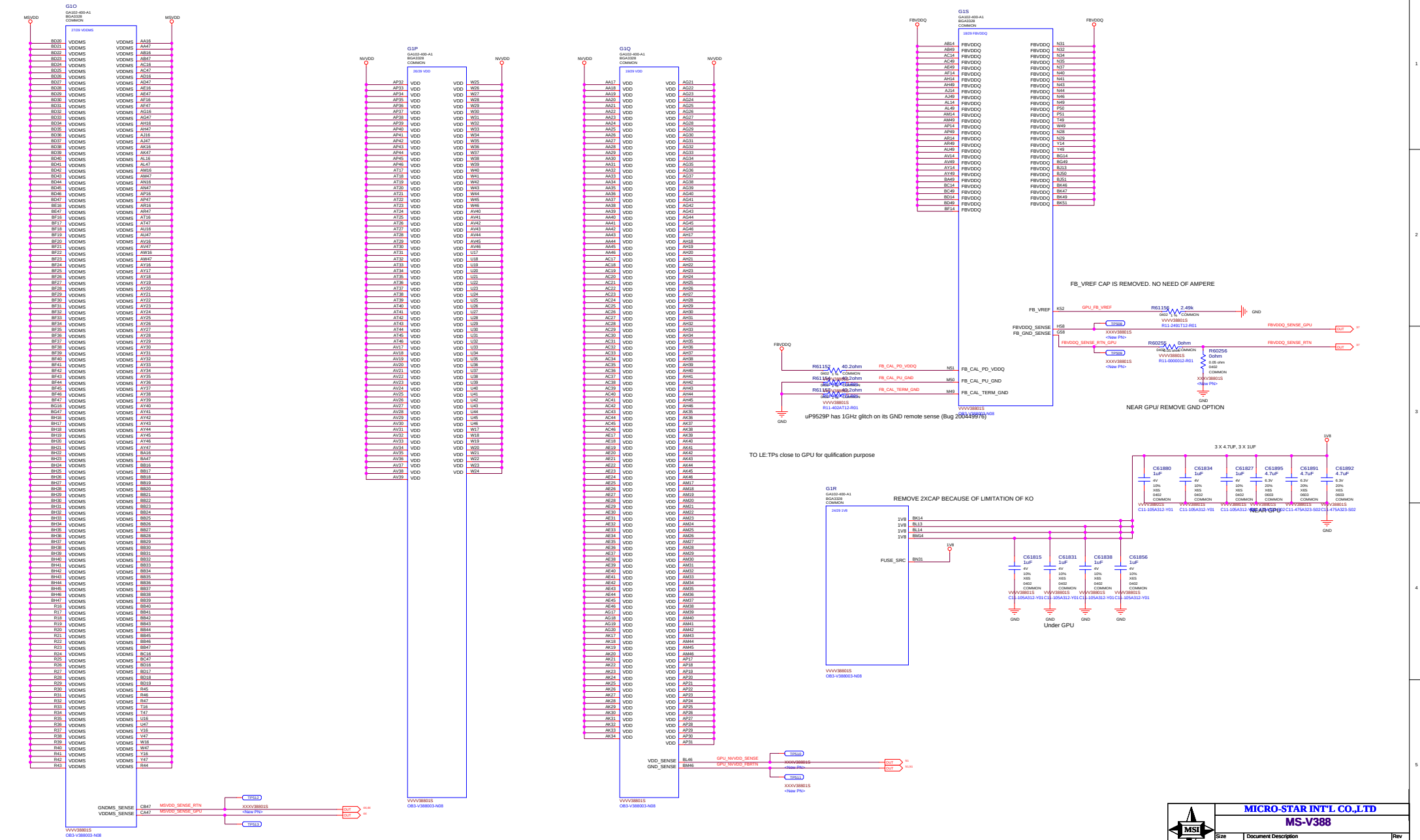








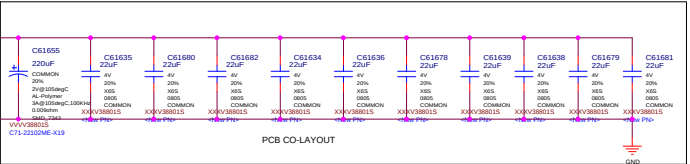
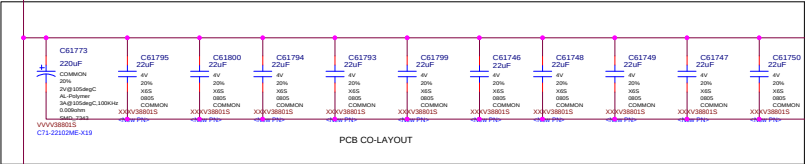
GPU PWR and GND



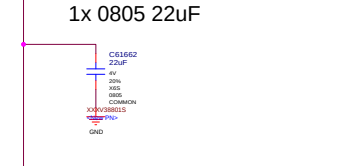
NVVDD

UNDER GPU

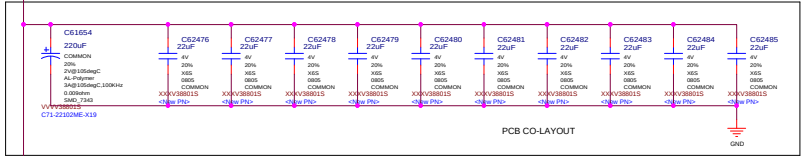
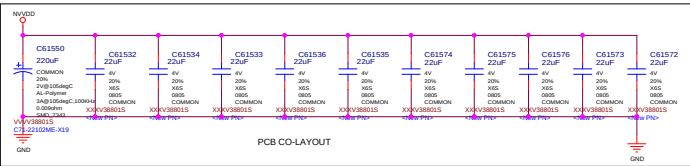
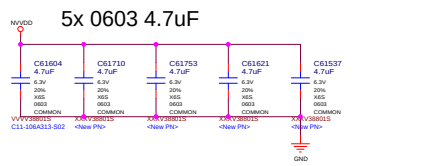
4x SMD7343 220uF (EACH CO-LAYOUT WITH 10x 0805 22uF)



1x 0805 22uF

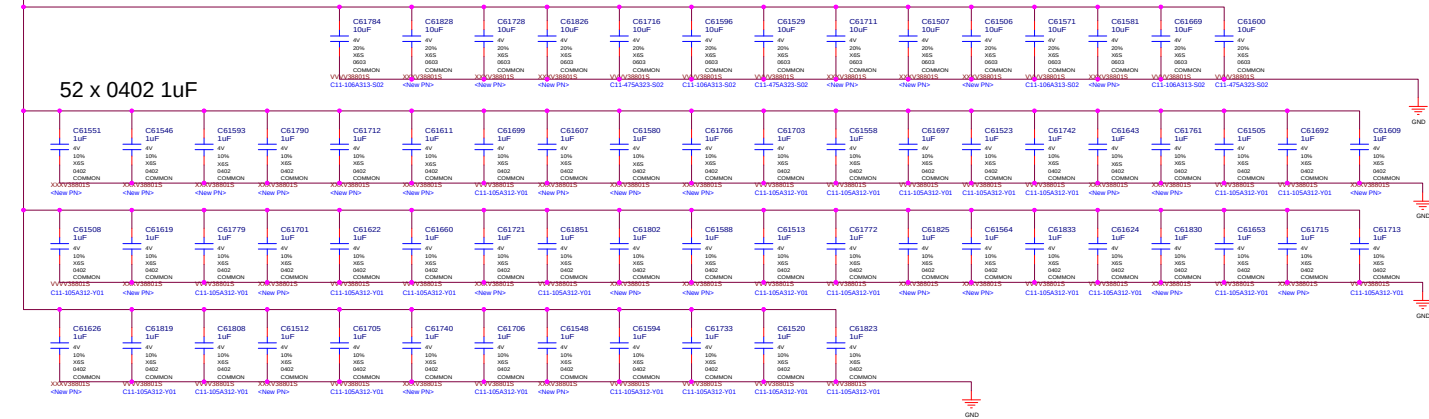


5x 0603 4.7uF



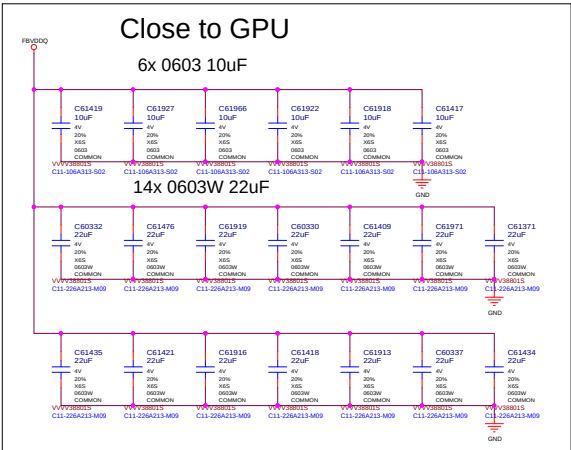
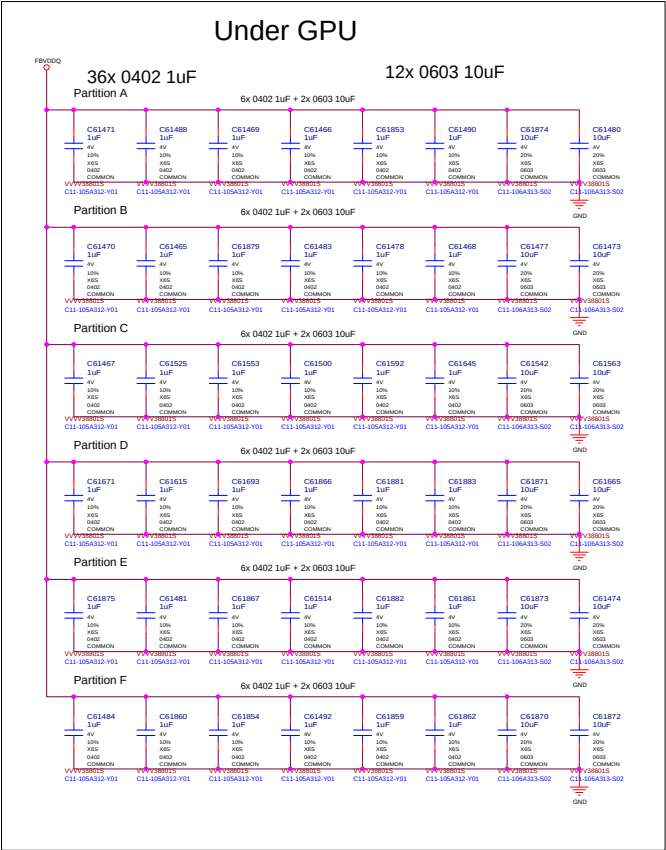
14x 0603 10uF

52 x 0402 1uF



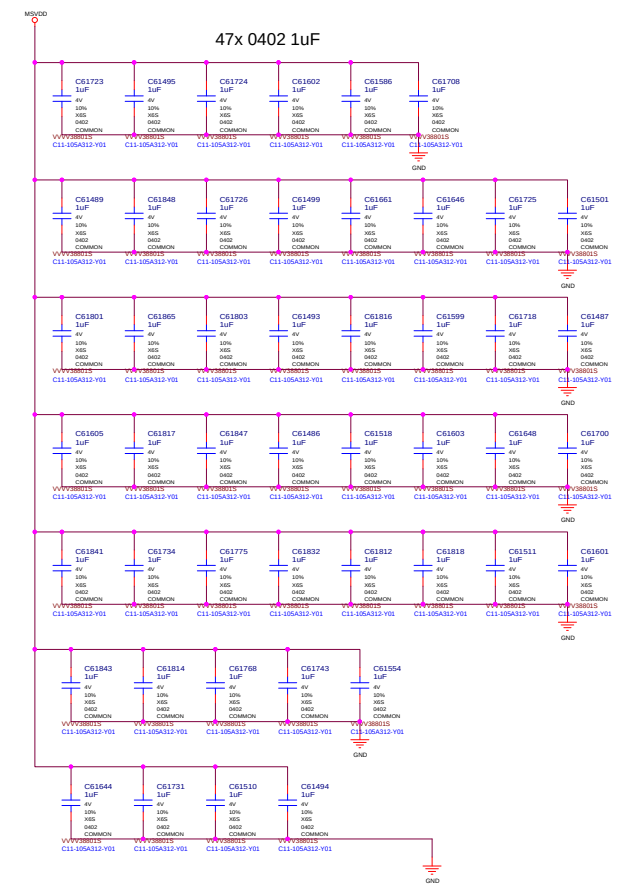
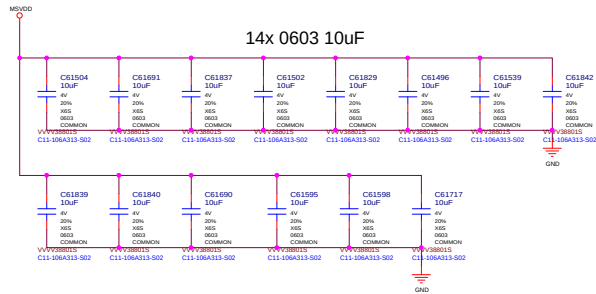
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| MS-V388                       |                       |                |
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| Custom                        | GPU: NVVDD DECOUPLING | 8.0            |
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FBVDDQ

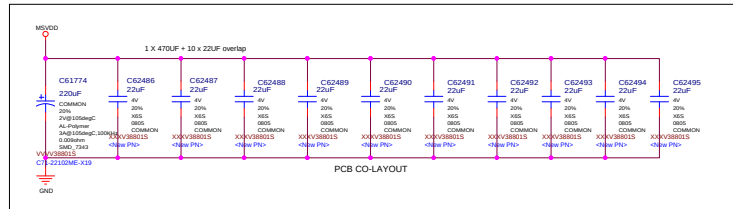


## MSVDD

## UNDER GPU



## 2x SMD7343 220uF (EACH CO-LAYOUT WITH 10x 22uF 0805)



REMOVED 3x 1uF 0402 DUE TO SPACE RESTRICTION

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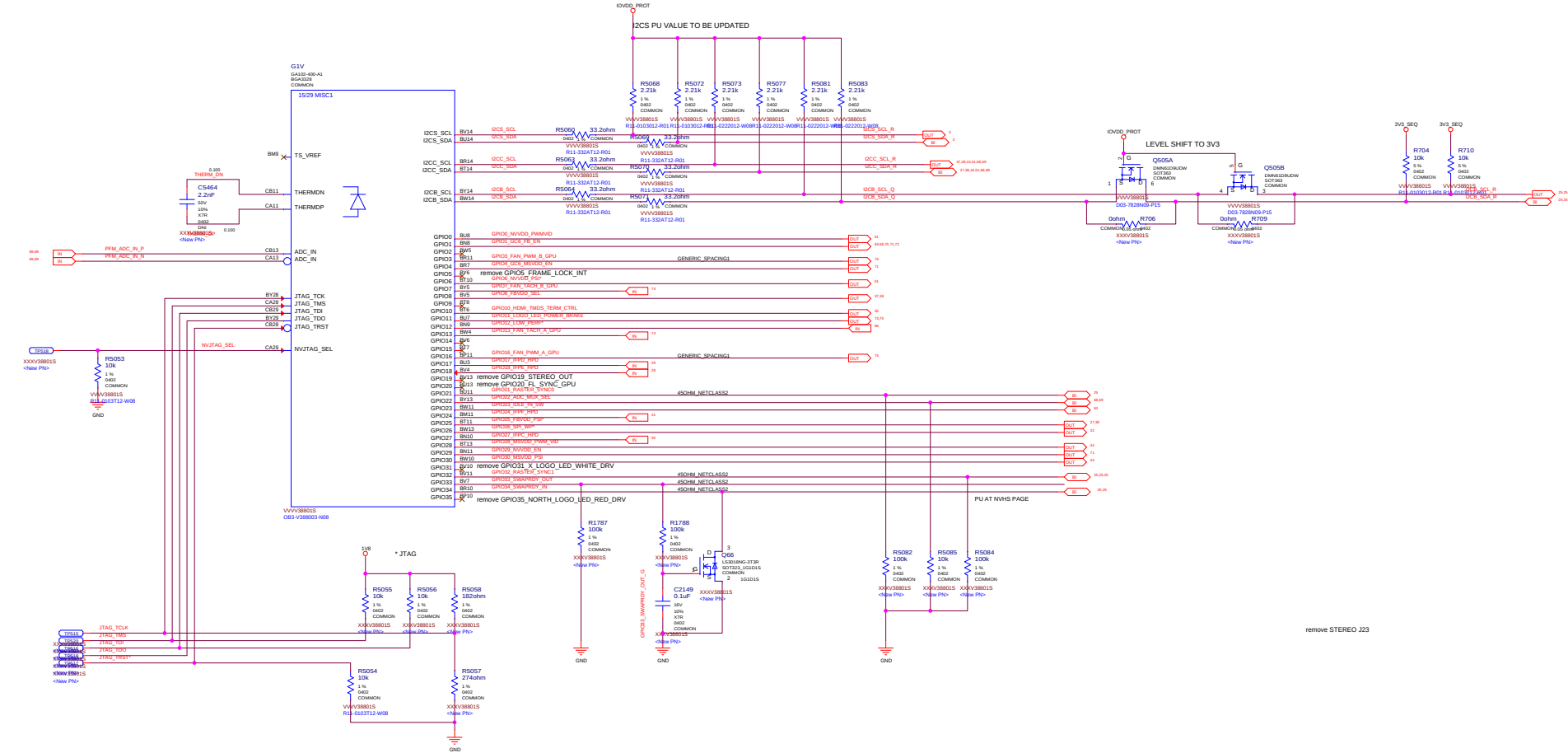


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| MS-V388                        |                      |          |
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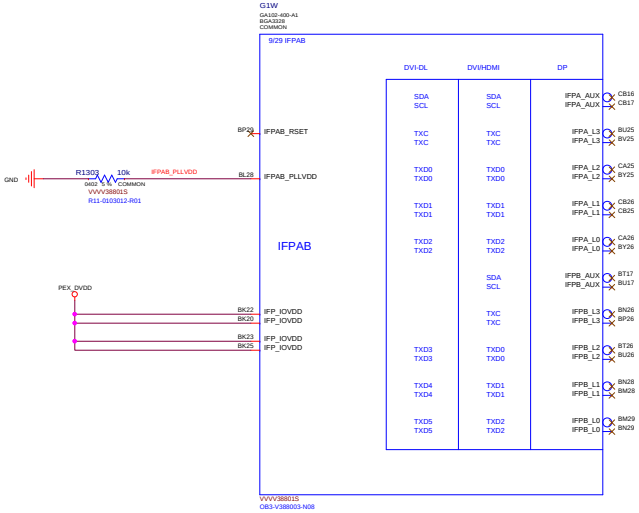




MISC1: Fan, Thermal, JTAG, GPIO, STEREO

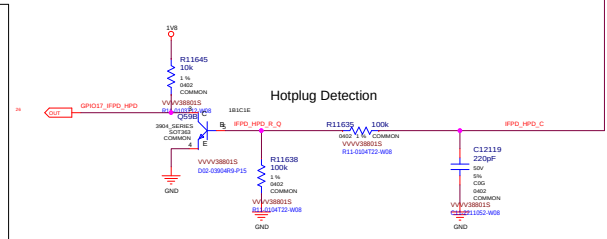
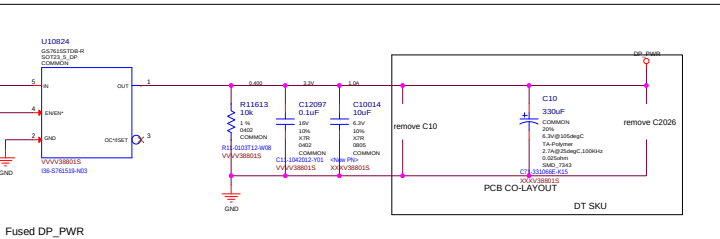
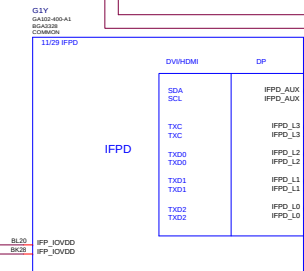
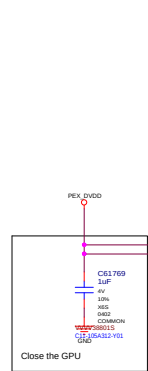


IFPA UNUSED, IFPB UNUSED





# IFPD DP







MISC: ROM, Straps

| STRAP2 | STRAP1 | STRAP0 | RAMCFG[4:0] |                          |
|--------|--------|--------|-------------|--------------------------|
| L      | L      | L      | 00000       | RAMCFG MICRON 8Gb 19Gbps |
| L      | H      | L      | 00010       | RAMCFG TBD               |
| H      | L      | L      | 00100       | RAMCFG TBD               |
| H      | L      | H      | 00101       | RAMCFG TBD               |
| H      | L      | H      | 00101       | RAMCFG MICRON 8Gb 21Gbps |

H=High :Tied to 1.8V  
M=Middle:Tied to 0.9V  
L=Low :Tied to 0V

DEFAULT

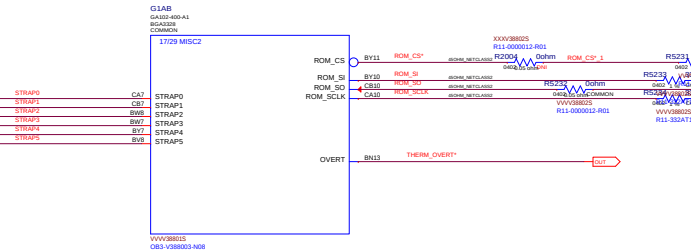
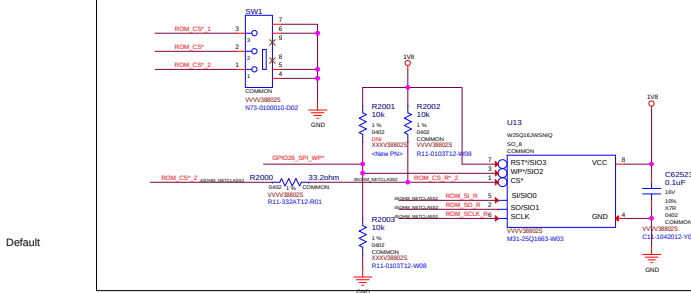
| ROM_SO | ROM_SI | ROM_SCLK | SMARTFAN[2:0]_FS_OVERT | 1:ENABLE 0:DISABLE |
|--------|--------|----------|------------------------|--------------------|
| H      | H      | H        | 0111                   | FS_OVERT ENABLE    |
| L      | L      | L        | 0000                   | FS_OVERT DISABLE   |

DEFAULT

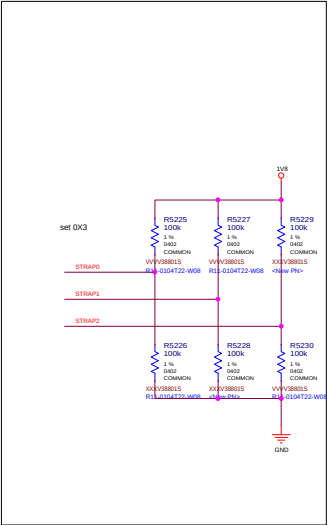
| STRAP5 | STRAP4 | STRAP3 | SMB_ALT_ADDR | DEVID_SEL | PCIE_CFG | VGA_DEVICE |
|--------|--------|--------|--------------|-----------|----------|------------|
| M      | H      | H      | 1            | 1         | 1        | 1          |
| M      | H      | L      | 1            | H         | 1        | 0          |
| M      | L      | H      | 1            | 1         | 0        | 1          |
| M      | L      | L      | 1            | 1         | 0        | 0          |
| L      | H      | M      | 1            | 0         | 1        | 1          |
| L      | M      | H      | 1            | 0         | 1        | 0          |
| L      | M      | L      | 1            | 0         | 0        | 1          |
| L      | L      | M      | 1            | 0         | 0        | 0          |
| H      | H      | H      | 0            | 1         | 1        | 1          |
| H      | H      | L      | 0            | 1         | 0        | 1          |
| H      | L      | H      | 0            | 1         | 0        | 0          |
| H      | L      | L      | 0            | 1         | 0        | 0          |
| L      | H      | H      | 0            | 0         | 1        | 1          |
| L      | H      | L      | 0            | 0         | 1        | 0          |
| L      | L      | H      | 0            | 0         | 0        | 1          |
| L      | L      | L      | 0            | 0         | 0        | 0          |

Default

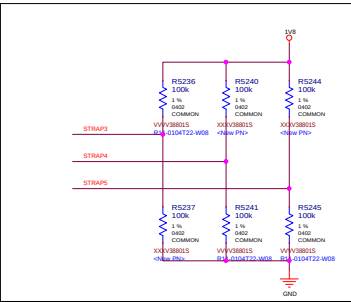
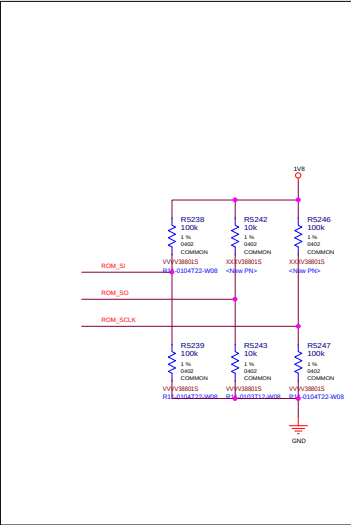
Dual BIOS



GROUP0

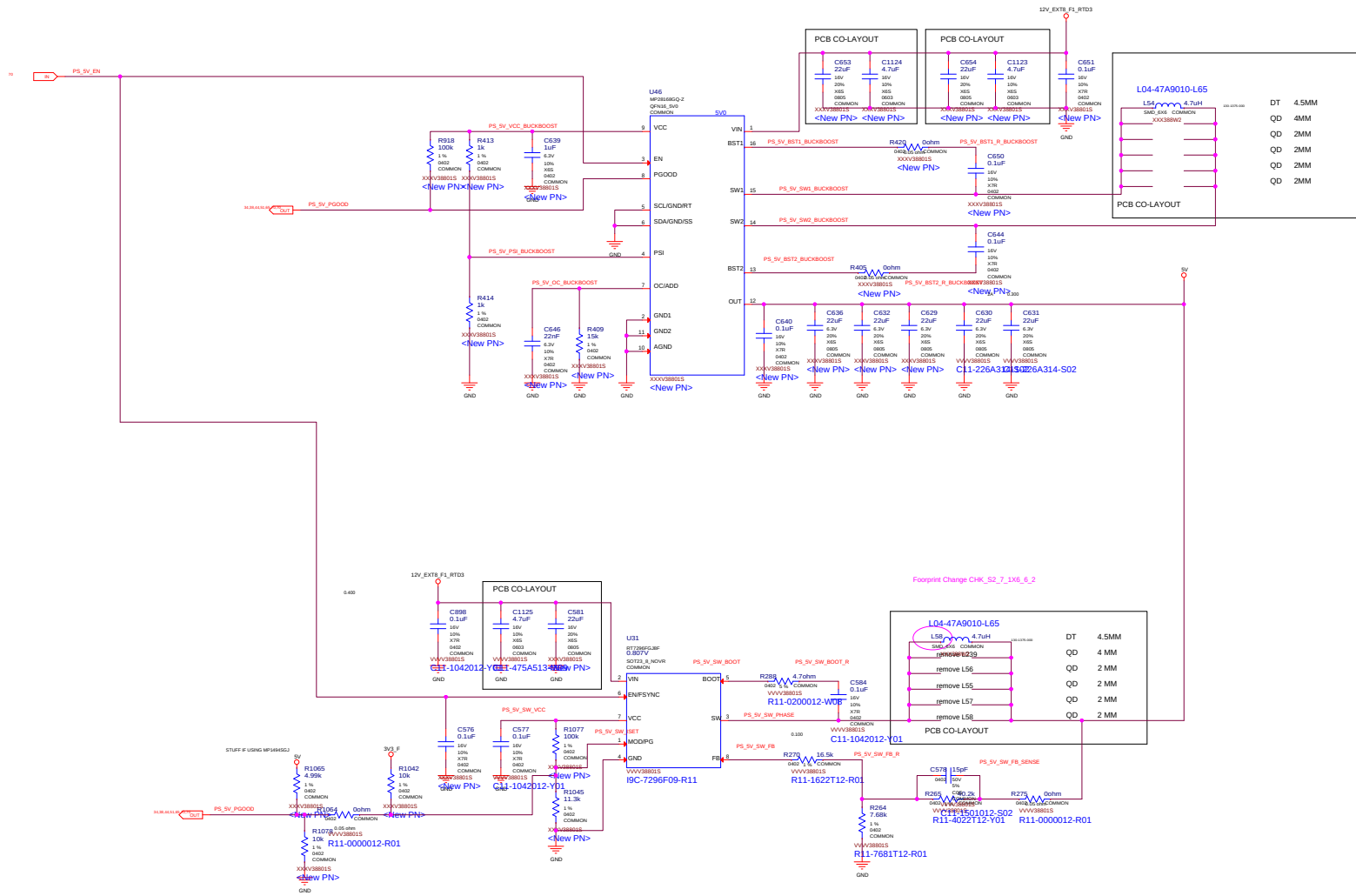


GROUP1

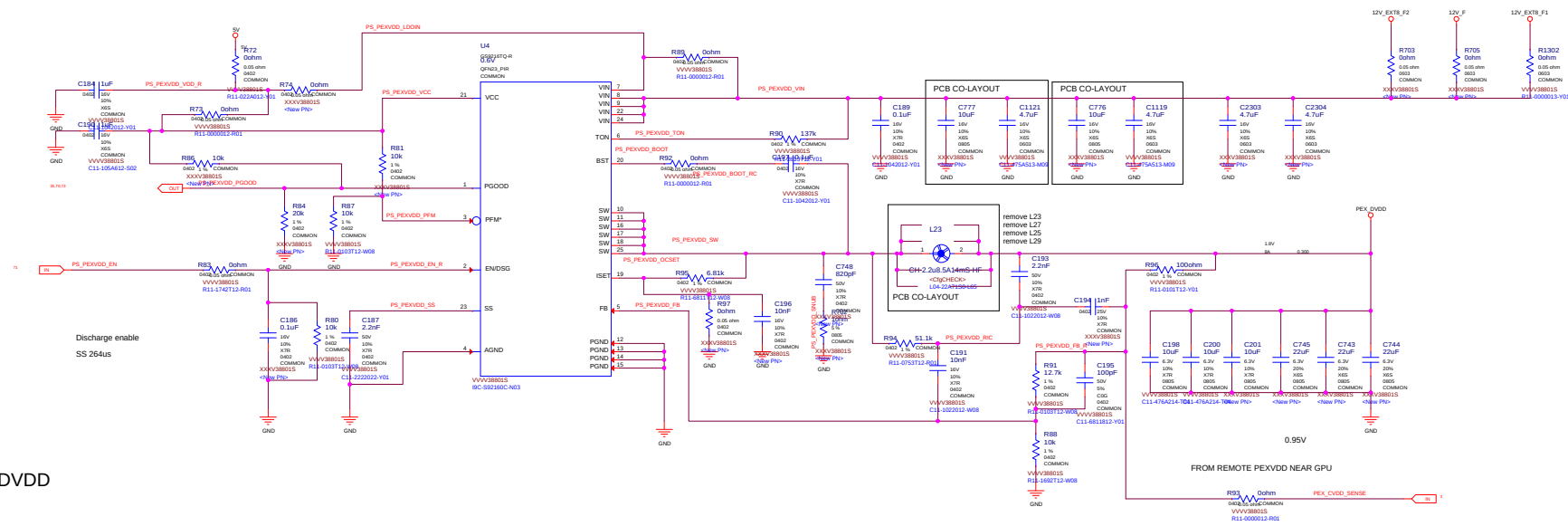




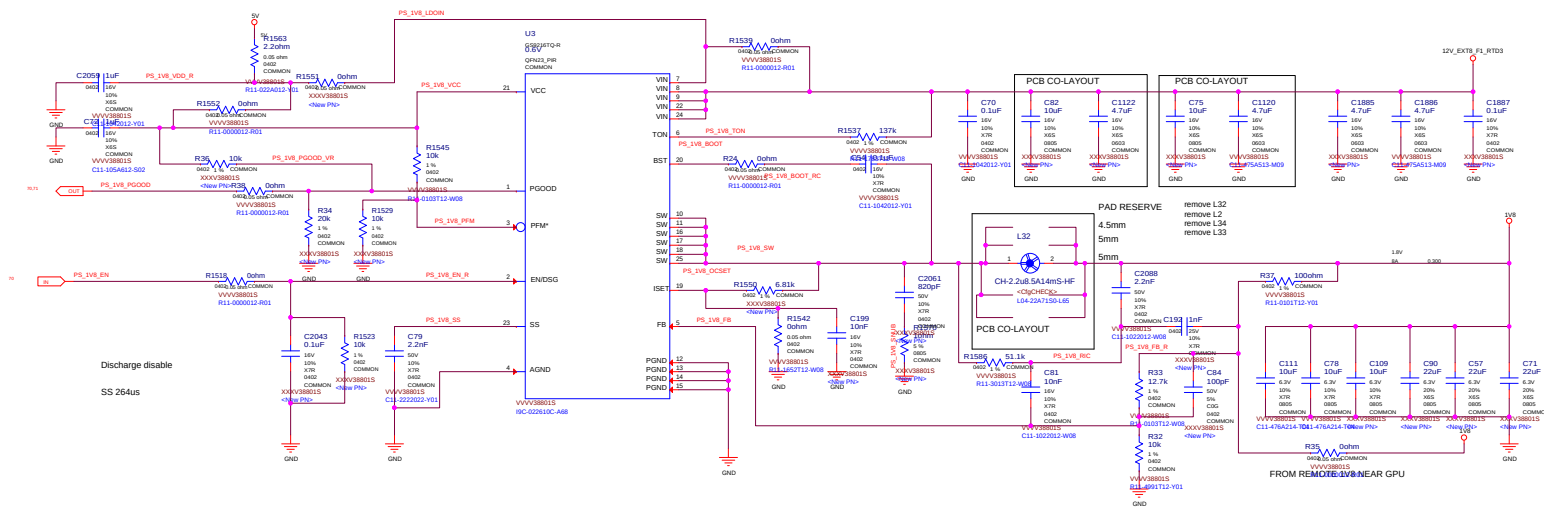




PS: PEX\_DVDD 1V8 Rail



## PEX\_DVDD



1V8



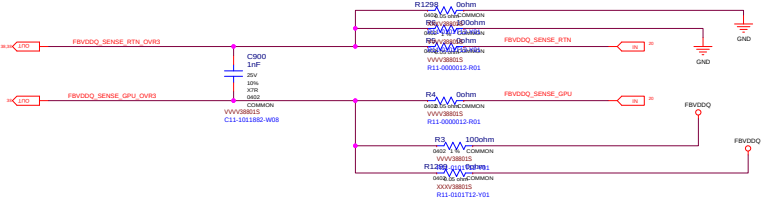
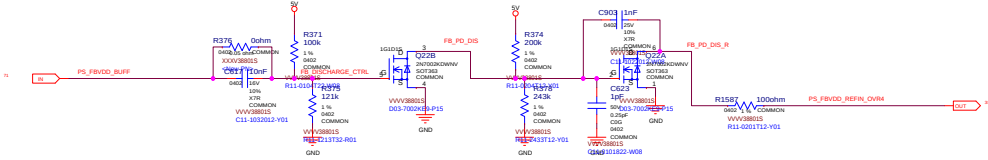
**MICRO-STAR INT'L CO.,LTD**  
**MS-V388**

|                               |                                                      |            |
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|--------------------------------|----------------------|----------|
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| MS-V388                        |                      |          |
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| Custom                         | BLANK                | 8.0      |
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FOR DT SKU













REFER TO PCB FOR BOM STUFFING

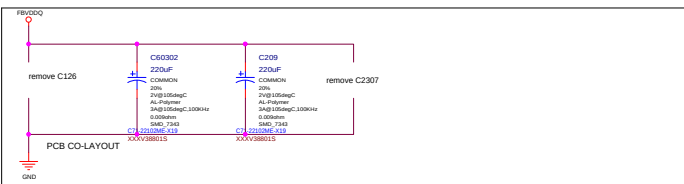
PH1 TOP



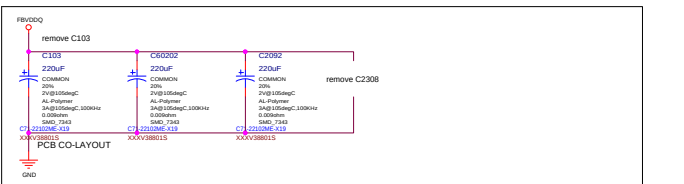
PH3 TOP



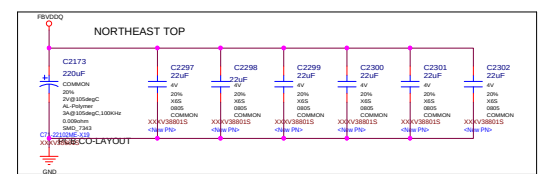
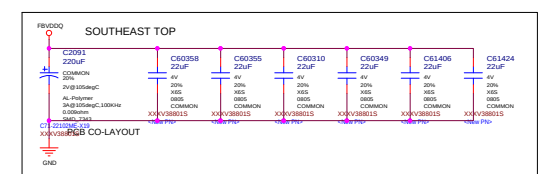
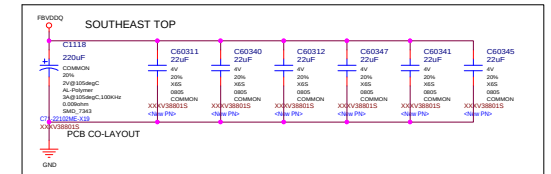
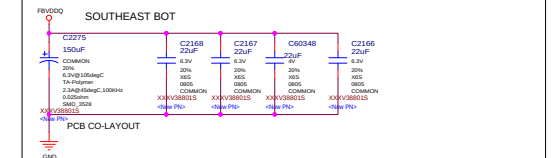
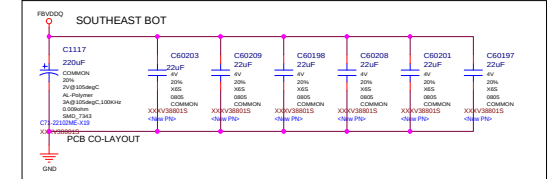
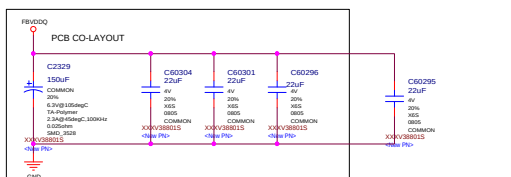
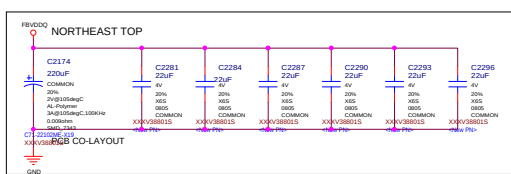
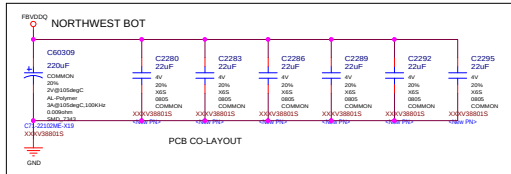
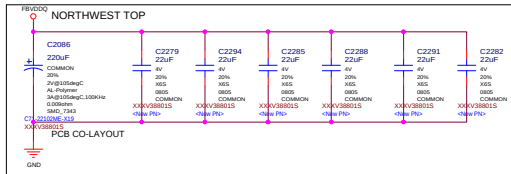
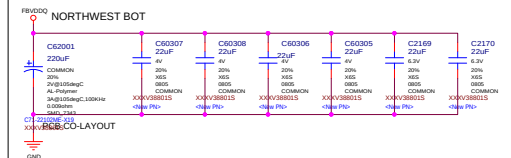
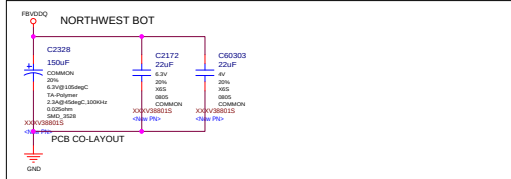
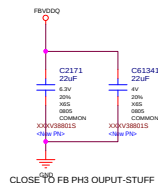
PH2 TOP



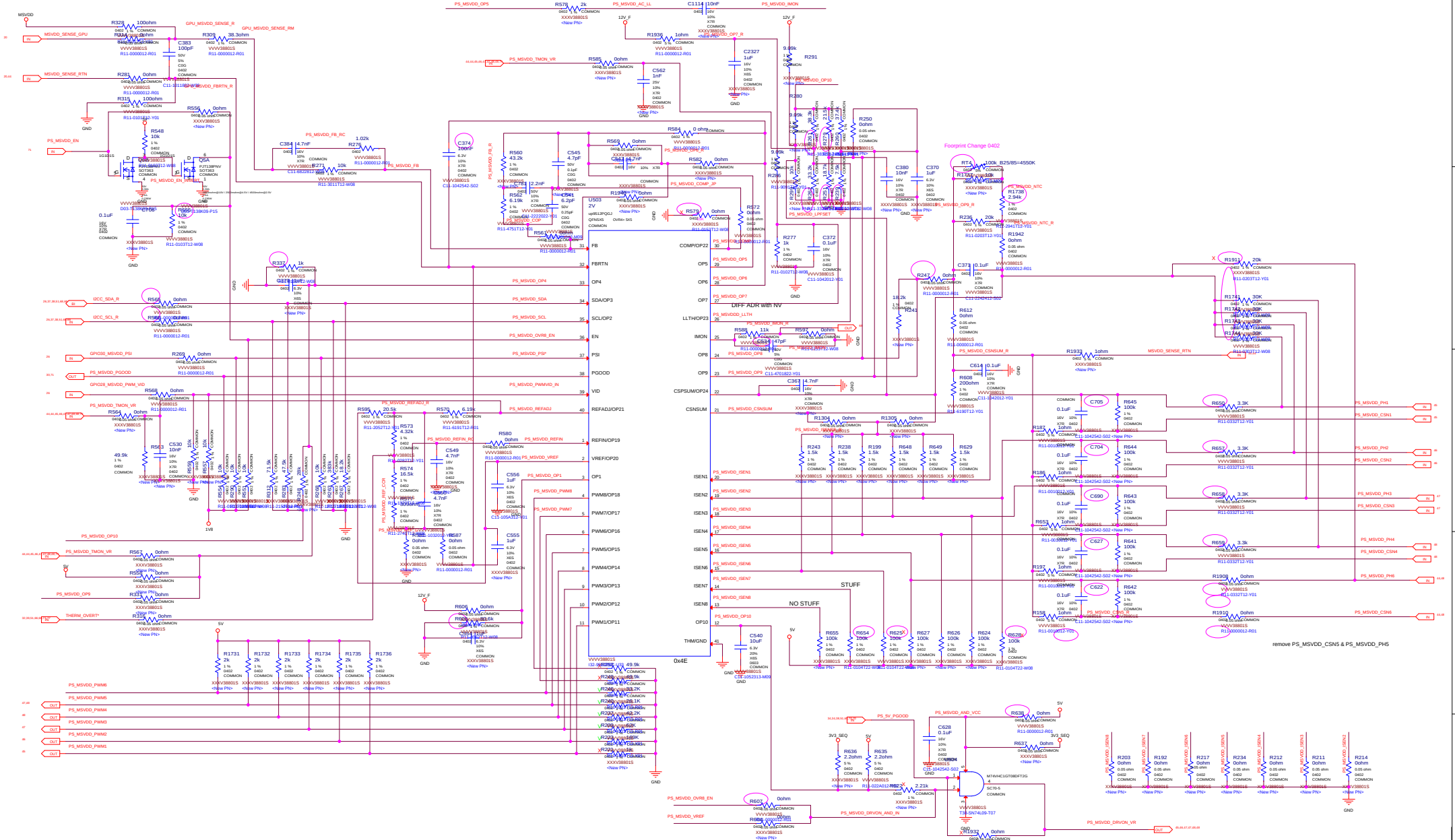
PH4 TOP



PH4 TOP



## PS: MSVDD



MICRO-STAR INT'L CO.,LTD

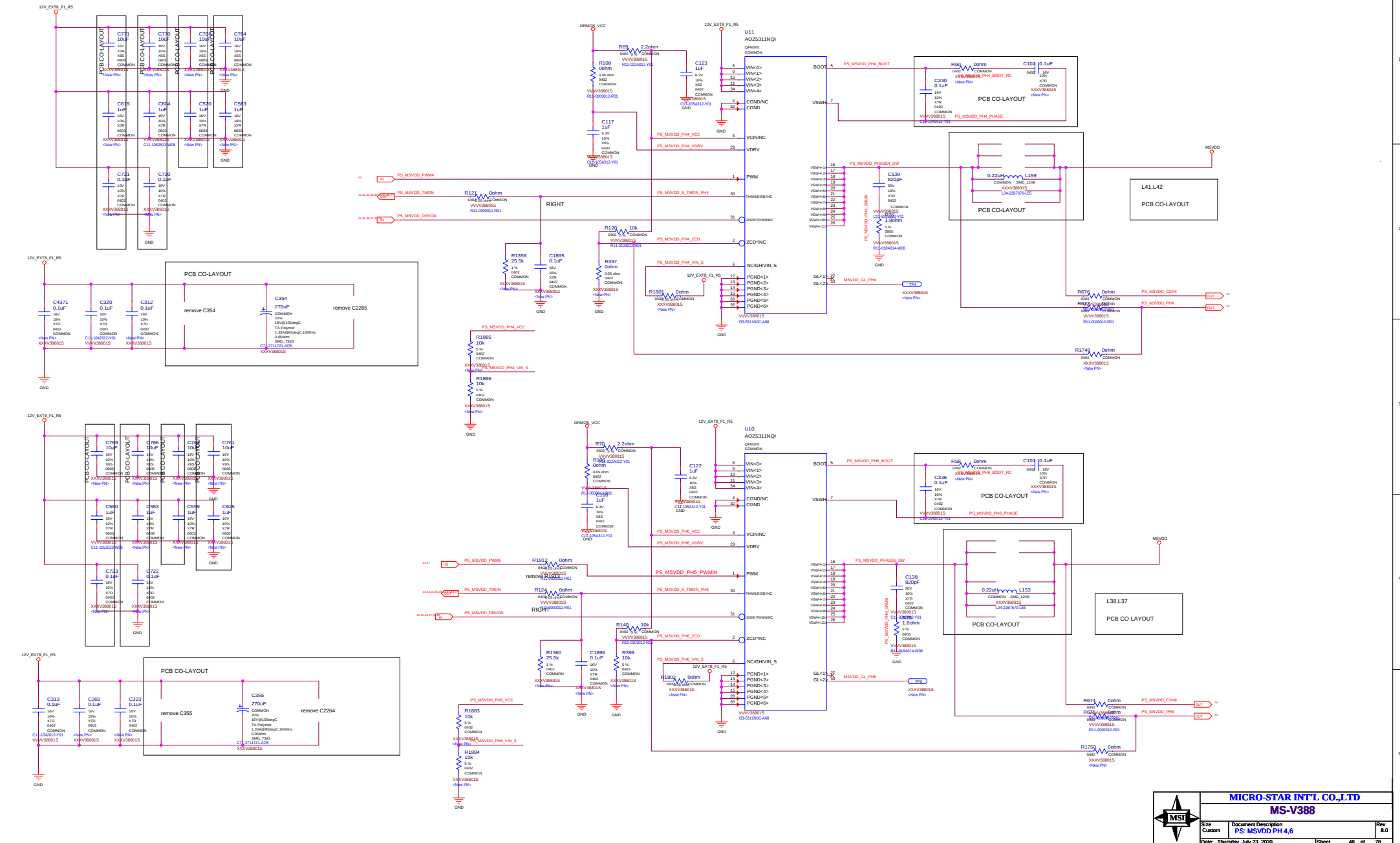
MS-V388

|                |                                   |            |
|----------------|-----------------------------------|------------|
| Size<br>Custom | Document Description<br>PS: MSVDD | Rev<br>8.0 |
|----------------|-----------------------------------|------------|





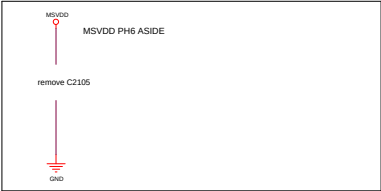
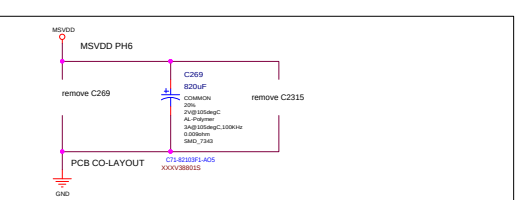
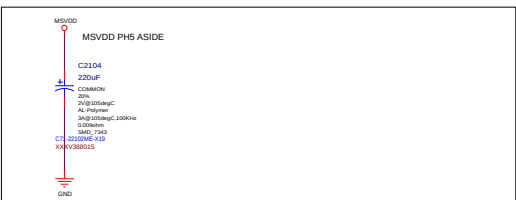
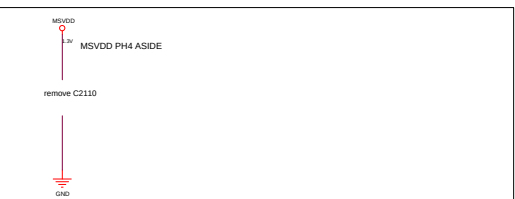
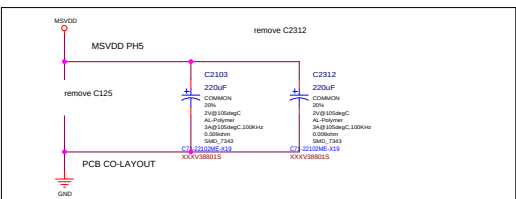
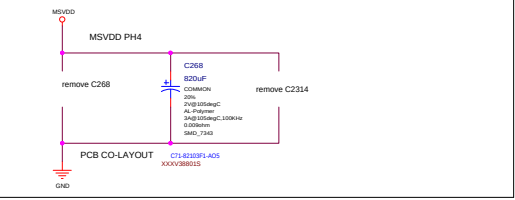
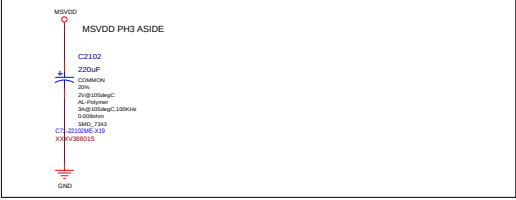
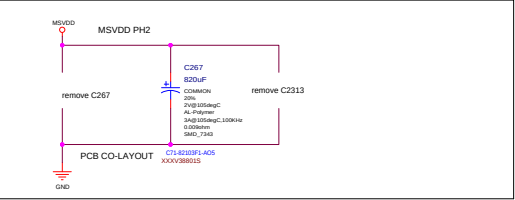
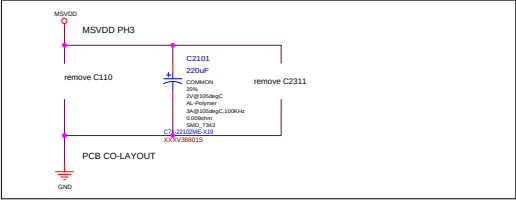
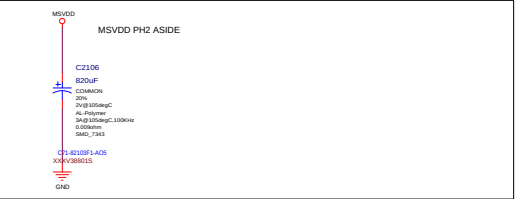
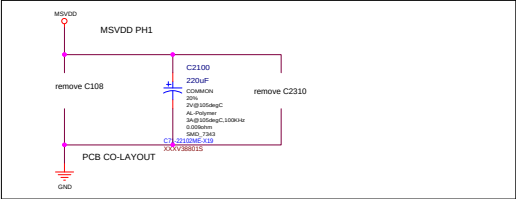






PS:MSVDD OUTPUT CAP (TOP)

REFER TO PCB FOR BOM STUFFING

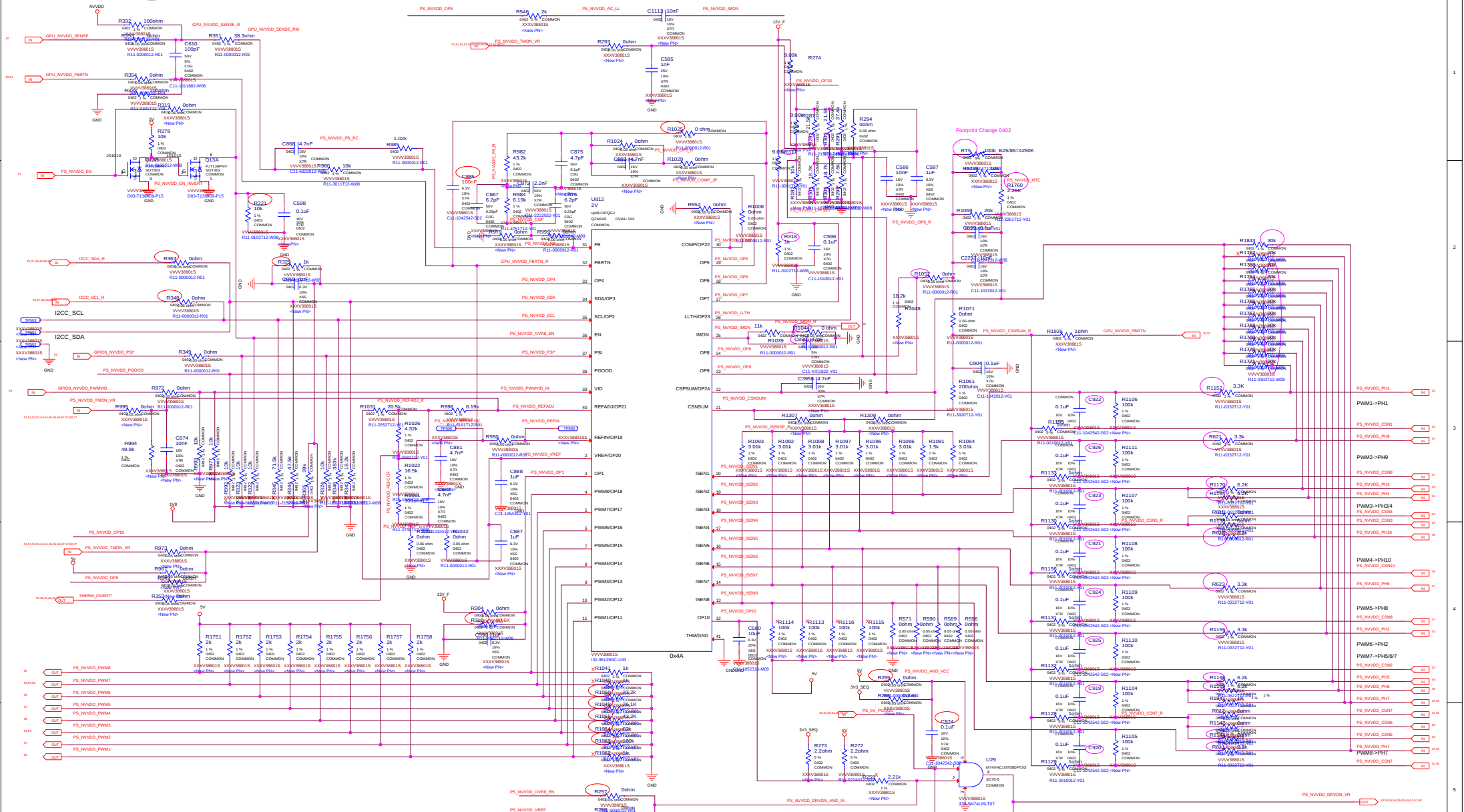


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|                                |                      |          |
|--------------------------------|----------------------|----------|
| MICRO-STAR INT'L CO.,LTD       |                      |          |
| MS-V388                        |                      |          |
| Size                           | Document Description | Rev      |
| Custom                         | BLANK                | 8.0      |
| Date: Wednesday, July 22, 2020 | Sheet                | 50 of 78 |

## PS: NVVDD Controller\_OVR8

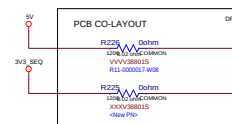


**MICRO-STAR INT'L CO.,LTD**  
**MS-V388**

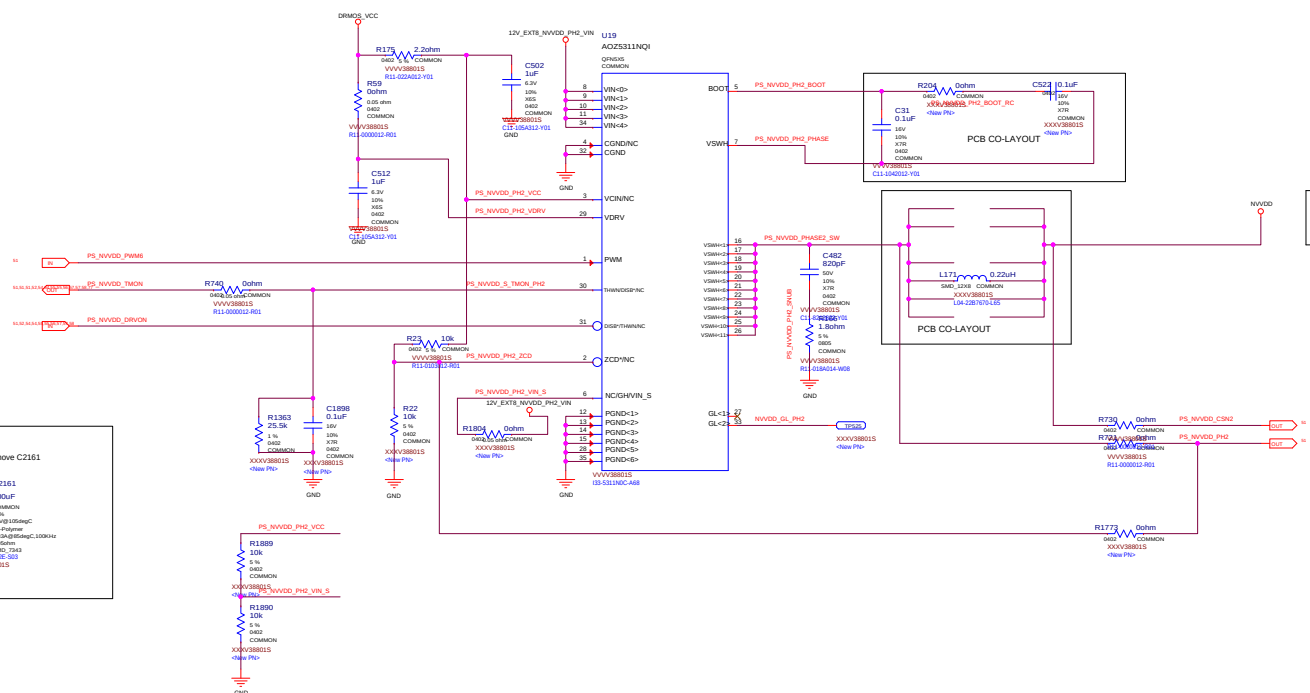
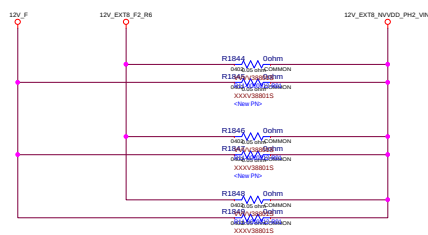
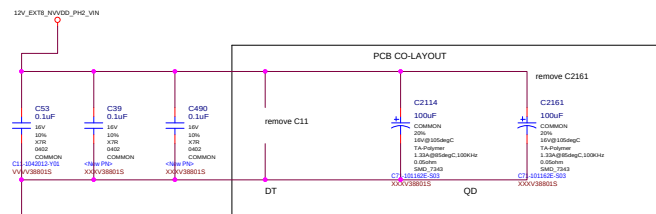
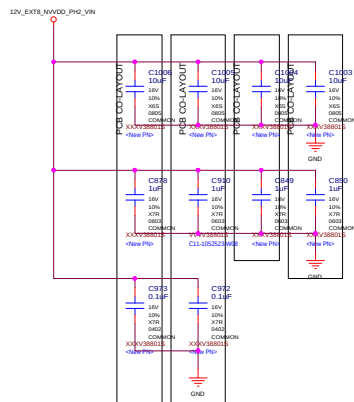
|                               |                                                         |            |
|-------------------------------|---------------------------------------------------------|------------|
| Size<br>Custom                | Document Description<br><b>PS: NVDD Controller_OVR8</b> | Rev<br>8.0 |
| Date: Thursday, July 23, 2020 | Sheet                                                   | 51 of 78   |



# PS: NVVDD Phase 2 (PWM6)



MPS DR MOS WITH 3V3 SUPPLY



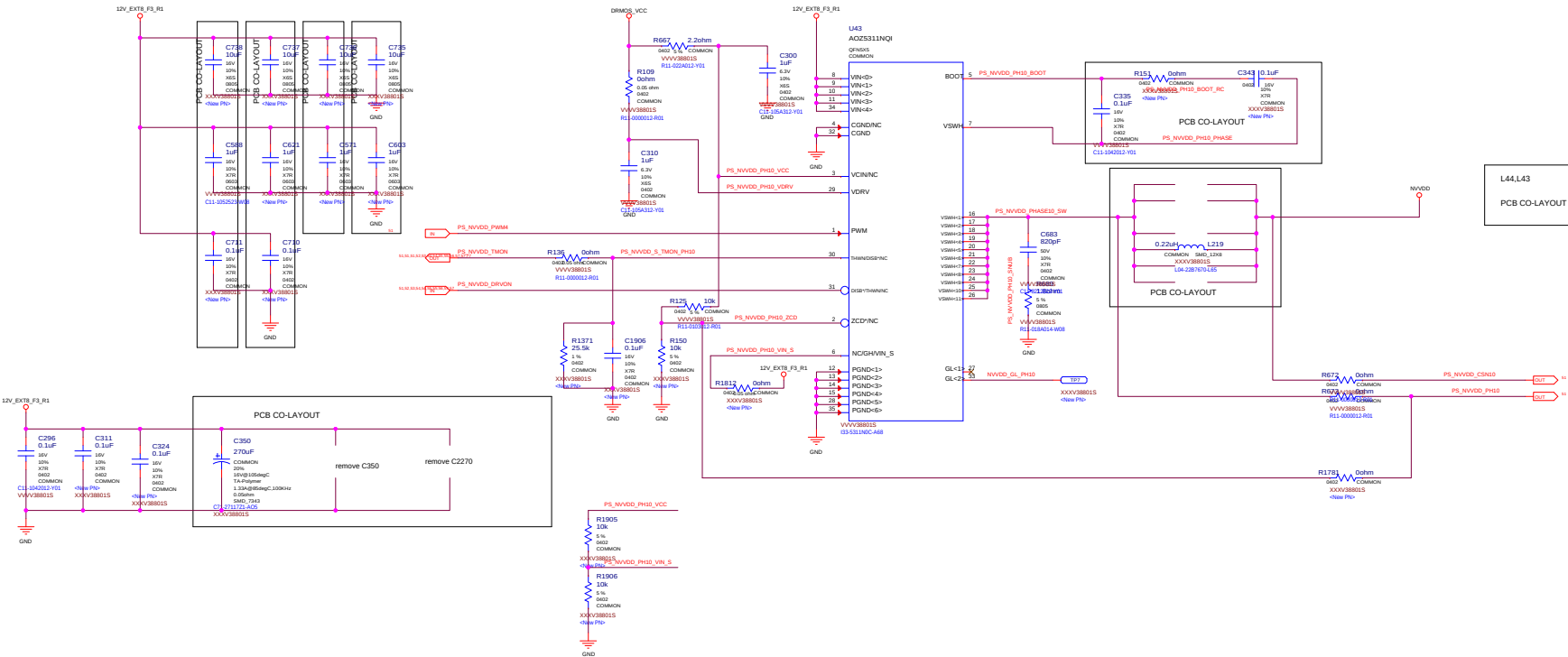












|   |   |   |   |   |   |   |   |   |
|---|---|---|---|---|---|---|---|---|
|   | A | B | C | D | E | F | G | H |
| 1 |   |   |   |   |   |   |   |   |
| 2 |   |   |   |   |   |   |   |   |
| 3 |   |   |   |   |   |   |   |   |
| 4 |   |   |   |   |   |   |   |   |
| 5 |   |   |   |   |   |   |   |   |
|   | A | B | C | D | E | F | G | H |



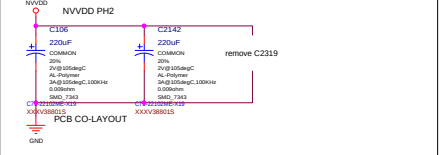
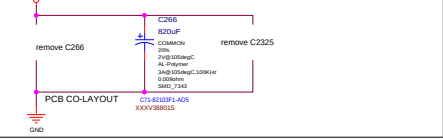
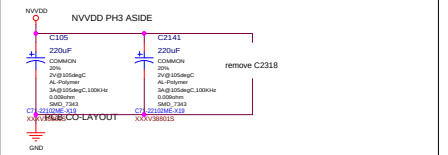
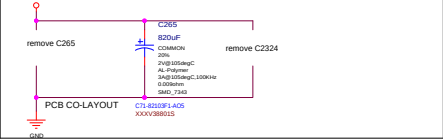
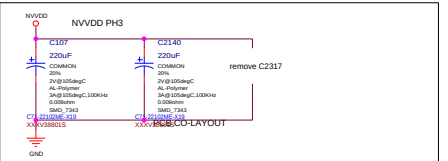
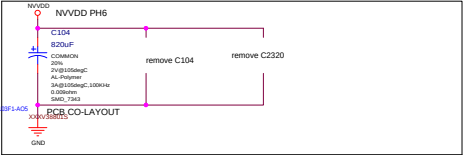
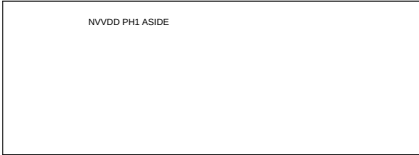
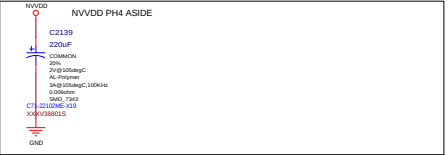
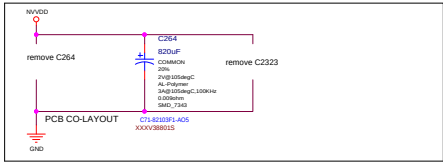
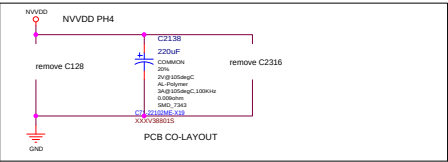
|                                |                      |          |
|--------------------------------|----------------------|----------|
| MICRO-STAR INT'L CO.,LTD       |                      |          |
| MS-V388                        |                      |          |
| Size                           | Document Description | Rev      |
| Custom                         | Colayout Notes       | 8.0      |
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|--------------------------------|----------------------|----------|
| MICRO-STAR INT'L CO.,LTD       |                      |          |
| MS-V388                        |                      |          |
| Size                           | Document Description | Rev      |
| Custom                         | BLANK                | 8.0      |
| Date: Wednesday, July 22, 2020 | Sheet                | 60 of 78 |

REFER TO PCB FOR BOM STUFFING



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|--------------------------------|----------------------|----------|
| MICRO-STAR INT'L CO.,LTD       |                      |          |
| MS-V388                        |                      |          |
| Size                           | Document Description | Rev      |
| Custom                         | BLANK                | 8.0      |
| Date: Wednesday, July 22, 2020 | Sheet                | 82 of 78 |

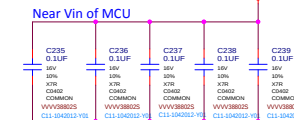


LED\_A

LED\_B

LED\_C

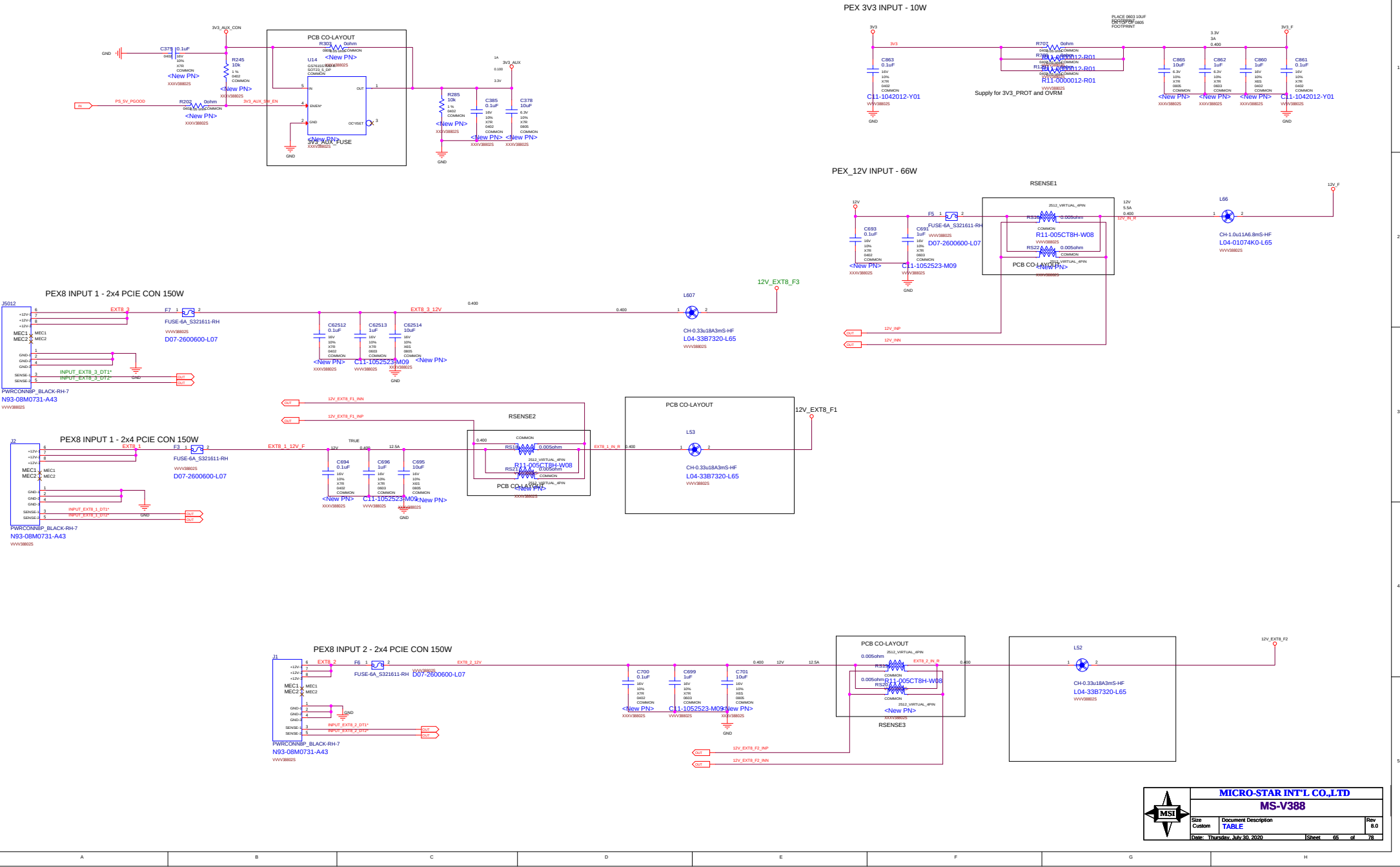
LED\_D



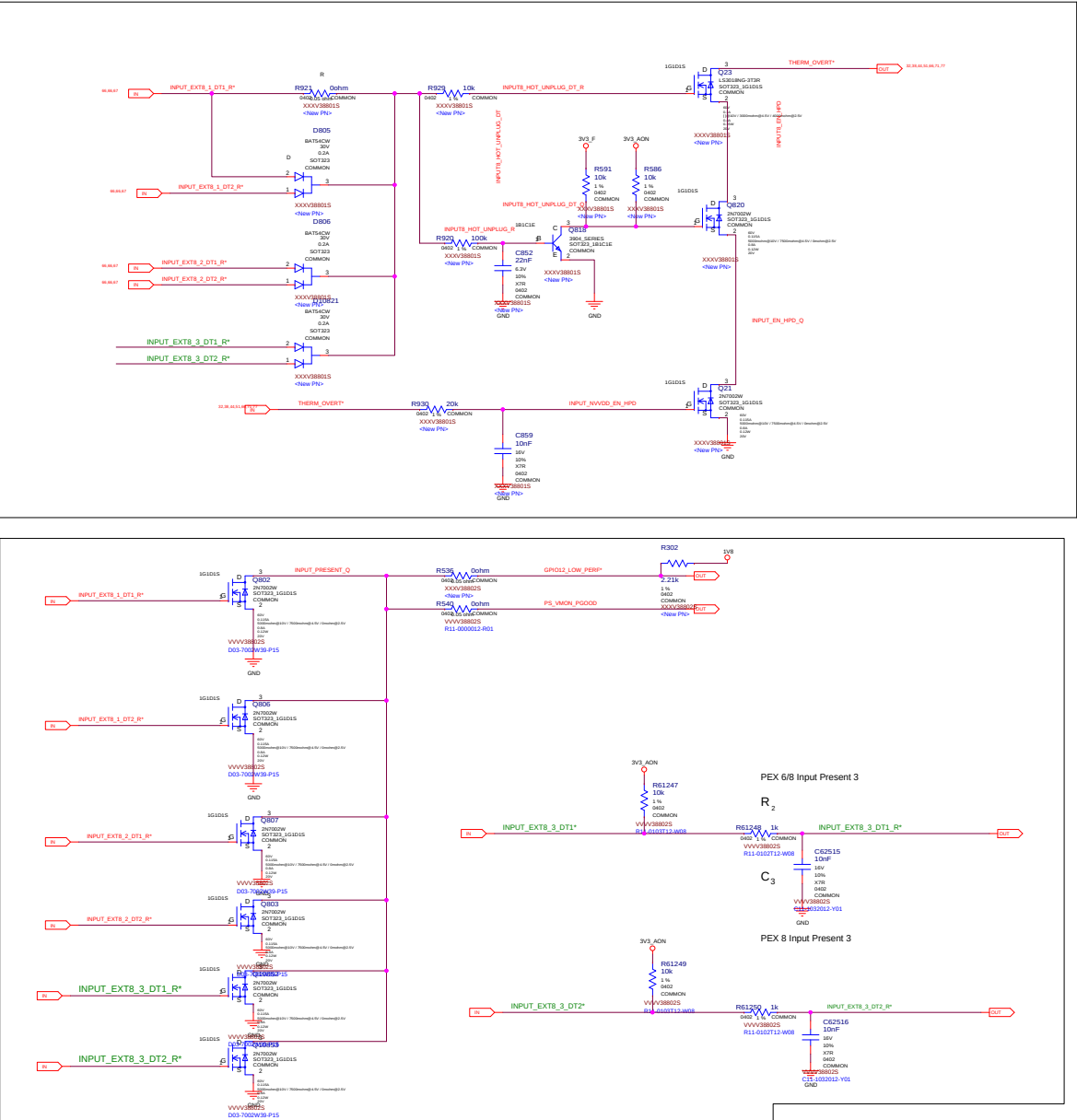
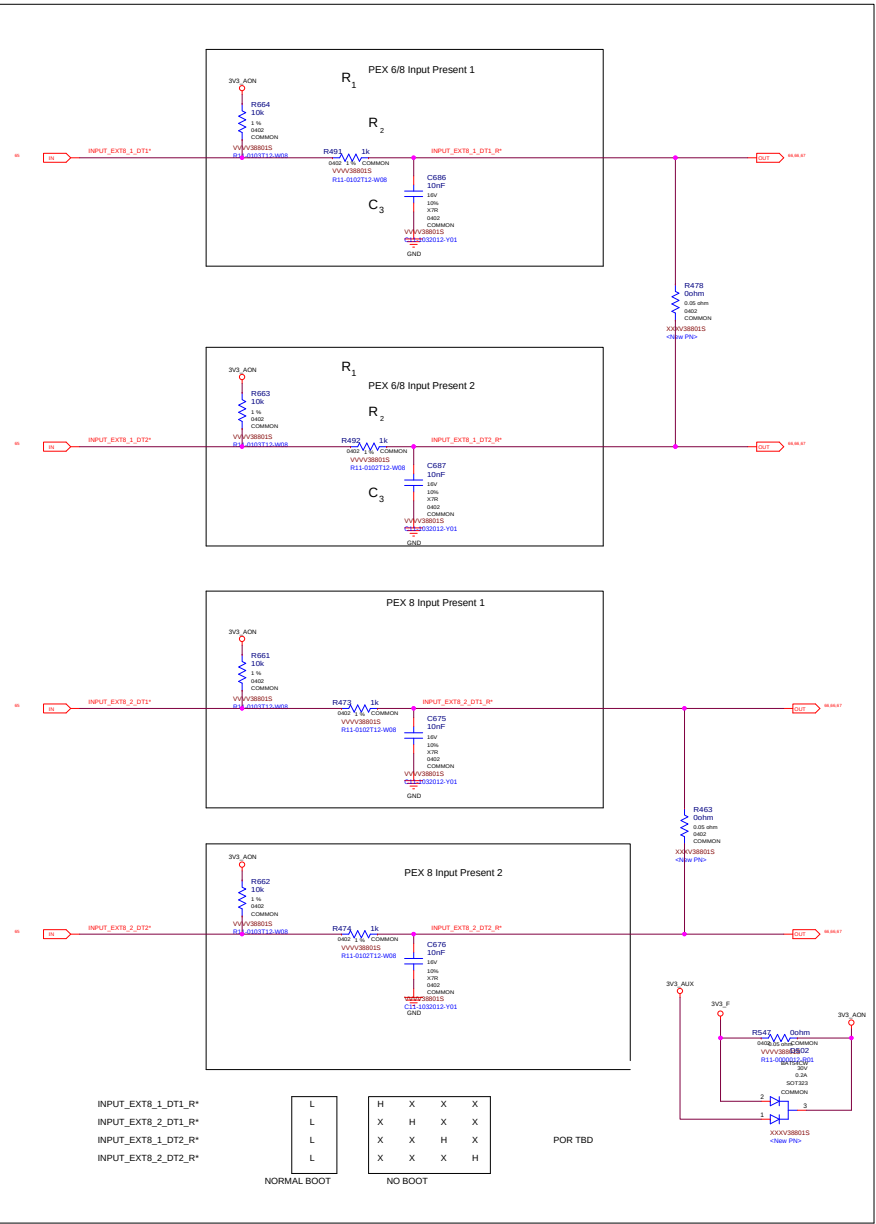
| MICRO-STAR INT'L CO.,LTD      |                      |       |          |
|-------------------------------|----------------------|-------|----------|
| MS-V388                       |                      |       |          |
| Size                          | Document Description | Rev   |          |
| Custom                        | TABLE                | 8.0   |          |
| Date: Thursday, July 23, 2020 |                      | Sheet | 64 of 78 |



PS: Inputs, Filtering, and Monitoring



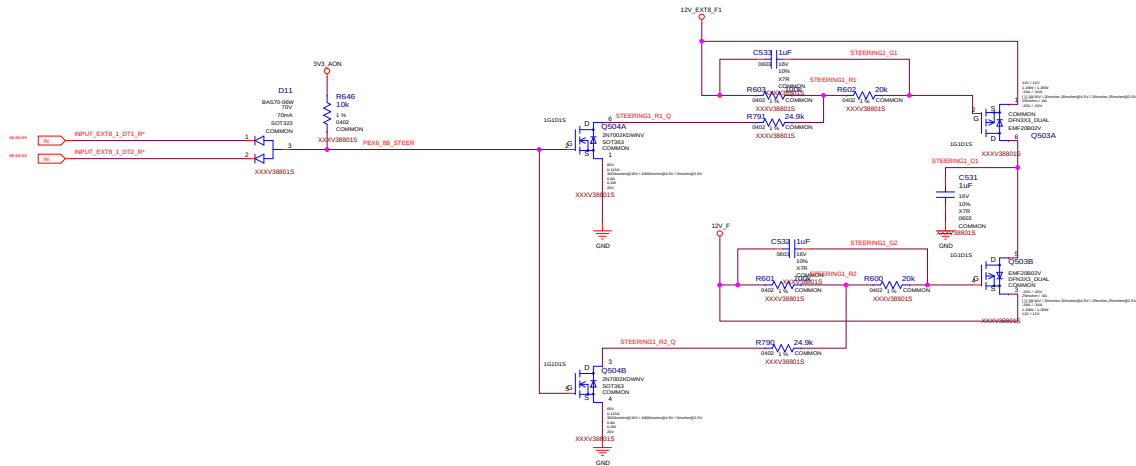
PS: 12V Current Steering & Hot Unplug Detect



PS: Discrete Steering

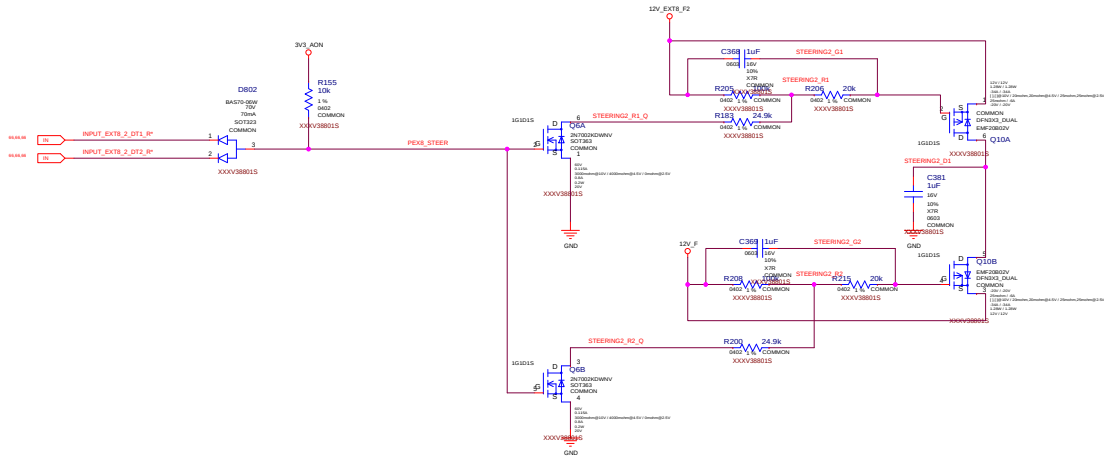
12V CURRENT STEERING (UNDER POWER BOOT):

PEX12V AND 12V\_EXT8\_F1



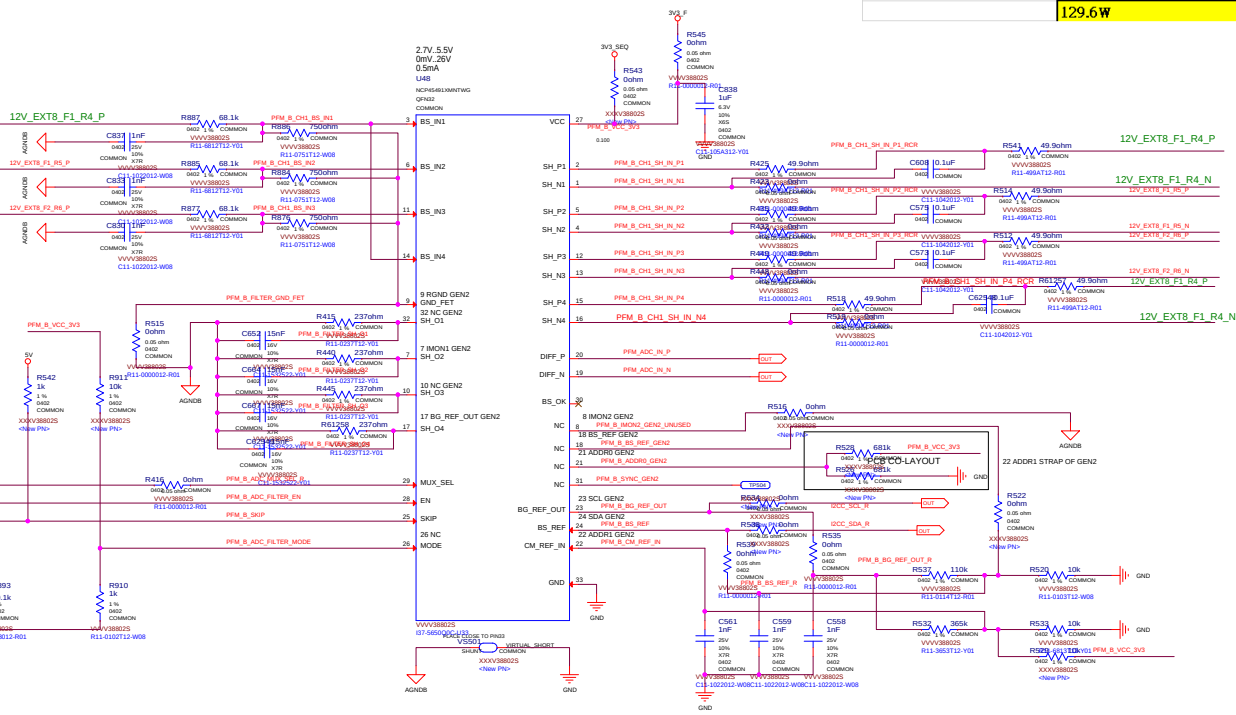
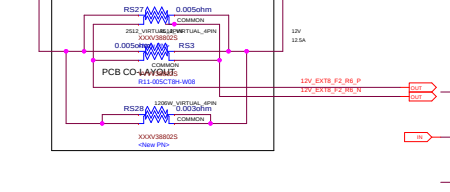
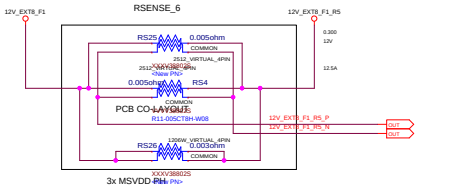
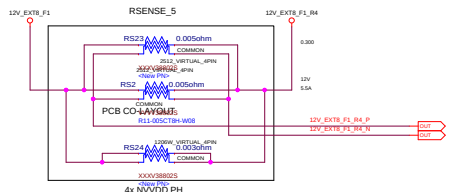
NO STUFF BY DEFAULT REF ONLY

PEX12V AND 12V\_EXT8\_F2





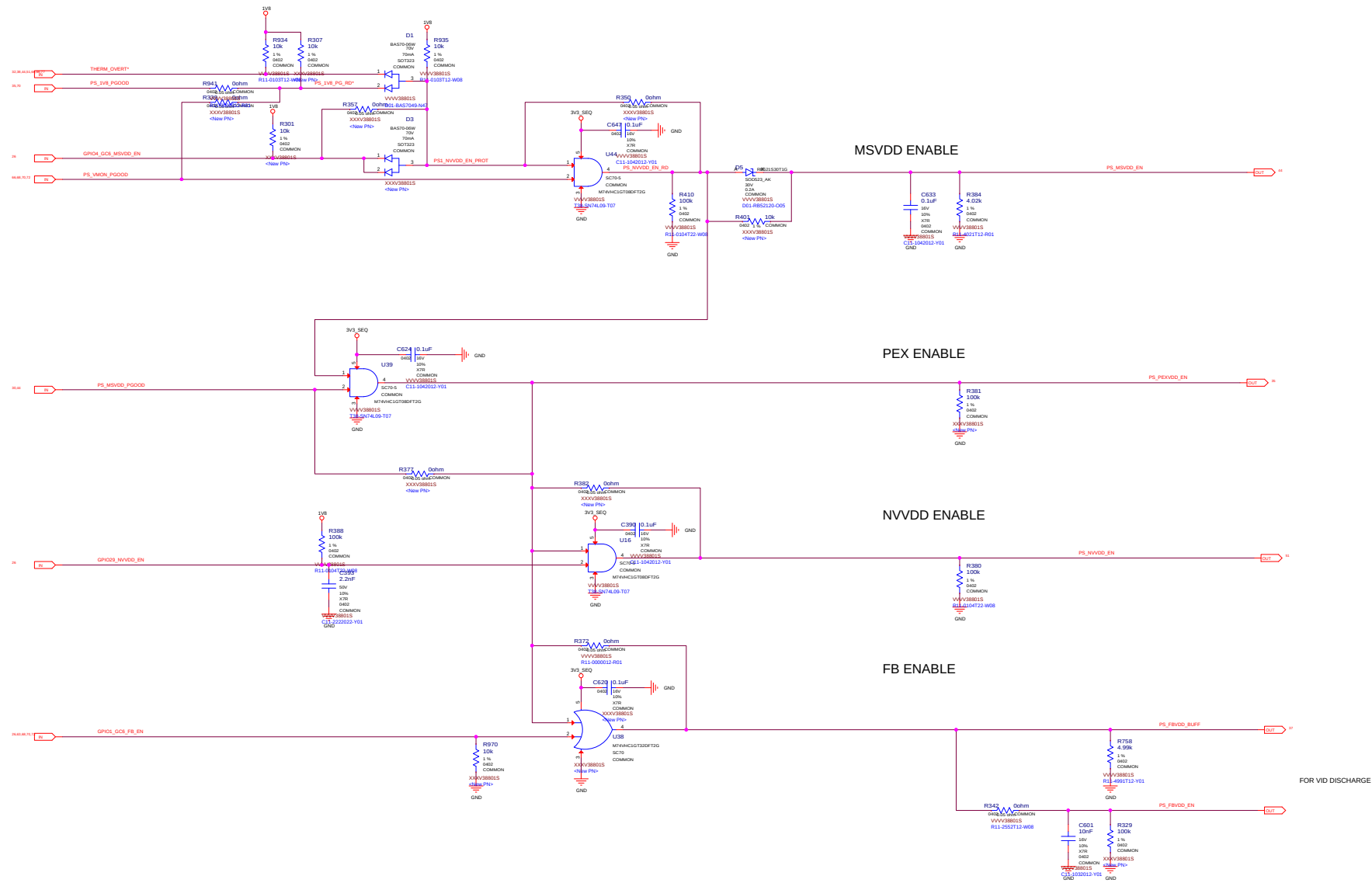
PS: Pre-Filter B

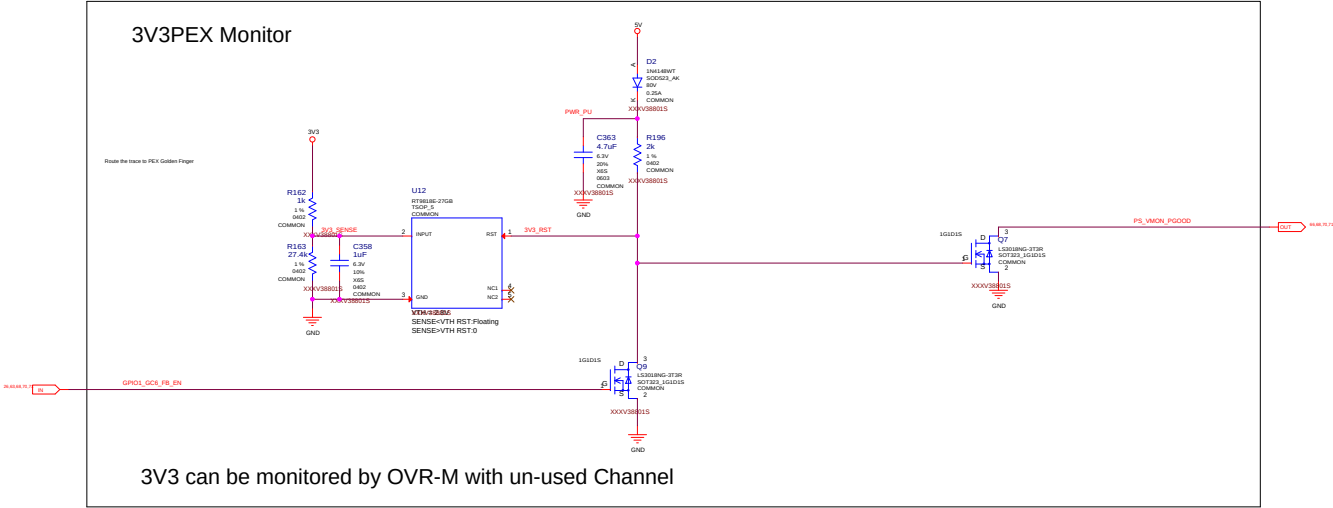


| BUS       | connect A                | connect B                | connect C                |
|-----------|--------------------------|--------------------------|--------------------------|
| (RSENSE1) | (RSENSE2)                | (RSENSE3)                | (RSENSE4)                |
| 12V_F     | NET NAME: F1             | NET NAME: F2             | NET NAME: 12V_EXT8_F3_R1 |
| FB 4      | PEXVDD                   | FB-2(左)                  |                          |
| MSVDD1    |                          | FB-3                     |                          |
| NVVD1     | (RSENSE2)                | MSVDD3(左)                | NVVD7                    |
|           | NET NAME: 12V_EXT8_F1_R1 | MSVDD5(左)                | NVVD8                    |
|           | FB 1                     | (RSENSE7)                | NVVD9                    |
|           | 5V                       | NET NAME: 12V_EXT8_F2_R6 | NVVD10                   |
|           | 1V8                      | NVVD2(左)                 | NVVD11                   |
|           | (RSENSE6)                | NVVD3(左)                 |                          |
|           | NET NAME: 12V_EXT8_F1_R5 | NVVD4(左)                 |                          |
|           | MSVDD2                   |                          |                          |
|           | MSVDD4                   |                          |                          |
| 52.8W     | MSVDD6                   | 73.5+27+30=130.5W        | 122.5W                   |
|           | (RSENSE5)                |                          |                          |
|           | NET NAME: 12V_EXT8_F1_R4 |                          |                          |
|           | NVVD5-F1-R4              |                          |                          |
|           | NVVD6-F1-R4              |                          |                          |
|           |                          |                          |                          |
|           | 129.6W                   |                          | 80W/6=13.3W/27W/11=24.5W |



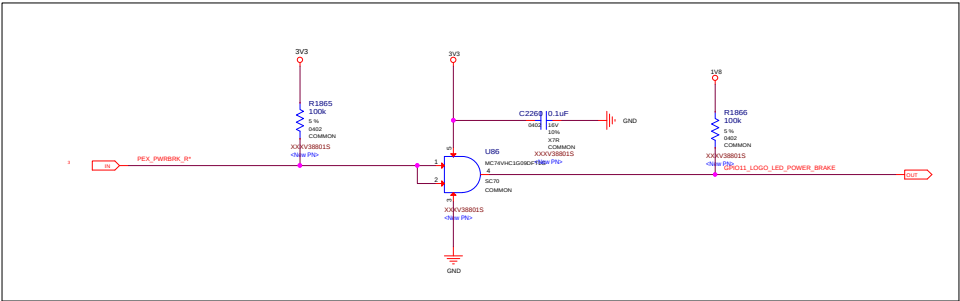
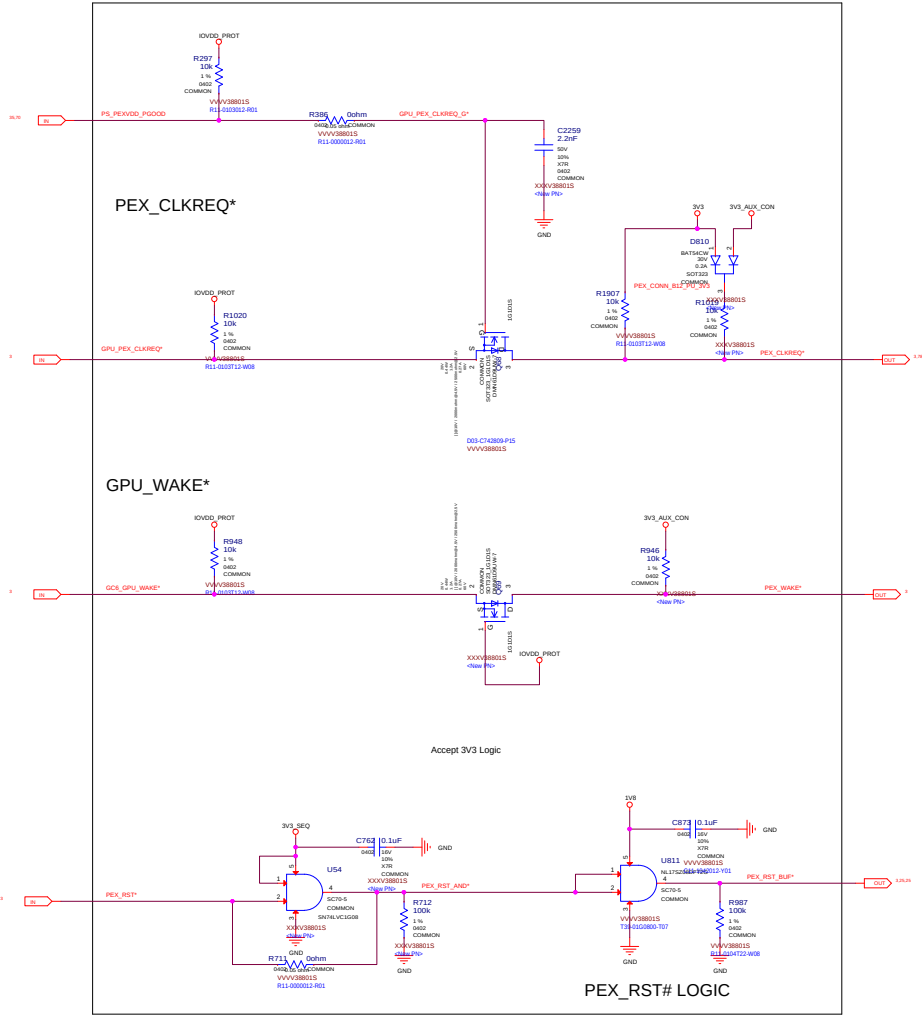
# SEQUENCE:NV,PEX,FB ENABLE







SEQUENCE:MISC

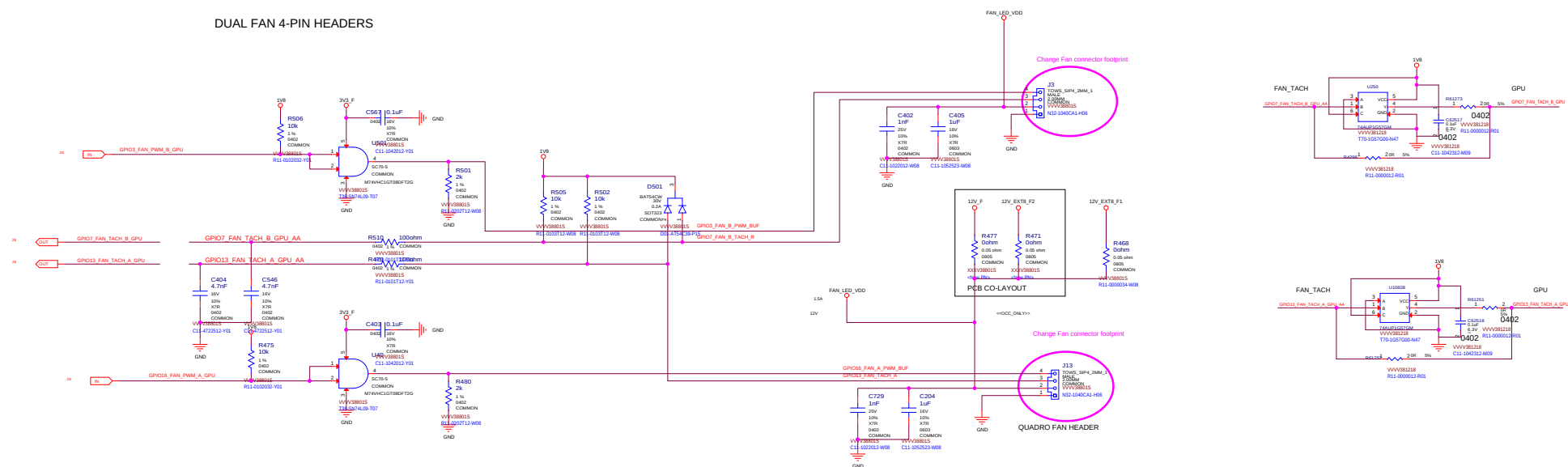


| MICRO-STAR INT'L CO.,LTD      |                      |                |  |
|-------------------------------|----------------------|----------------|--|
| MS-V388                       |                      |                |  |
| Size                          | Document Description | Rev            |  |
| Custom                        | SEQUENCE:MISC        | 8.0            |  |
| Date: Thursday, July 23, 2020 |                      | Sheet 73 of 78 |  |

# MISC: LED & FAN

2-PIN LED HEADER

DUAL FAN 4-PIN HEADERS



MISC: RGBW LED REF

CONNECTOR SIZE TO FIT LAYOUT  
ALL CIRCUIT NO STUFF BY DEFAULT  
ALINGN WITH PASH  
IF ADDITIONAL POWER CONSUPTION R  
NEEDED, IT SHOULD BE ON LED MOD.

1: 12V  
2: BLUE  
3: GREEN  
4: RED  
5: WHITE

WHITE DRIVE

LOGO LED RED

RED DRIVE

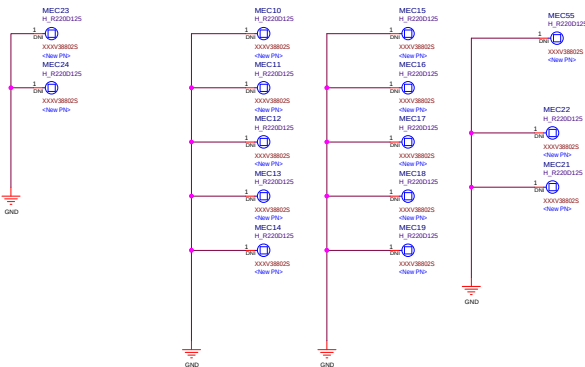
GREEN DRIVE

BLUE DRIVE

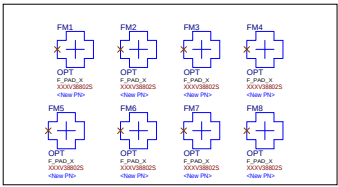
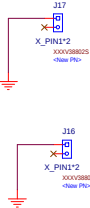
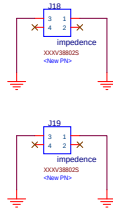
|                                                                                       |                          |                |
|---------------------------------------------------------------------------------------|--------------------------|----------------|
|  | MICRO-STAR INT'L CO.,LTD |                |
|                                                                                       | MS-V388                  |                |
| Size                                                                                  | Document Description     | Rev            |
| Custom                                                                                | MISC: RGBW LED REF       | 8.0            |
| Date: Wednesday, July 22, 2020                                                        |                          | Sheet 75 of 78 |

Mechanical: Mounting holes

Mechanical Holes Symbol



BACK STIFFENER:



|   |   |   |   |   |   |   |   |
|---|---|---|---|---|---|---|---|
| A | B | C | D | E | F | G | H |
|---|---|---|---|---|---|---|---|

