

P2131_A00

GK208 GDDR5 X16 64BIT

DVI-I/SlimVGA + HDMI

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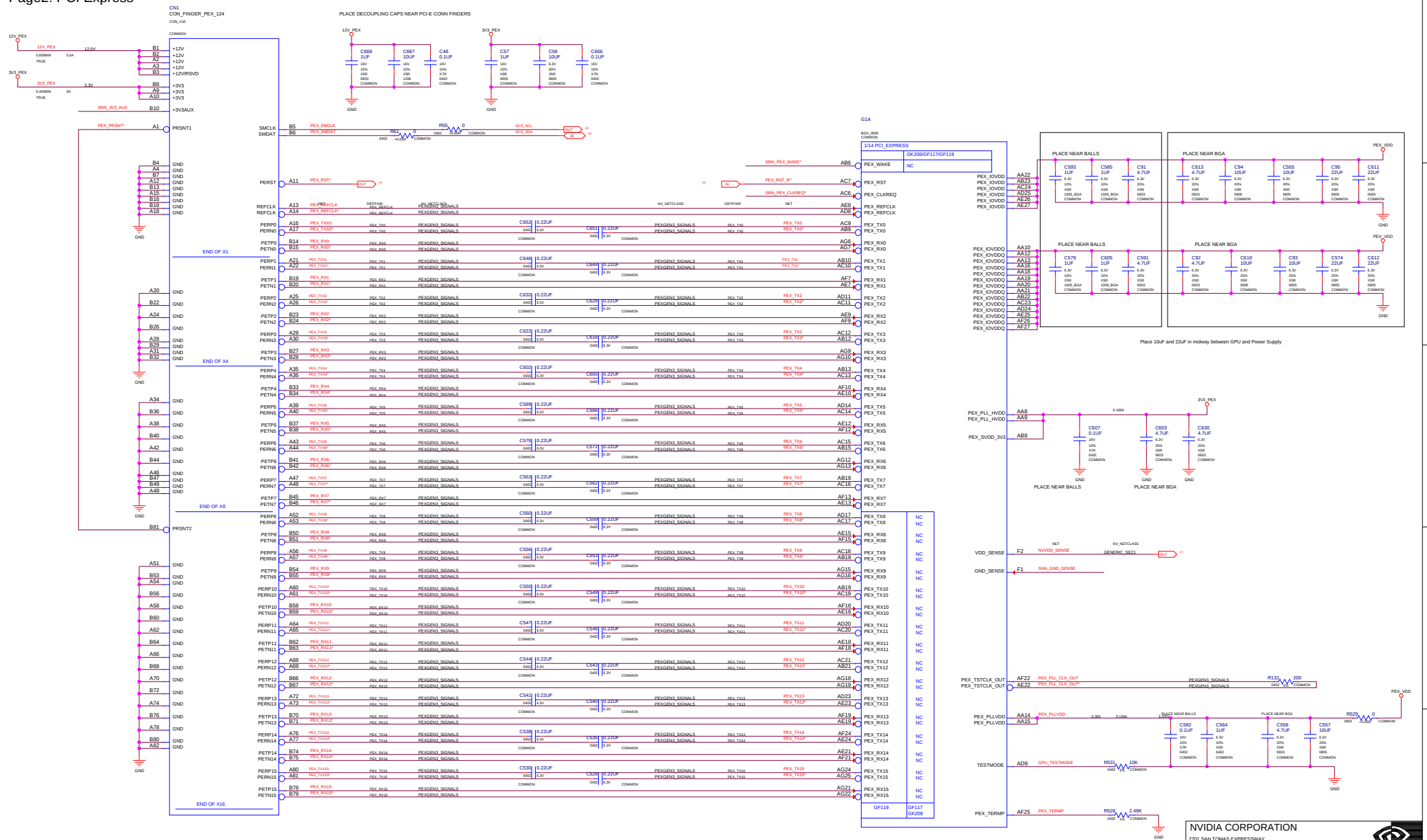
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PAGE DETAIL	PCI EXPRESS

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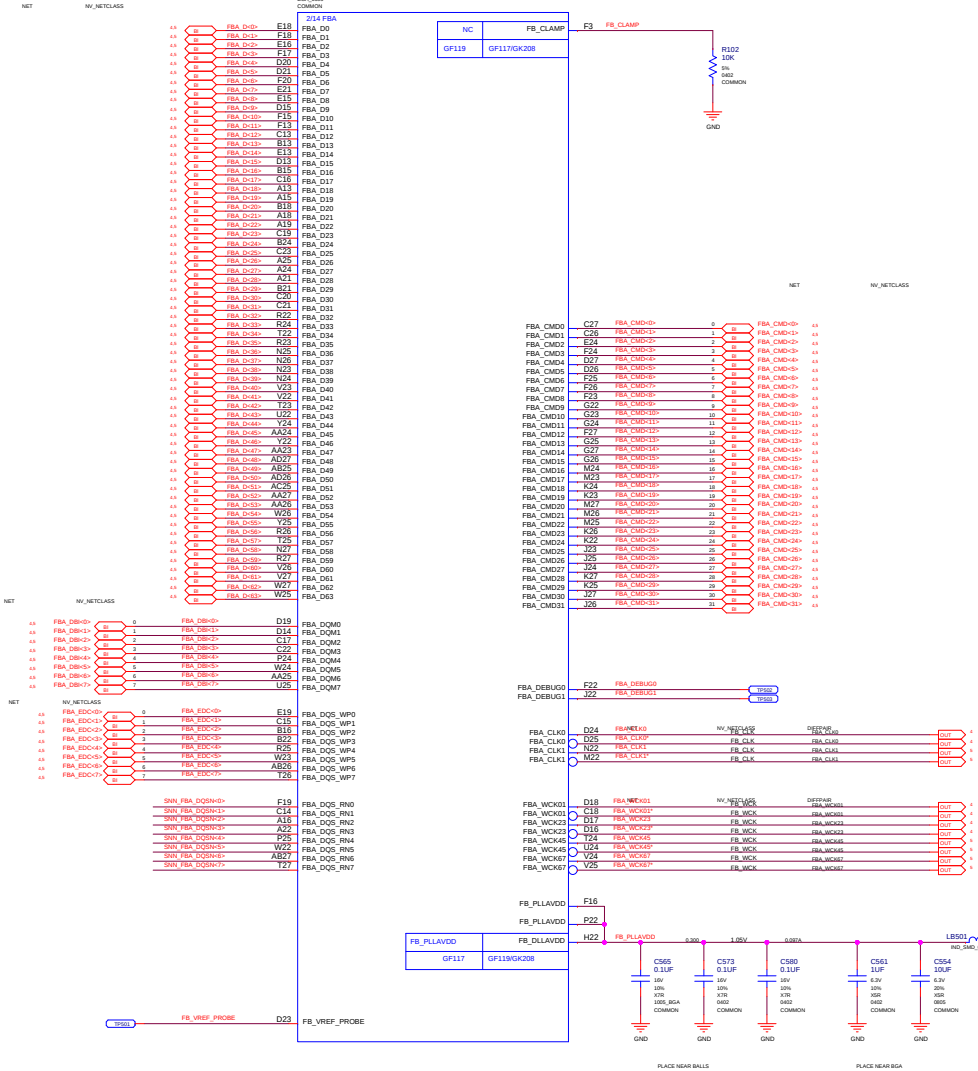


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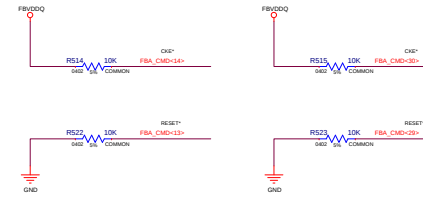
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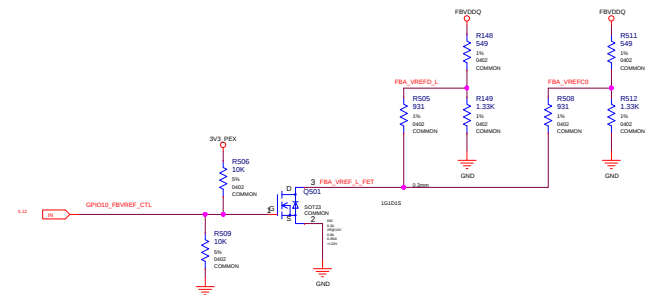
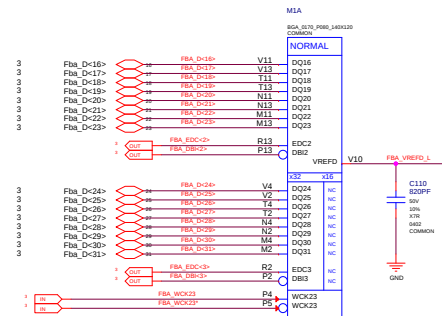
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CMD	32.0	32.30
CMD0	CP	
CMD1	A0_BA3	
CMD2	A2_BA0	
CMD3	A4_BA2	
CMD4	A6_BA1	
CMD5	W0	
CMD6	A12_A8	
CMD7	A6_A11	
CMD8	AB7	
CMD9	A12_SF0U	
CMD10	A6_A10	
CMD11	A12_A10	
CMD12	BA0	
CMD13	BA1	
CMD14	CB1	
CMD15	CAD	
CMD16		CP
CMD17		A0_BA3
CMD18		A2_BA0
CMD19		A4_BA2
CMD20		A6_BA1
CMD21		W0
CMD22		A12_A8
CMD23		A6_A11
CMD24		AB7
CMD25		A12_SF0U
CMD26		A6_A10
CMD27		A12_A10
CMD28		BA0
CMD29		BA1
CMD30		CB1
CMD31		CAD



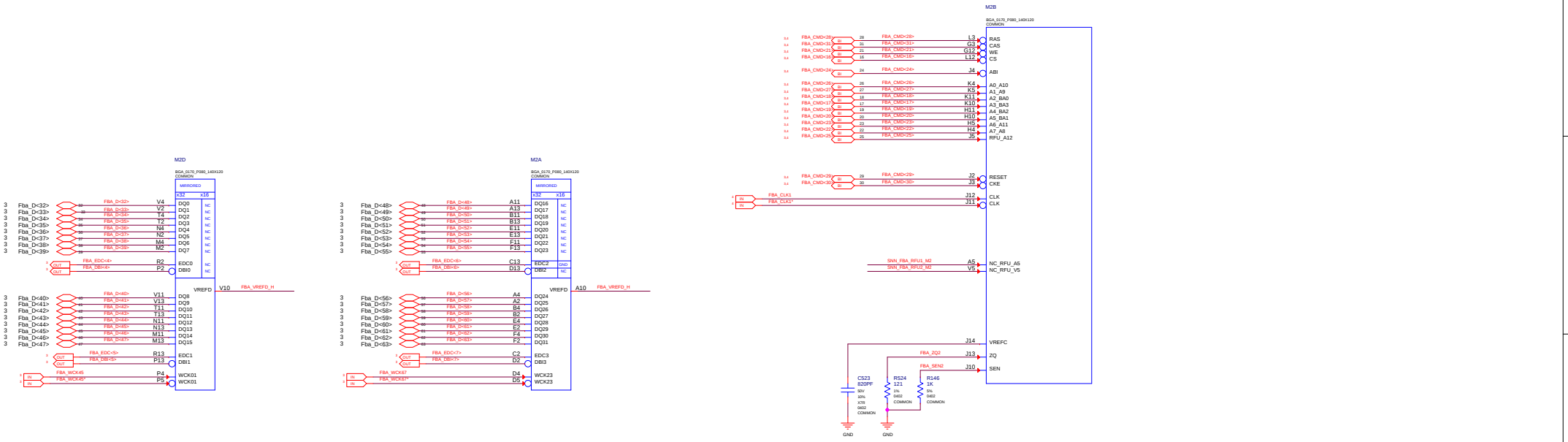
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Cmd	Blk	63..32
CM00	CS*	
CM01	A3,BA3	
CM02	A2,BA2	
CM03	A1,BA1	
CM04	WE*	
CM05	A7,A8	
CM06	A6,A11	
CM07	AB*	
CM08	A12,BFU	
CM09	A0,A10	
CM10	A1,A9	
CM11	BA*	
CM12	RD*	
CM13	CS*	
CM14	CS*	
CM15	CS*	
CM16	CS*	
CM17	A3,BA3	
CM18	A2,BA2	
CM19	A1,BA1	
CM20	WE*	
CM21	A7,A8	
CM22	A6,A11	
CM23	AB*	
CM24	A12,BFU	
CM25	A0,A10	
CM26	A1,A9	
CM27	BA*	
CM28	RD*	
CM29	CS*	
CM30	CS*	
CM31	CS*	

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ASSEMBLY	FBA Partition A Dec 63..32
PAGE DETAIL	

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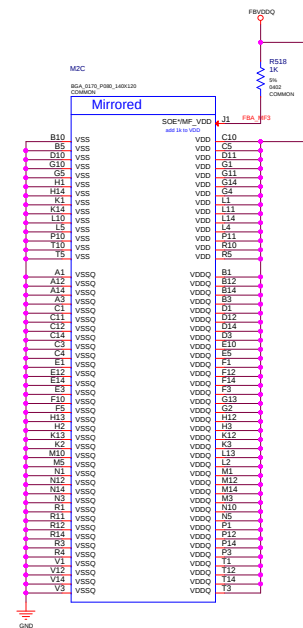
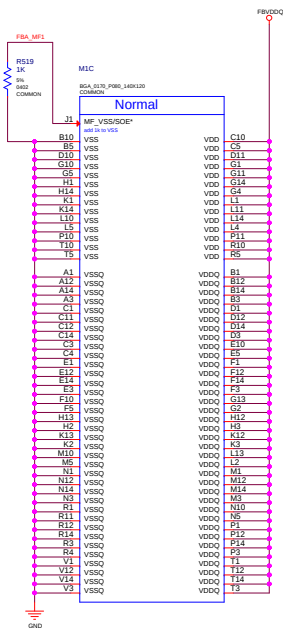
PAGE

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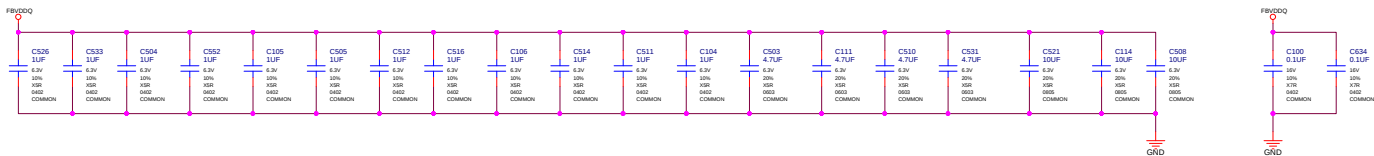
DATE

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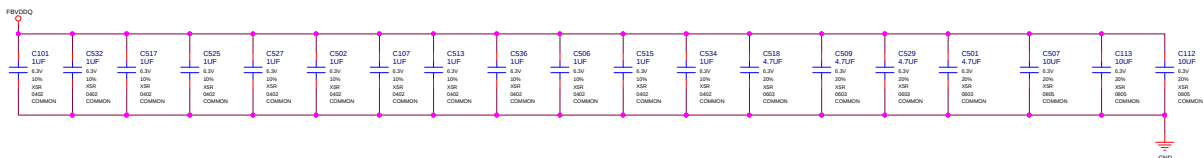




DECOUPLING FOR M1 and M501 DEVICE



DECOUPLING FOR M2 and M502 DEVICE



PLACE 1uF CAPS CLOSEST TO THE MEMORY DEVICES

PLACE LARGER CAPACITORS SLIGHTLY FARTHER AWAY

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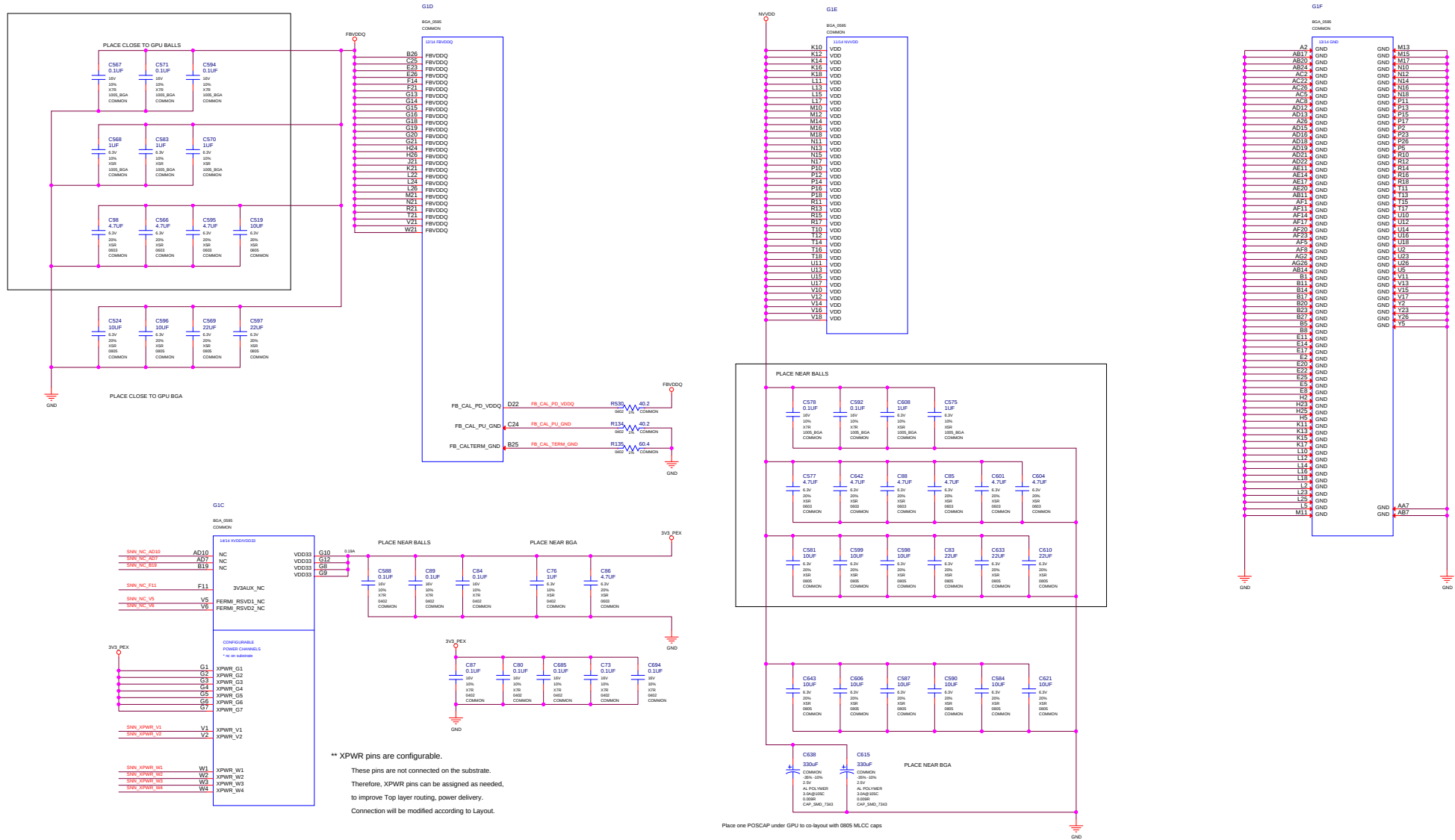
NV_PN 600-12131-BASE-000

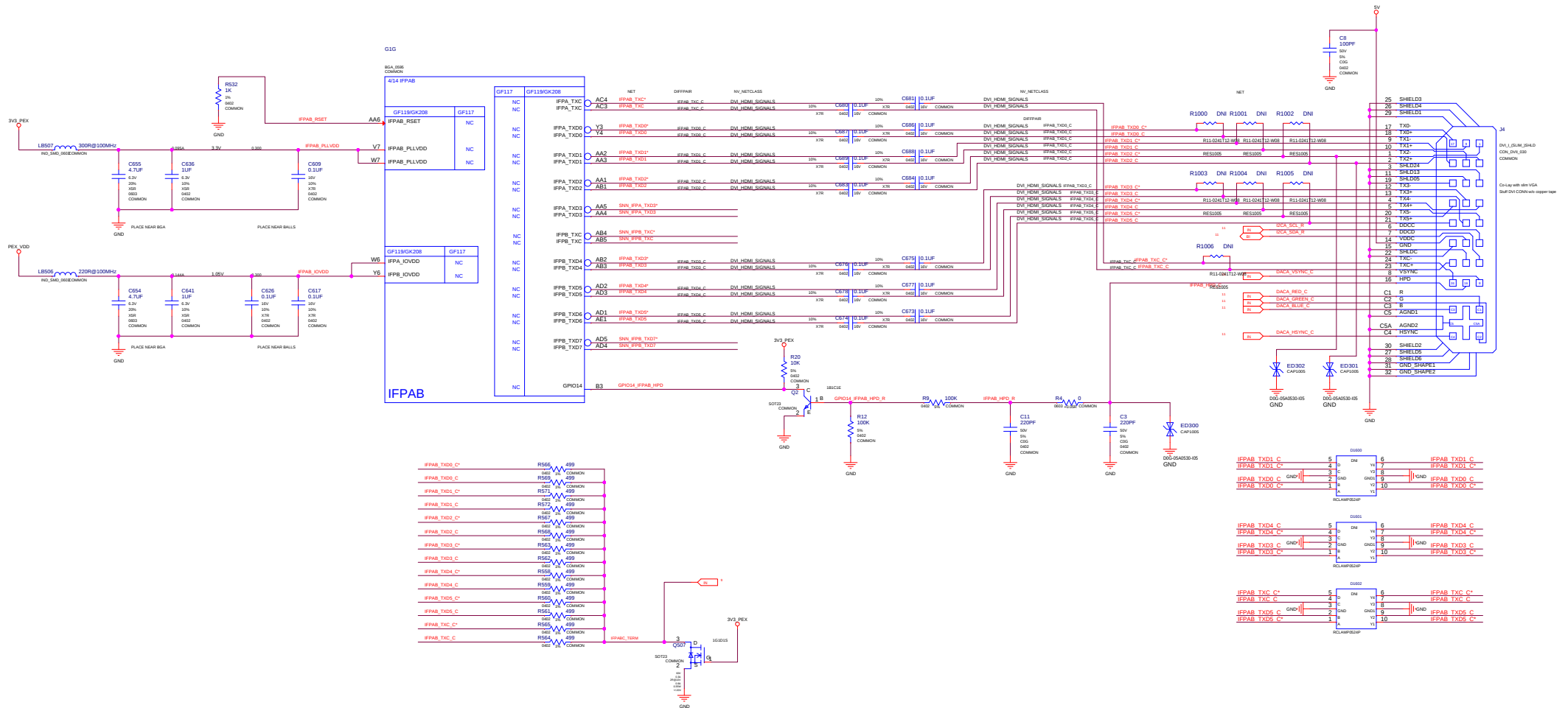
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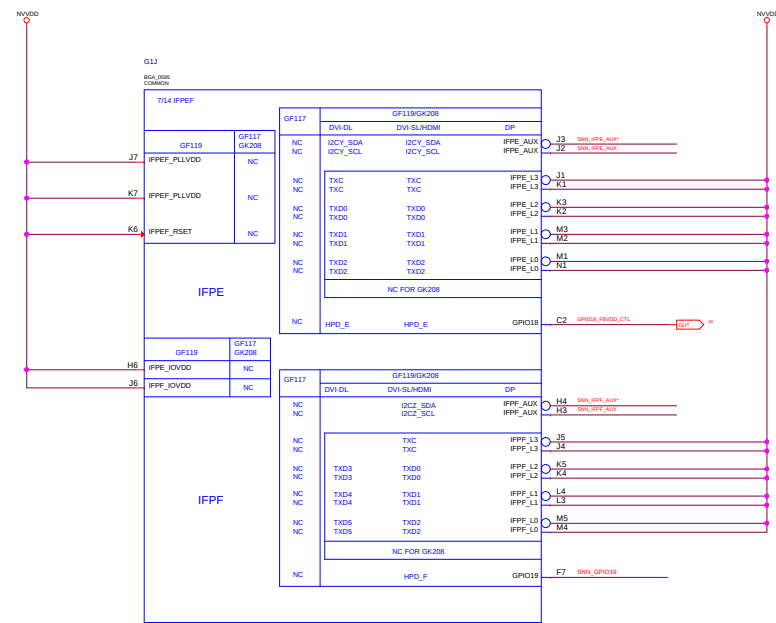
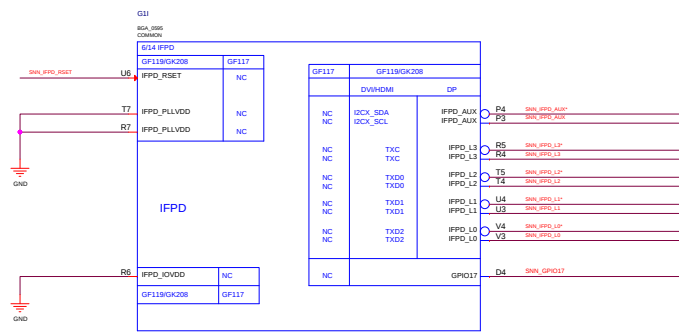
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PAGE DETAIL	IFPA/B TMD5 DUAL LINK

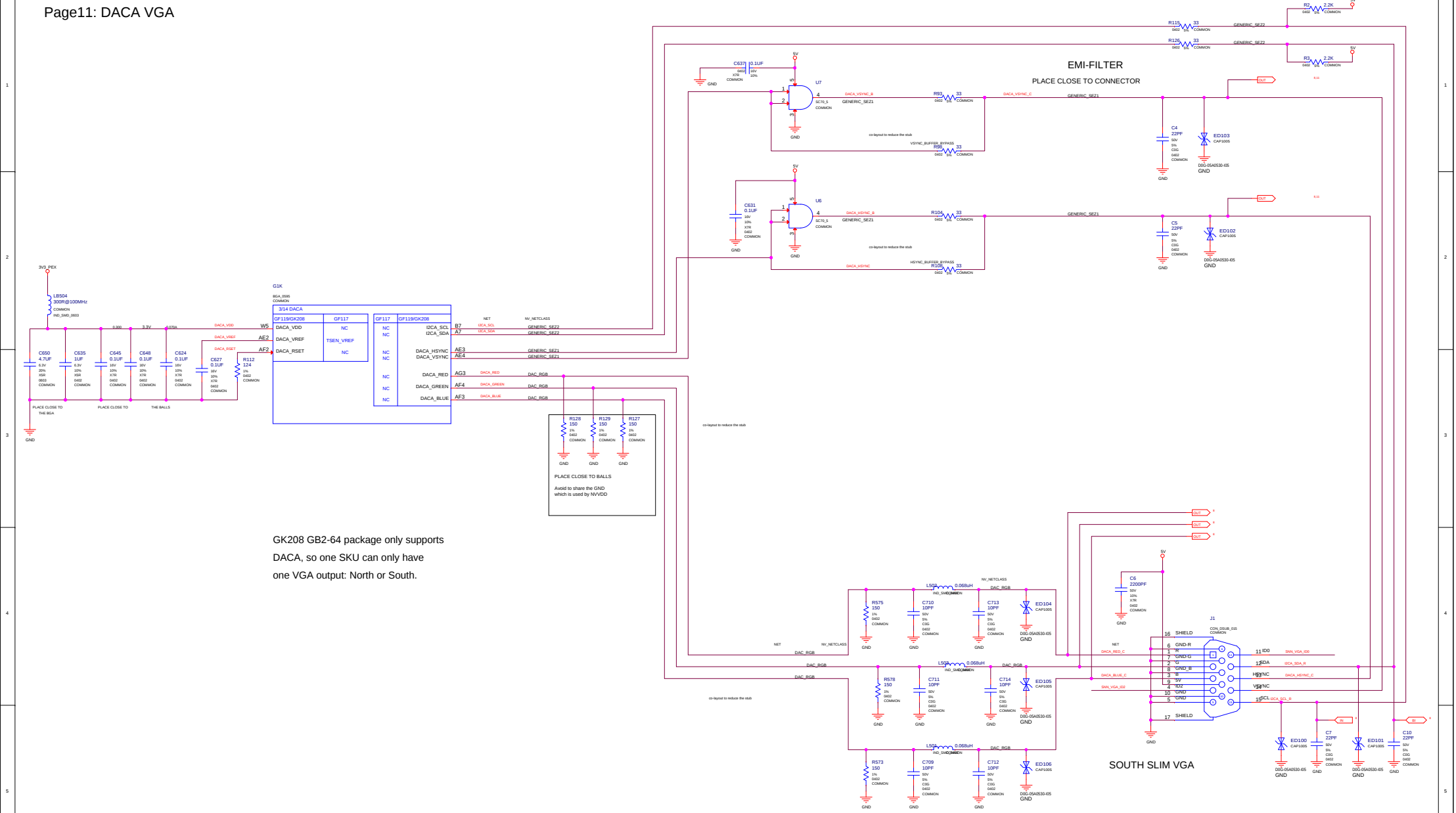
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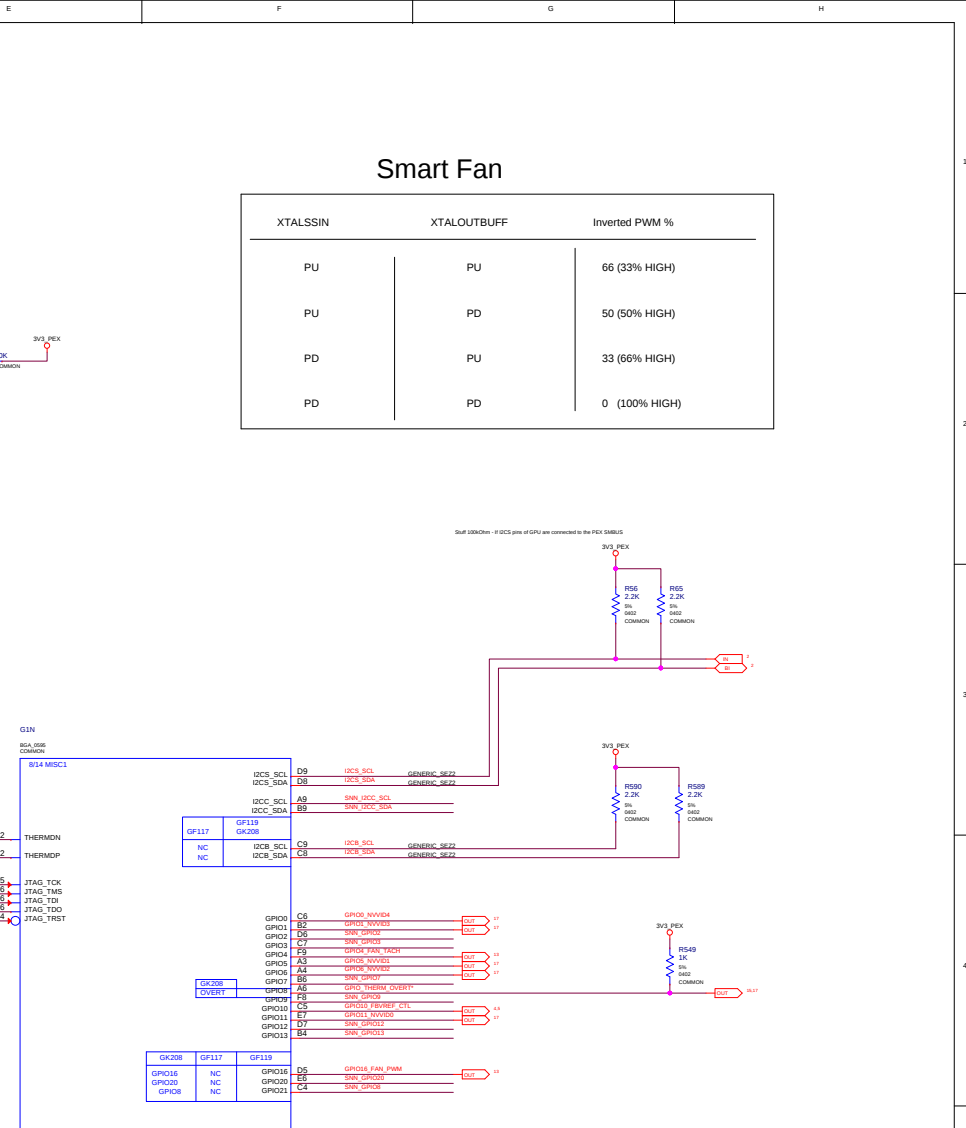
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PAGE DETAIL	UNUSED IFPD, IFPE & IFPF

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Smart Fan		
XTALSSIN	XTALOUTBUFF	Inverted PWM %
PU	PU	66 (33% HIGH)
PU	PD	50 (50% HIGH)
PD	PU	33 (66% HIGH)
PD	PD	0 (100% HIGH)

Smart Fan

XTALSSIN	XTALOUTBUFF	Inverted PWM %
PU	PU	66 (33% HIGH)
PU	PD	50 (50% HIGH)
PD	PU	33 (66% HIGH)
PD	PD	0 (100% HIGH)

Snail 10000rpm - If I2CS pins of GPU are connected to the PEX SINKBUS

3V3_PEX

R56 2.2K
R55 2.2K
Sn 402 COMMON

3V3_PEX

R580 2.2K
R581 2.2K
Sn 402 COMMON

3V3_PEX

R549 1K
Sn 402 COMMON

GF117	GF119	GF119
I2CS_SCL	D9	I2CS_SCL
I2CS_SDA	D8	I2CS_SDA
I2CC_SCL	A9	SNV_I2CC_SCL
I2CC_SDA	B9	SNV_I2CC_SDA
I2CB_SCL	C9	I2CB_SCL
I2CB_SDA	C8	I2CB_SDA

GF117	GF119	GF119
GPI00	D6	GPI00_M4VDD4
GPI01	E6	GPI01_NVDD3
GPI02	D6	SNV_GPI02
GPI03	C7	SNV_GPI03
GPI04	F9	GPI04_FAN_VDD4
GPI05	A9	GPI05_NVDD3
GPI06	A2	GPI06_NVDD3
GPI07	B6	SNV_GPI07
GPI08	A6	GPI08_TBSRU_OVBRT1
GPI09	F8	SNV_GPI09
GPI010	C5	GPI010_FANVDD_CIL
GPI011	E7	GPI011_NVDD3
GPI012	D7	SNV_GPI012
GPI013	B4	SNV_GPI013

GF117	GF119	GF119
GPI016	D5	GPI016_FAN_PWM
GPI020	E6	SNV_GPI020
GPI021	C4	SNV_GPI021

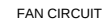


0000	5.1K to GND	1000	5.1K to VCC
0001	10K to GND	1001	10K to VCC
0010	15K to GND	1010	15K to VCC
0011	20K to GND	1011	20K to VCC
0100	24.9K to GND	1100	24.9K to VCC
0101	30.1K to GND	1101	30.1K to VCC
0110	34.9K to GND	1110	34.9K to VCC
0111	45.2K to GND	1111	45.2K to VCC

Bit Signal	Values
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Bit Signal	Values
02 RAMCFG[2]	
02 RAMCFG[2]	0000 Reserved
	0001 128Mx16 GDDR5 X16 64bit E-jrpt
01 RAMCFG[1]	0000 128Mx16 GDDR5 X16 64bit Hynix A-die
	0001 128Mx16 GDDR5 X16 64bit Samsung D-die

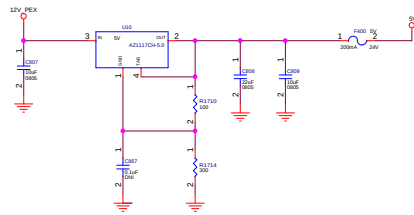


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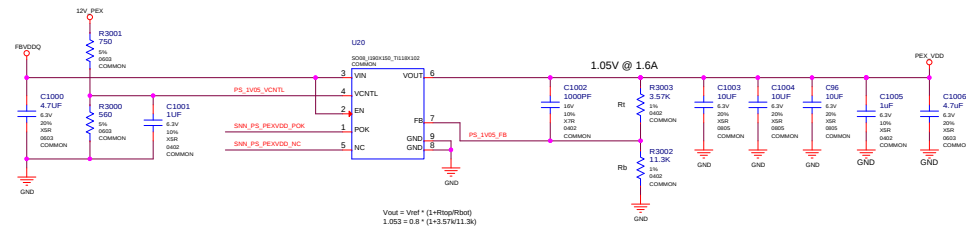
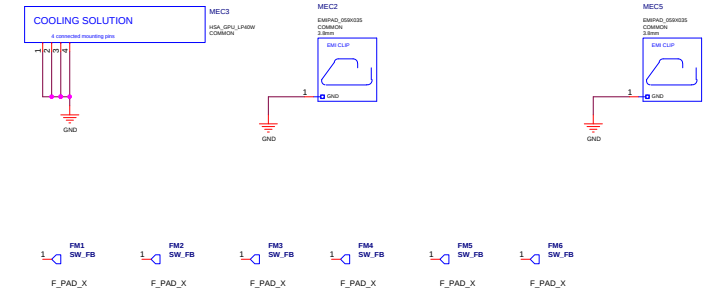


5V Power

STUFF FOR DVI/HDMI/VGA DISPLAY SKU

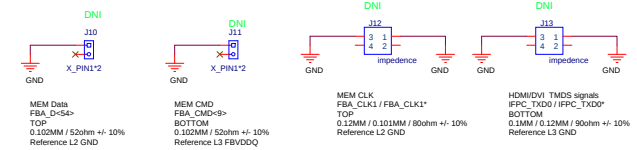


Mechanical



PEX_VDD

Reserve J10 - J13 for Impedance check



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PAGE DETAIL	PS: 5V, PEXVDD

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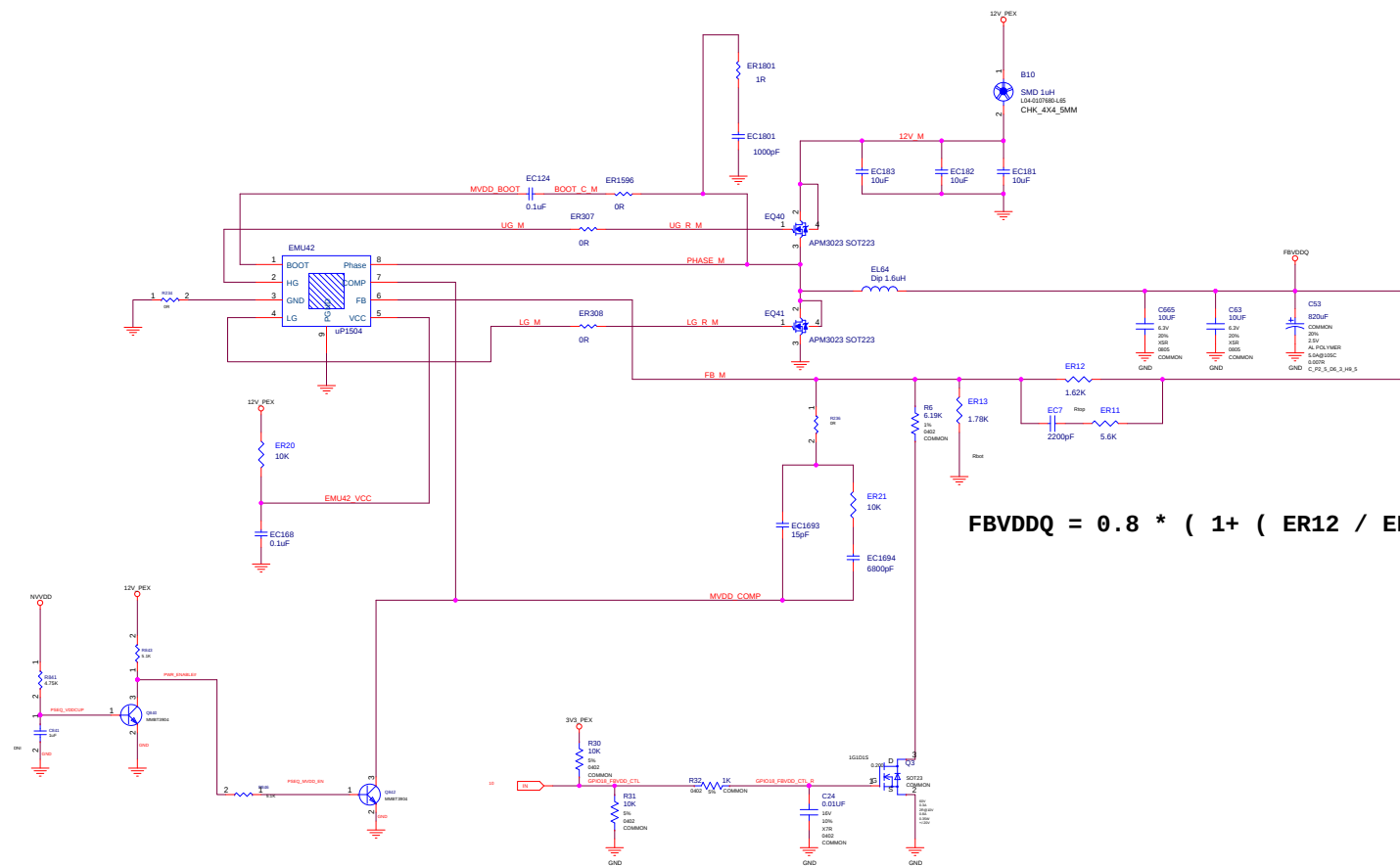
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$$FBVDDQ = 0.8 * (1 + (ER12 / ER13))$$

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