

P2131_A00

GK208 GDDR5 256X32Bit

DVI-I/SlimVGA + HDMI

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
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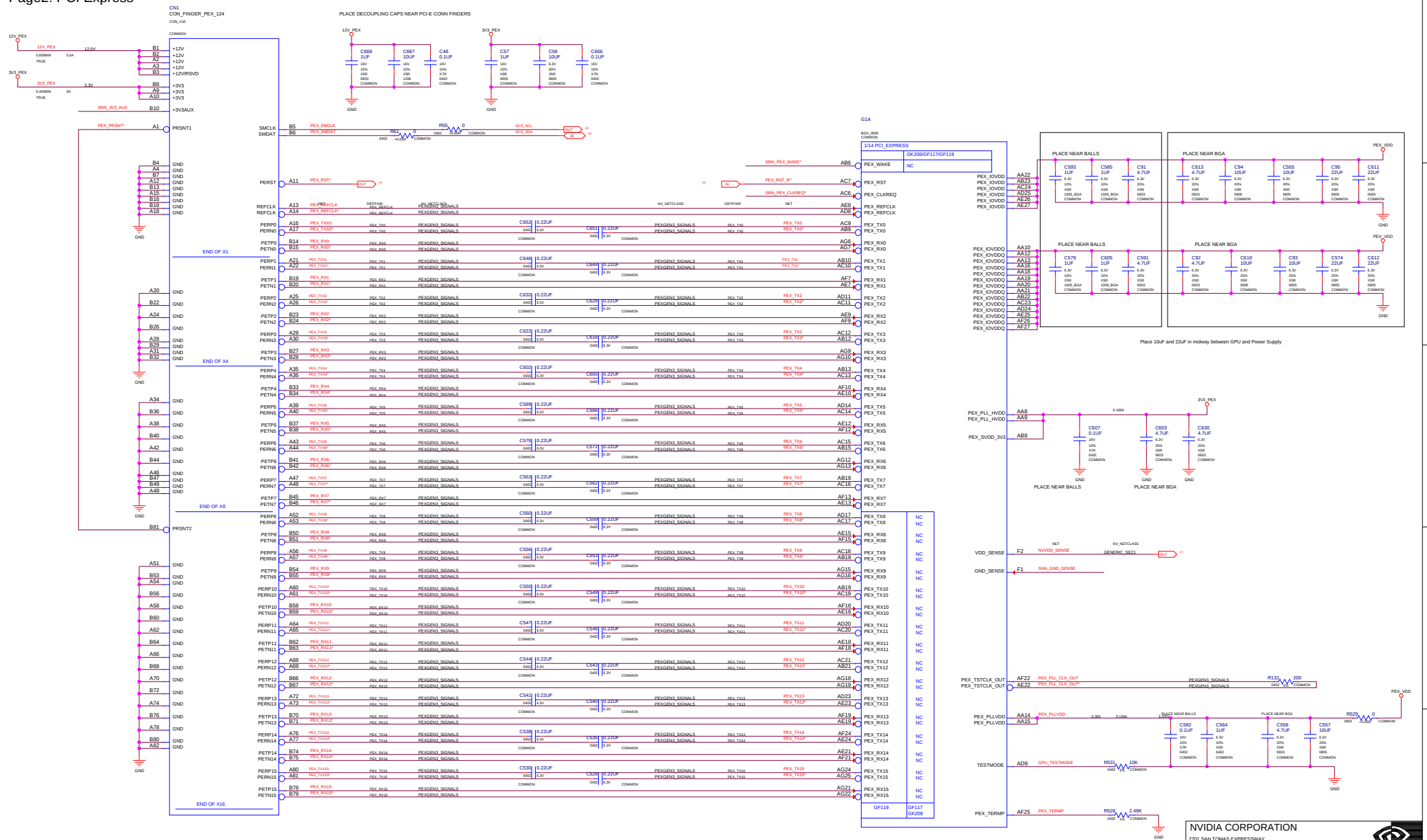
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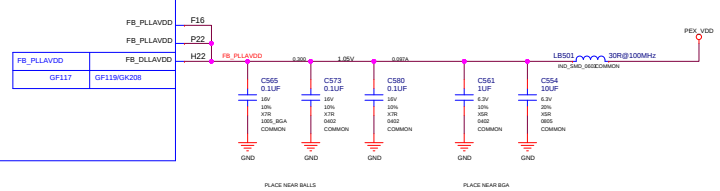
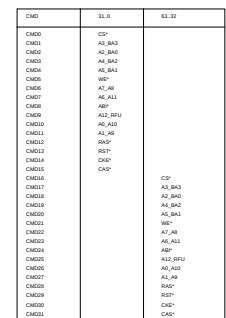


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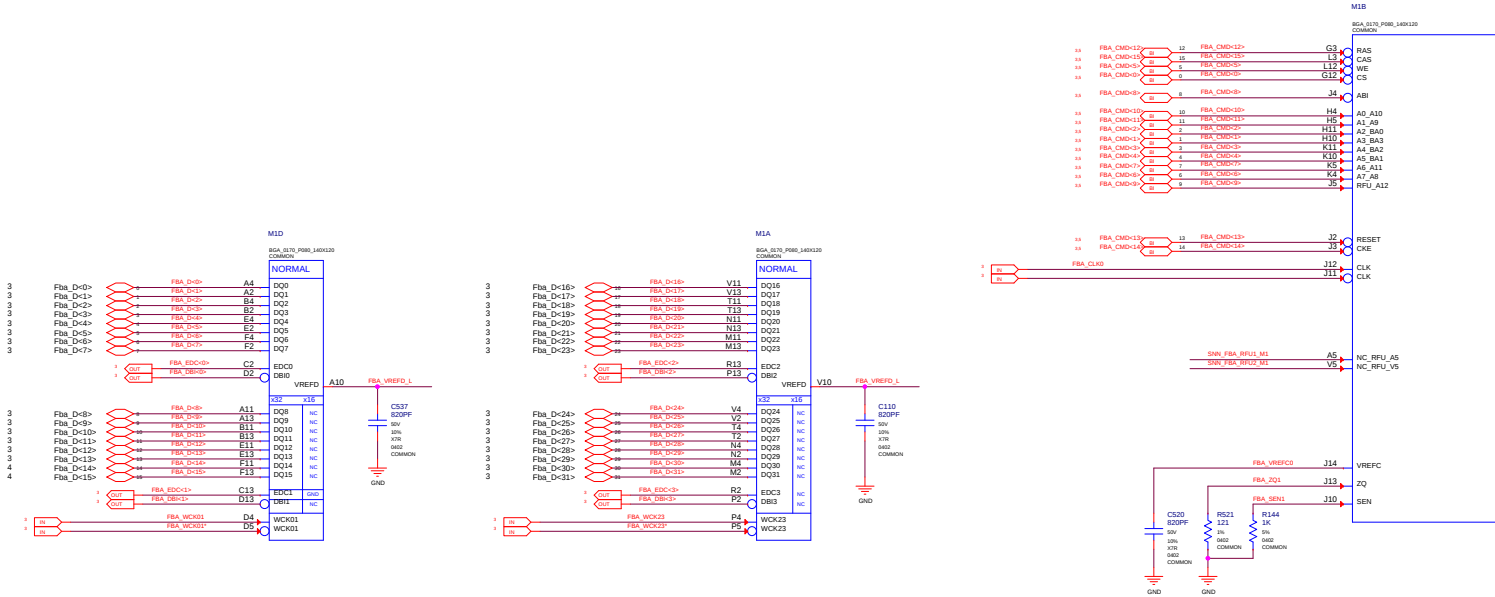
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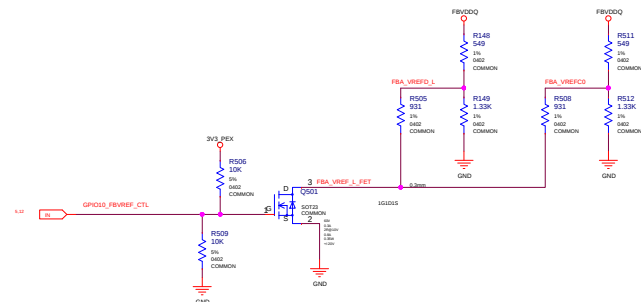
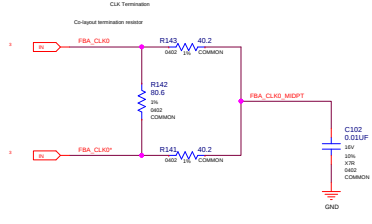
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QND	31.0	63.31
QND0	CS	
QND1	A2_BA3	
QND2	A2_BA0	
QND3	A1_BA2	
QND4	A5_BA1	
QND5	WE	
QND6	A7_A8	
QND7	AB	
QND8	AE_A11	
QND9	A12_RFU	
QND10	AB_A0	
QND11	A1_A9	
QND12	WE	
QND13	RS	
QND14	CS	
QND15	CS	
QND16	AB_BA3	
QND17	A2_BA0	
QND18	A1_BA2	
QND19	A5_BA1	
QND20	WE	
QND21	A7_A8	
QND22	AB	
QND23	AE_A11	
QND24	A12_RFU	
QND25	AB_A0	
QND26	A1_A9	
QND27	WE	
QND28	RS	
QND29	CS	
QND30	CS	
QND31	CS	



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ASSEMBLY
PAGE DETAIL
FB Partition A Des 31..0

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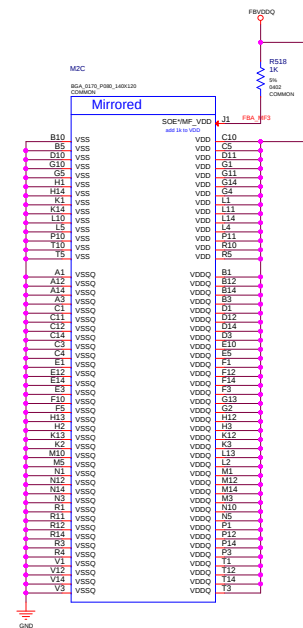
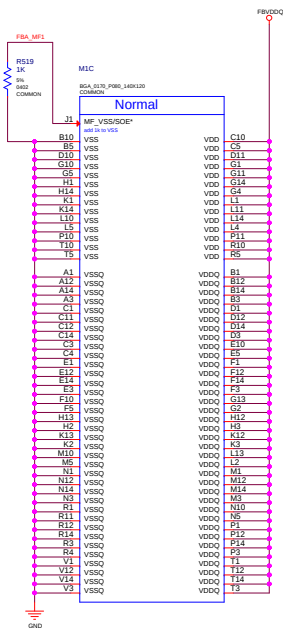
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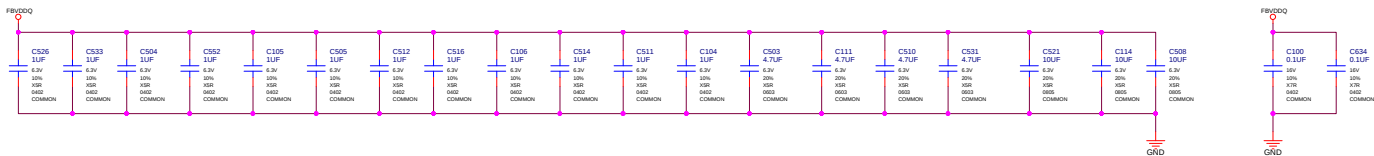
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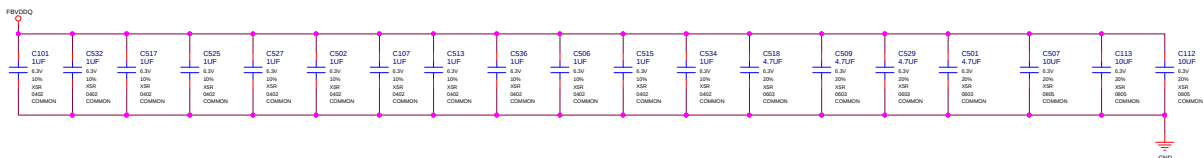
[illegible]



DECOUPLING FOR M1 and M501 DEVICE



DECOUPLING FOR M2 and M502 DEVICE



PLACE 1uF CAPS CLOSEST TO THE MEMORY DEVICES

PLACE LARGER CAPACITORS SLIGHTLY FARTHER AWAY

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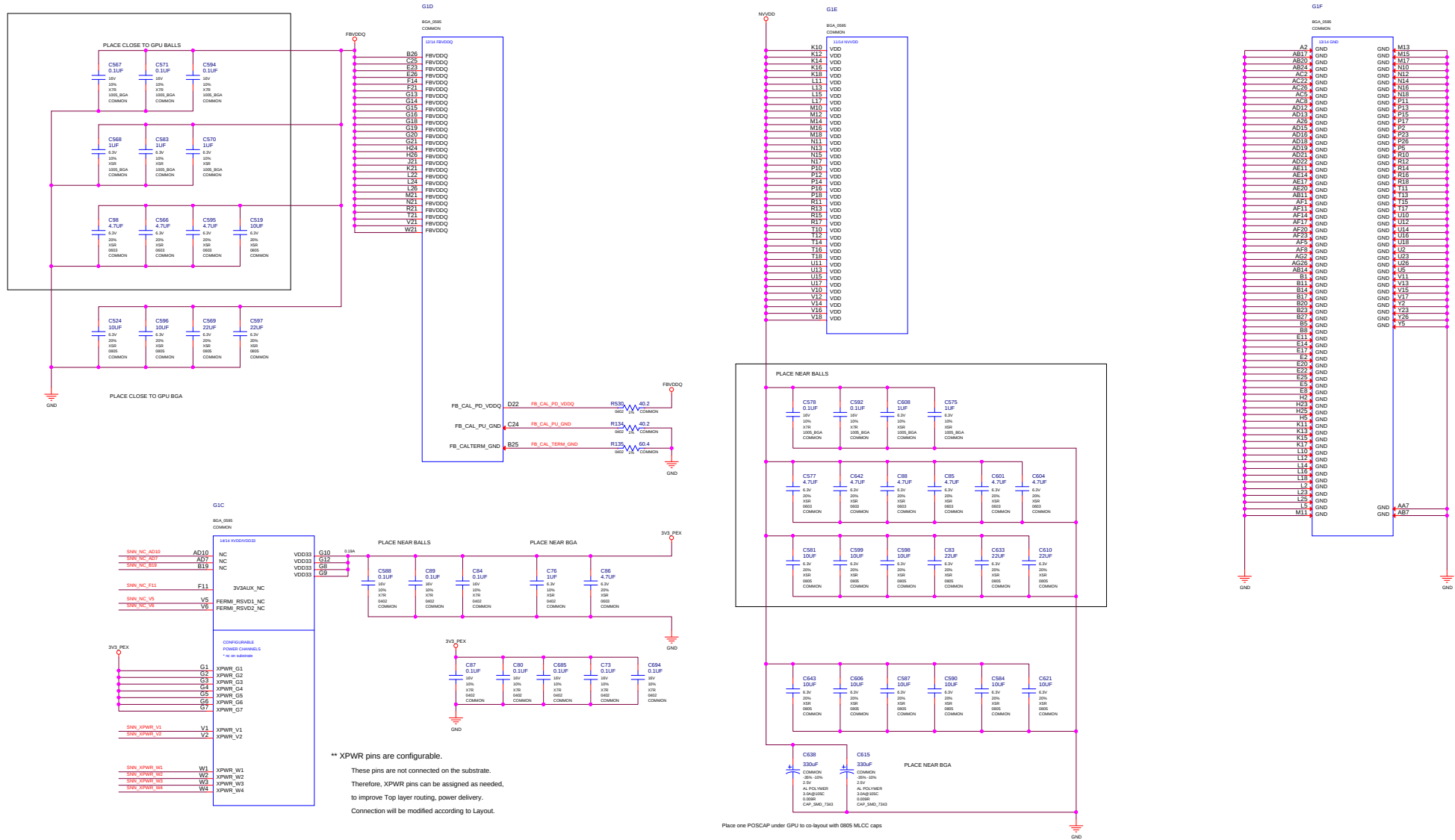
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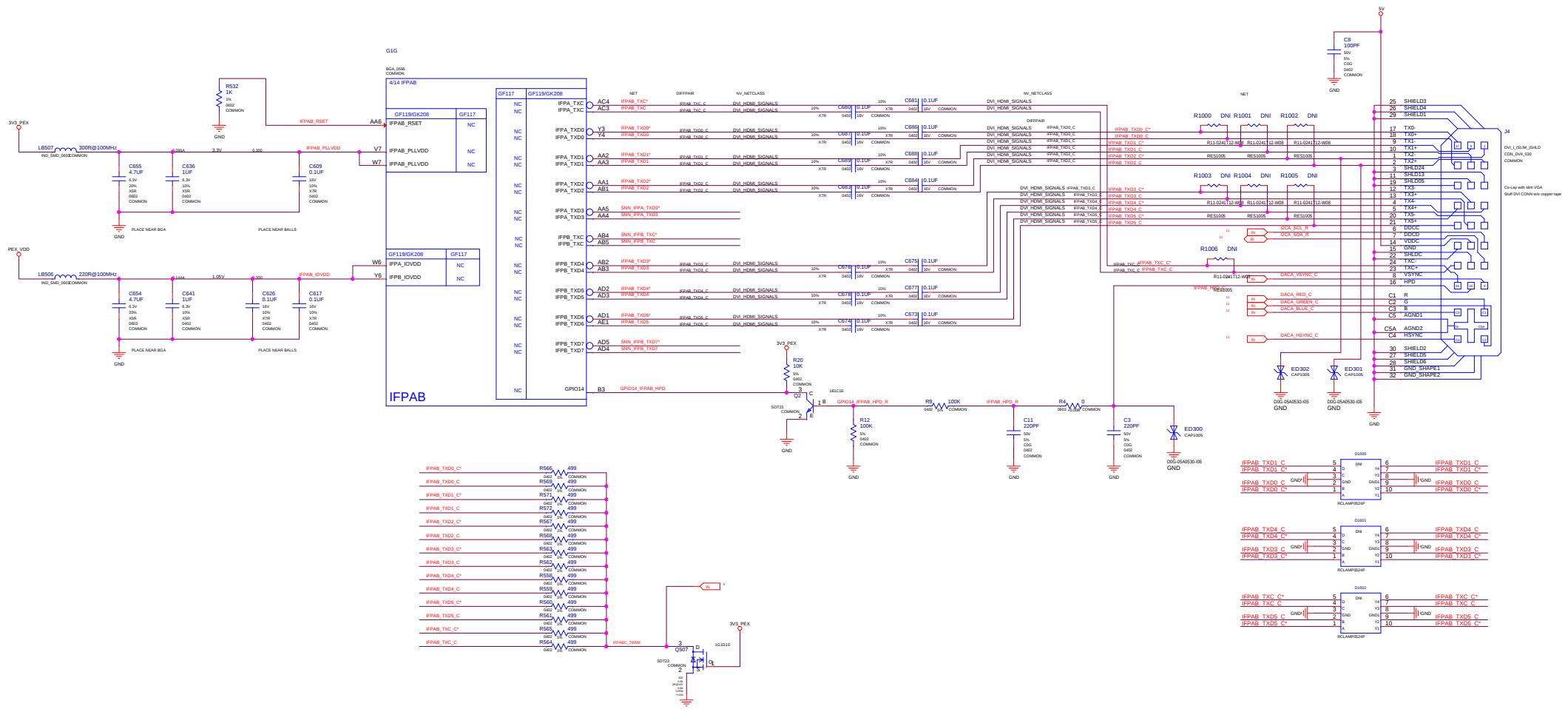


ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	POWER/DECOUPLING: NVVDD, FBVDDQ, VDD33, GND

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	IFPA/B TMD5 DUAL LINK

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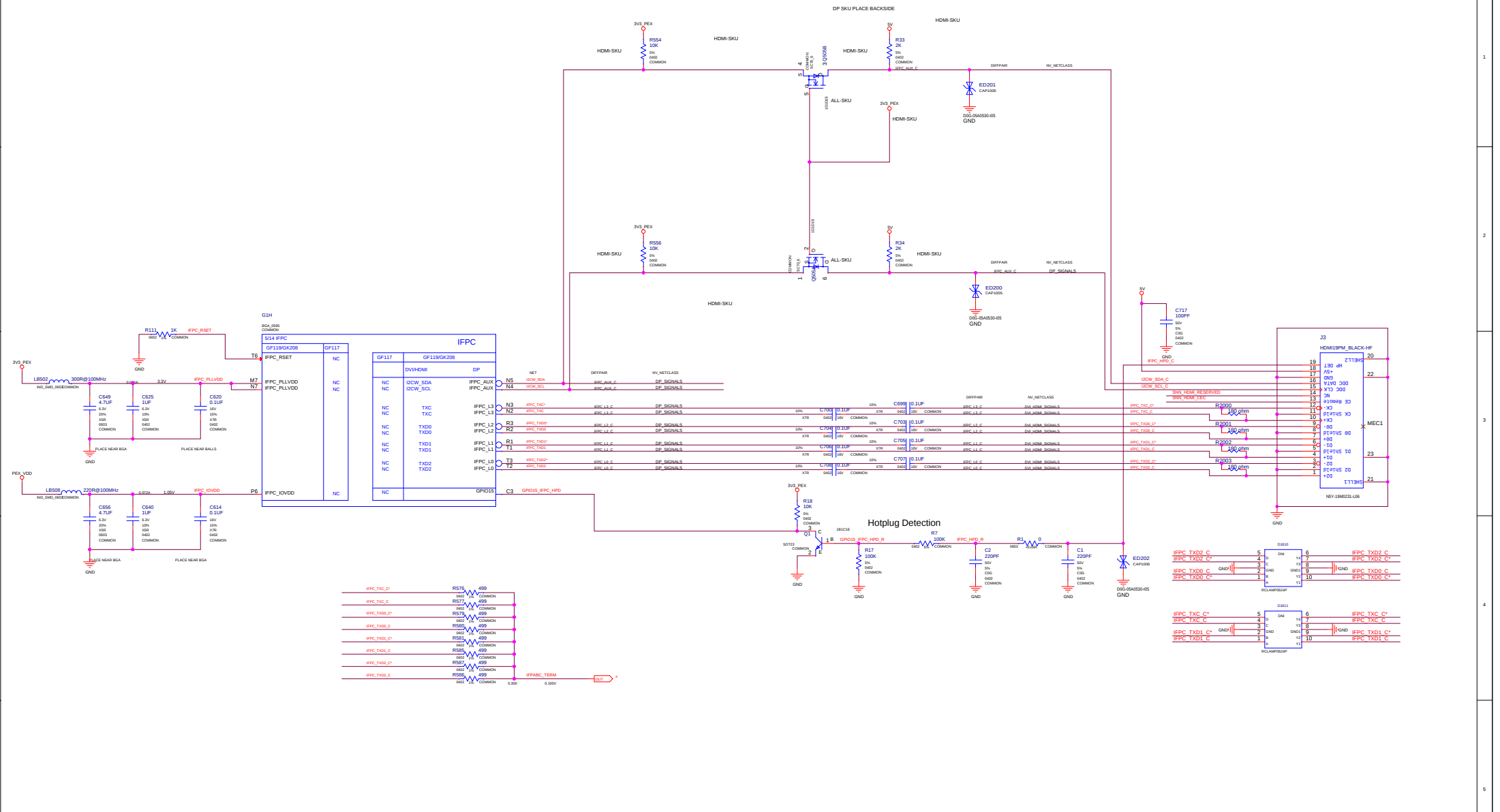
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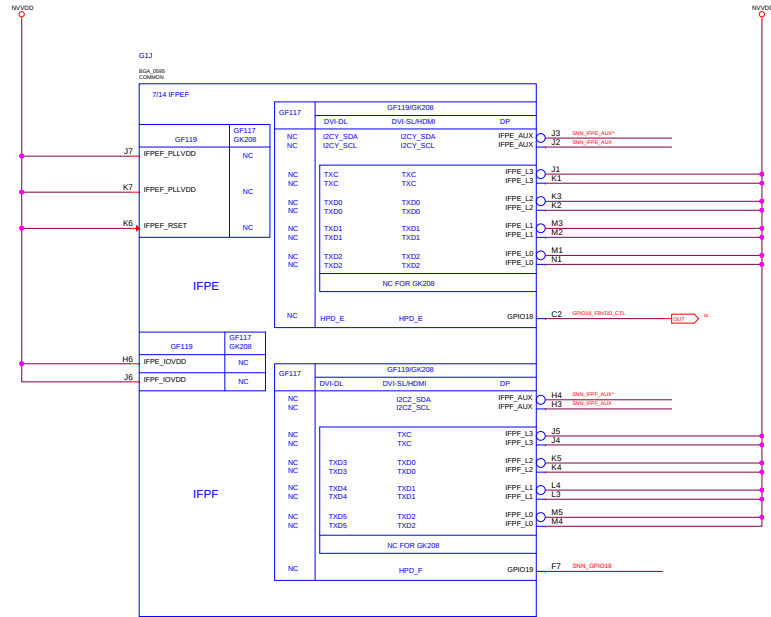
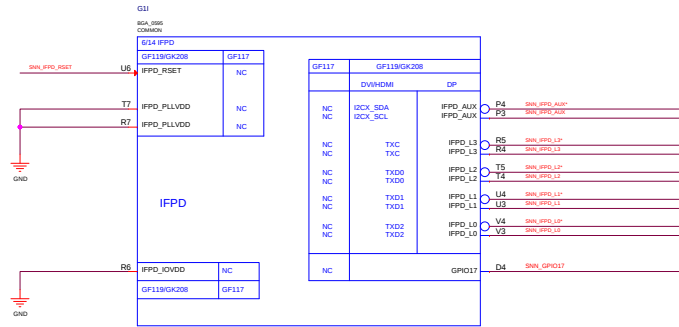
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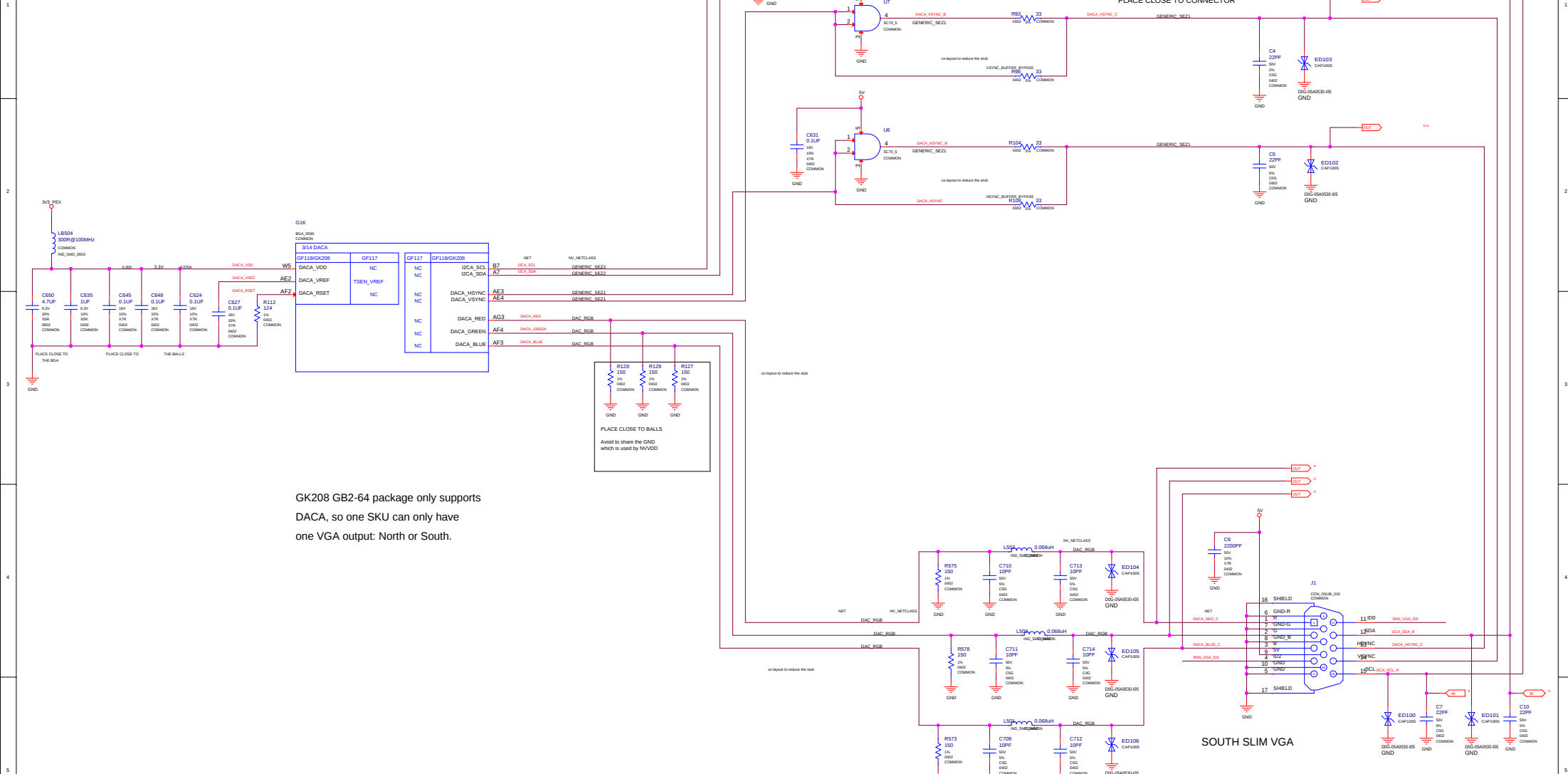




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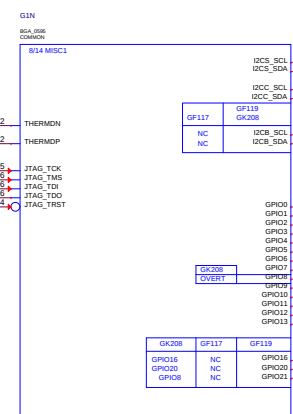
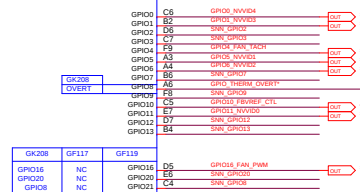
ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	UNUSED IFPD, IFPE & IFPF

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Smart Fan

Multilevel Straps

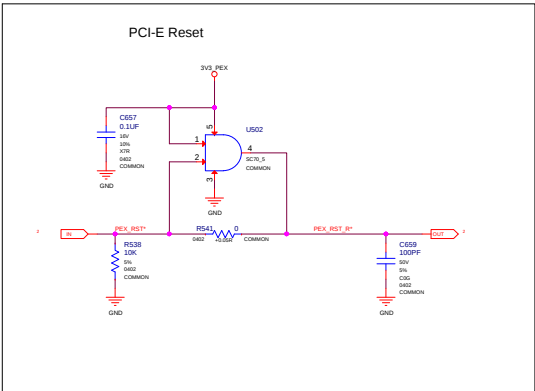
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0001	20K to GND	1001	10K to VCC
0002	10K to GND	1002	10K to VCC
0003	10K to GND	1003	10K to VCC
0004	20K to GND	1004	20K to VCC
0005	20K to GND	1005	20K to VCC
0006	20K to GND	1006	20K to VCC
0007	20K to GND	1007	20K to VCC
0008	20K to GND	1008	20K to VCC
0009	20K to GND	1009	20K to VCC
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0011	20K to GND	1011	20K to VCC

GK208 Straps

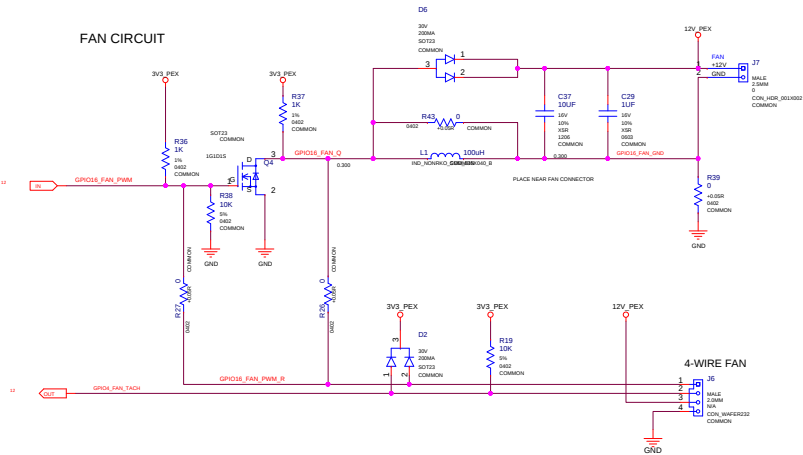
Bit Signal

Values

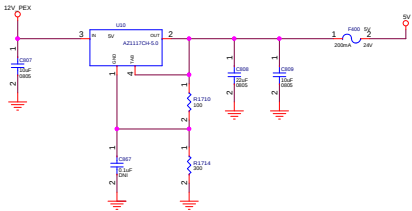
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02	RAMCFG[0]	0001	128MB GDDR5 X16 64bit Equiv			
01	RAMCFG[0]	0000	128MB GDDR5 X16 64bit Equiv A-die			
00	RAMCFG[0]	0001	128MB GDDR5 X16 64bit Equiv B-die			
02	FBI[0]	01	128MB			ROM_SO
01	FBI[0]	00	128MB			
01	SMB_ALT_ADDR	0	DISABLED	1	ENABLED	ROM_SCLK
00	VGA_DEVICE	0	DISABLED	1	ENABLED	
03	PCI_DEV0_EXT	SKL0	0	SKL1	TBD	
02	SUB_VENDOR	0	NO_BIOS	1	BIOS	
01	PCI_DEV0_EXT_RTS	SKL0	0	SKL1	TBD	
00	PCI_FAL_RN_TERM000	0	DISABLED	1	ENABLED	
03	USE[RE]	0000	NOT			Strap0
02	USE[RE]					
01	USE[RE]					
00	USE[RE]					
03	3G00_PADCFG_LUT_ADDR0	0000	DISKTOP_DEFAULT	0000	DISKTOP_1THRS	
02	3G00_PADCFG_LUT_ADDR0	0001	MOBILE_DEFAULT	0001	MOBILE_1THRS_LAMP	
01	3G00_PADCFG_LUT_ADDR0	0002	MOBILE_1THRS_LAMP	0002	MOBILE_1THRS_LAMP	
00	3G00_PADCFG_LUT_ADDR0	0000	MOBILE_1THRS_LAMP	0000	MOBILE_1THRS_LAMP	
03	3G00_PADCFG_LUT_ADDR0	0001	MOBILE_1THRS_LAMP	0001	MOBILE_1THRS_LAMP	
02	3G00_PADCFG_LUT_ADDR0	0002	MOBILE_1THRS_LAMP	0002	MOBILE_1THRS_LAMP	
01	3G00_PADCFG_LUT_ADDR0	0000	MOBILE_1THRS_LAMP	0000	MOBILE_1THRS_LAMP	
00	3G00_PADCFG_LUT_ADDR0	0001	MOBILE_1THRS_LAMP	0001	MOBILE_1THRS_LAMP	
03	PCI_DEV000	SKL0	0000			Strap1
02	PCI_DEV000					
01	PCI_DEV000					
00	PCI_DEV000					
03	SC0L_EXP000	0	DISABLED	1	ENABLED	Strap2
02	SC0L_EXP000	0	DISABLED	1	ENABLED	
01	SC0L_EXP000	0	DISABLED	1	ENABLED	
00	SC0L_EXP000	0	DISABLED	1	ENABLED	
03	PCI_DEV000	SKL0	0000			Strap3
02	PCI_DEV000					
01	PCI_DEV000					
00	PCI_DEV000					
03	PCI_DEV000	SKL0	0000			Strap4
02	PCI_DEV000					
01	PCI_DEV000					
00	PCI_DEV000					



FAN CIRCUIT



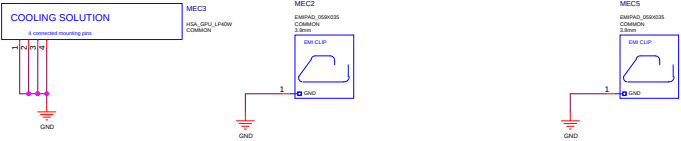
5V Power



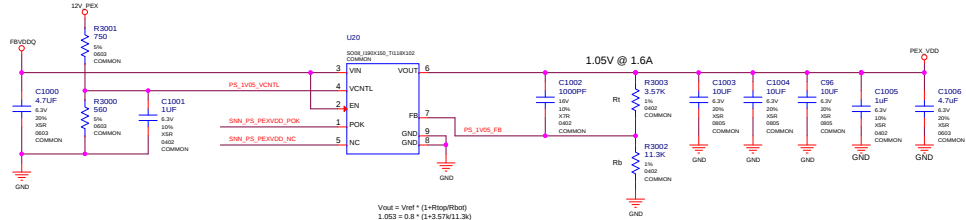
STUFF FOR DVHDMVGA DISPLAY SKU

MT1
MT_Hole 0.136 TM 5.5 BM 7.0

Mechanical

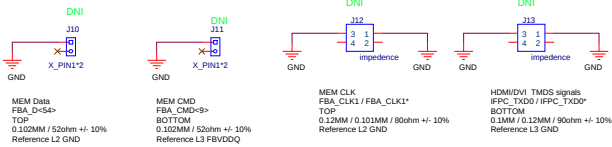


PEX_VDD



$V_{out} = V_{in} * (1 + R_{top}/R_{bot})$
 $1.053 = 0.8 * (1 + 5.79/11.3k)$

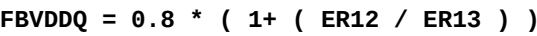
Reserve J10 - J13 for Impedance check



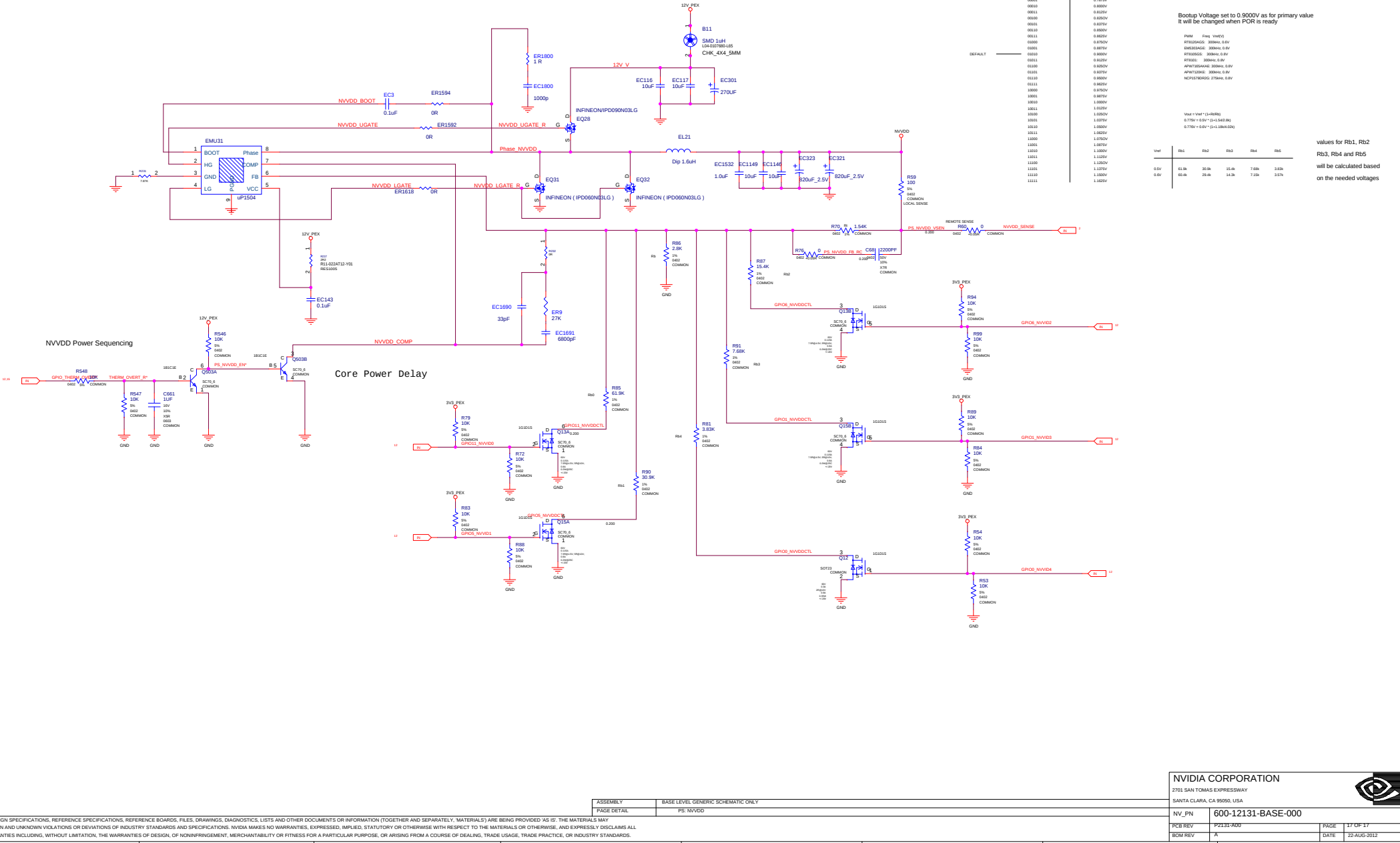
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PAGE DETAIL	PS 5V, PEXVDD

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