

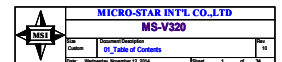
Comanche 192b GDDR5, <150W, 2-way SLI
Tall DVI-I + DP + DP + DP/HDMI + DP

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Note:

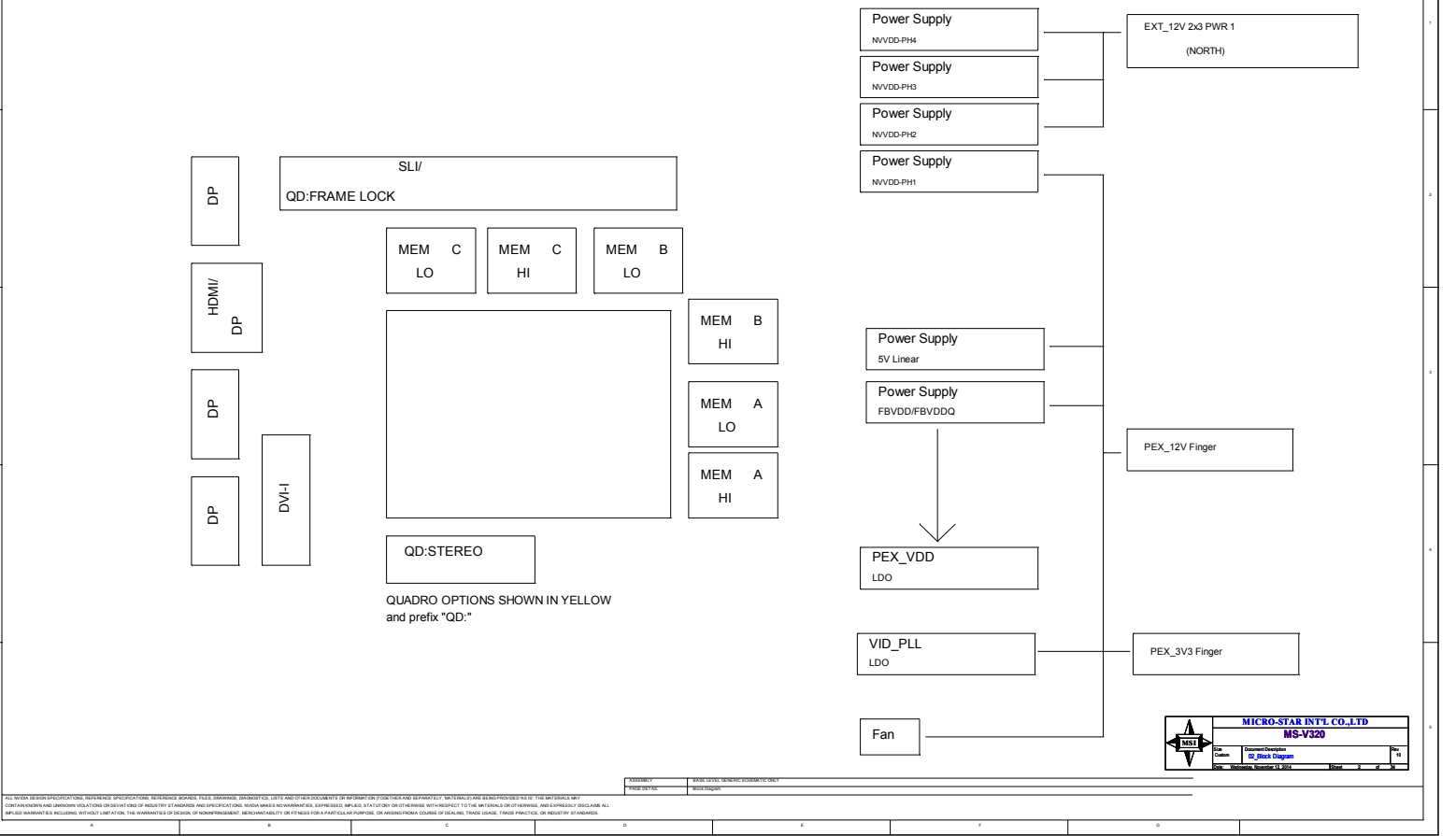
- 1 page:4-11 修改net name,加<>
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- 4 page:16/17/19 修改DP footprint
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ASSEMBLY	BAND LEVEL ORGANIC SYNTHETIC ONLY
PART DETAIL	TUBES OF CATHODE

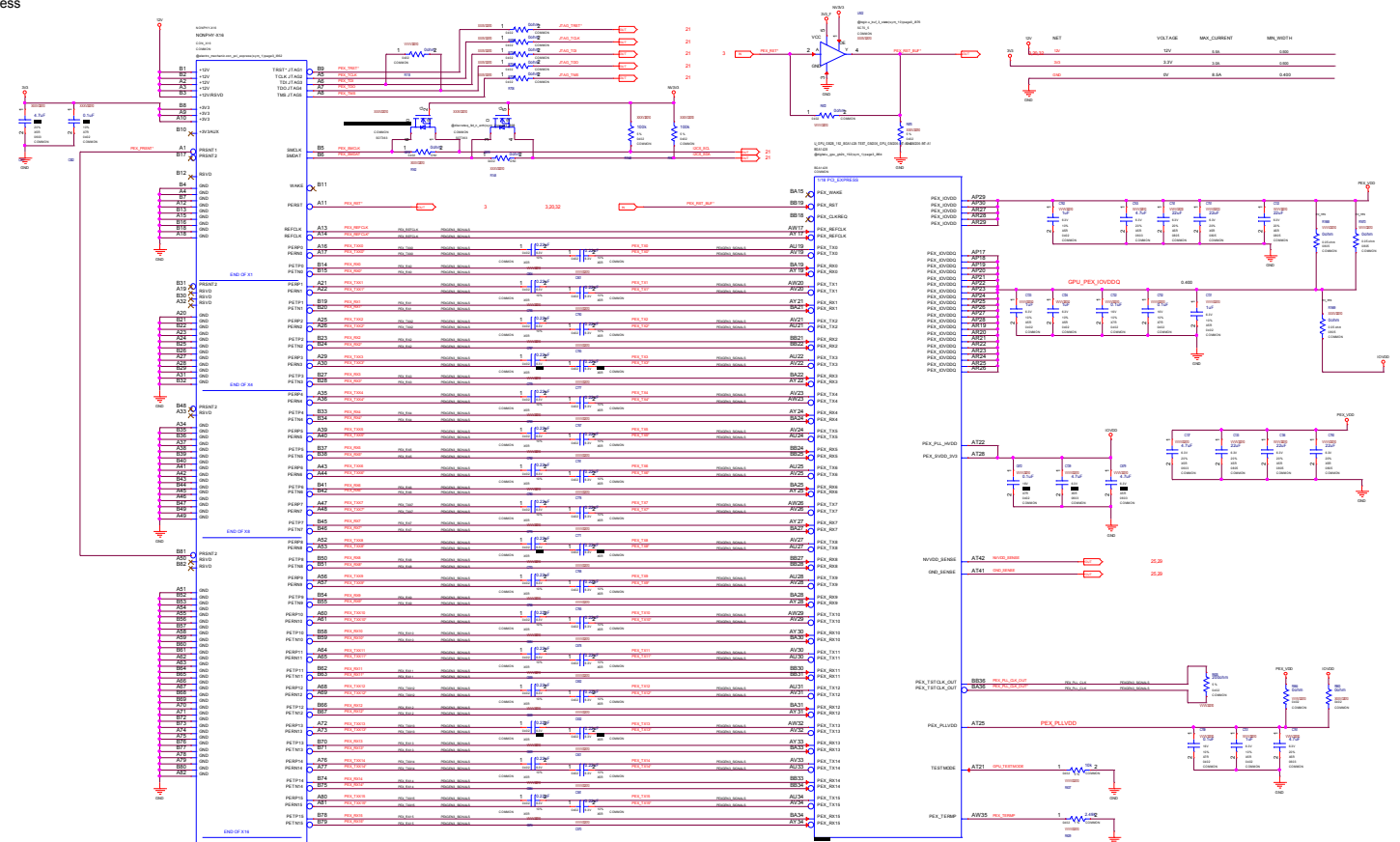
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Block Diagram



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PCI Express



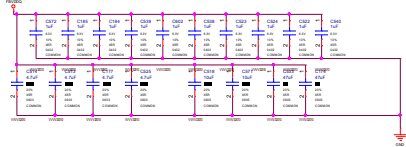
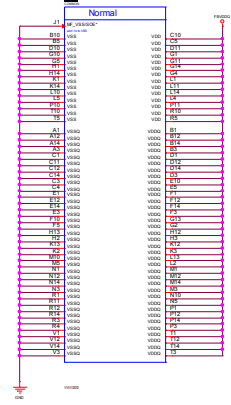
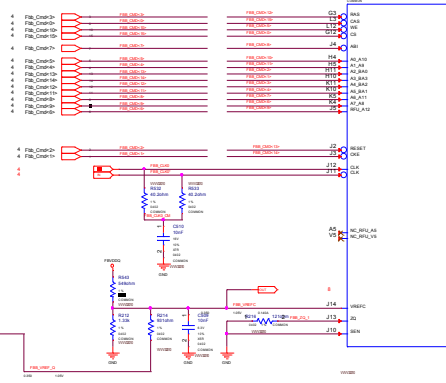
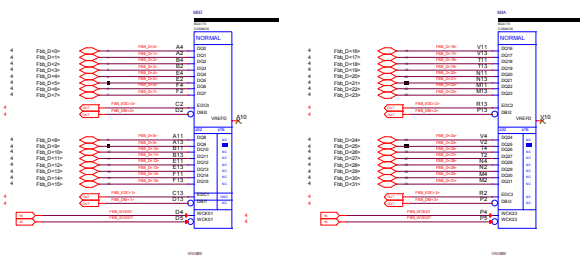
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PAGE DETAIL	PCI Express

	MICRO-STAR INT'L CO.,LTD MS-V320			
	Title Custom		Document Description 03_PCI Express	
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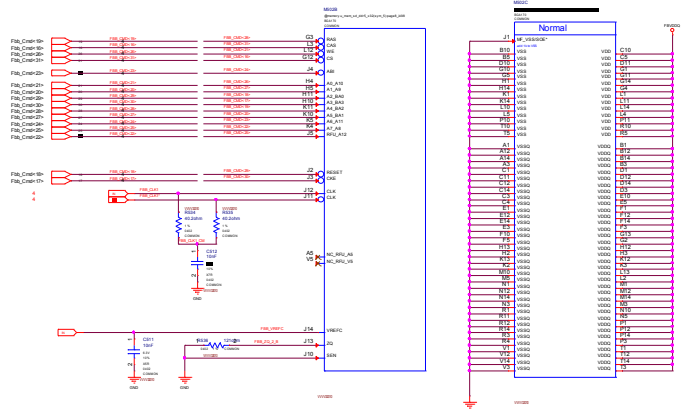
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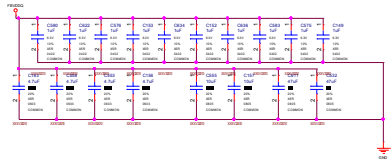
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MS-V320

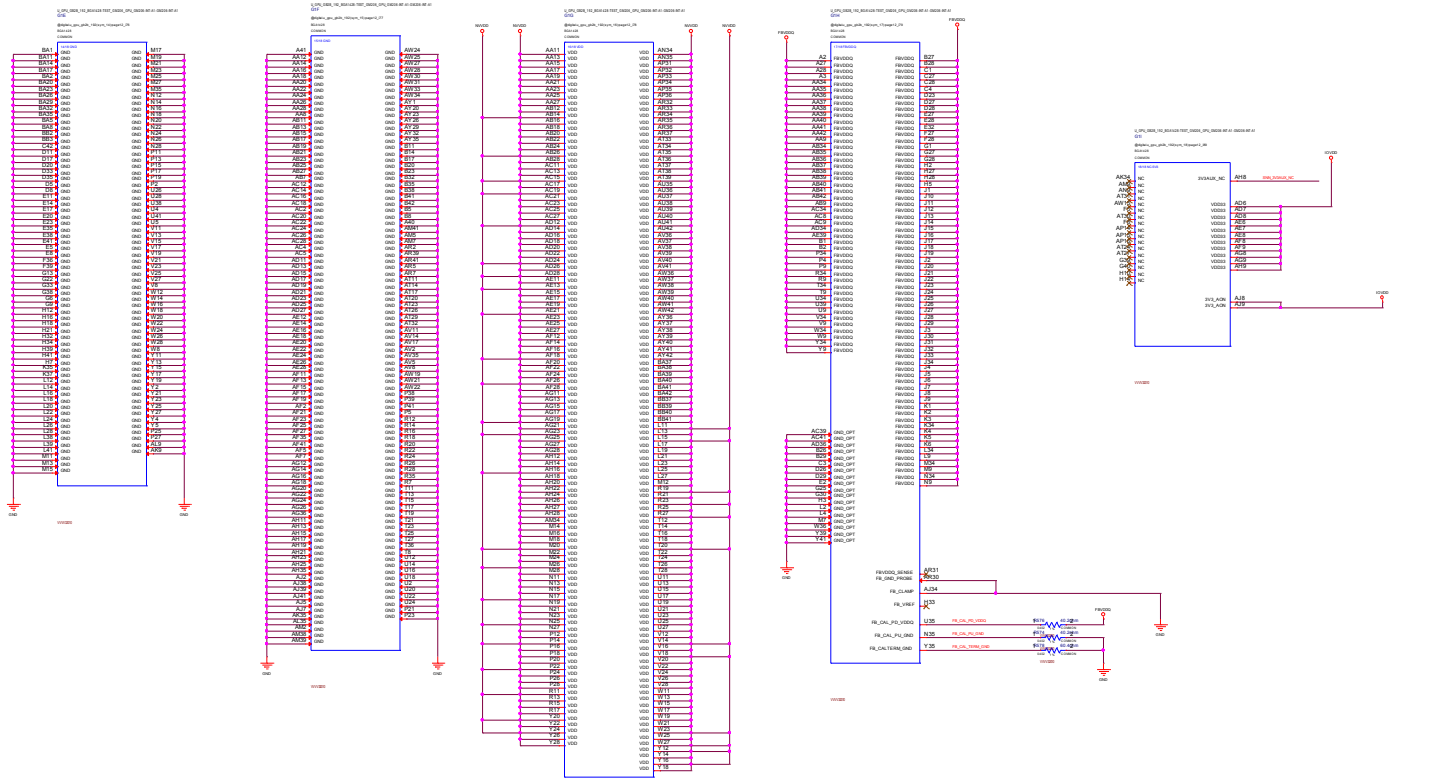
Rev: 1.0

Doc: 01_MEMORY_FBB_P0_0





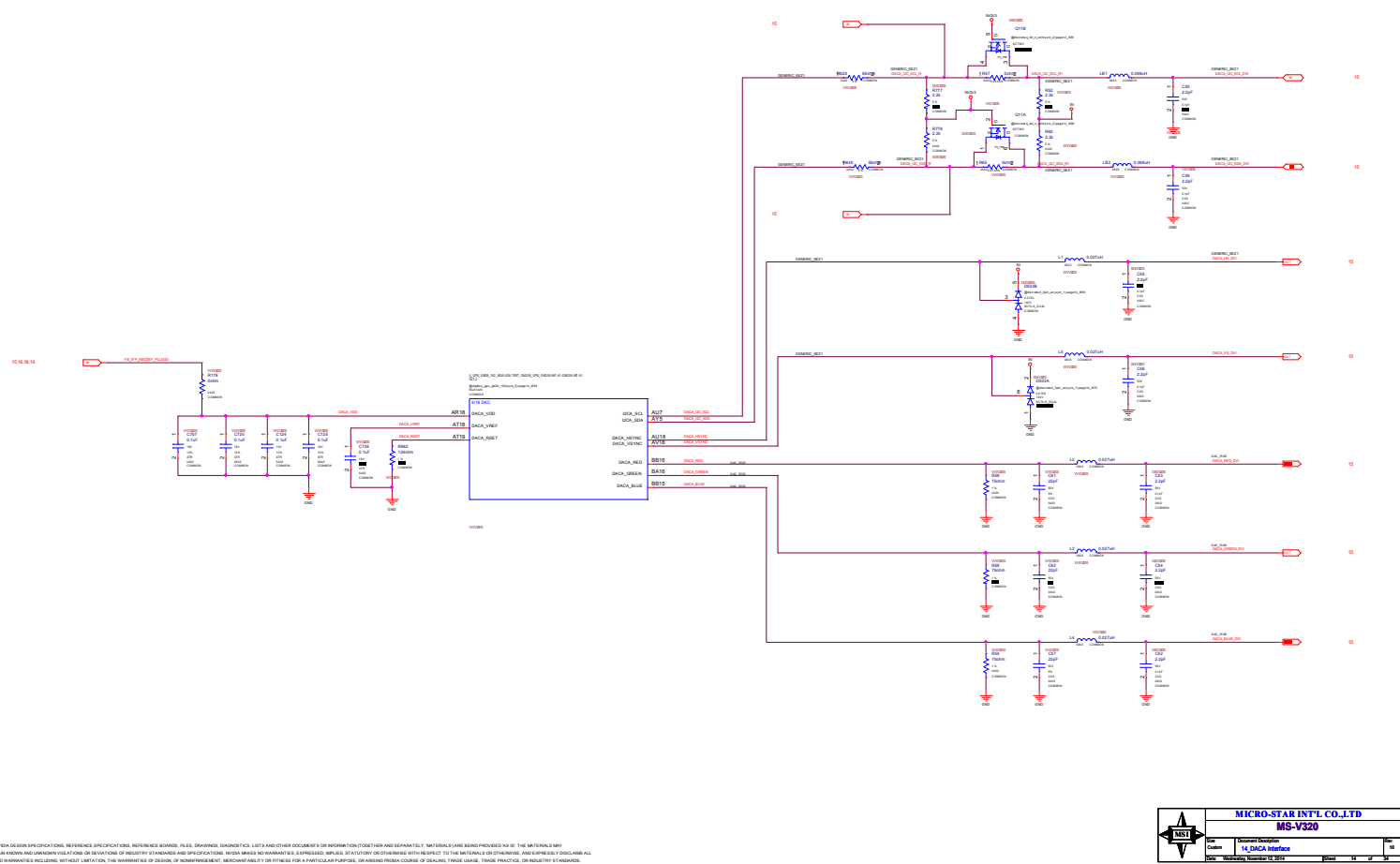
GPU PWR and GND



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MS-V320	
Rev	Document Description
001	12 GPU PWR and GND
Rev	Rev
001	001

DACA Interface



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	MS-V320		
Item Custom	Equipment Description <u>14_DACA Interface</u>	Date Wednesday, November 12, 2014	Page 14 of 24





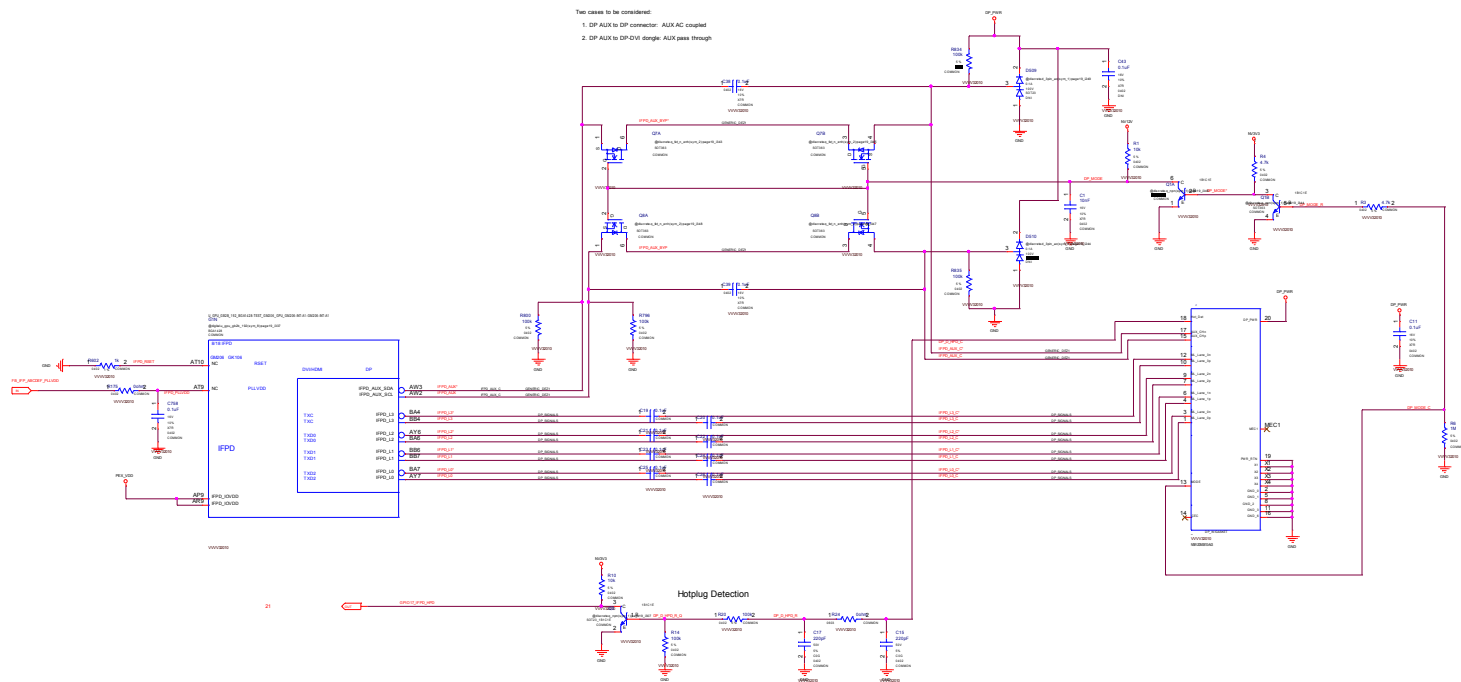
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Two cases to be considered

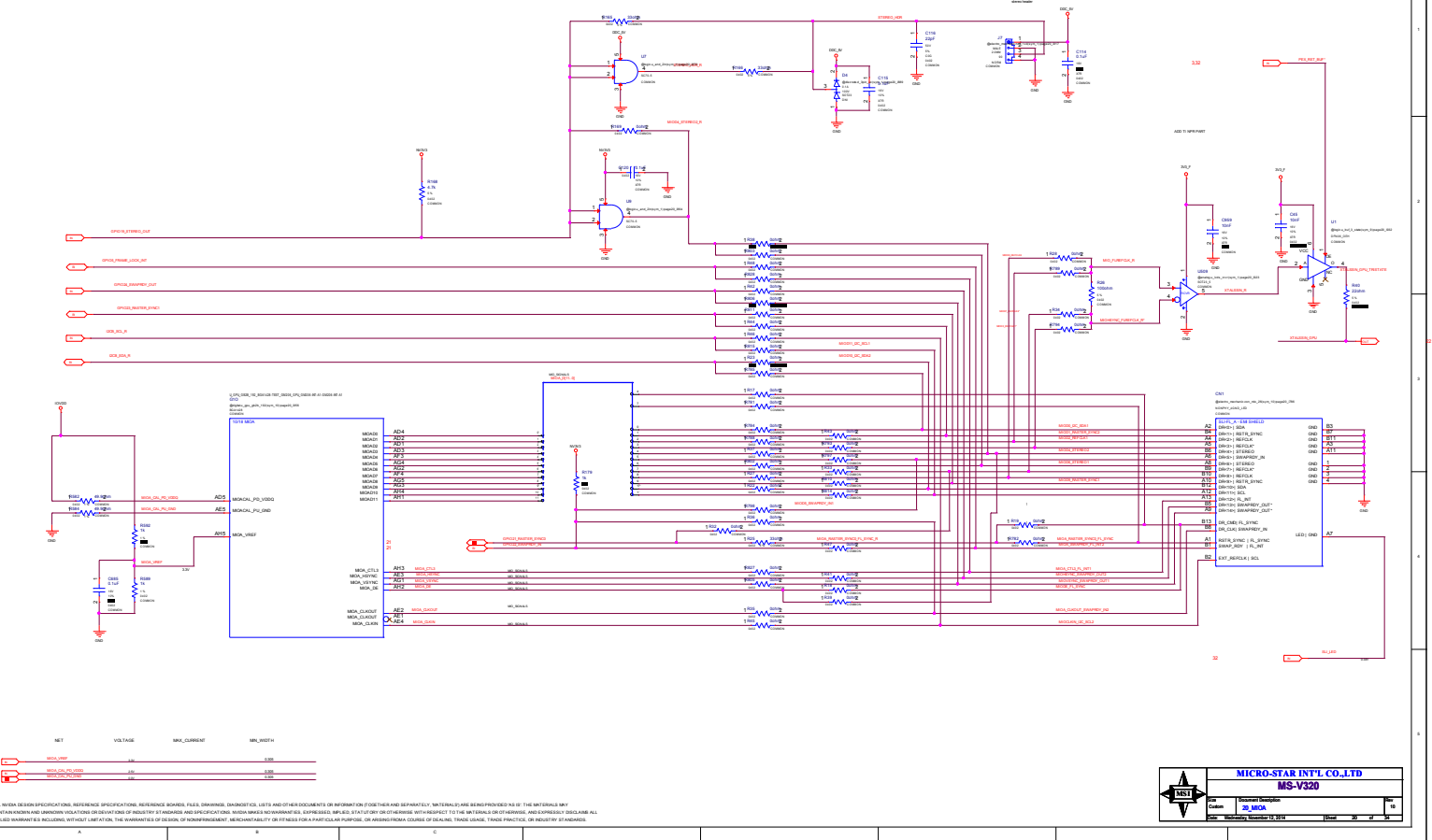
1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI bridge: AUX pass through



14ci203



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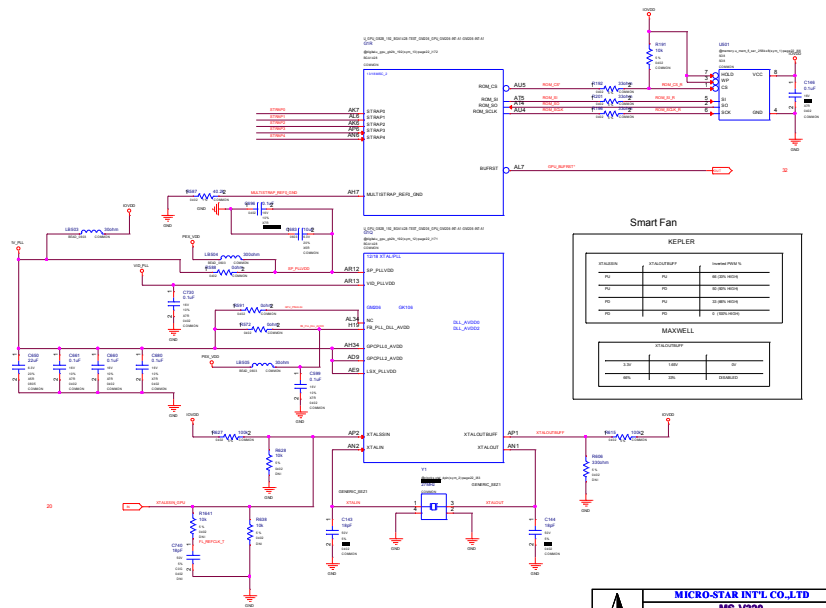
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MISC2: ROM, XTAL, Straps

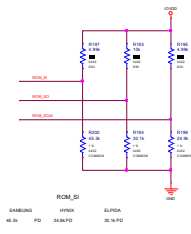
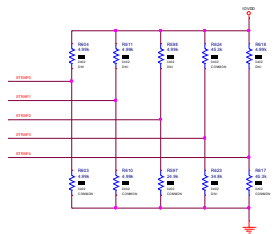
	KEPLER	MAXWELL
STRAP0	UBR0_INT[3:0]'	
STRAP1	3IG0_PADCFG_1_UT_ADR'	
STRAP2	PC0_DEVID[3:0]'	
STRAP3	SOR0_EXPOSED[3:0]'	
STRAP4	DP_FLT_VDDO_33V"	
	PEX_MAX_SPEED'	
	PEX_SFDP_CHANGE_SIGN"	
	*	
ROM_S	RAMCFG[0]'	RAMCFG[0]'
	RAMCFG[1]'	RAMCFG[1]'
	RAMCFG[2]'	RAMCFG[2]'
	RAMCFG[3]'	RAMCFG[3]'
ROM_SO	VGA_DEVICE"	VGA_DEVICE"
	SMB_ALT_ADR'	SMB_ALT_ADR'
	FB[0]_APERTURE_SIZE"	PCE_CFG"
	FB[1]_APERTURE_SIZE"	DEVID_SEL"
ROM_SCLK	PEX_FLT_EN_TERM100"	SOR0_EXPOSED'
	PC0_DEVID_EXT[0]'	SOR1_EXPOSED'
	SUB_VINDOCP'	SOR2_EXPOSED'
	PC0_DEVID_EXT[4]'	SOR3_EXPOSED'

	GND	V _{V3}
5k	0000	1000
10k	0001	1001
15k	0010	1010
20k	0011	1011
25k	0100	1100
30k	0101	1101
35k	0110	1110
40k	0111	1111

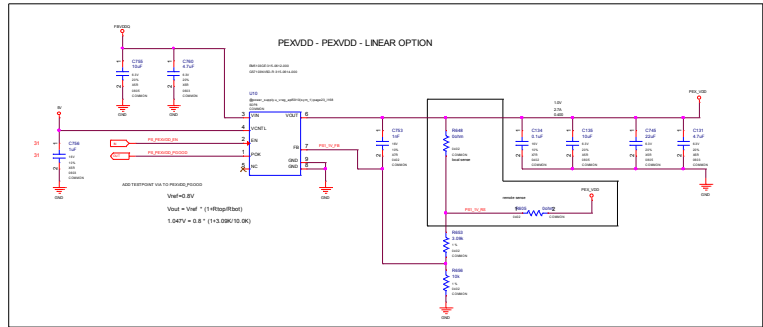
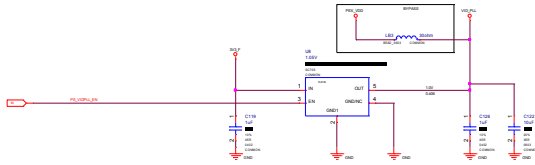
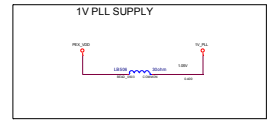


MULTI_STRIP_REFS_END	
BINARY PRODUCTION	NO
BINARY BRIDGEUP	NO
MULTILEVEL	NO OR YES TO END

STAPPS		
3.3v	1.65v	0v
SCP+ ISLAND ENABLED	SCP+ ISLAND MODE	SCP+ ISLAND DISABLED

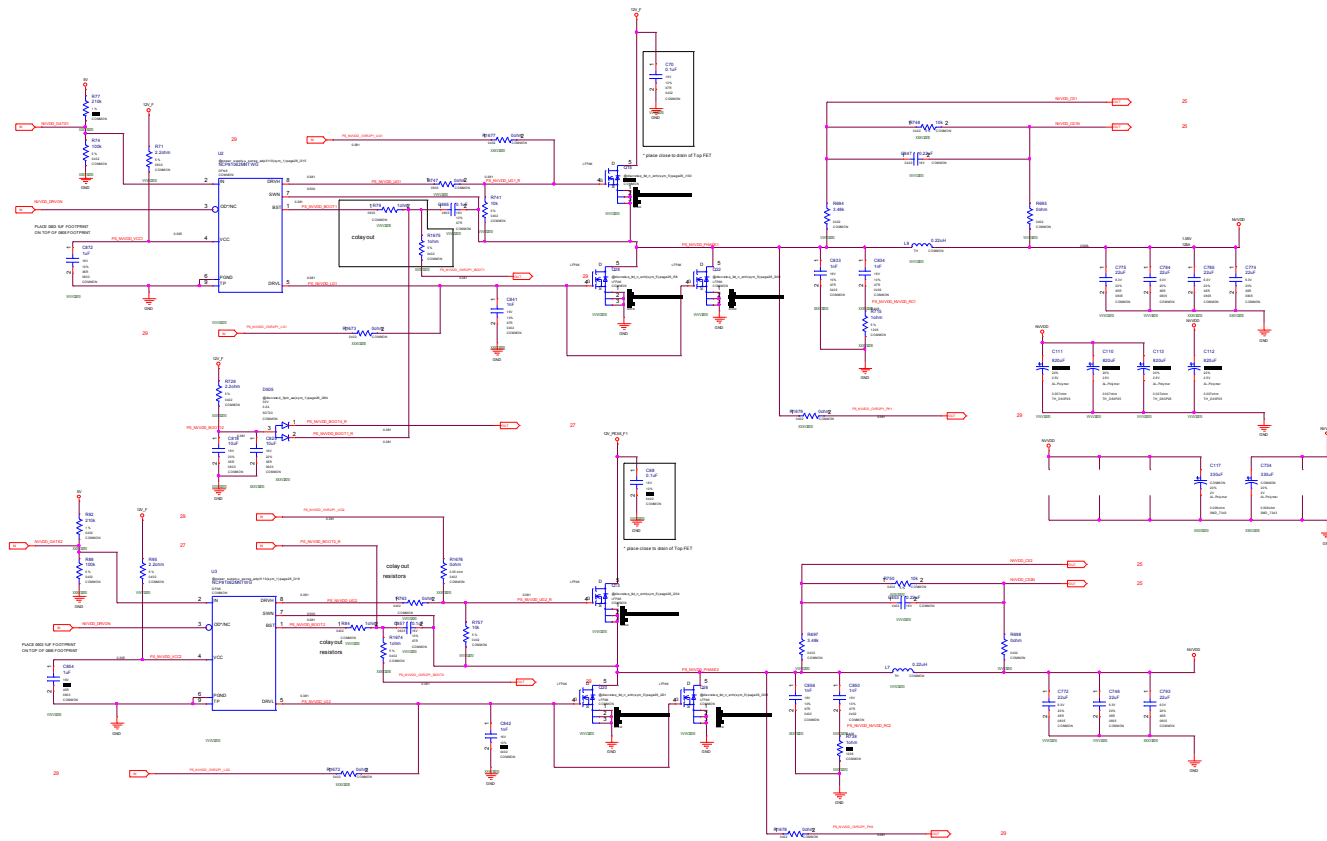


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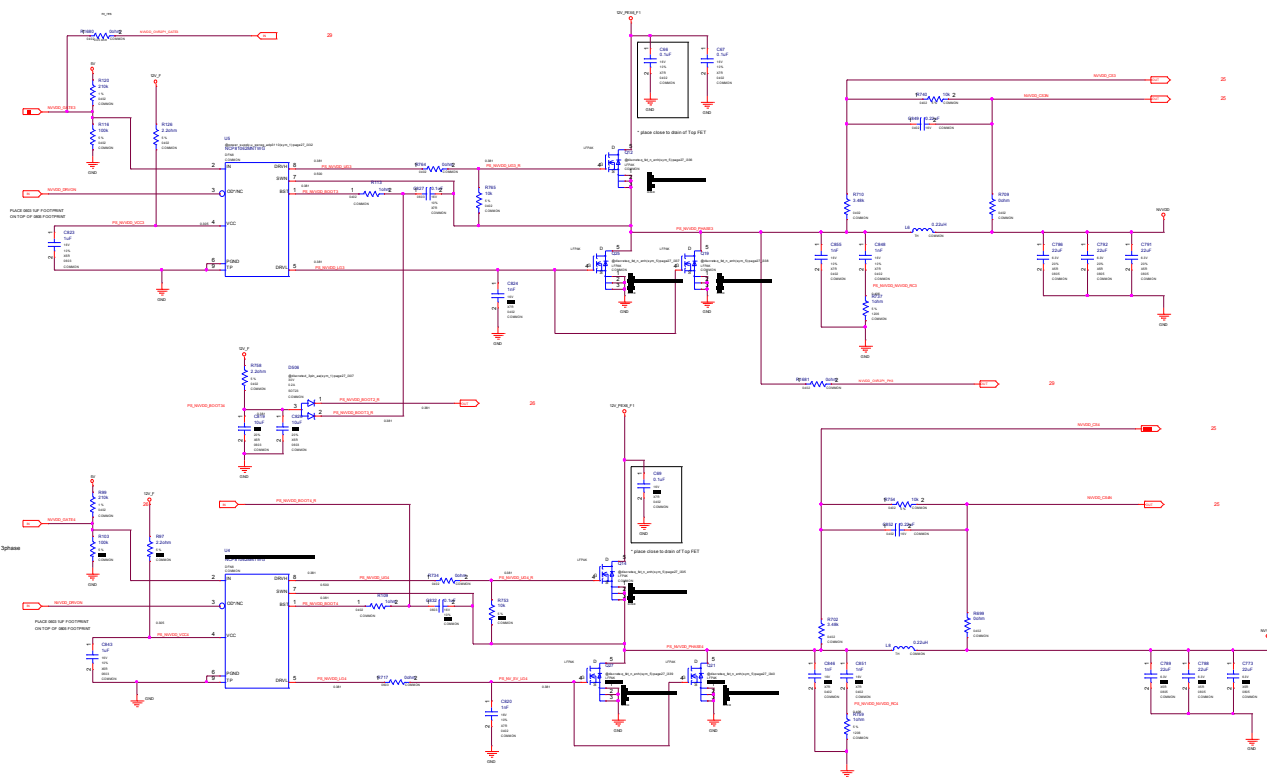








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14ci203
MICRO-STAR INT'L CO., LTD.
MS-V320

Part	14ci203	Rev	1.0
Order	27_PS_NVVDD Phase 3,4	Rev	1.0
Doc	Microstar-14ci203	Rev	1.0

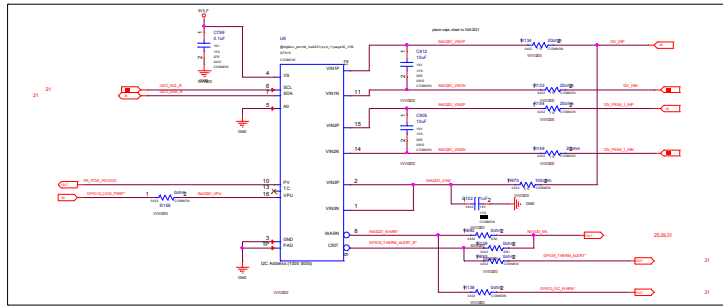
[RESERVED]

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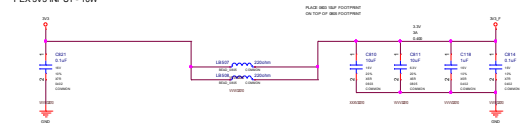
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MS-V320	
DATE	2024/08/01
BY	2024/08/01
REV	1.0



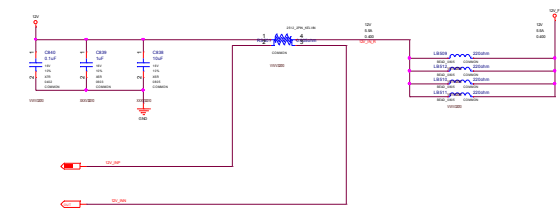
PS: Inputs, Filtering, and Monitoring



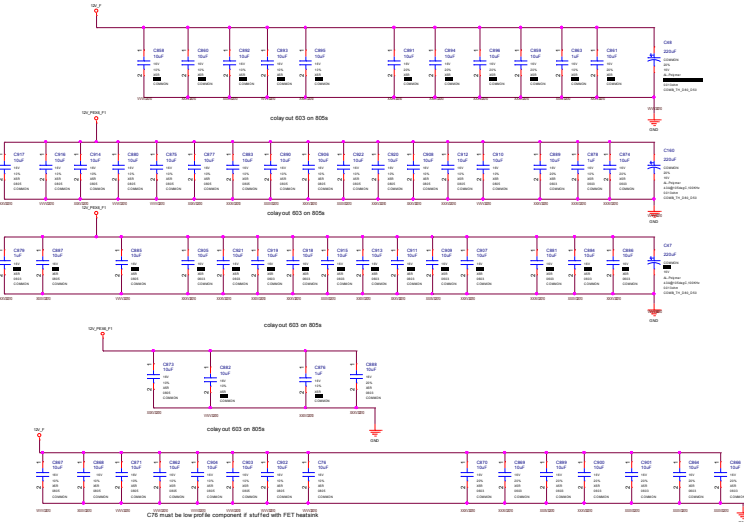
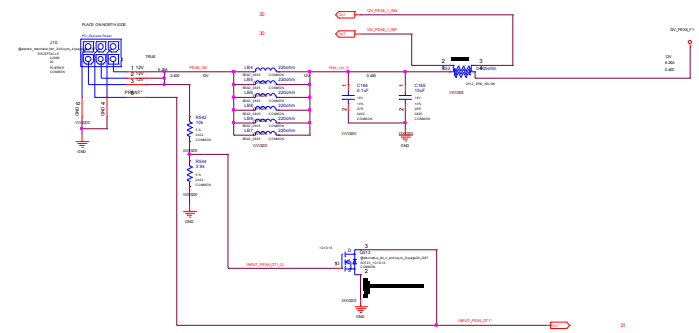
PEX_3V3 INPUT - 10W



PEX_12V INPUT - 60W



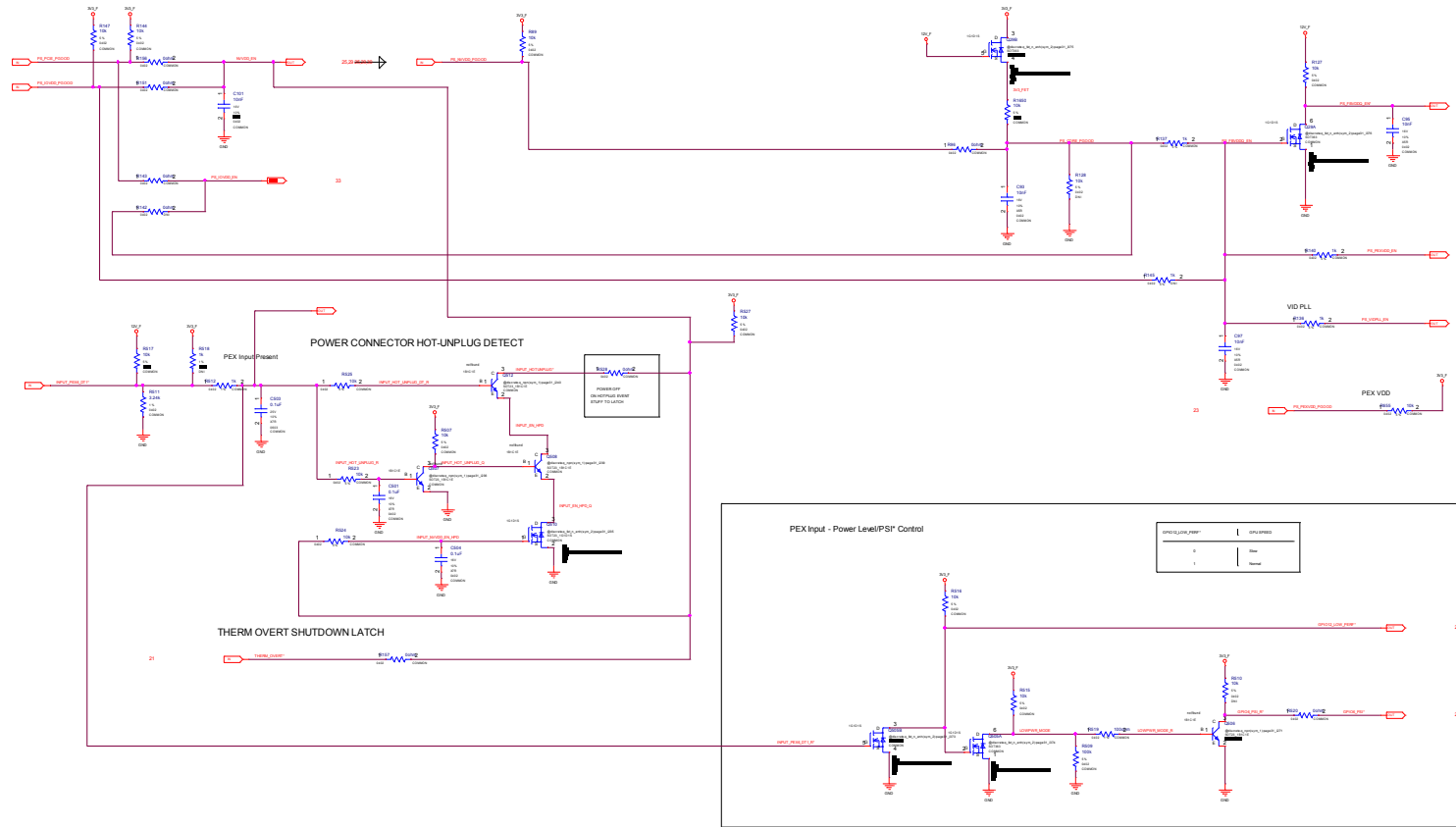
PEX_12V INPUT - 2x3 PCIe CON 75W



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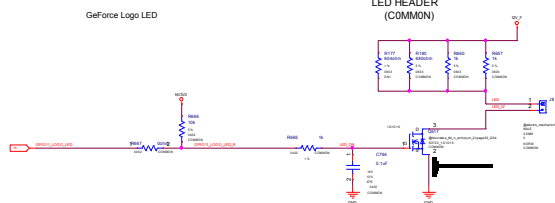
MICRO-STAR INT'L CO., LTD	
MS-V320	
Model	MS-V320
Version	1.0
Date	2023.01.10

PS: Sequence and Shutdown

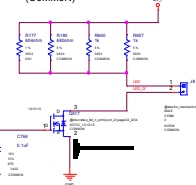


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GeForce Logo LED

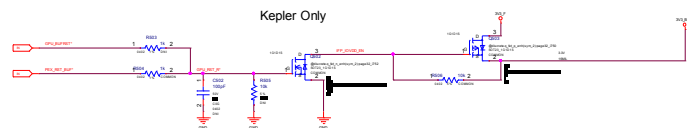


LED HEADER (COMMON)



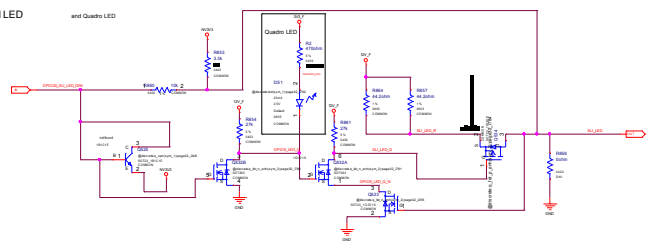
IFP_IOVDD (backdrive prevention)

Kepler Only



SLI LED

and Quadco LED



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BYPASS STUFF FOR 280m

The diagram shows three parallel branches connected between a 5V input and a 0V output. Each branch contains a 100mH inductor in series with a 100kΩ resistor. The components are labeled as follows:

- Top branch: 100mH inductor (L1) and 100kΩ resistor (R1).
- Middle branch: 100mH inductor (L2) and 100kΩ resistor (R2).
- Bottom branch: 100mH inductor (L3) and 100kΩ resistor (R3).

The input voltage is 5V and the output voltage is 0V.

	MICRO-STAR INT'L CO.,LTD		
	MS-V320		
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Brackets:



HDMI FPC
SSSSSS

HDMI FPC
SSSSSS

HDMI FPC
SSSSSS

HDMI FPC
SSSSSS

SPECIAL MECHANIC

SPECIAL MECHANIC





MICRO-STAR INT'L CO., LTD

MS-V320

Rev: 01

Doc: 34_MECH_Thermal

Rev: 01

Doc: 34_MECH_Thermal

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