

PG301 A02

Comanche 192b GDDR5, <150W, 2-way SLI

Tall DVI-I + DP + DP + DP/HDMI + DP

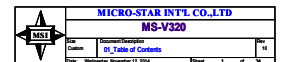
TABLE OF CONTENTS

Page	Description
1	Table of Contents
2	Block Diagram
3	PCI Express
4	MEMORY: GPU Partition A/B
5	MEMORY: FBA[31:0]
6	MEMORY: FBA[63:32]
7	MEMORY: FBB[31:0]
8	MEMORY: FBB[63:32]
9	MEMORY: GPU Partition C
10	MEMORY: FBC[31:0]
11	MEMORY: FBC[63:32]
12	GPU PWR and GND
13	GPU Decoupling
14	DACA Interface
15	IFPAB DVI+DL
16	IFPEF with IFPE DP
17	IFPF DP
18	IFPC HDMI/DP
19	IFPD DP
20	MICA
21	MISC1: Fan, Thermal, JTAG, GPIO
22	MISC2: ROM, XTAL, Straps
23	PS: SV, PEX_VDD, VD_PLL
24	PS: FBVDD/IQ
25	PS: NVVDD Controller OVR4 option

Page	Description
26	PS: NVVDD Phase 1,2
27	PS: NVVDD Phase 3,4
28	[RESERVED]
29	PS: NVVDD OVR2+1 option
30	PS: Inputs, Filtering, and Monitoring
31	PS: Sequence and Shutdown
32	LEDs
33	PS: IOVDD Regulator
34	MECH: Bracket/Thermal

Note:

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- page:8 增加頁符號
- page:15 修改DVI footprint
- page:16/17/19 修改DP footprint
- page:18 修改HDMI footprint
- page:26/27 修改output chike footprint



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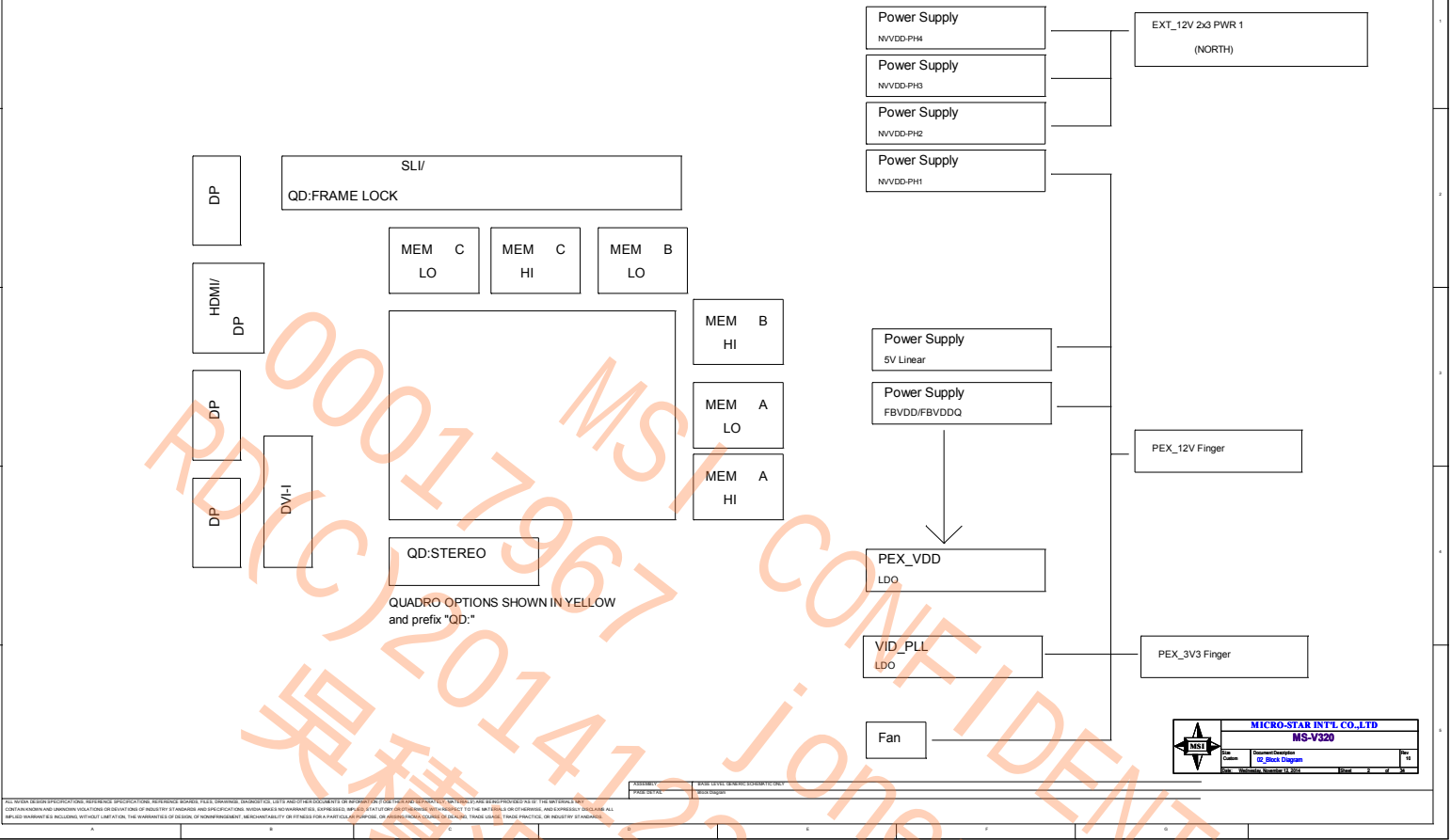
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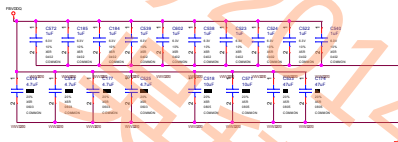
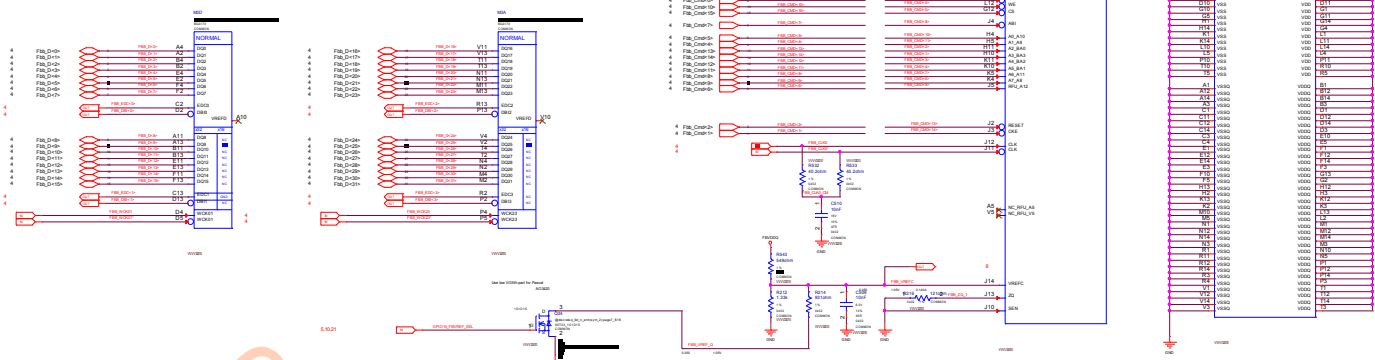
DATE OF THIS DOCUMENT

DATE OF THIS DOCUMENT



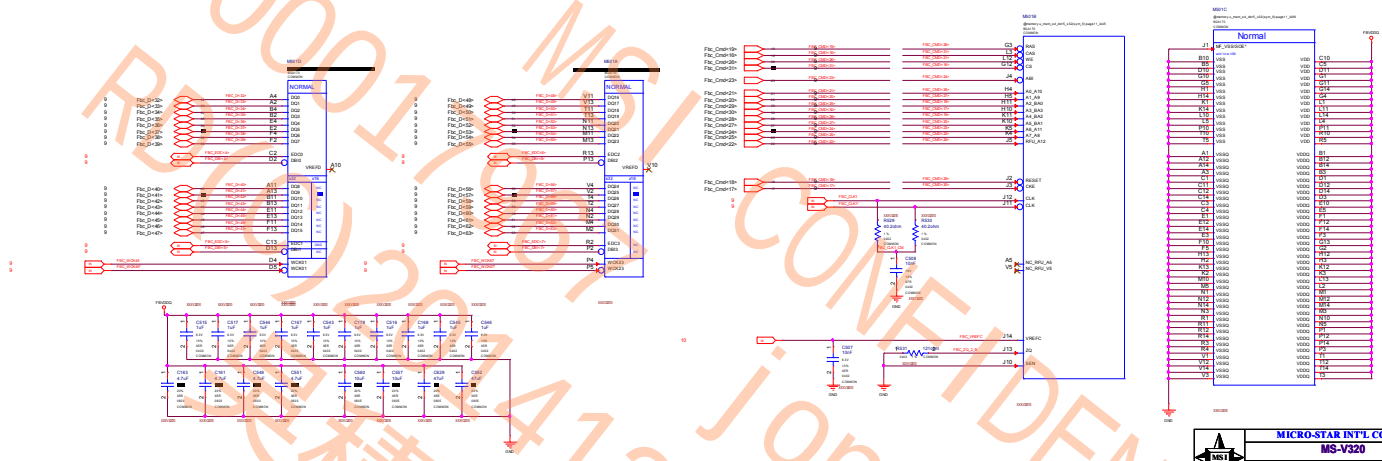






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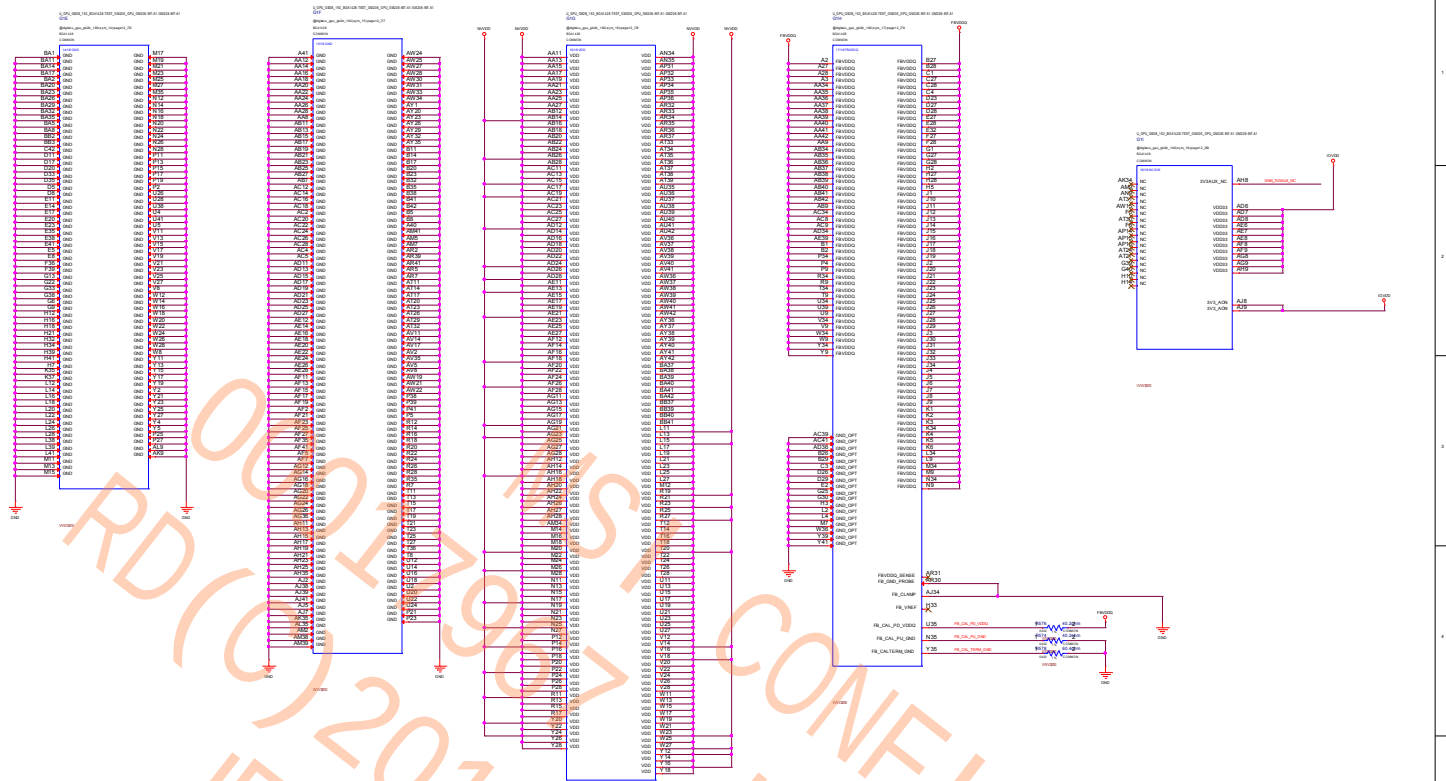
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MS-V320

Rev: 1.0

Doc: 11-MEMORY-FBC01_02

GPU PWR and GND



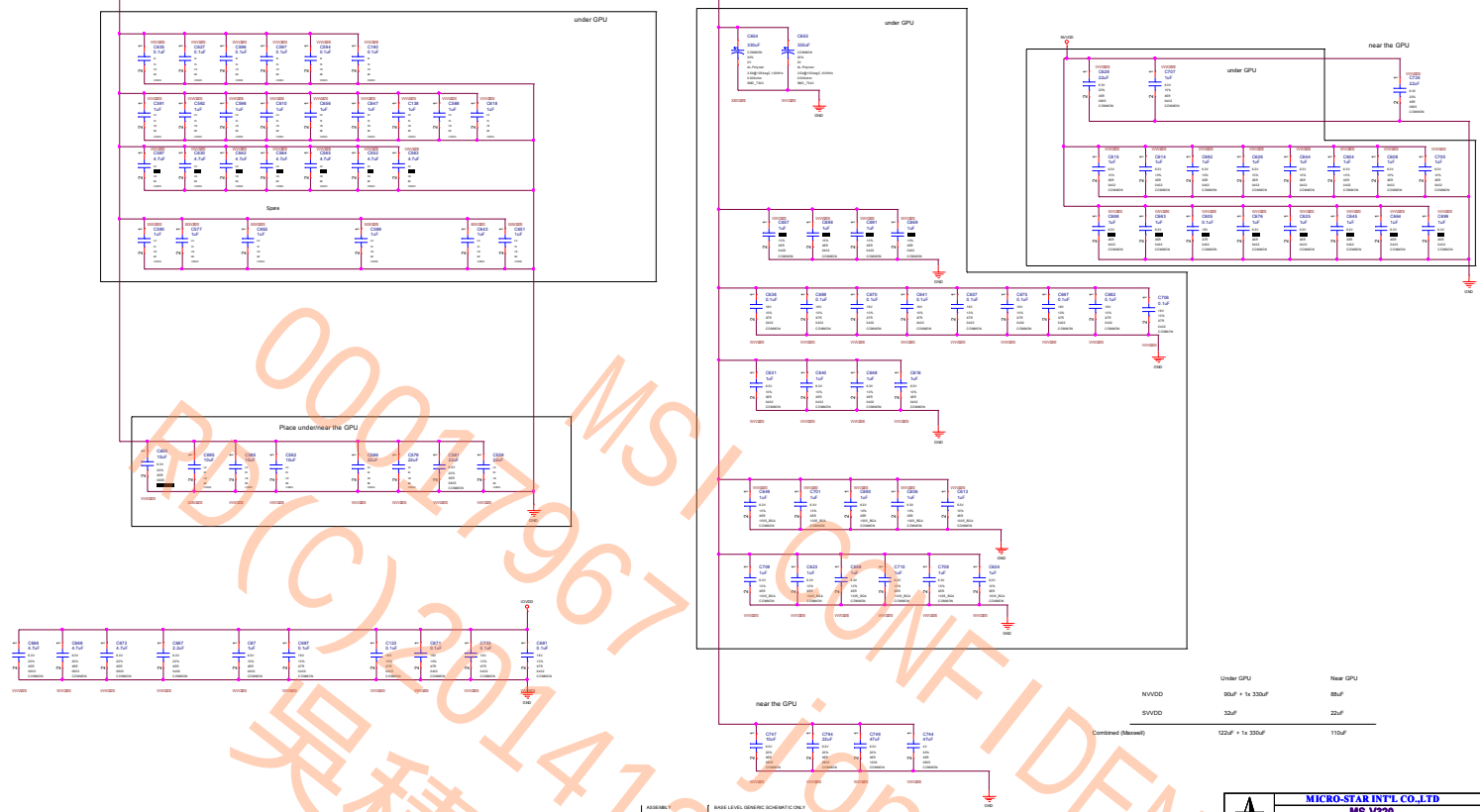
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GPU Decoupling

FBVDDQ

NVDD



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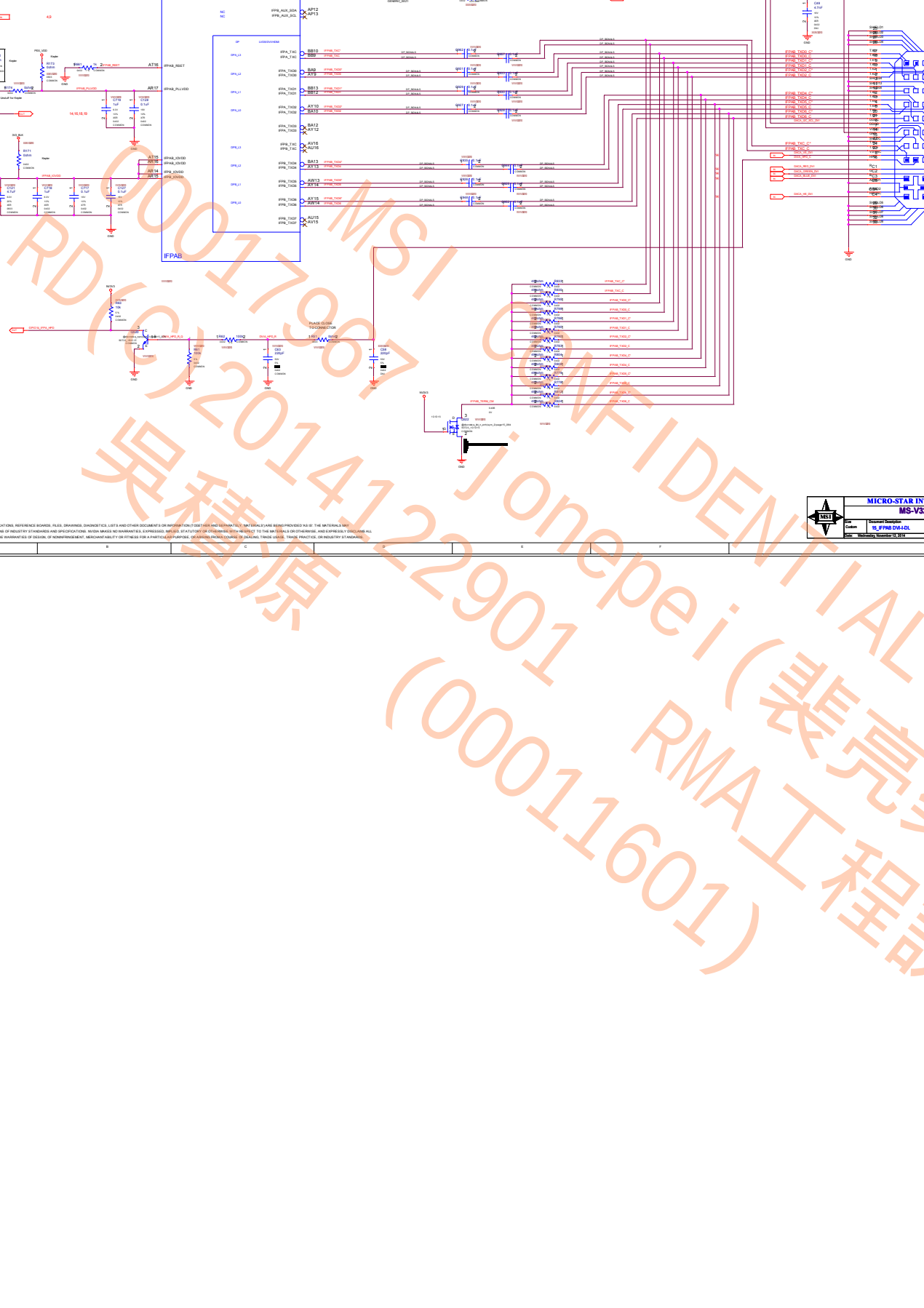
ASSEMBLY
PAGE 01/01

BASE LEVEL SCHEMATIC ONLY
GPU Decoupling



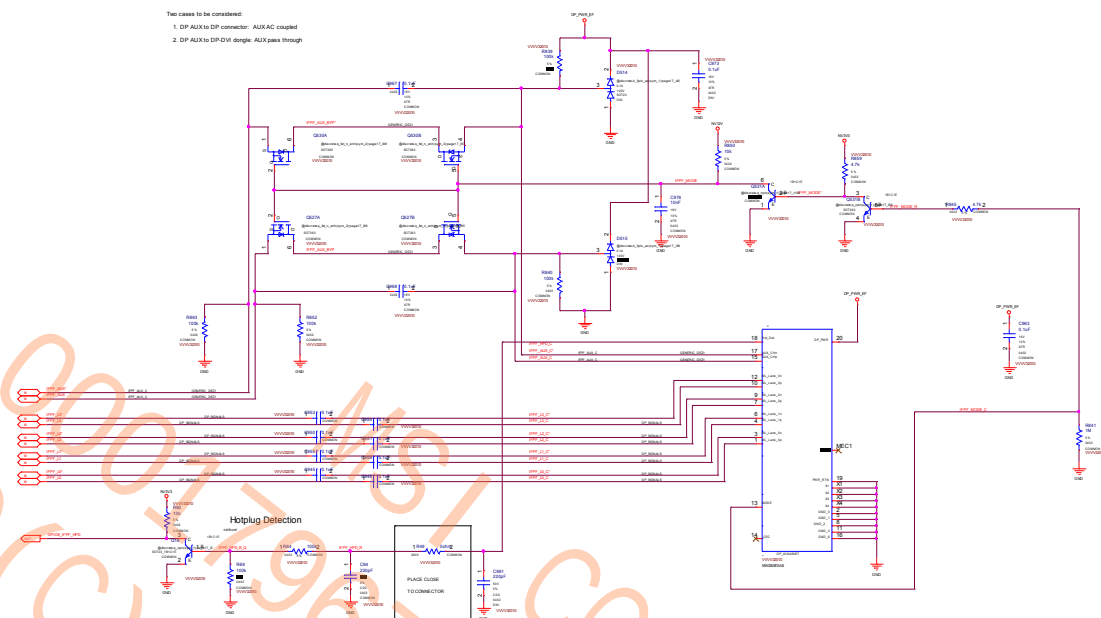
MICRO-STAR INT'L CO., LTD.	
MS-V320	
Doc. Name	Decoupling
Doc. Number	13_GPU Decoupling
Doc. Version	1.0





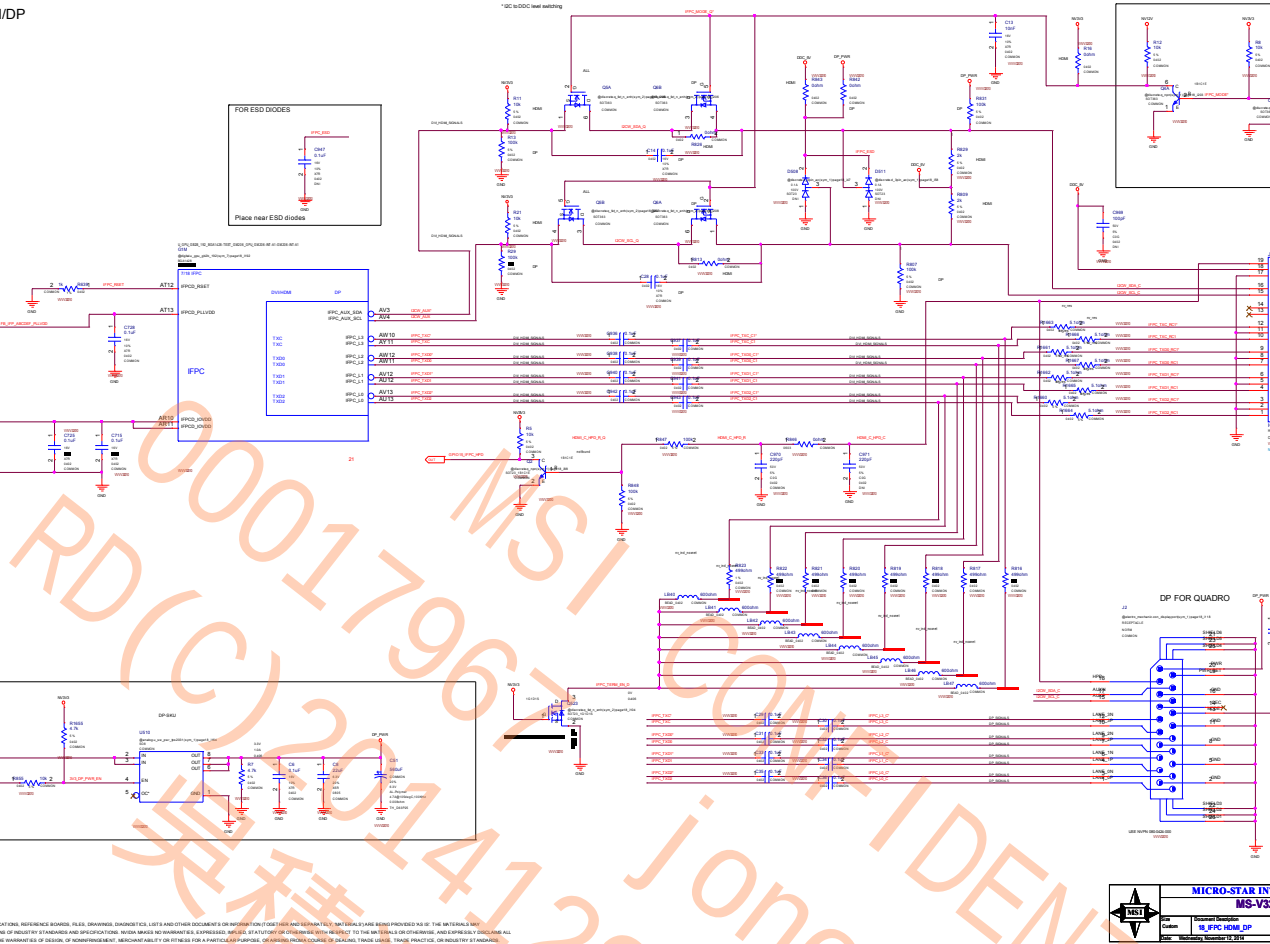


- Two cases to be considered:
1. DP AUX to DP connector: AUXAC capped
 2. DP AUX to DP-DVI single: AUX pass through



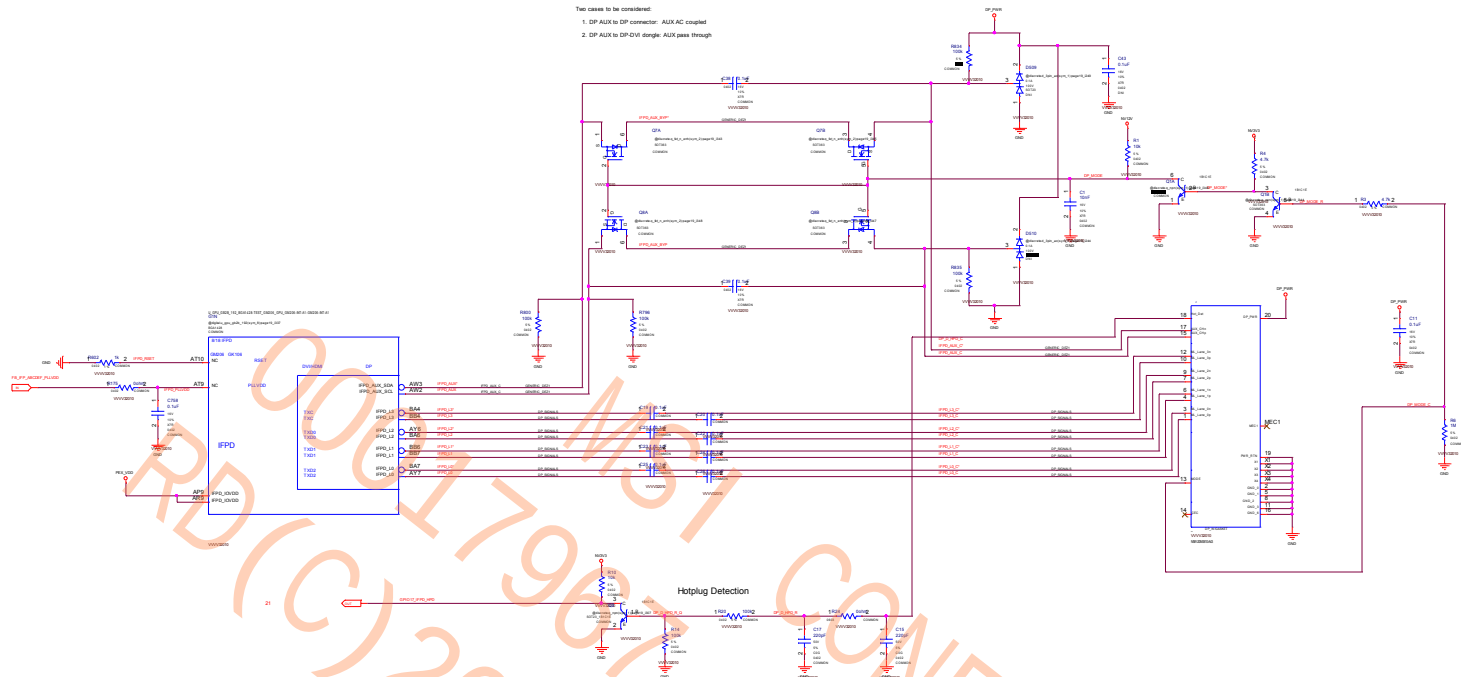
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MS-V320	
Rev	1.0
Doc No	17,899 DP
Rev	1.0
Rev	1.0



Two cases to be considered

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



14ci203

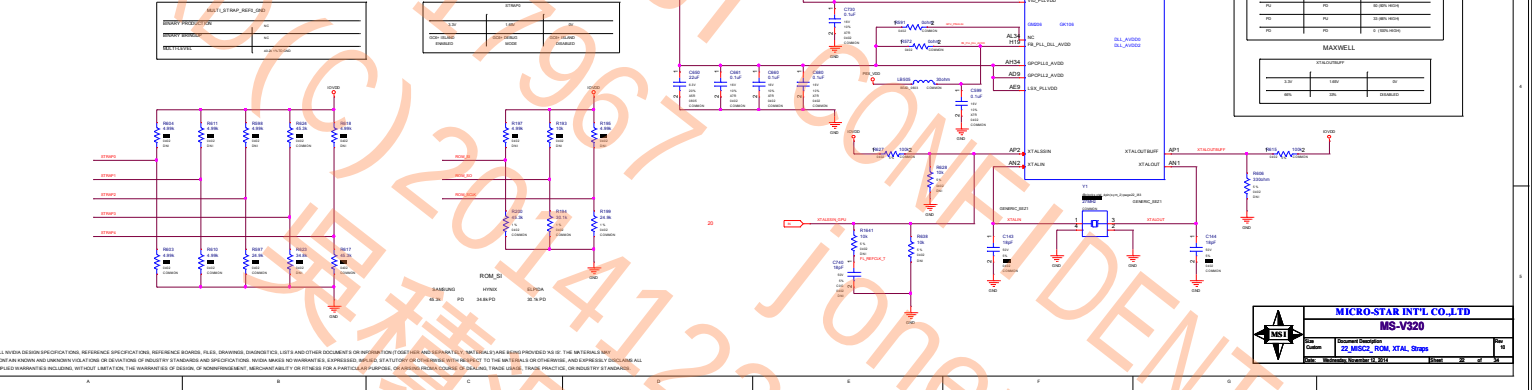


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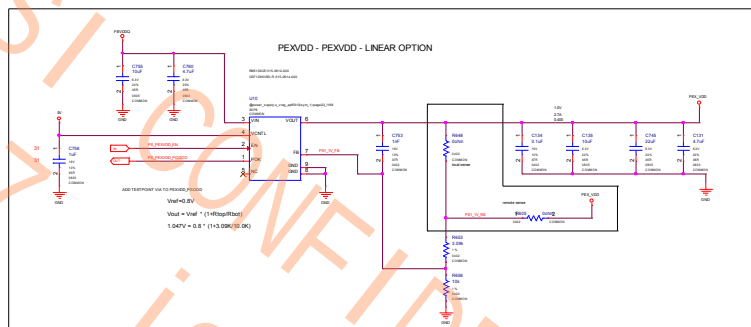
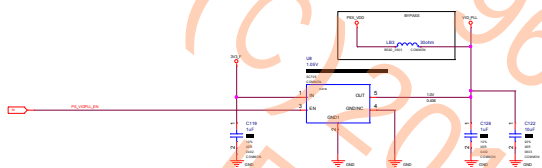
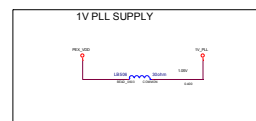
MISC2: ROM, XTAL, Straps

	KEPLER	MAXWELL
STRAP0	USER_BIT [3:0]'	
STRAP1	3IGD_PRDCFG_LUT_ADR'	
STRAP2	PCO_DEVID [3:0]'	
STRAP3	SOR1_EXPOSED [3:0]'	
STRAP4	DP_PLL_VCO_DIV'	
	PEX_MAX_SPEED'	
	PEX_SPD_CHANCE_GEN'	
ROM_S1	RAMCFG[0]'	RAMCFG[0]'
	RAMCFG[1]'	RAMCFG[1]'
	RAMCFG[2]'	RAMCFG[2]'
	RAMCFG[3]'	RAMCFG[3]'
ROM_S0	VGA_DEVICE'	VGA_DEVICE'
	SMB_ALT_ADDR'	SMB_ALT_ADDR'
	FB[0]_APERTURE_SIZE'	PCE_CFO'
	FB[1]_APERTURE_SIZE'	DEVID_SEL'
ROM_SCLK	PEX_PLL_PLN_TERM02'	SOR0_EXPOSED'
	PCO_DEVID_EXT[0]'	SOR1_EXPOSED'
	SUB_VENDOR'	SOR2_EXPOSED'
	PCO_DEVID_EXT[1]'	SOR3_EXPOSED'

QW0	QW1
5A	0000 1000
15A	0001 1001
15B	0010 1010
25A	0011 1011
25B	0100 1100
35A	0101 1101
35B	0110 1110
45A	0111 1111



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	MICRO-STAR INT'L CO.,LTD		
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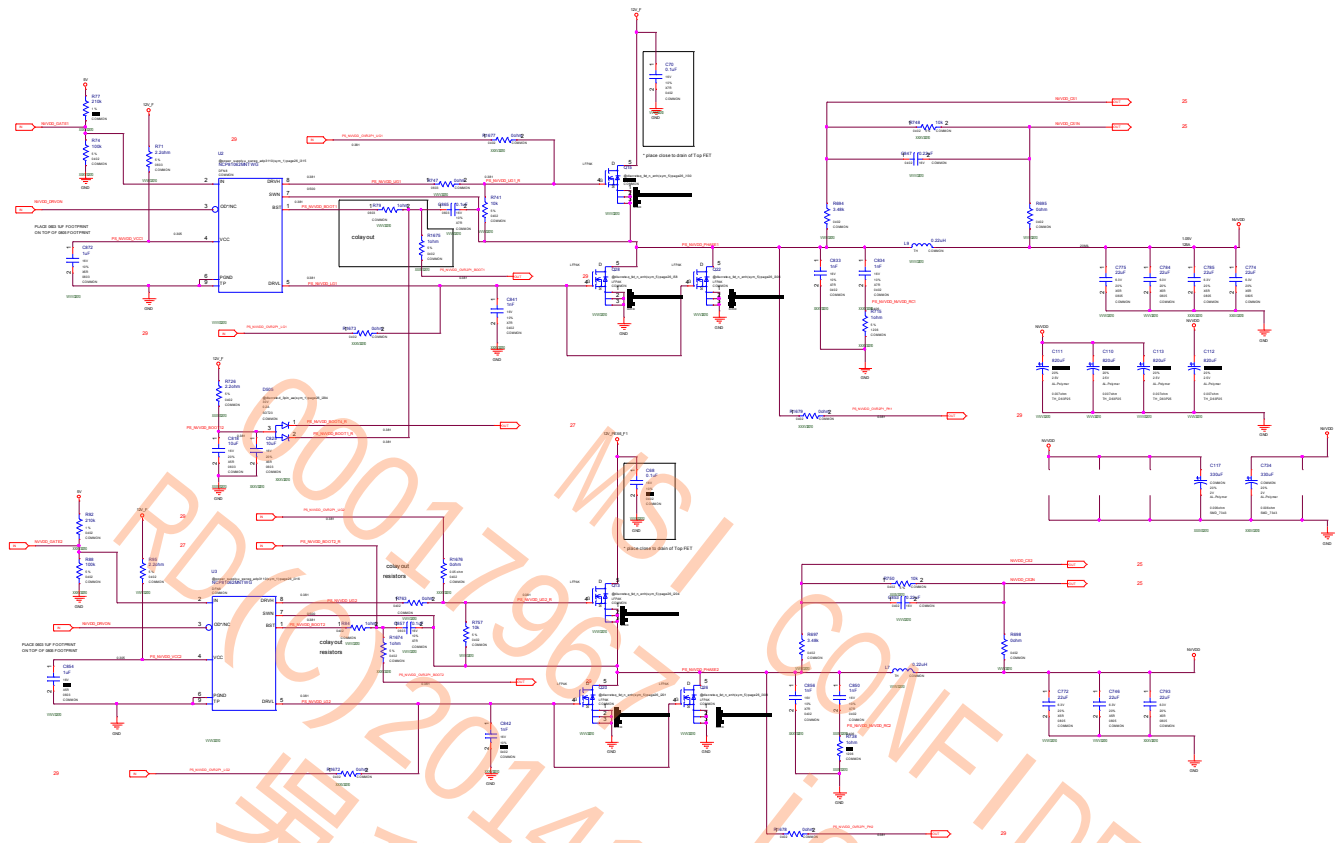


MICRO-STAR INT'L CO.,LTD
MS-V320

MS-V32

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Wednesday, November 12, 2014

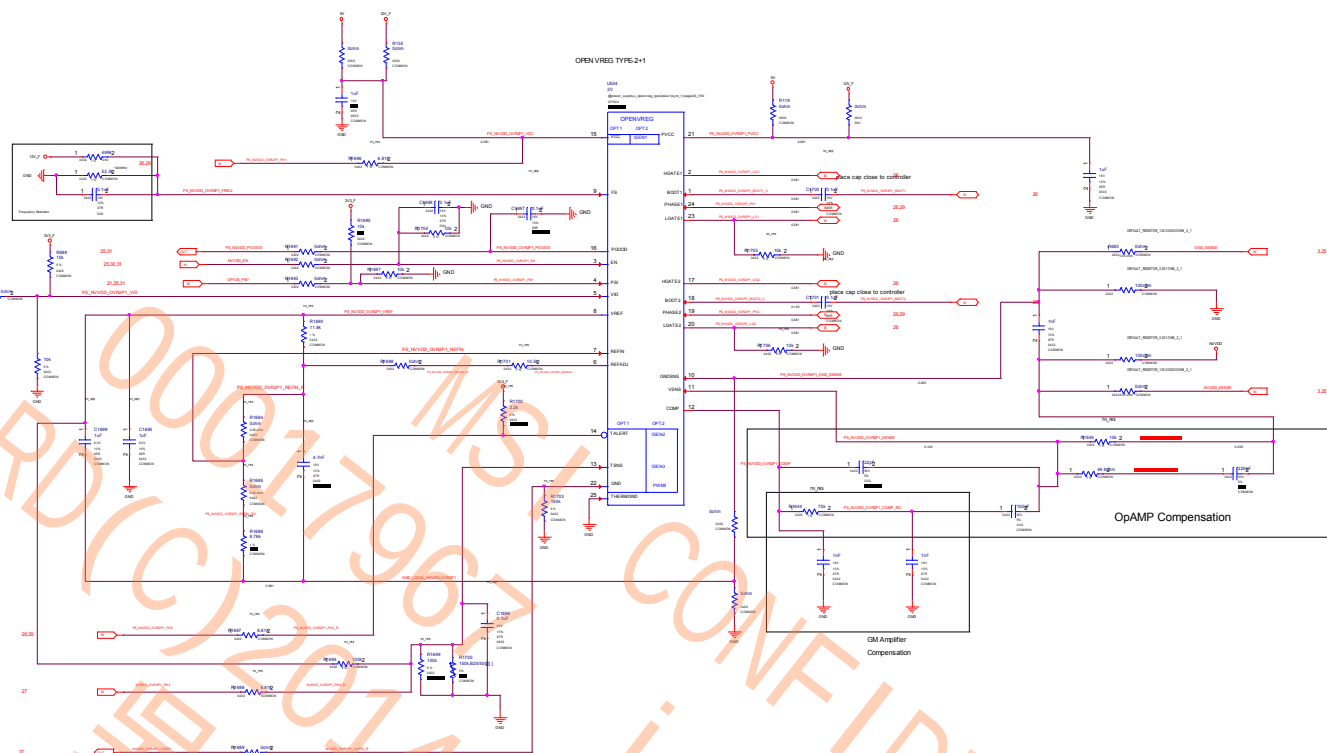


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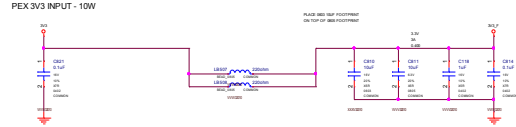


[RESERVED]

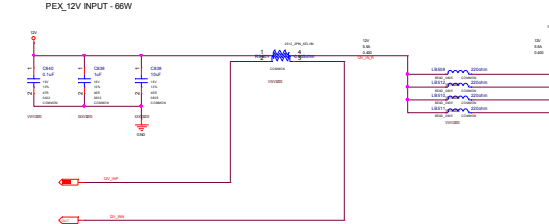
MICRO-STAR INT'L CO., LTD.	
MS-V320	
Rev	01
Issue	01
Date: 2014.11.20	



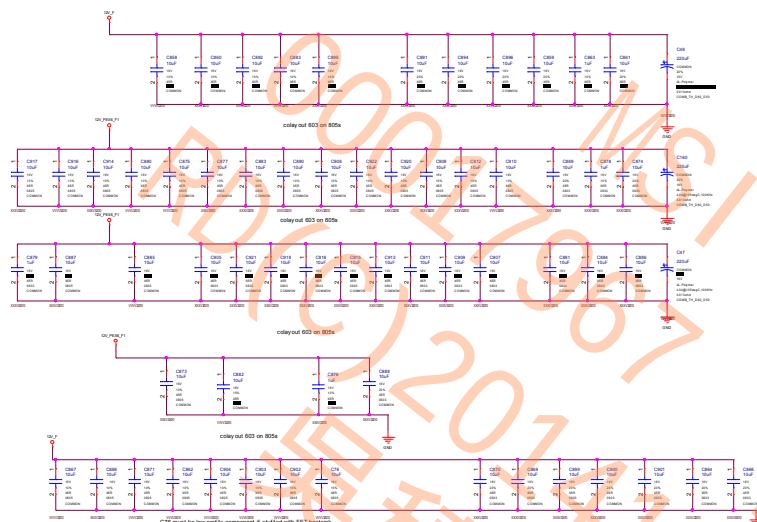
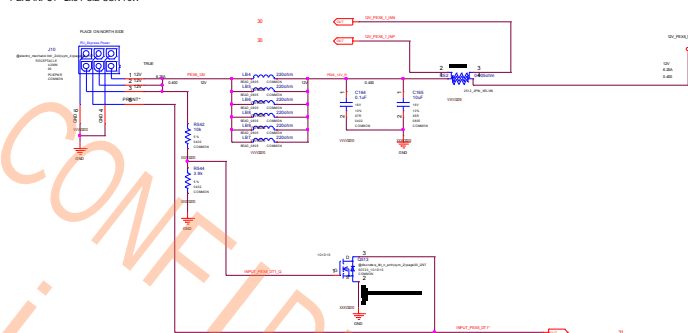
MICRO-STAR INT'L CO., LTD.	
MS-V320	
Document Description	PS_PS_NVVDD_OVR2+1 option
Version	1.0
Created	2014/11/10
Modified	2014/11/10



PEX 12V INPUT - 66W

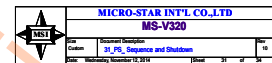


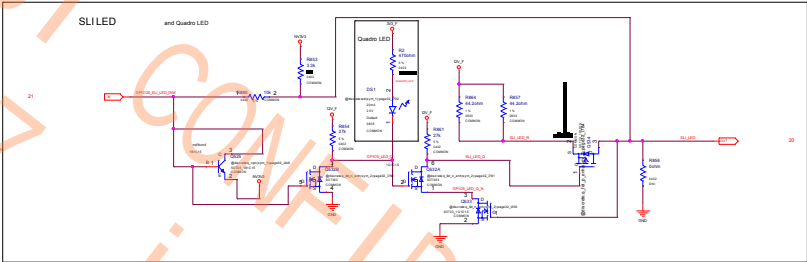
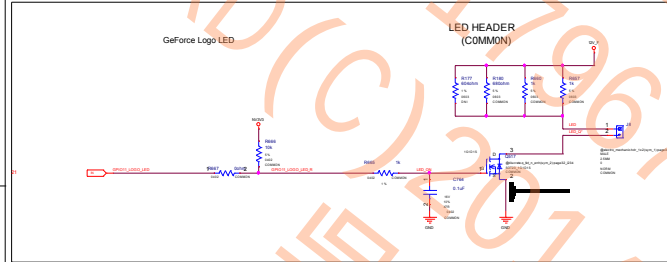
PEX8 INPUT - 2x3 PCIE CON 75W

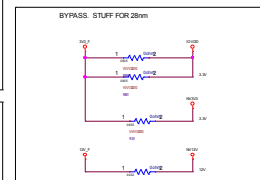
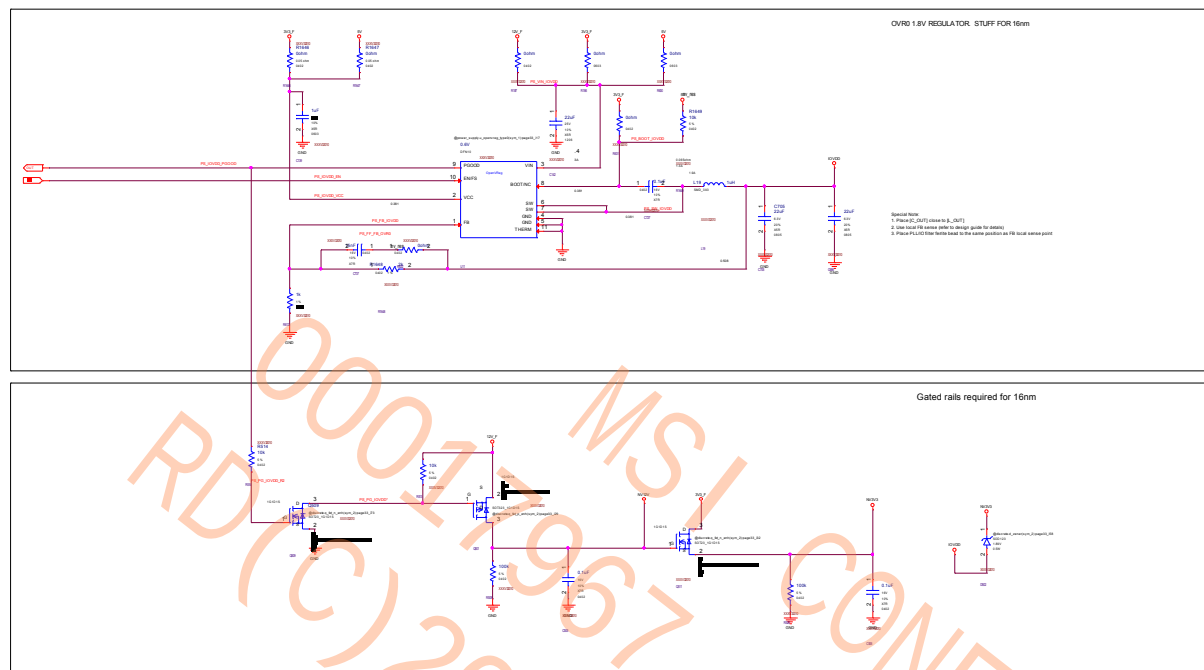


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MS-V320	
Part Number	MS-V320
Revision	1.0
Author	MS-V320 Regulator
Check	MS-V320 Regulator
Drawn	MS-V320 Regulator
Scale	1:1

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