

PG600-B02 NIGHTHAWK

GB3B-384, 6GB GDDR5 128Mx32

DP + HDMI/DP + DP + DP + DVI-I

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03 Remove Jtag
04-18 REMOVE PCMD0/DAT
21 ADD DQG-0500500-005
22 REMOVE: footprint
23 REMOVE: footprint
24 REMOVE: footprint & COLAY/DP
25 REMOVE footprint
26 REMOVE COLAY
30 REMOVE PWR
31-35 REMOVE V217 1.1 NVVDD POWER
36 REMOVE NO STUFF FOR GEFORCE SKUS ONLY / input choke
39 REMOVE
40 Remove Bypass filter option
41 Remove MCU

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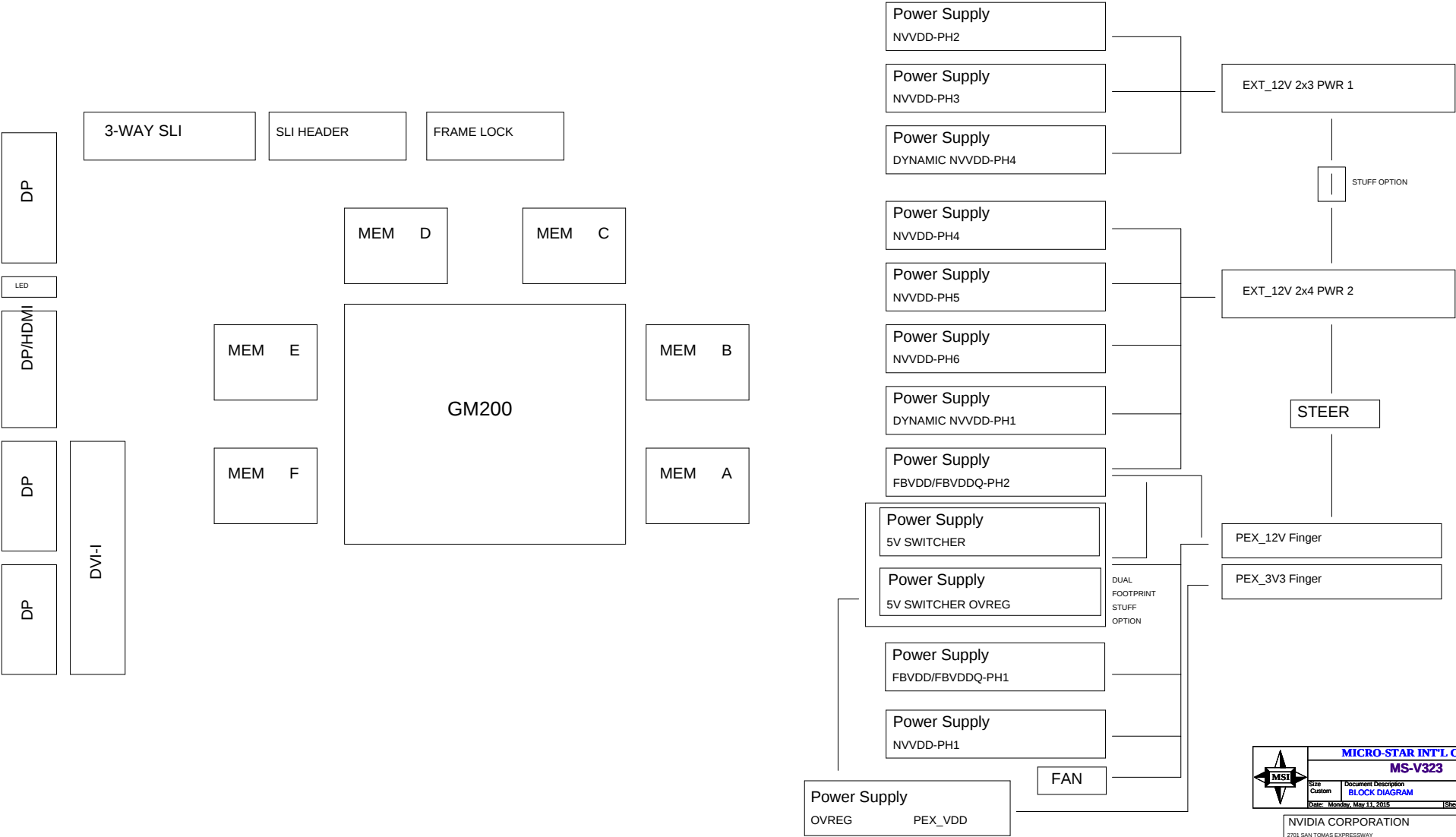
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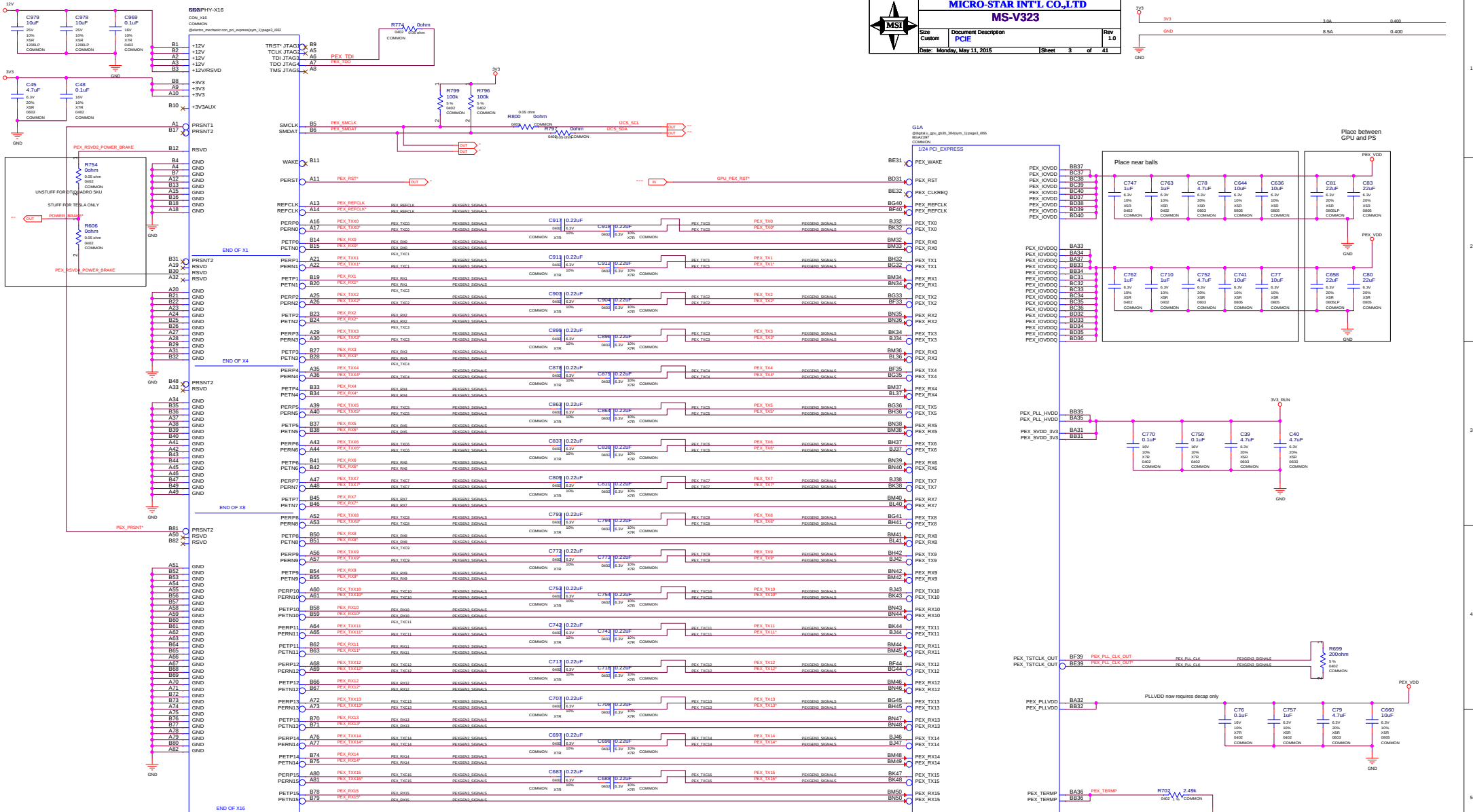
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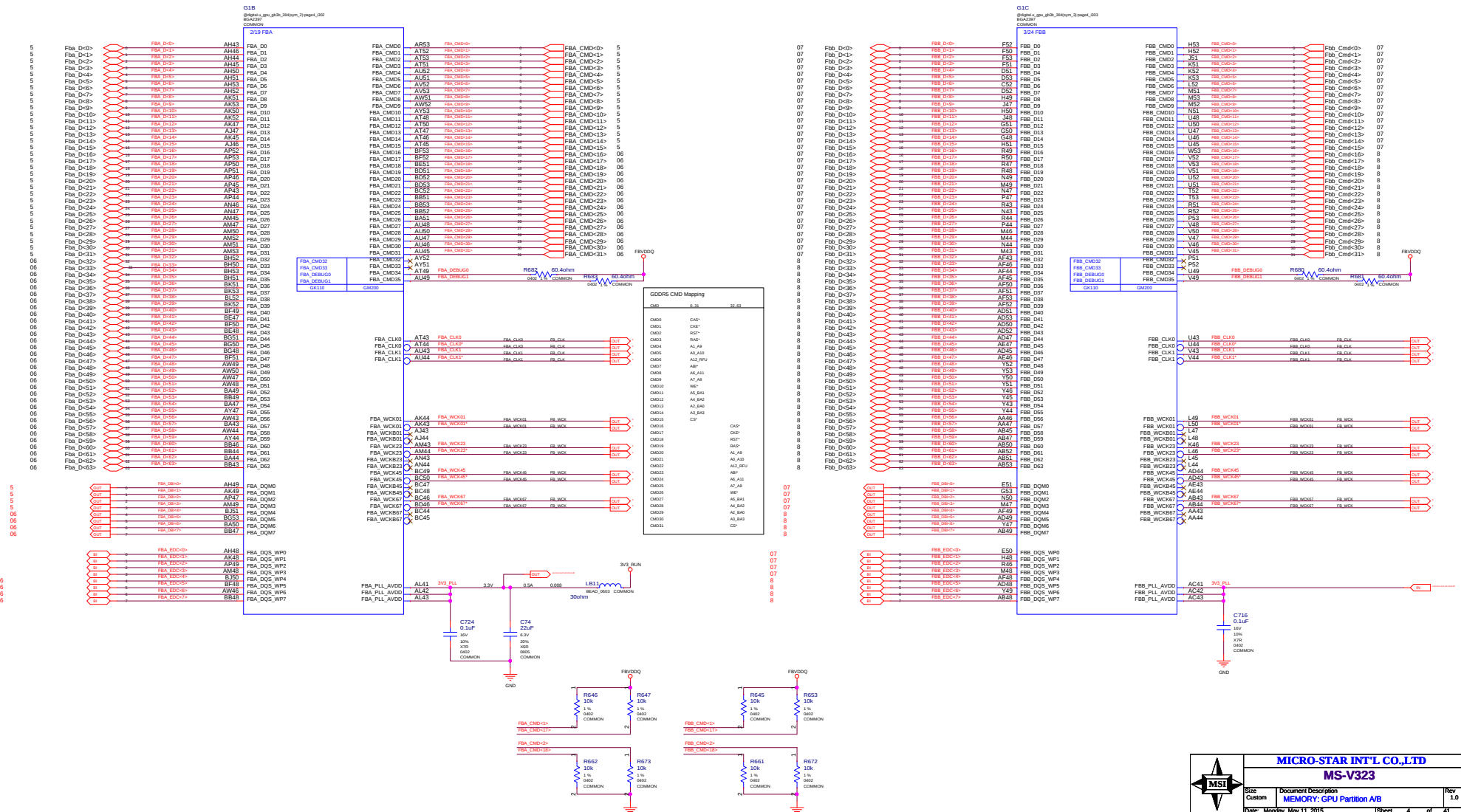




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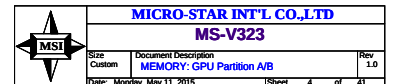
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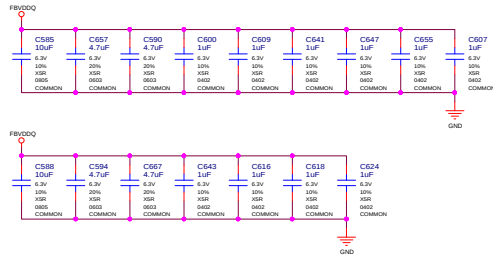
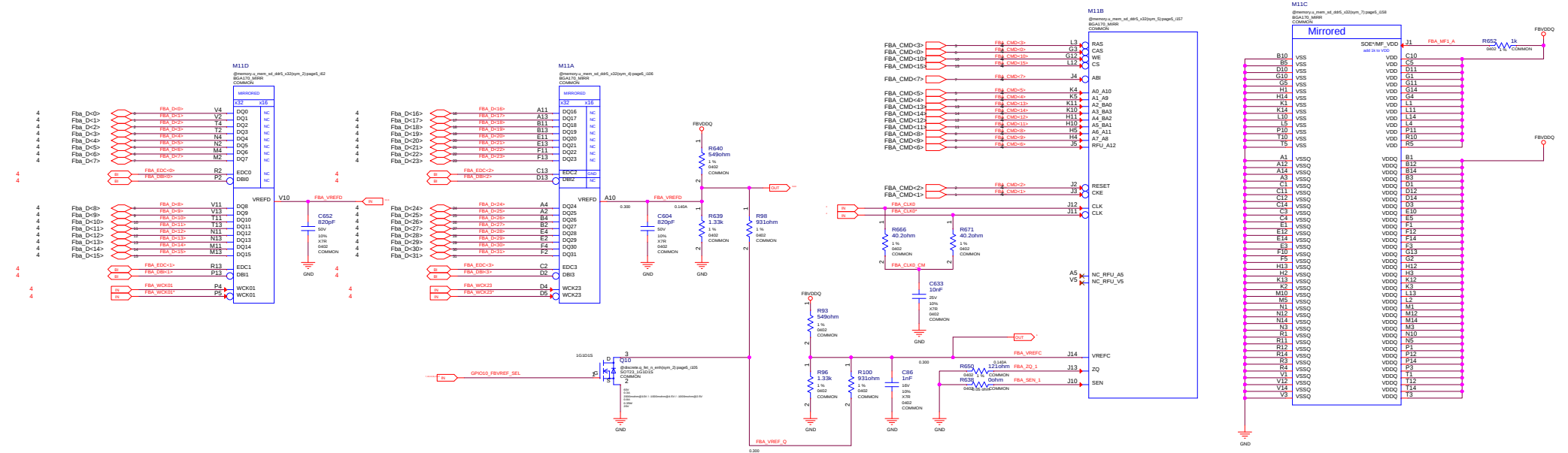


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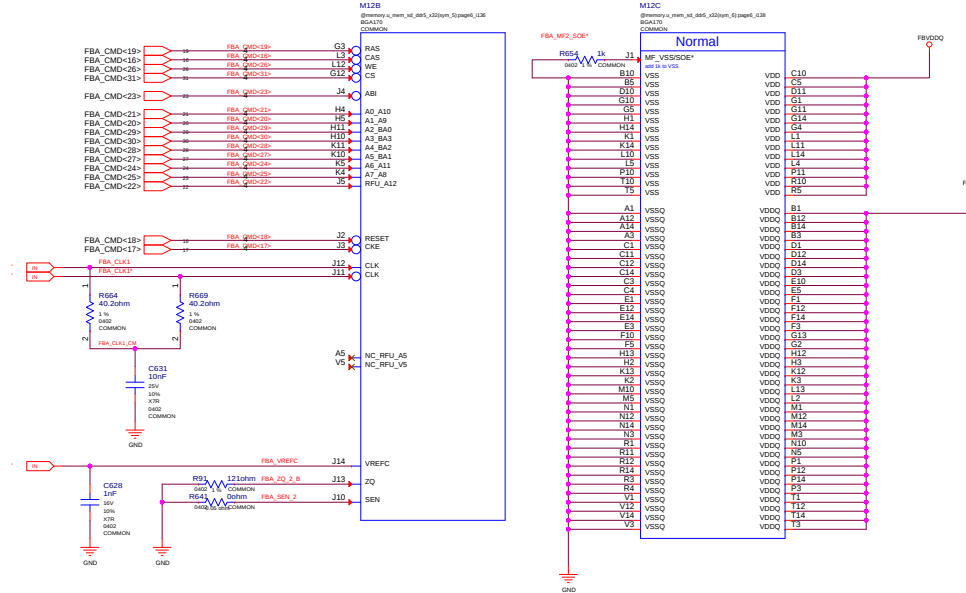
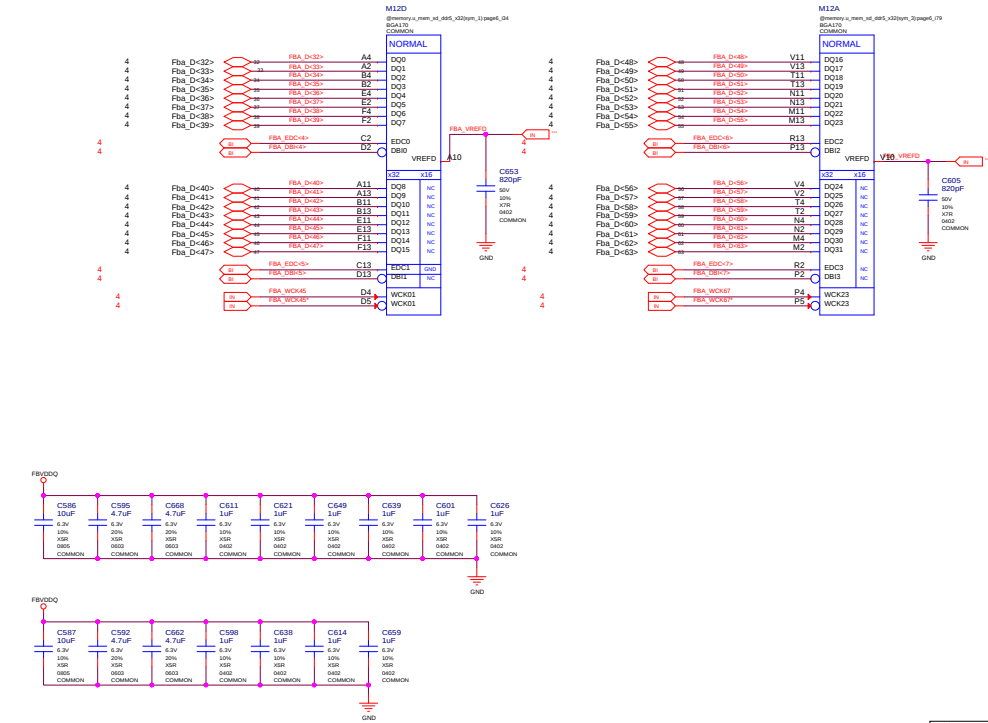
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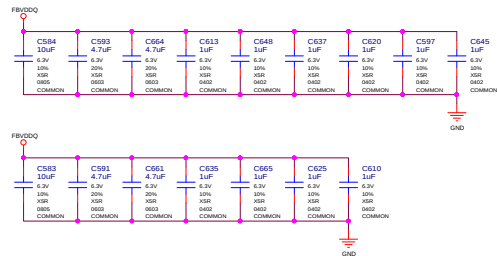
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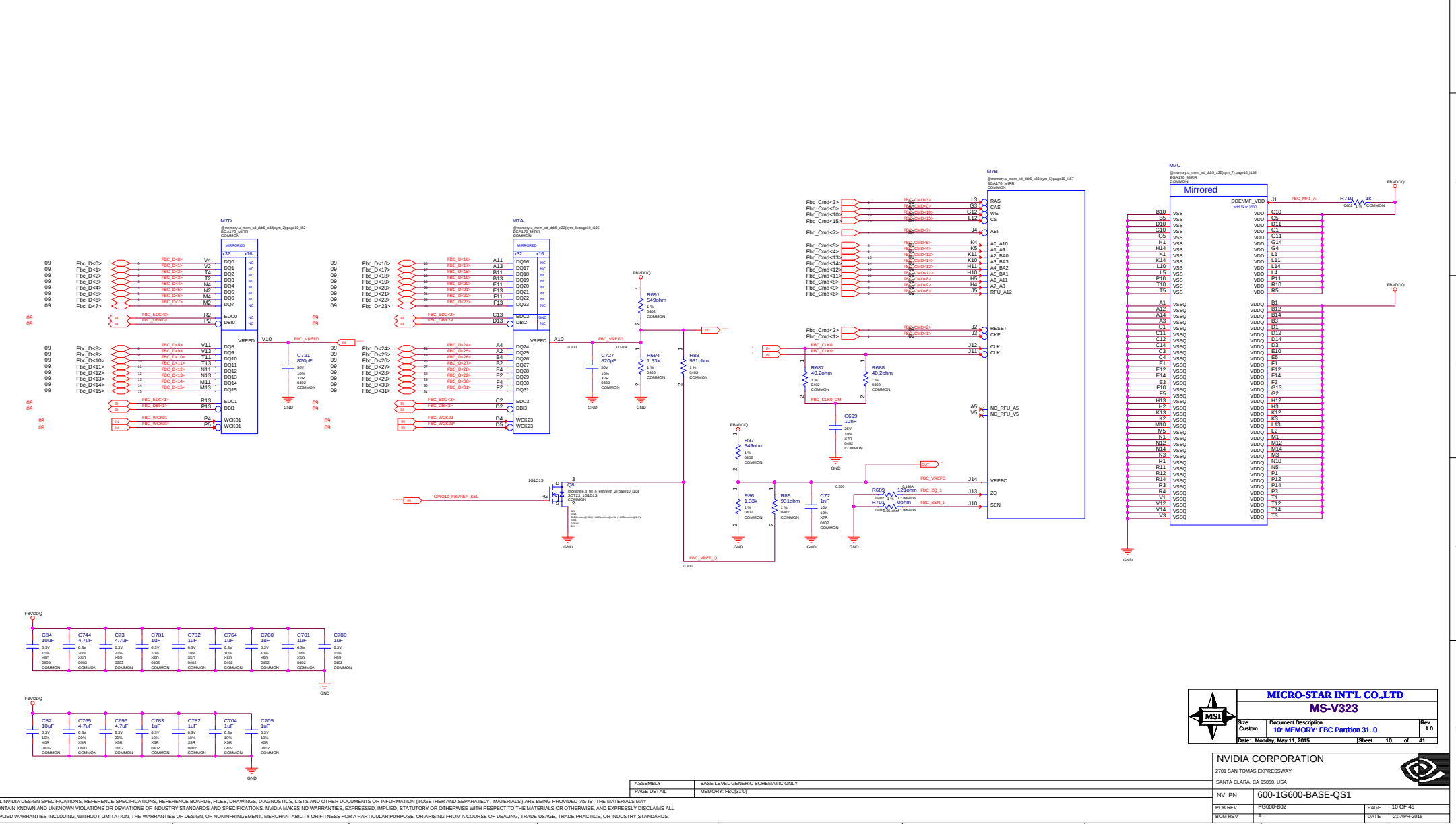


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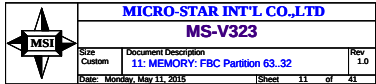
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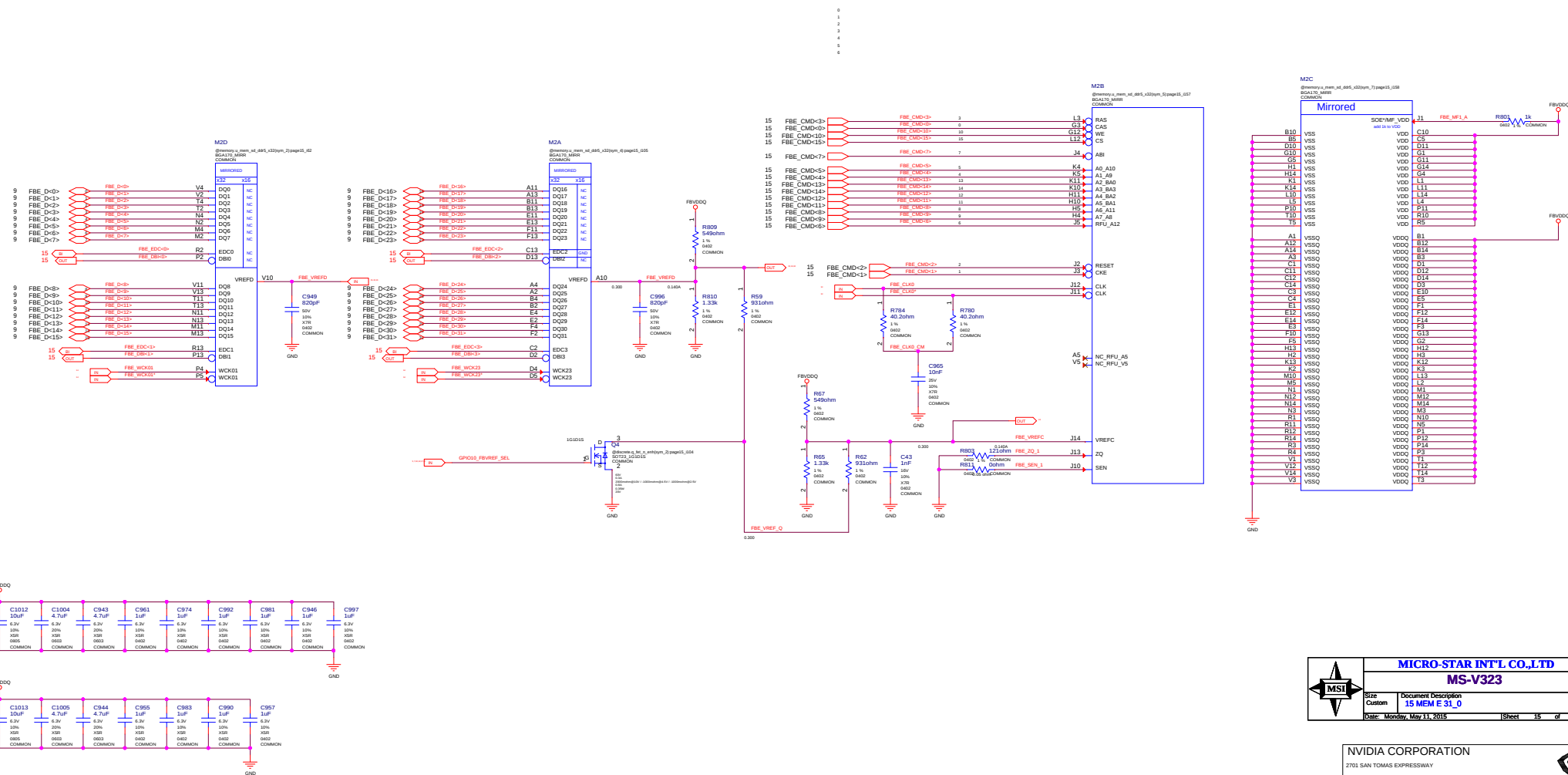
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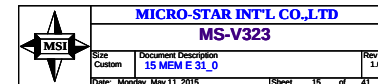


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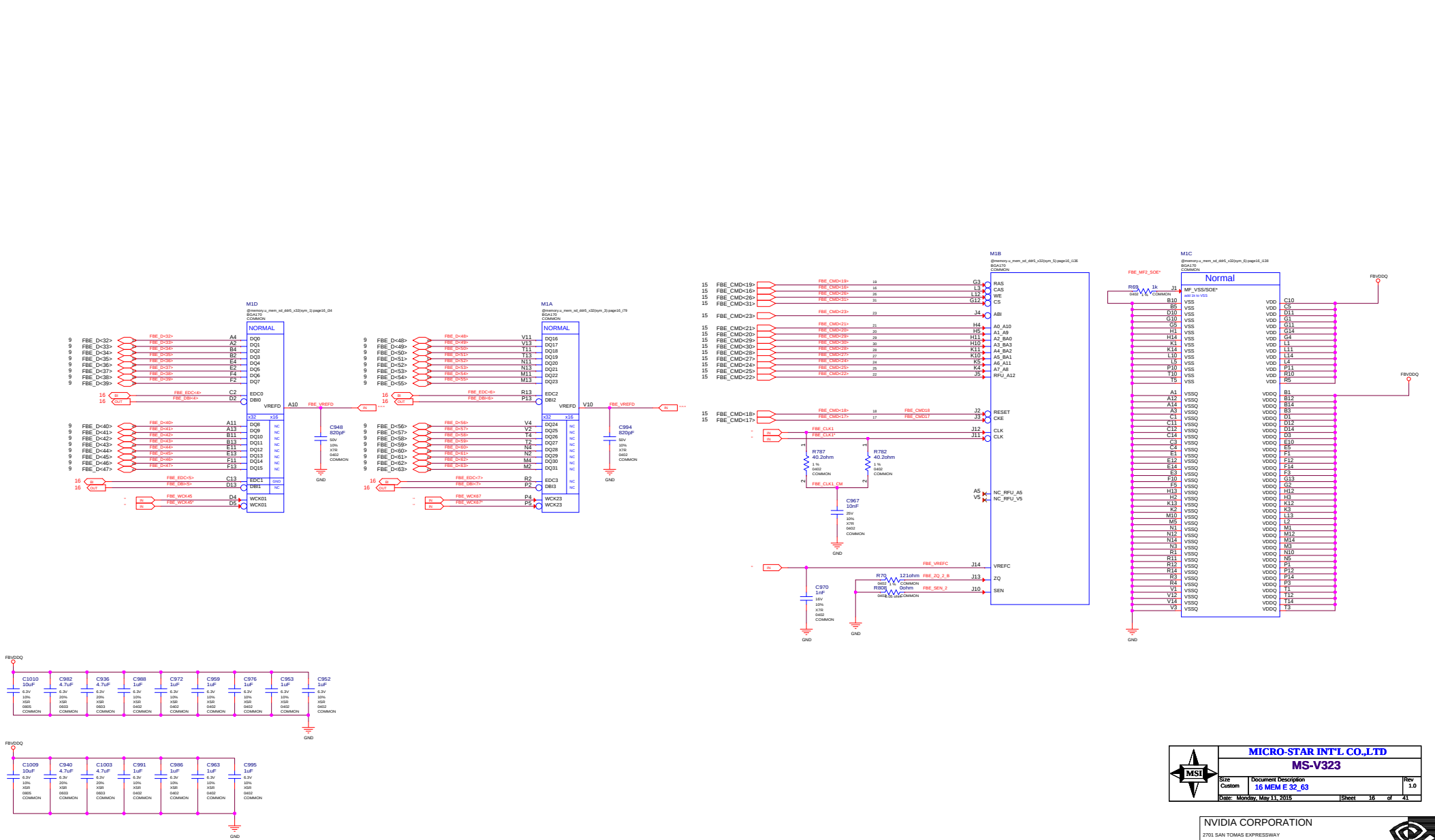
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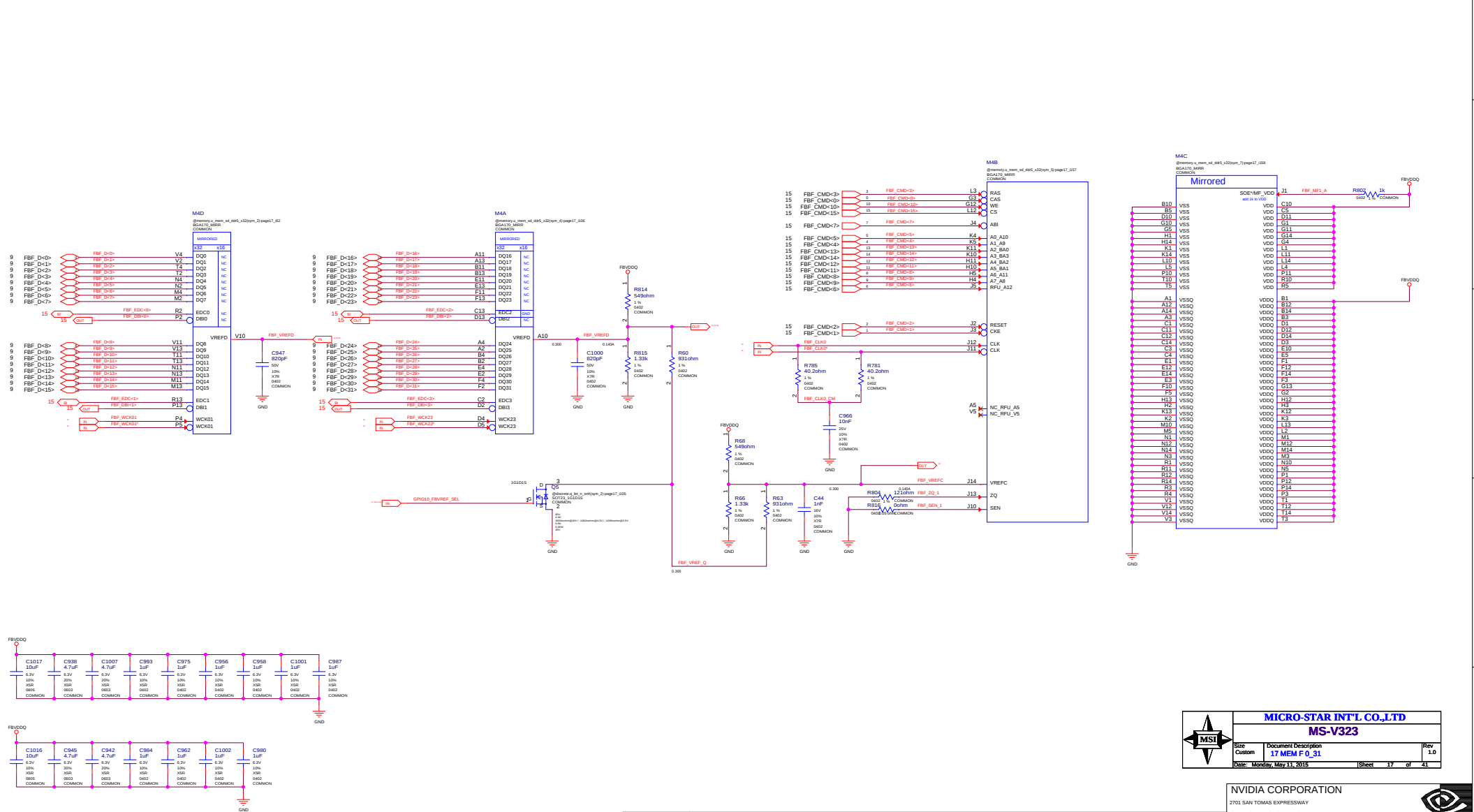
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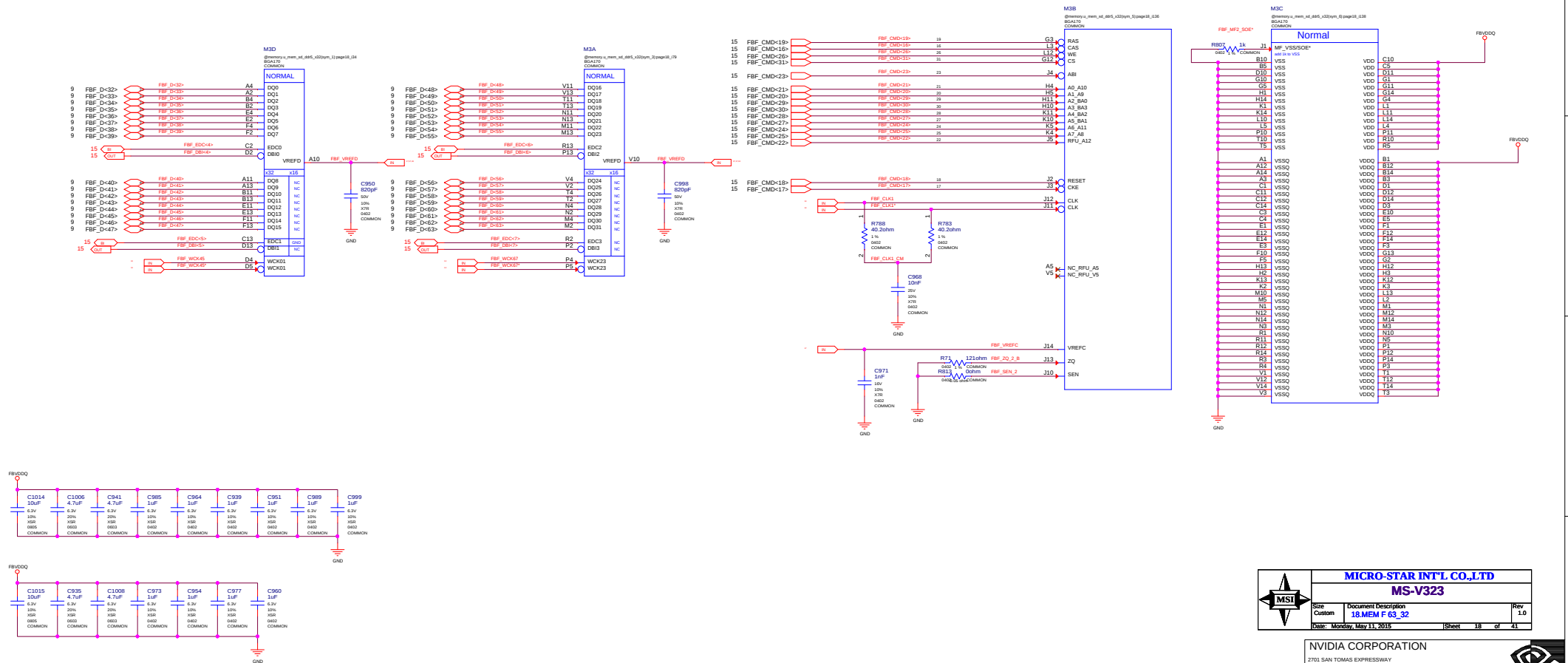
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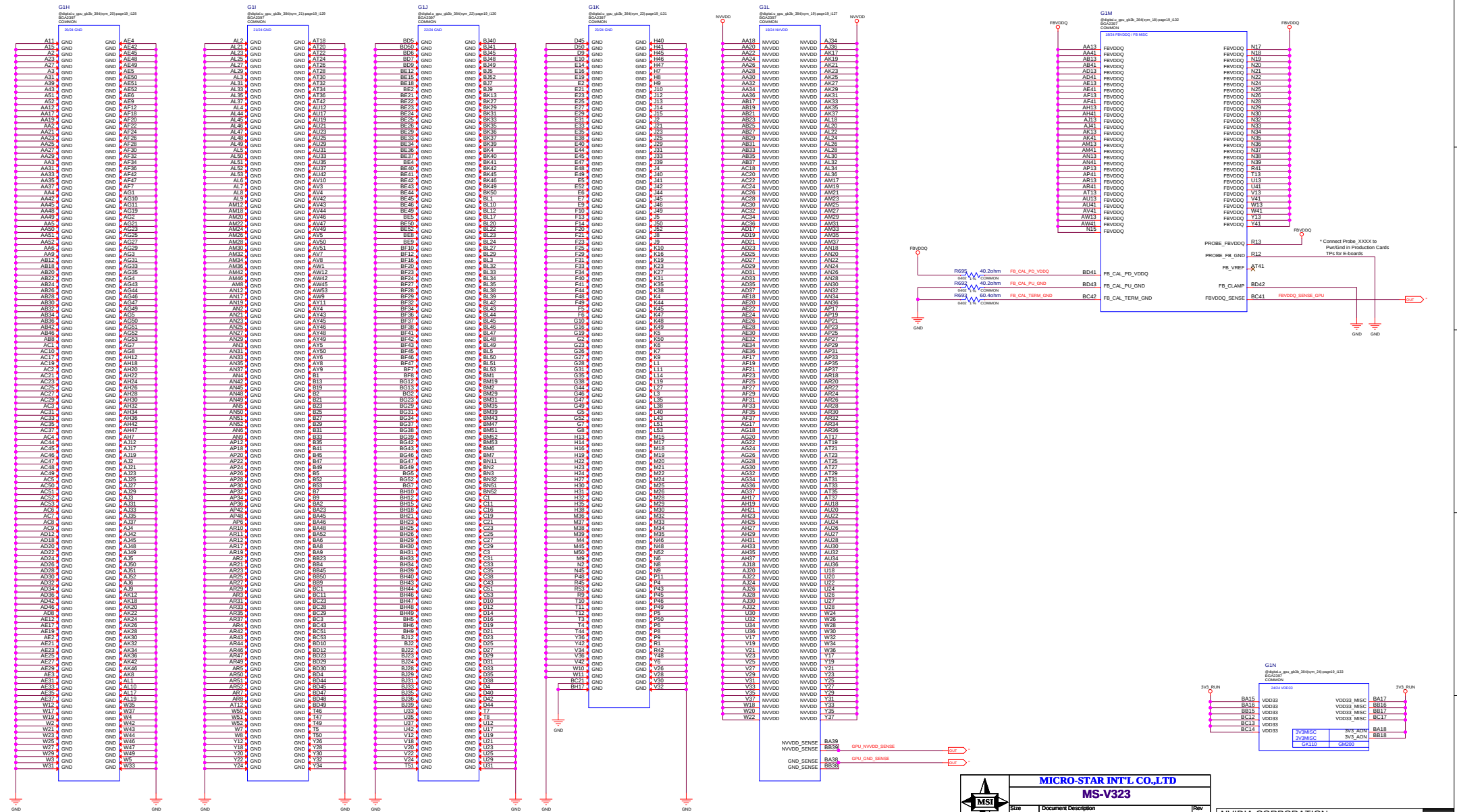
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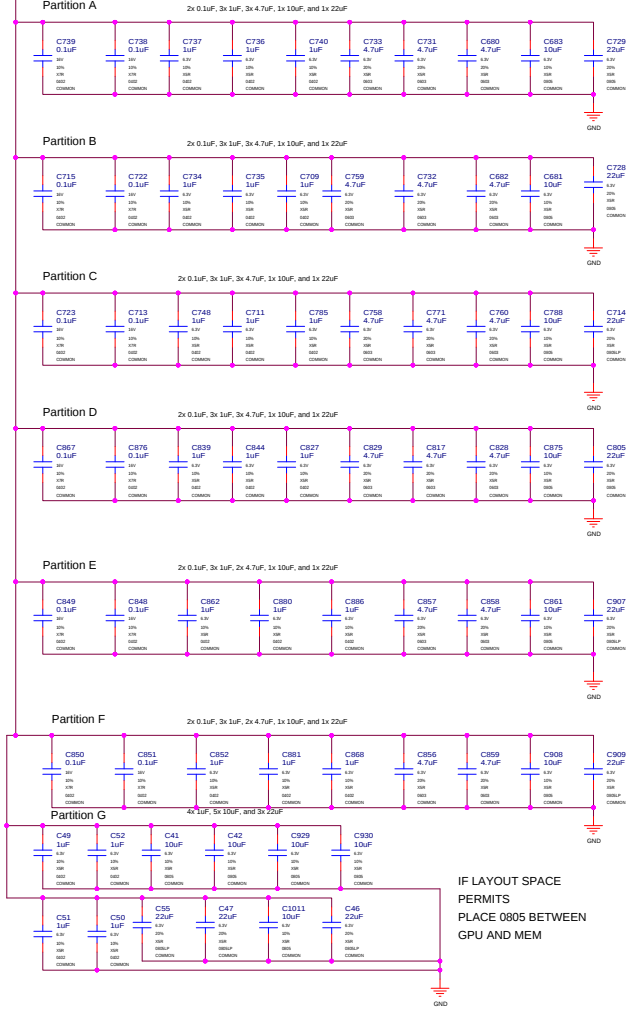
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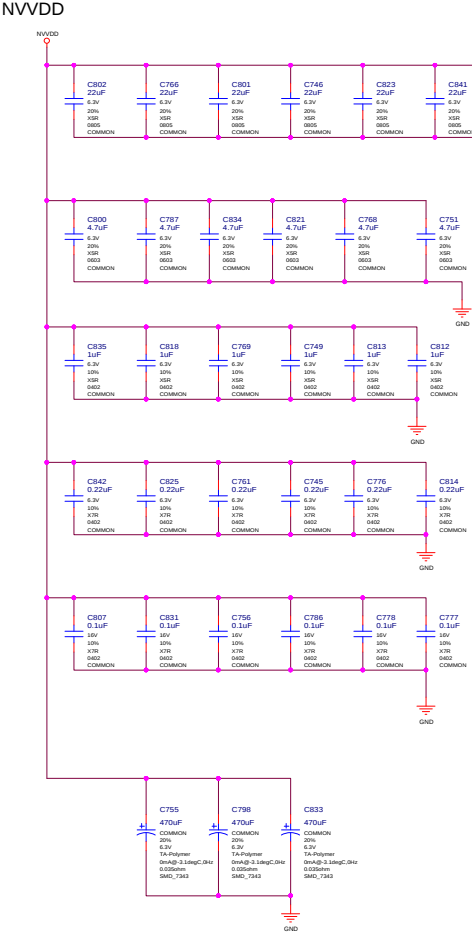
Based on GB3-X GDDR5 FBVDDQ Decap Guideline

FBVDDQ
0.1uF, 1uF, 0402 (Place Under GPU)
4.7uF, 0603 (Place Near GPU)
10uF, 0805 (Place Near GPU)
22uF, 0805 (Place Near GPU)



IF LAYOUT SPACE
PERMITS
PLACE 0805 BETWEEN
GPU AND MEM

NVVDV Decoupling caps. Place under GPU.



8x 22uF, 0805

6x 4.7uF, 0603

6x 1uF, 0402

6x 0.22uF, 0402

6x 0.1uF, 0402

3x 470uF, SMD7343

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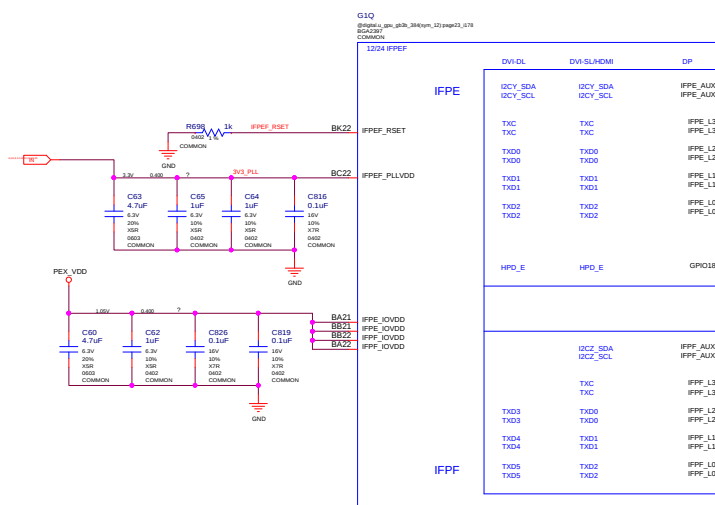
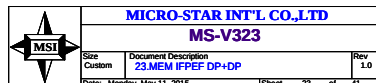


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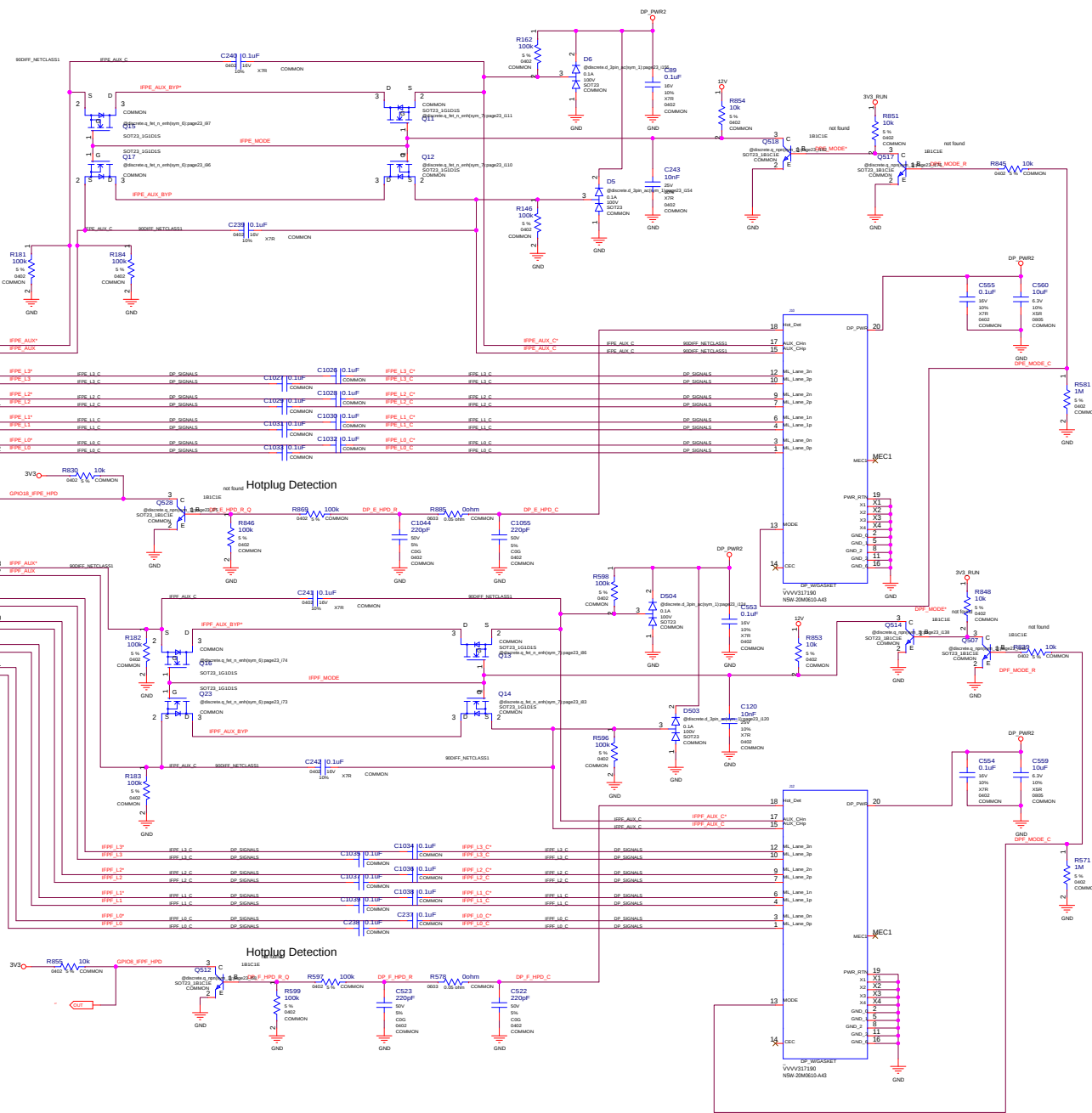
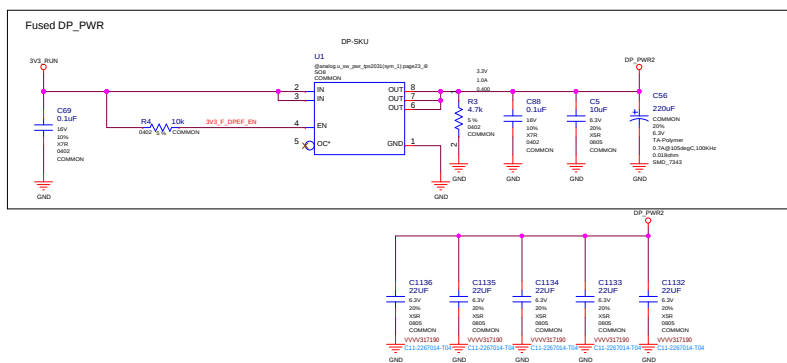
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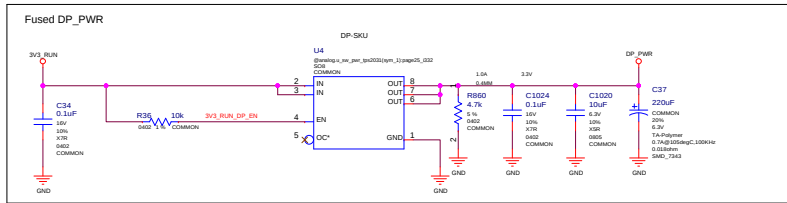


Two cases to be considered:

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



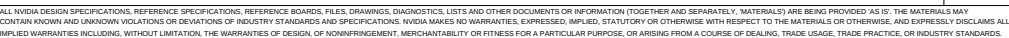
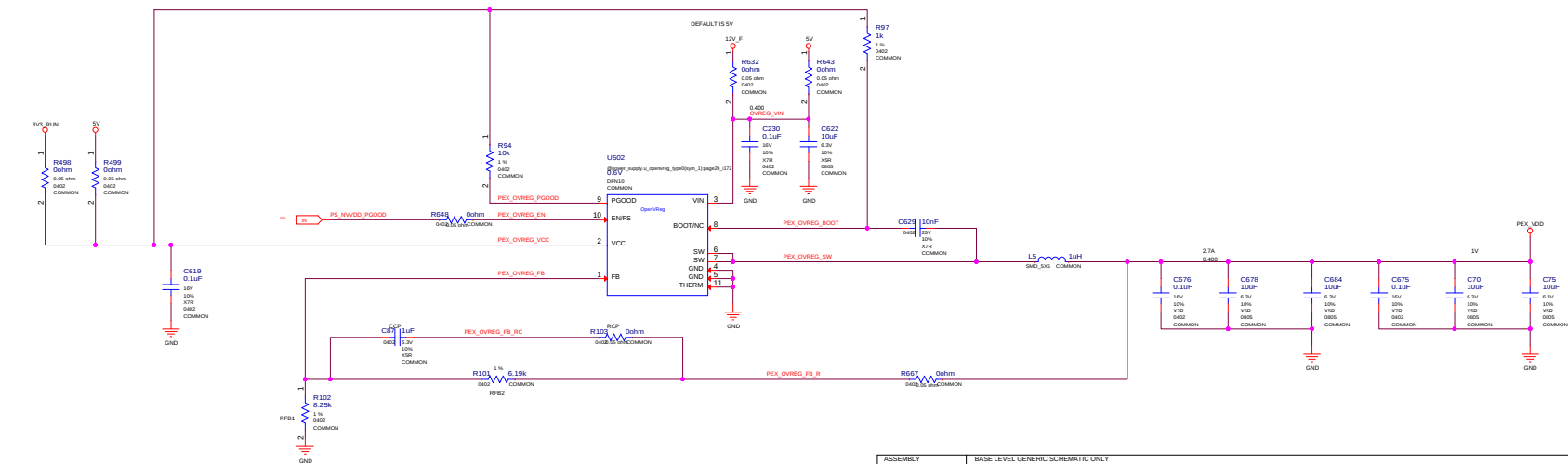
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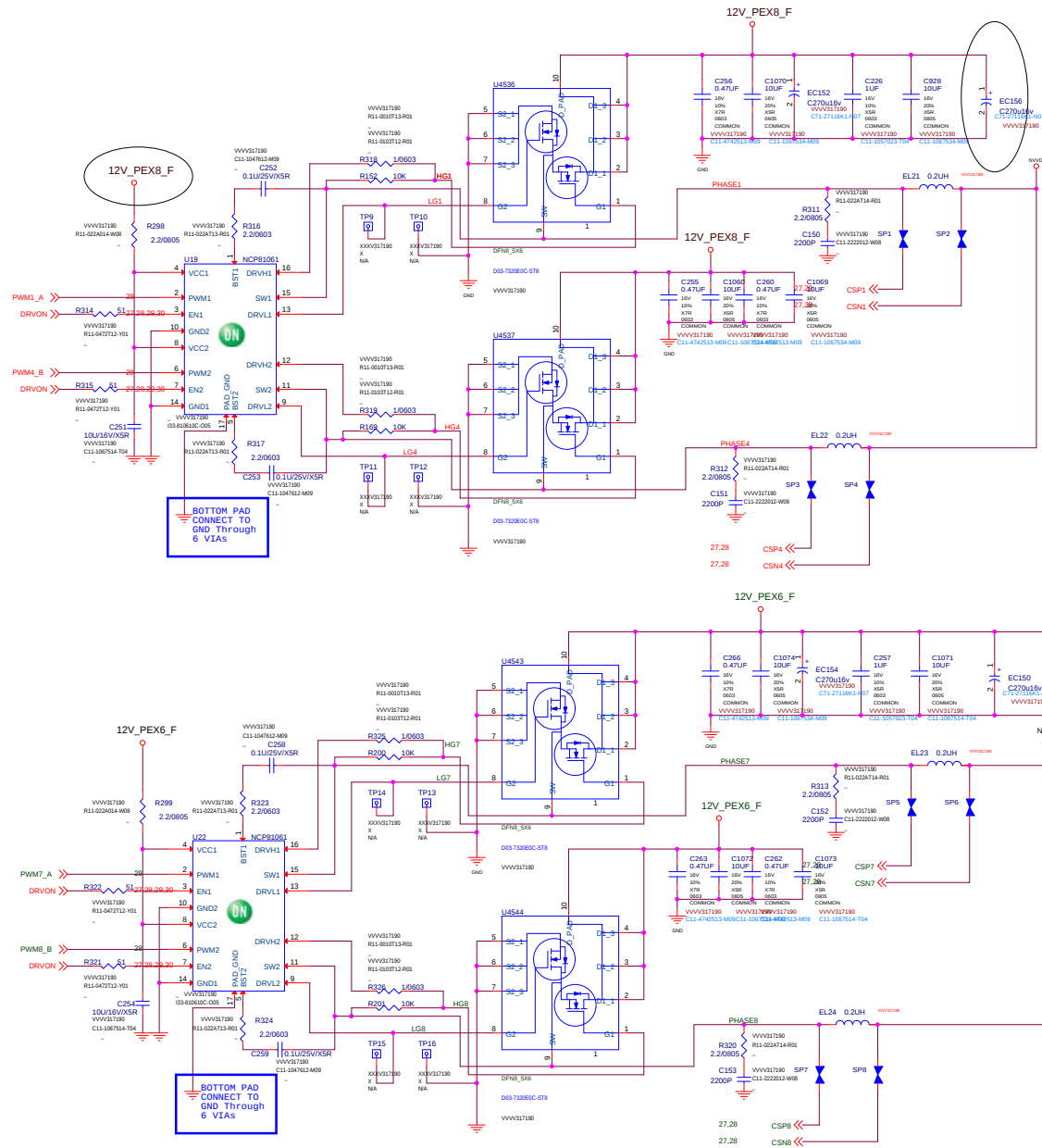
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PAGE DETAIL	MIO/A/B and FRAMELOCK I/F

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ASSEMBLY BASE LEVEL GENERIC SCHEMATIC ONLY
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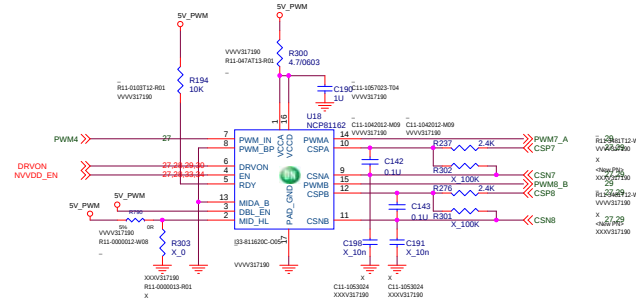
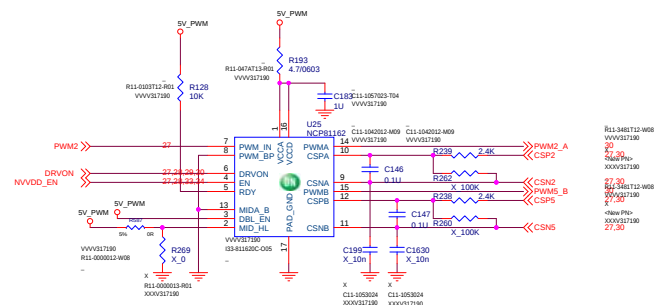
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MS-V323			
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Custom	32 NVVDD 1/47/8	1.0	
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	PS: NVDD Phase 5,6

VID Table

GPI04 VID_5	GPI03 VID_4	GPI02 VID_3	GPI01 VID_2	GPI00 VID_1	VOUT
0	0	0	0	0	1.2125V
0	0	0	0	1	1.2000V
0	0	0	1	0	1.1875V
0	0	0	1	1	1.1750V
0	0	1	0	0	1.1625V
0	0	1	0	1	1.1500V
0	0	1	1	0	1.1375V
0	0	1	1	1	1.1250V
0	1	0	0	0	1.1125V
0	1	0	0	1	1.1000V
0	1	0	1	0	1.0875V
0	1	0	1	1	1.0750V
0	1	1	0	0	1.0625V
0	1	1	0	1	1.0500V
0	1	1	1	0	1.0375V
0	1	1	1	1	1.0250V
1	0	0	0	0	1.0125V
1	0	0	0	1	1.0000V
1	0	0	1	0	0.9875V
1	0	0	1	1	0.9750V
1	0	1	0	0	0.9625V
1	0	1	0	1	0.9500V
1	0	1	1	0	0.9375V
1	0	1	1	1	0.9250V
1	1	0	0	0	0.9125V
1	1	0	0	1	0.9000V
1	1	1	0	1	0.8875V
1	1	0	1	1	0.8750V
1	1	1	1	0	0.8625V
1	1	1	0	1	0.8500V
1	1	1	1	0	0.8375V
1	1	1	1	1	0.8250V





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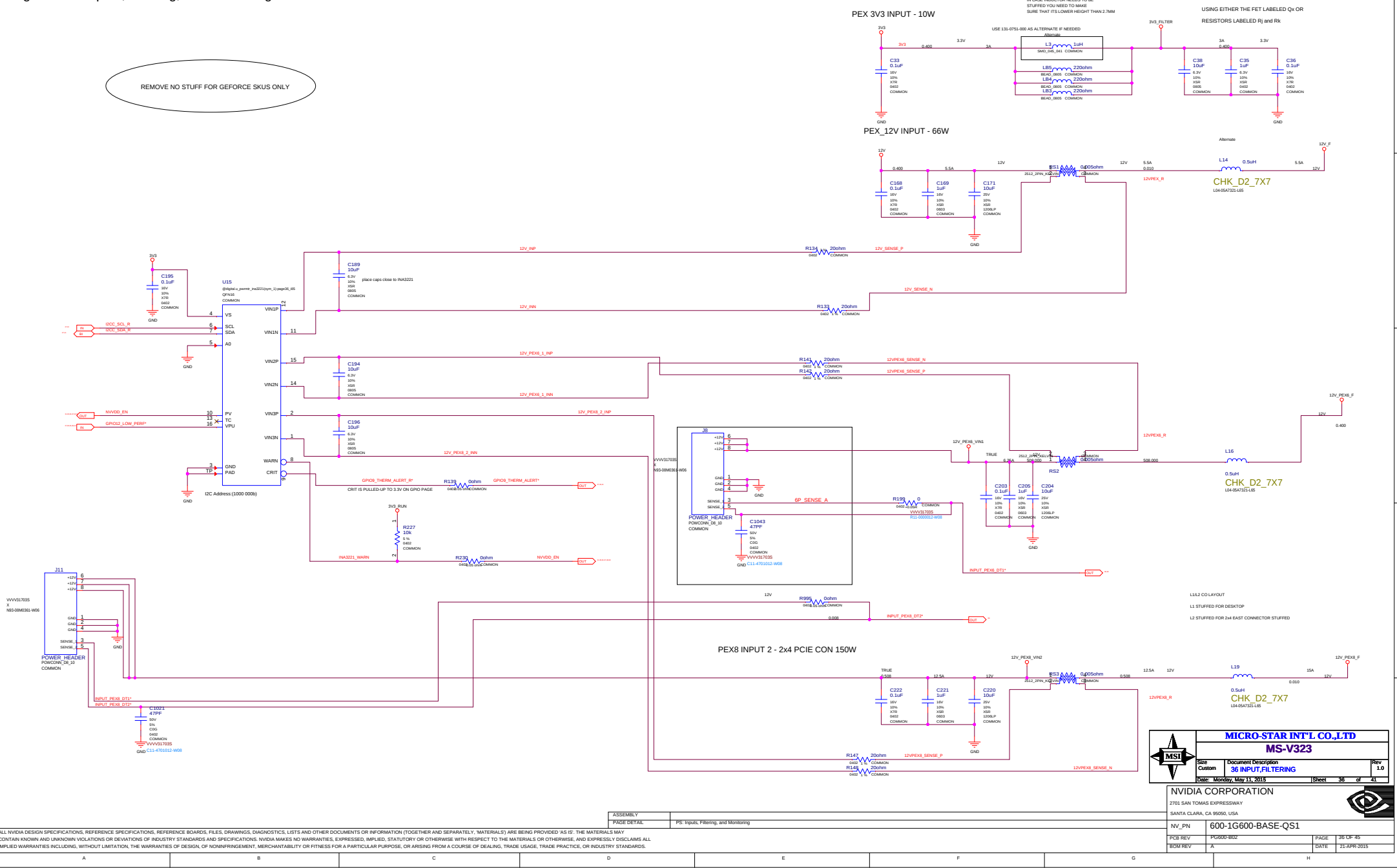
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IFP_IOVDD (backdrive prevention)

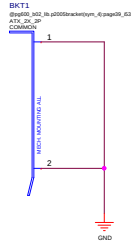


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Brackets:

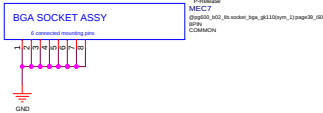
DVIDVI_HDMI_DP: 151-10001-0441-092



Bracket Screw



GPU SOCKET



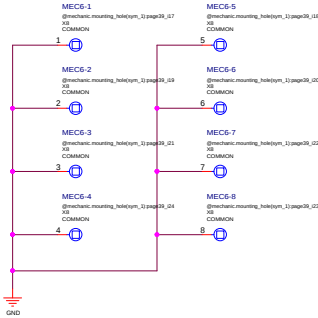
GPU Stiffener (BRING-UP COOLER)



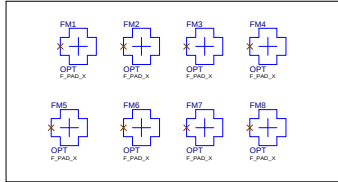
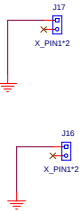
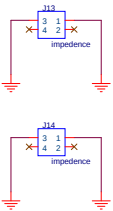
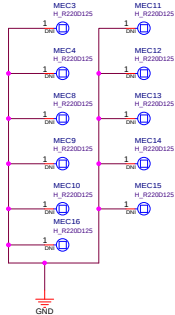
Stiffener



Bring Up Cooler



Mechanical Holes Symbol



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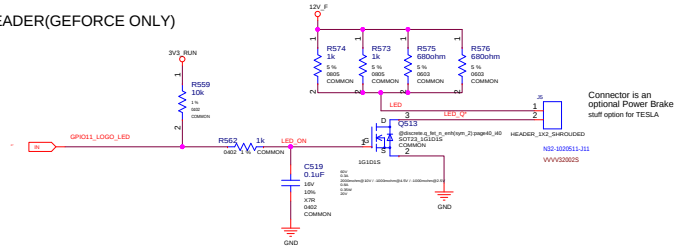


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LED HEADER(GEFORCE ONLY)

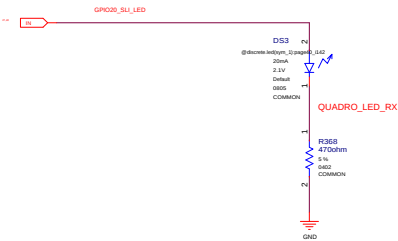


Remove Bypass filter option

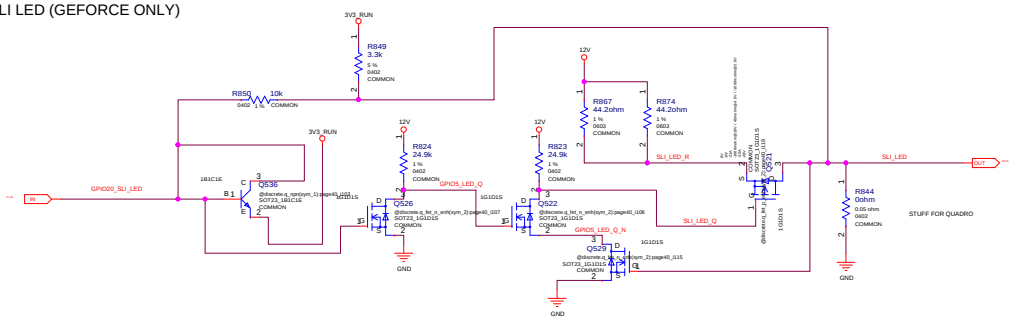
USING GPIO5 TO DIM
GEFORCE AND SLI LED



LED FOR QUADRO
PLACE BETWEEN DP Connectors
CONNECTORS



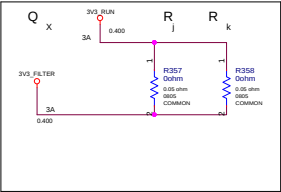
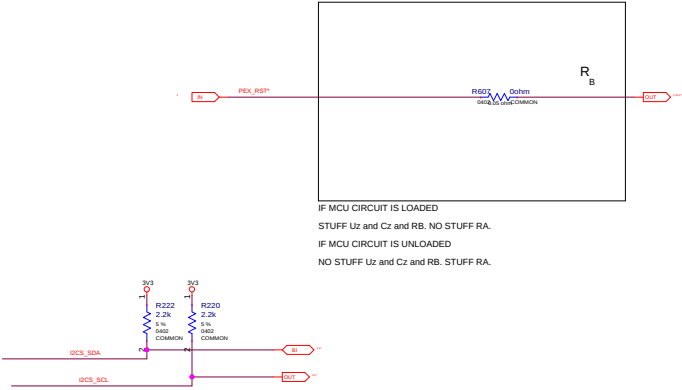
SLI LED (GEFORCE ONLY)



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